Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware

Bjorn Andersson

Dionisio de Niz

Mark Klein

Softw are Engineering Institute Carnegie Mellon University Pittsburgh, PA 15213

[DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution.

Carnegie Mellon University Software Engineering Institute Copyright 2022 Carnegie Mellon University.

This material is based upon work funded and supported by the Department of Defense under Contract No. FA8702-15-D-0002 with Carnegie Mellon University for the operation of the Software Engineering Institute, a federally funded research and development center.

The view, opinions, and/or findings contained in this material are those of the author(s) and should not be construed as an official Government position, policy, or decision, unless designated by other documentation.

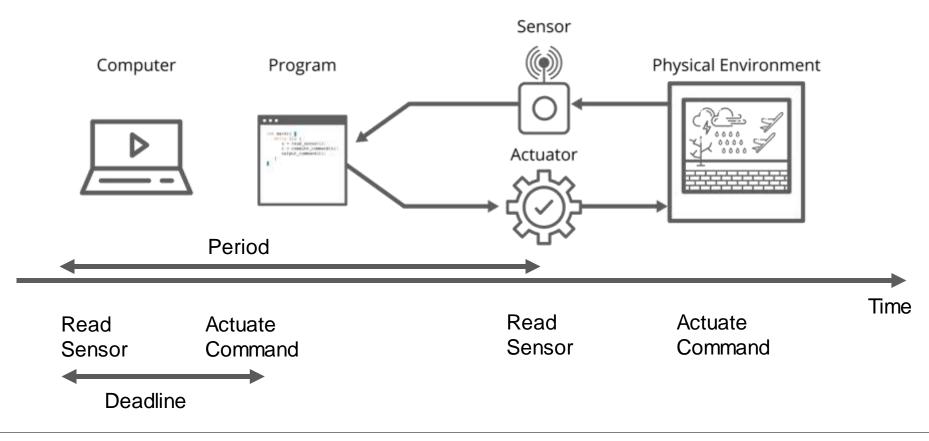
NO WARRANTY. THIS CARNEGIE MELLON UNIVERSITY AND SOFTWARE ENGINEERING INSTITUTE MATERIAL IS FURNISHED ON AN "AS-IS" BASIS. CARNEGIE MELLON UNIVERSITY MAKES NO WARRANTIES OF ANY KIND, EITHER EXPRESSED OR IMPLIED, AS TO ANY MATTER INCLUDING, BUT NOT LIMITED TO, WARRANTY OF FITNESS FOR PURPOSE OR MERCHANTABILITY, EXCLUSIVITY, OR RESULTS OBTAINED FROM USE OF THE MATERIAL. CARNEGIE MELLON UNIVERSITY DOES NOT MAKE ANY WARRANTY OF ANY KIND WITH RESPECT TO FREEDOM FROM PATENT, TRADEMARK, OR COPYRIGHT INFRINGEMENT.

[DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution. Please see Copyright notice for non-US Government use and distribution.

This material may be reproduced in its entirety, without modification, and freely distributed in written or electronic form without requesting formal permission. Permission is required for any other use. Requests for permission should be directed to the Software Engineering Institute at permission@sei.cmu.edu.

DM22-0733

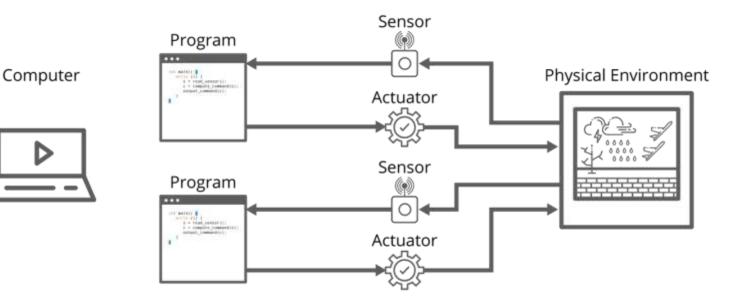
Single-core real-time systems



Carnegie Mellon University Software Engineering Institute

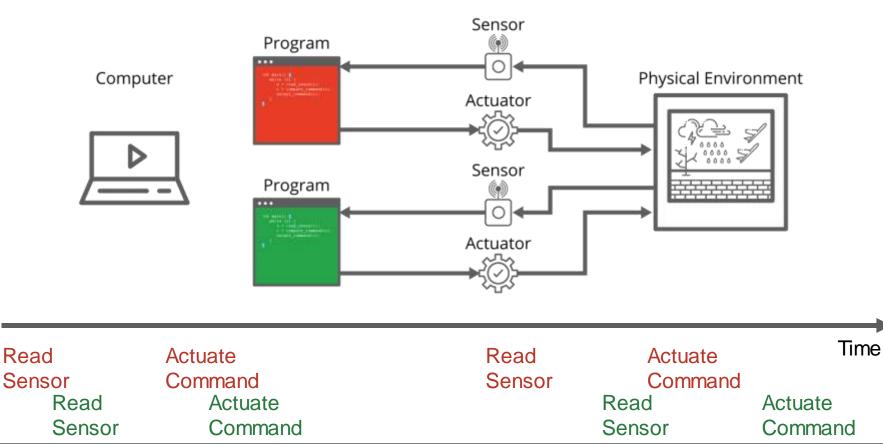
Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Carnegie Melon University [DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution.

Single-core real-time systems



Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Carnegie Melon University

Single-core real-time systems



Carnegie Mellon University Software Engineering Institute

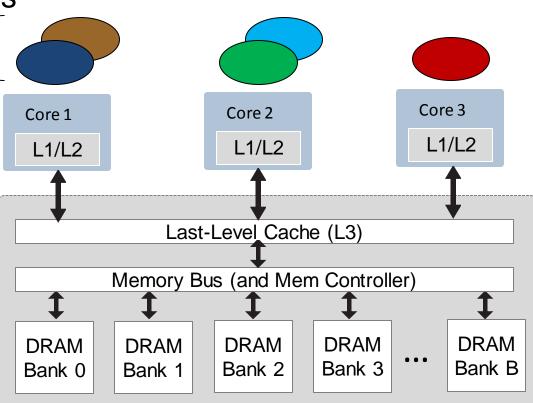
Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Camegie Melon University [DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution.

Multicore real-time systems

Processes -

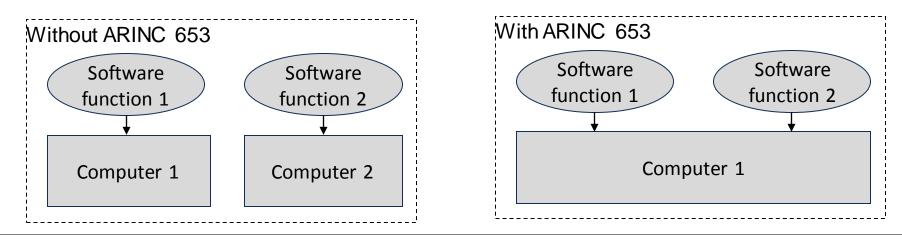
Issues

- Shared hardware resources impact timing.
- 103 times slowdown has been observed [Yun15].
- Most current methods cannot deal with undocumented resources.
- Even when resources are documented, current methods can only analyze/manage a small set of them.
- The problem is getting worse:
 - * Slowdown increasing
 - * More undocumented h/w



ARINC 653

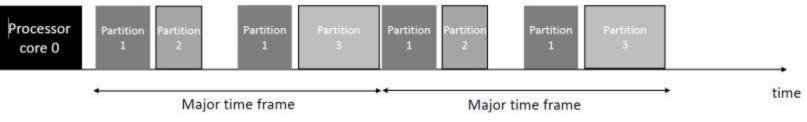
 Standard that facilitates integration of many software subsystems (called partitions) on a single computer



Carnegie Mellon University Software Engineering Institute

Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Canegie Melon University [DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution.

- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- ARINC 653 initially only supported single-core systems



- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- Today, ARINC 653 supports multicore systems

- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- Today, ARINC 653 supports multicore systems

2.2.1 Core Modules with Multiple Processor Cores

This standard includes support for the ARINC 653 services to be utilized with a core module that contains a single or multiple processor cores. With multiple processor cores, additional scheduling paradigms are feasible on a core module:

- Multiple processes within a partition scheduled to execute concurrently on different processor cores.
- Multiple partitions scheduled to execute concurrently on different processor cores.

- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- Today, ARINC 653 supports multicore systems

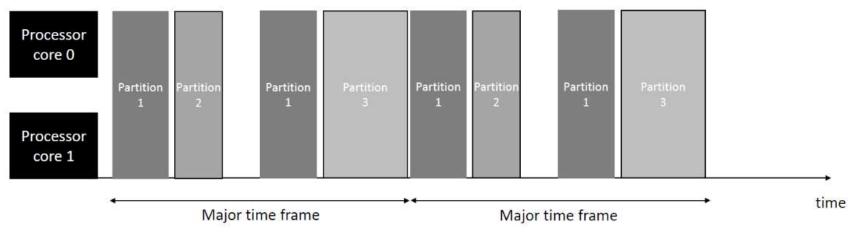
2.2.1 Core Modules with Multiple Processor Cores

This standard includes support for the ARINC 653 services to be utilized with a core module that contains a single or multiple processor cores. With multiple processor cores, additional scheduling paradigms are feasible on a core module:

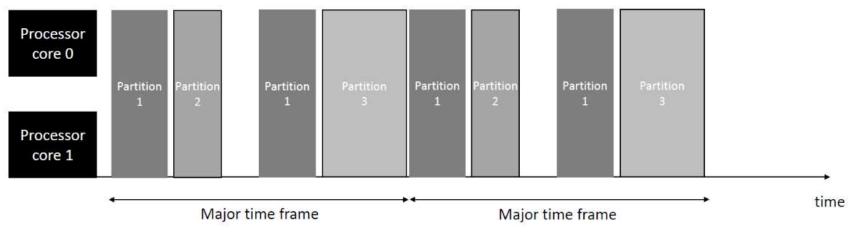
- Multiple processes within a partition scheduled to execute concurrently on different processor cores.
- Multiple partitions scheduled to execute concurrently on different processor cores.

In the rest of this talk, we will assume this paradigm.

- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- Today, ARINC 653 supports multicore systems



- Standard that facilitates integration of many software subsystems (called partitions) on a single computer
- ARINC 653 uses a time-triggered scheduler to supply processing time to partitions.
- Today, ARINC 653 supports multicore systems



• How do we analyze real-time software executing over ARINC 653 on multicore processor with undocumented hardware?

Previous work

This Paper Compared to State-Of-The-Art

Can analyze	Yes	[5,6,7]	This paper
ARINC 653	No		[8]
		No	Yes
		Can analyze undocumented hardware	

References

[5] Y.-H. Lee, D. Kim, M. Younis, and J. Zhou, "Scheduling tool and algorithm for integrated modular avionics systems," DASC, 2000.

[6] A. Mok, D.-C. Tsou, and R. de Rooij, "The MSP.RTL real-time scheduler synthesis tool," RTSS, 1996.

- [7] N. C. Audsley and A. J. Wellings, "Analysing APEX applications," RTSS, 1996.
- [8] B. Andersson, H. Kim, D. de Niz, M. Klein, R. Rajkumar, and J. Lehoczky, "Schedulability Analysis of Tasks with Co-Runner-Dependent Execution Times," TECS, 2018.

Previous work

In this paper, we adapt [8] for ARINC 653.

This Paper Compared to State-Of-The-Art

Can analyze ARINC 653	Yes	[5,6,7]	This paper
	No		[8]
		No	Yes
		Can analyze undocumented hardware	

References

[5] Y.-H. Lee, D. Kim, M. Younis, and J. Zhou, "Scheduling tool and algorithm for integrated modular avionics systems," DASC, 2000.

[6] A. Mok, D.-C. Tsou, and R. de Rooij, "The MSP.RTL real-time scheduler synthesis tool," RTSS, 1996.

[7] N. C. Audsley and A. J. Wellings, "Analysing APEX applications," RTSS, 1996.

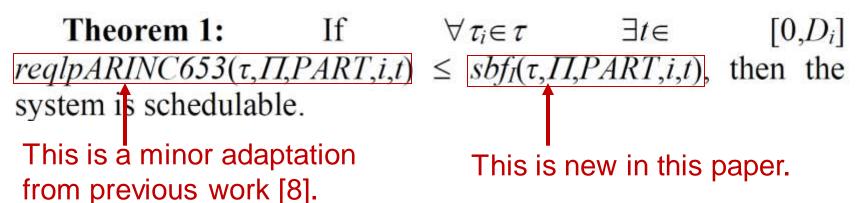
[8] B. Andersson, H. Kim, D. de Niz, M. Klein, R. Rajkumar, and J. Lehoczky, "Schedulability Analysis of Tasks with Co-Runner-Dependent Execution Times," TECS, 2018.

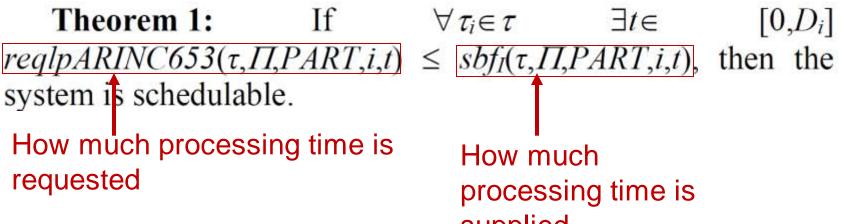
Previous work

This Paper Compared to State-Of-The-Art

Can analyze	Yes	[5,6,7]	This paper
ARINC 653	No		[8]
		No	Yes
		Can analyze undocumented hardware	

[8] uses a model that describes the effect of sharing of hardware resources in the memory system without actually modelling the resources in the memory system. \Rightarrow allows analysis of software executing on undocumented hardware.





supplied

Theorem 1: If $\forall \tau_i \in \tau \quad \exists t \in [0,D_i]$ reqlpARINC653(τ , Π ,PART,i,t) $\leq sbf_I(\tau,\Pi$,PART,i,t), then the system is schedulable.

> The ARINC 653 standard can be interpreted in different ways. This yields different bounds on supply of processing time.

Theorem 1: If $\forall \tau_i \in \tau \quad \exists t \in [0,D_i]$ reqlpARINC653($\tau,\Pi,PART,i,t$) $\leq sbf_1(\tau,\Pi,PART,i,t)$, then the system is schedulable.

> The ARINC 653 standard can be interpreted in different ways. This yields different bounds on supply of processing time.

I allows you to specify which interpretation you want to use.

Carnegie Mellon University Software Engineering Institute Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Canegie Melon University [DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution.

Theorem 1: If $\forall \tau_i \in \tau \quad \exists t \in [0,D_i]$ reqlpARINC653($\tau,\Pi,PART,i,t$) $\leq sbf_I(\tau,\Pi,PART,i,t)$, then the system is schedulable.

For details: see paper.

Carnegie Mellon University Software Engineering Institute

Satisfying Real-Time Requirements of Multicore Software on ARINC 653: The Issue of Undocumented Hardware © 2022 Carnegie Mellon University Conclusion It is possible to satisfy real-time requirements of multicore software on ARINC 653 and deal with the issue of undocumented multicore.