



# POWER AND MOBILITY



## HIGH TEMPERATURE (250 °C) SIC POWER MODULE FOR MILITARY HYBRID ELECTRICAL VEHICLE APPLICATIONS

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Castagno

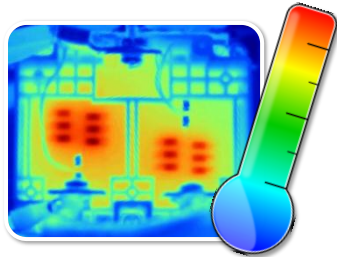
# Report Documentation Page

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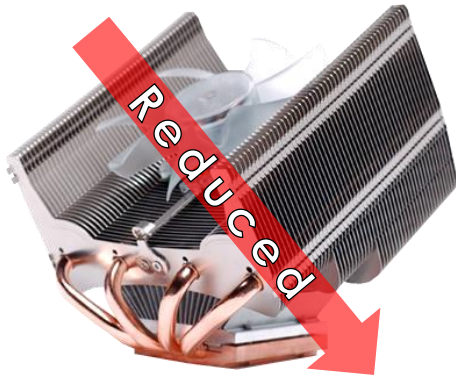
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# Why High Temperature?



What if temperature was *not a limitation*?



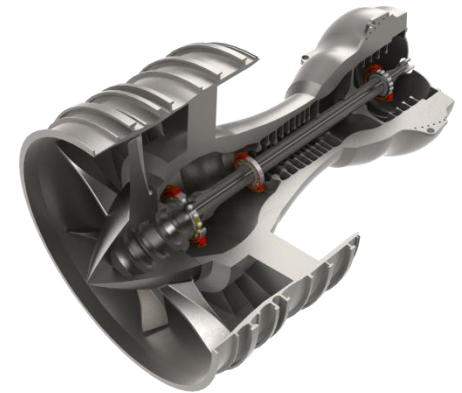
**Cooling Systems**



**Thermal Shielding**

- *Efficiency*
- *Power Density*
- *Size & Weight*
- *Complexity*
- *Cost*

**Design Tradeoffs**

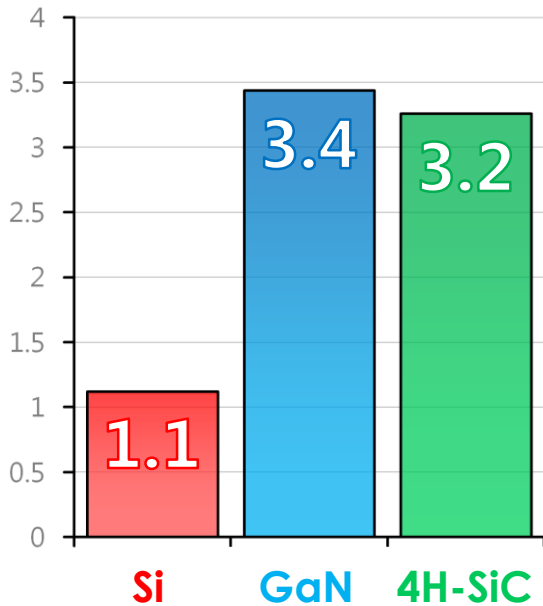


**Extreme Environments**

# Wide Band Gap Semiconductors

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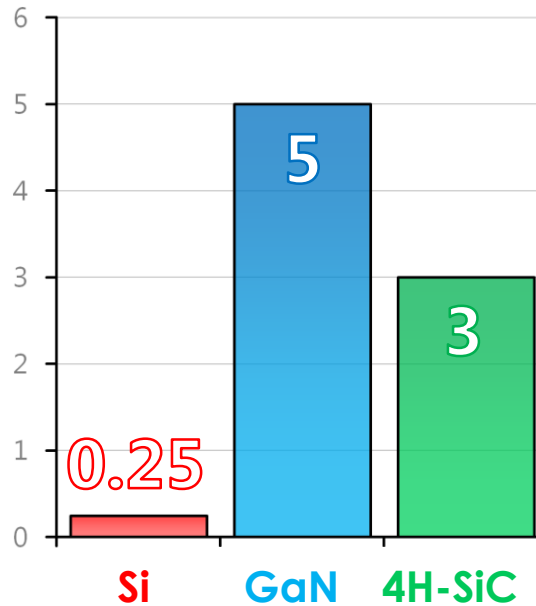
### Band Gap (eV)



larger band gaps mean...

- ↓ Intrinsic Carriers
- ↑ Operating Temperature

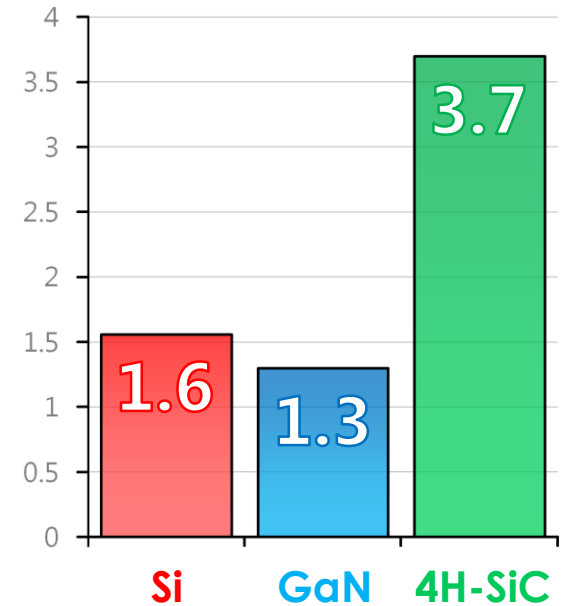
### Breakdown Electric Field (MV/cm)



higher critical fields result in...

- ↑ Blocking Voltages
- ↓ On-Resistance
- ↑ Switching Speed

### Thermal Conductivity (W/cm·K)



increased thermal cond. allows...

- ↑ Heat Dissipation
- ↑ Power Density

## Motor Drives

**Military**  
Hybrid / Fully Electric Vehicles



**Commercial**  
Hybrid / Fully Electric Vehicles



**Aerospace**  
More Electric Aircraft



**Industrial**  
Modernized Power Grid



**Commercial**  
Fault Current Limiter



**Military**  
Advanced Warships



## Power Converters

**Solar / Wind**  
Grid-Tie Inverters



**Geological**  
Down Hole Instrumentation



**Aerospace**  
Power Conversion



**Industrial**  
Power Turbine Sensors



**Aerospace**  
Jet Engine / Turbine Sensors



## High Voltage

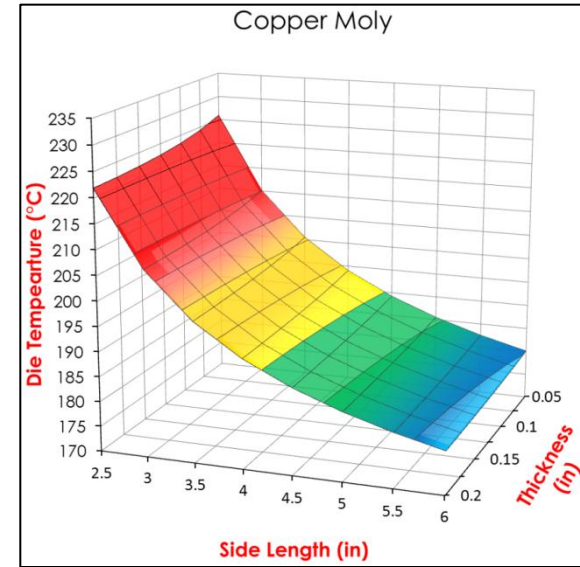
## Wireless Telemetry





# Design

*philosophy and processes*



Use the most suitable device for a given application

JFETs

SemiSouth  
infineon  
Microsemi

MOSFETs

CREE  
ROHM  
SEMICONDUCTOR

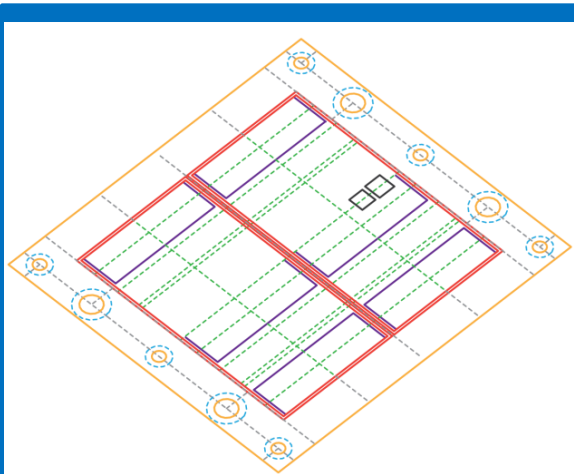
BJTs

CREE  
TransiC

Diodes

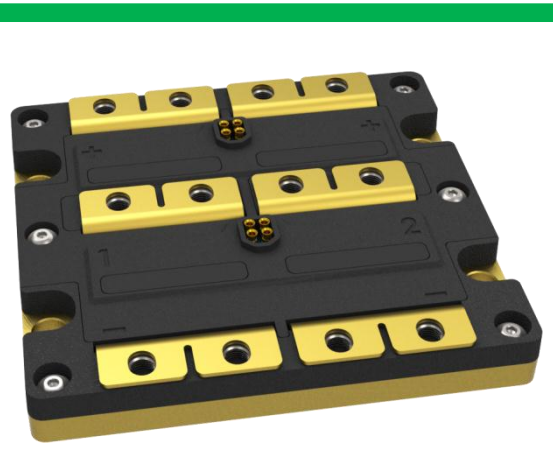
CREE  
SemiSouth  
infineon  
GeneSiC  
SEMICONDUCTOR  
IXYS  
Microsemi  
ROHM  
SEMICONDUCTOR

## Technique which allows for rapid configuration of a design with minimal user input



### Reference Sketches

Geometry is driven by relationships, equations, and named variables.



### Assembly

Components are defined in context and driven by the referenced design variables.



### Configurations

Thousands of variations may be rapidly analyzed with this process.



**Using an adaptive CAD model and FEA simulation software, thousands of configurations may be investigated**

## Base Plate

material  
geometry

## Power Substrate

ceramic type  
ceramic thickness  
metal type  
metal thickness

## Die Attach

material  
thickness



## Spacing

die to die  
die to edge  
substrate to base plate  
substrate etch lines  
clearances  
tolerances

## Tradeoffs

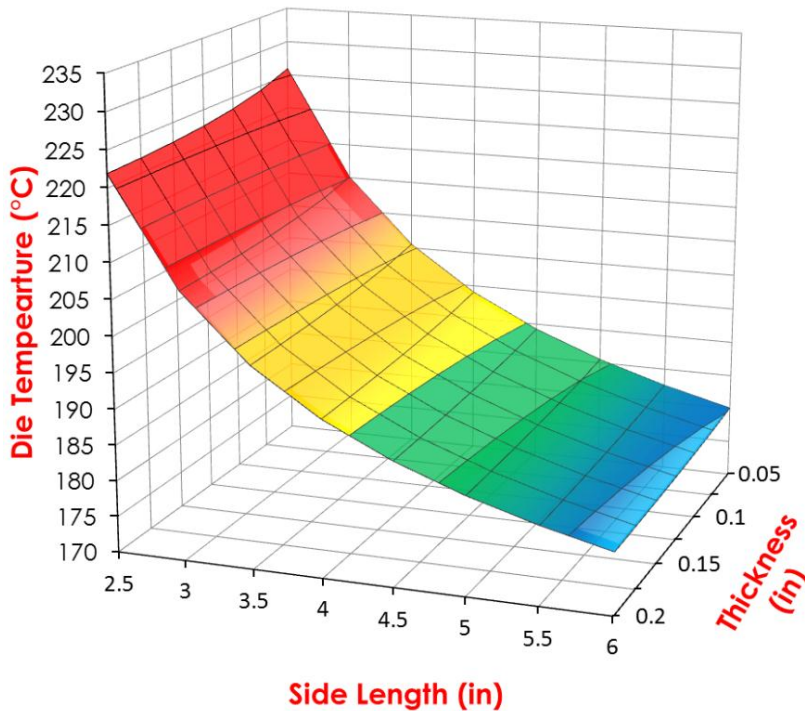
thermal performance  
stress & displacement  
weight vs. performance  
volume vs. performance  
plastic reinforcements

# Example Base Plate Analysis

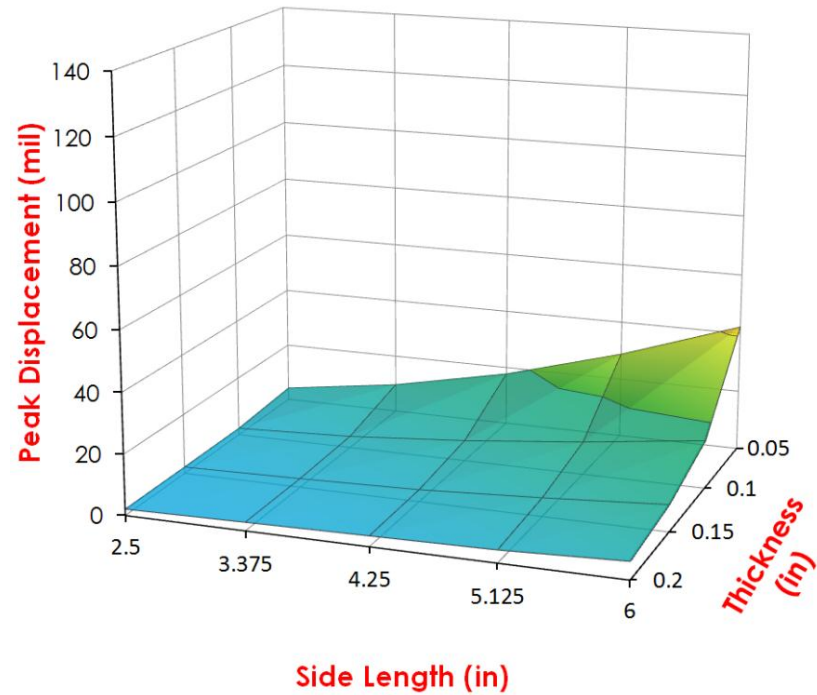
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**Simulation data is extracted and organized into design surfaces. Tradeoffs are identified and visualized**

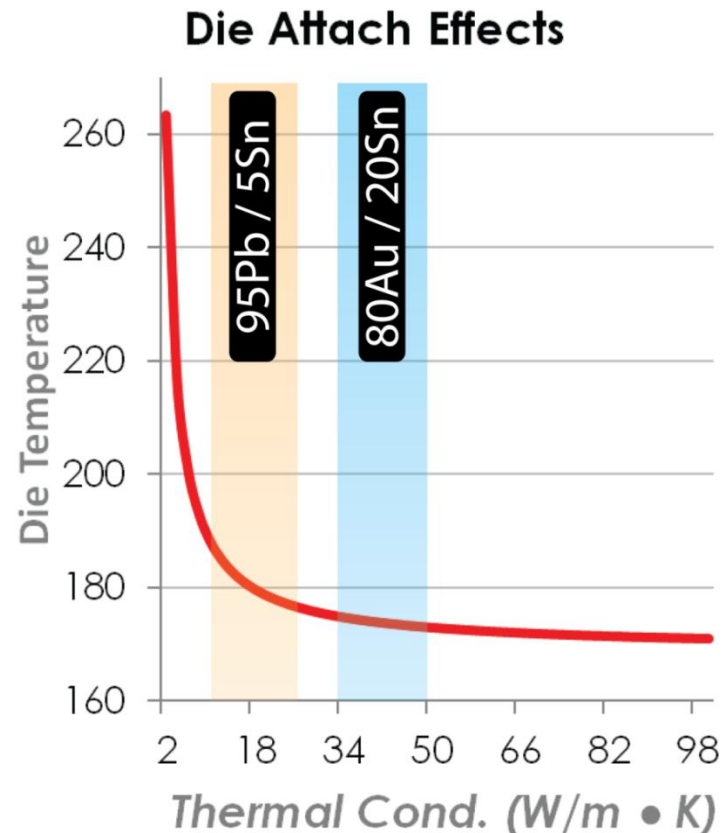
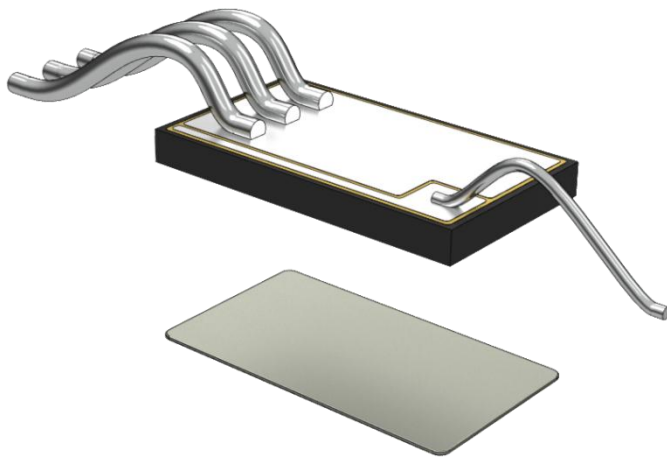
Copper Moly



Copper Moly

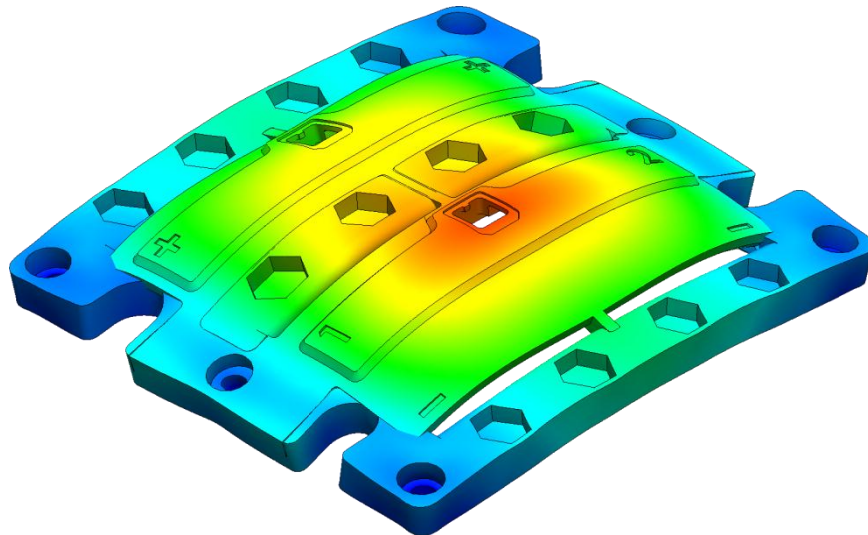
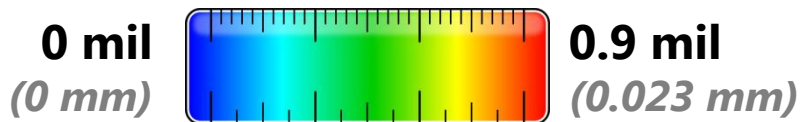


The thermal conductivity of the die attach exhibits diminishing returns

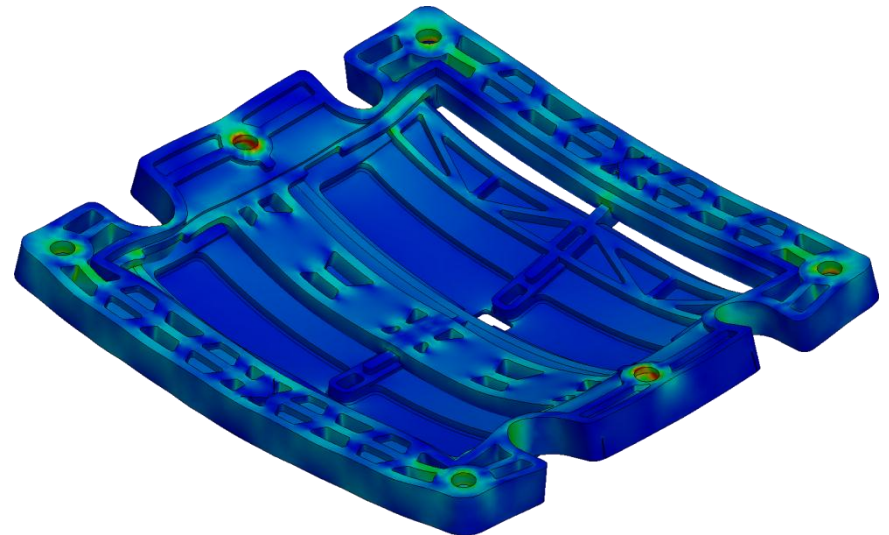
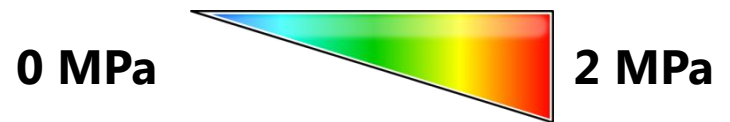


**Plastic reinforcing features are carefully designed for minimal stress & displacement**

Displacement @ **200°C**



Von Mises Stress @ **200°C**





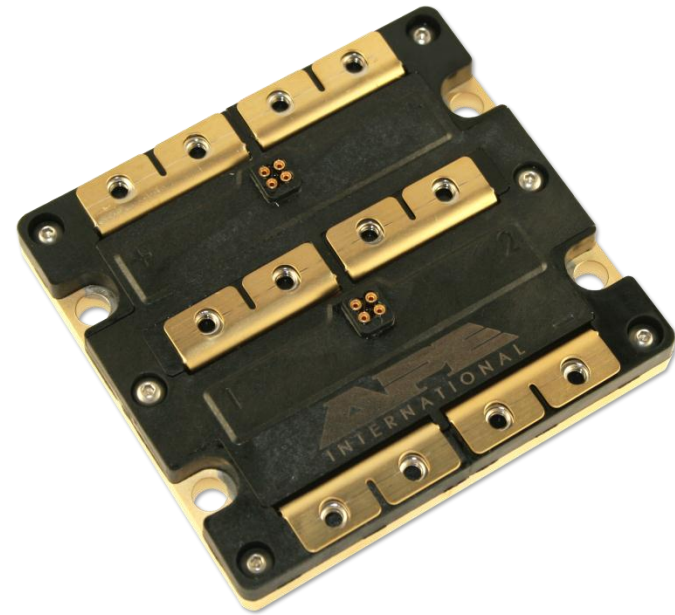


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# **HT-2000**

*design and features*





# HT-2000 Series

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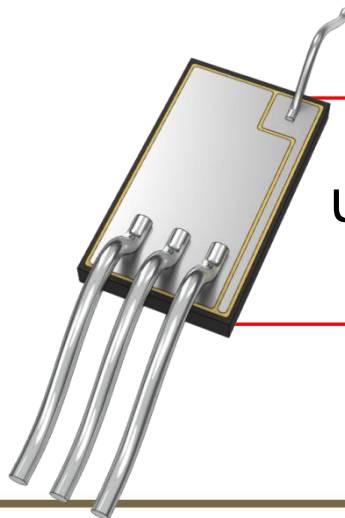
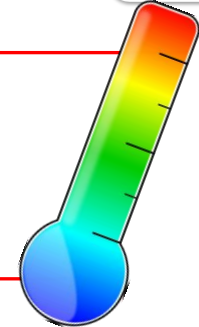


High temperature, high frequency, high power density all SiC half or full-bridge power stage.



**Ratings**  
1200V  
>150A

**Temperature**  
250°C peak  
(packaging)



**Devices**  
up to 16 die in parallel  
per switch position

\* pictured: *SemiSouth 50mΩ JFET (SJEC120R050)*

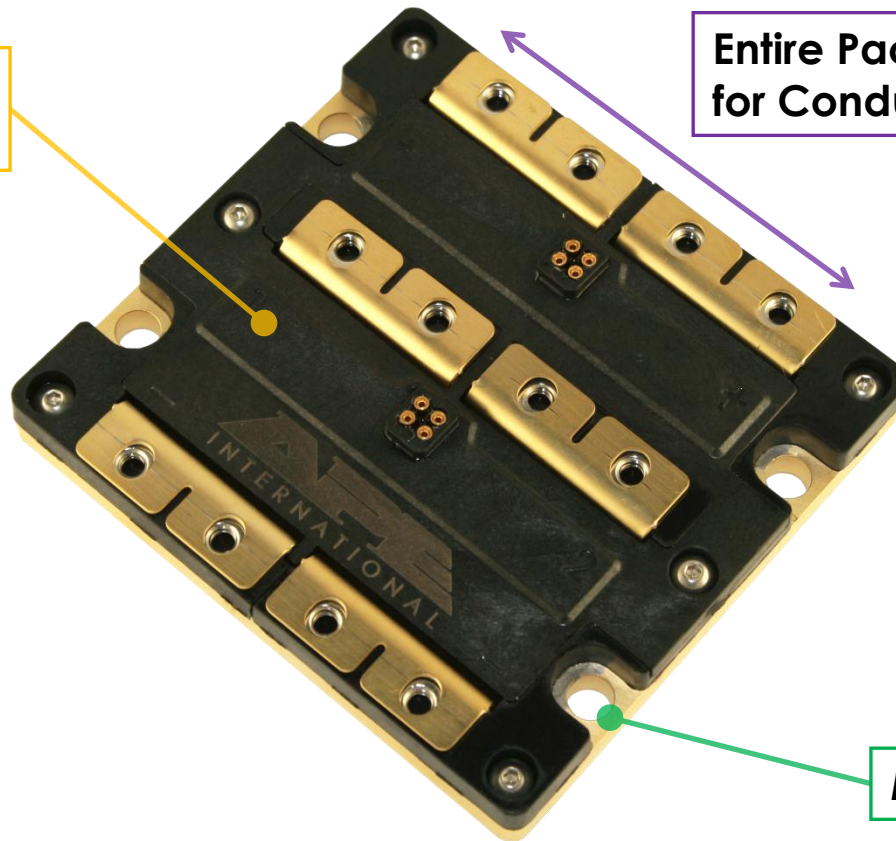
## Multiple Material Choices Based on Application

High Temp. Plastic Housing

Entire Package Width Used for Conduction

Very Low Profile  
0.43 in (10.9 mm)

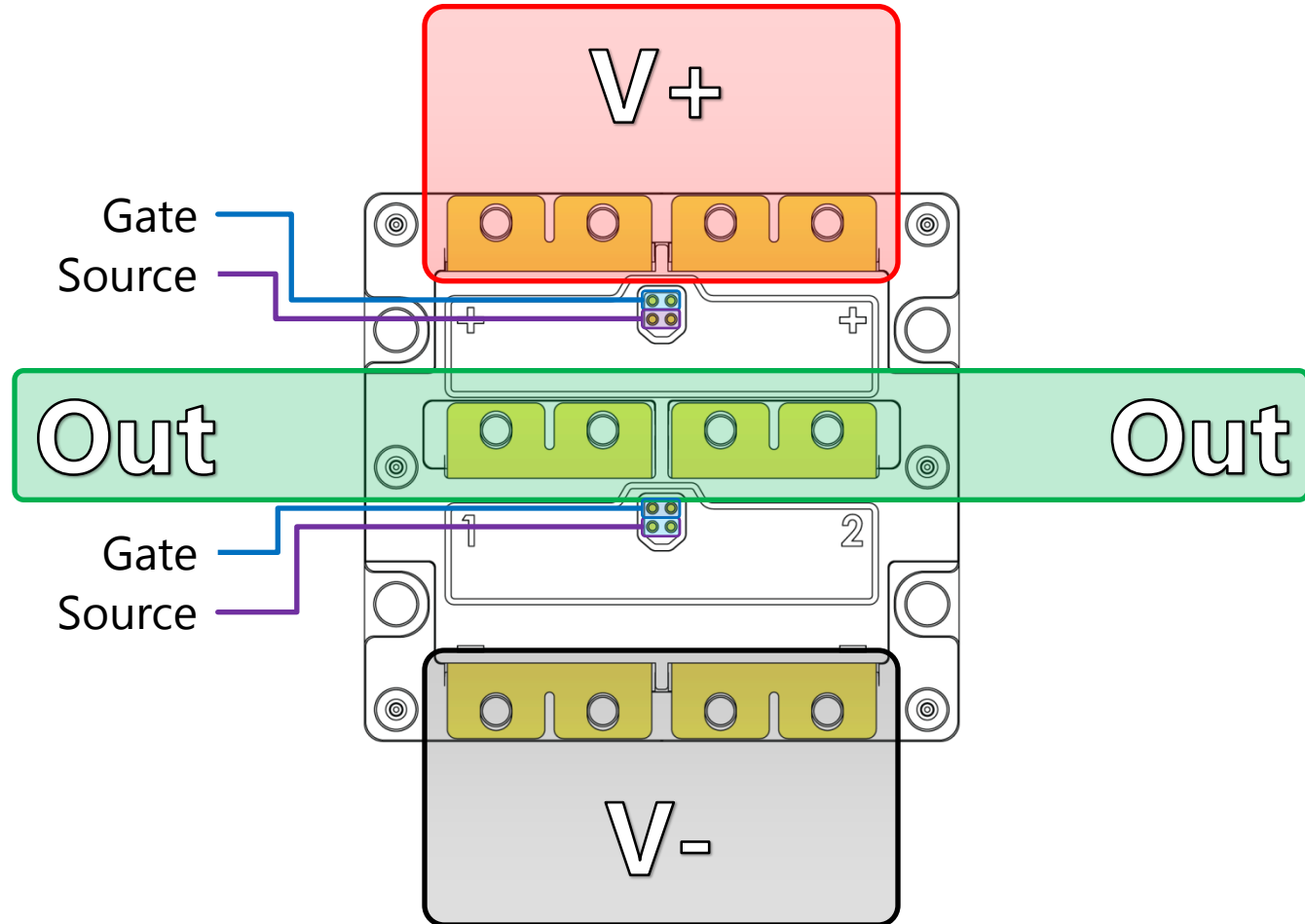
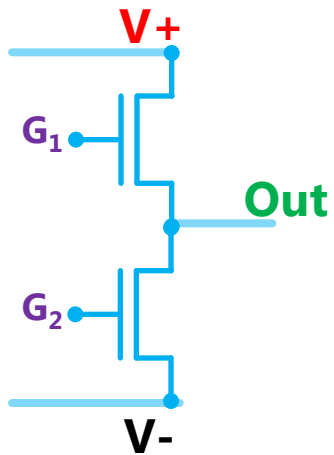
Completely Flux Free Assembly



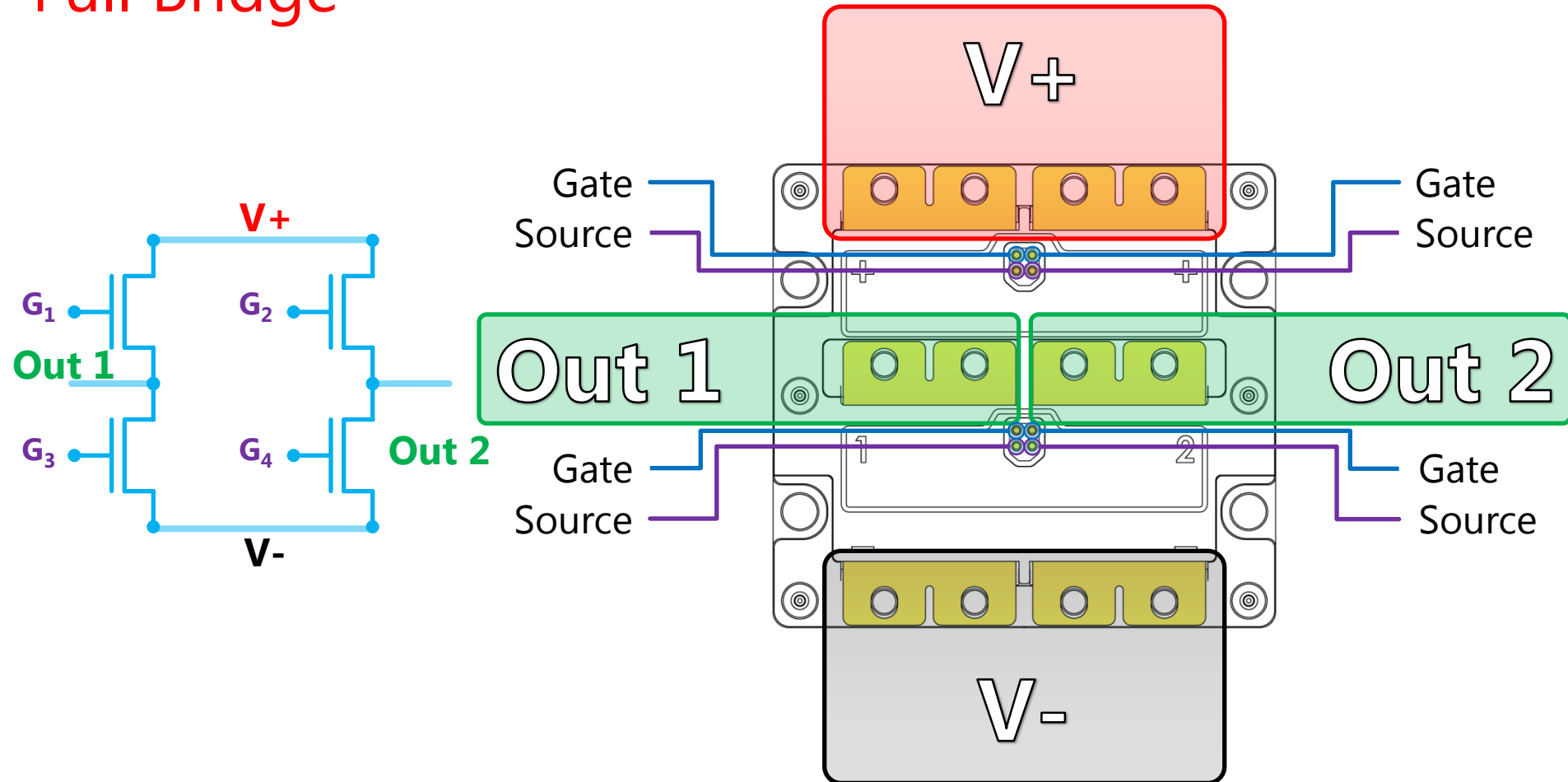
MMC Base Plate

Each module contains four switch positions. Multiple configurations are possible through external bussing

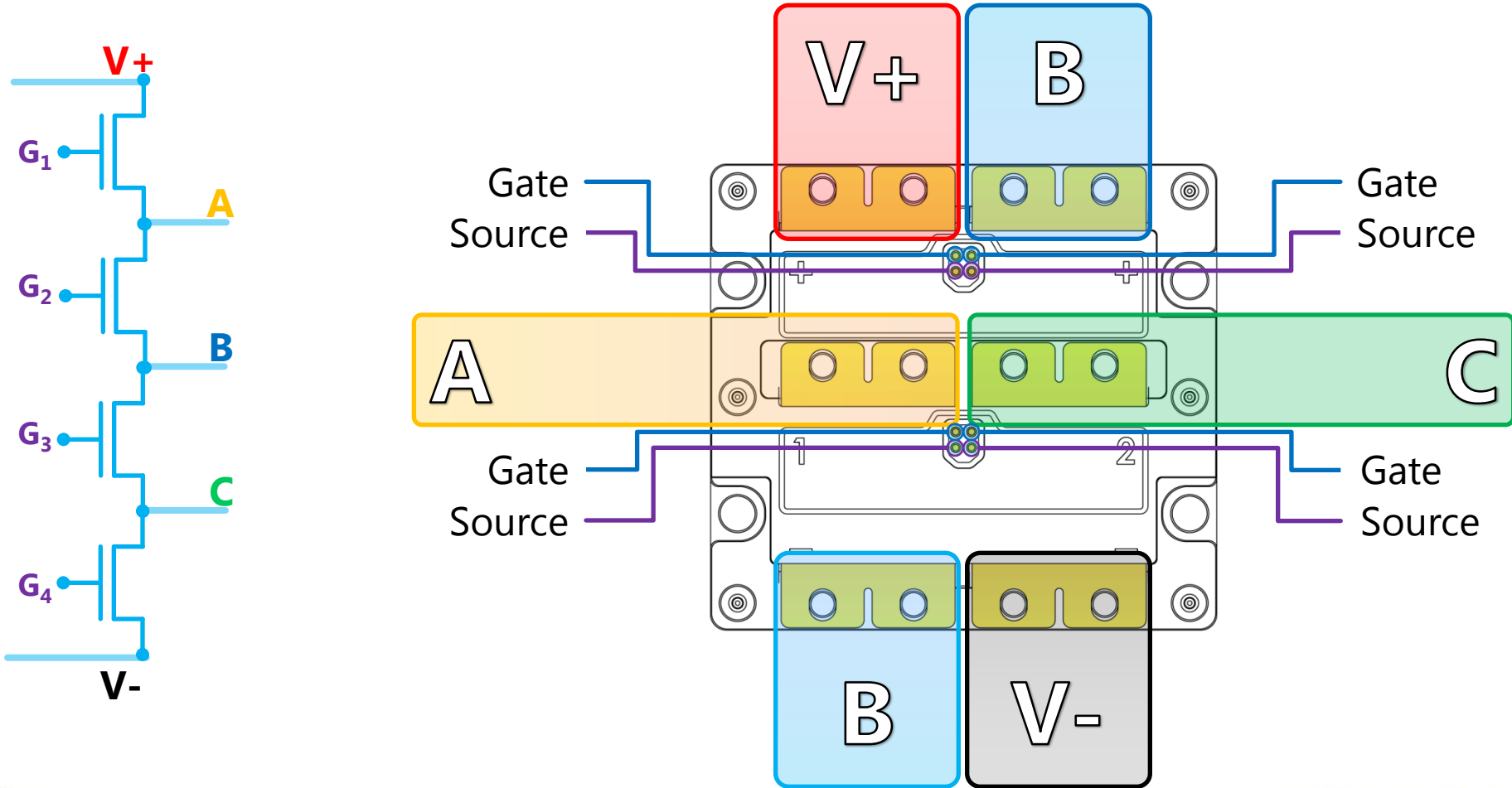
## Half Bridge



## Full Bridge

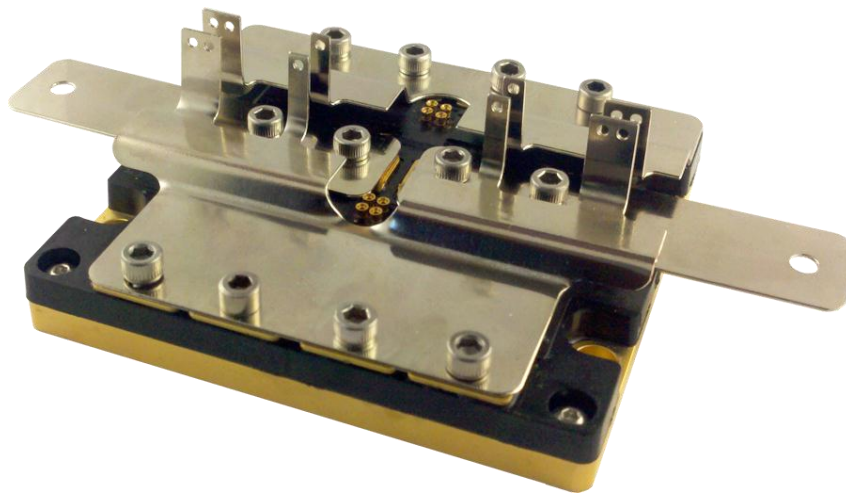


## Series

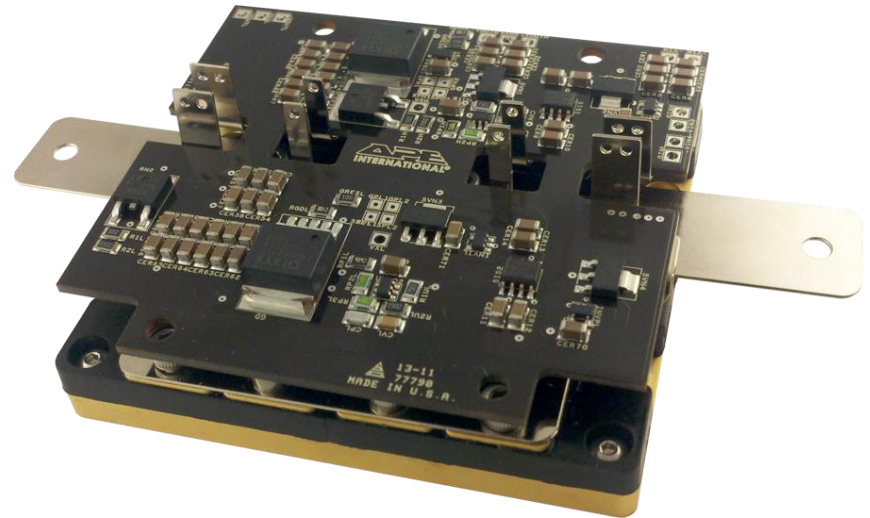




**HT-2000 modules are available with custom bussing and gate drives for rapid evaluation**



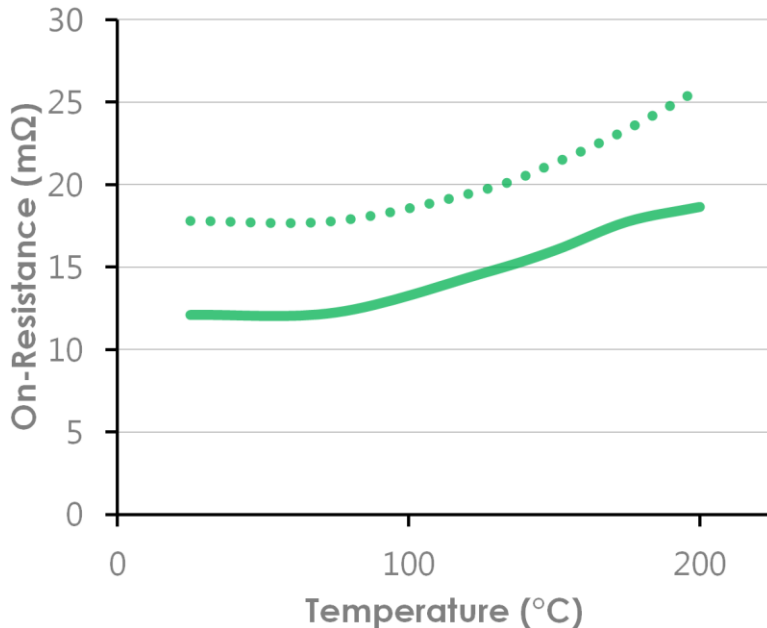
Etched  
Copper Bussing



High Frequency  
Gate Drive

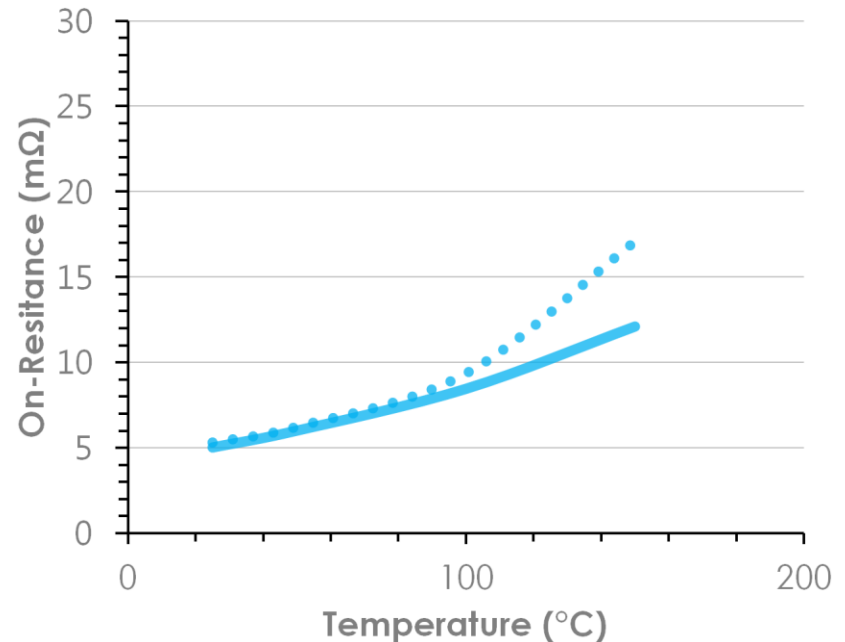
## The paralleled switch positions exhibit very low on state resistances, even at high temperature

MOSFET Configuration  
6 MOSFETs per switch position



●●●●●●●● 200 A  
————— 20 A

JFET Configuration  
8 JFETs per switch position



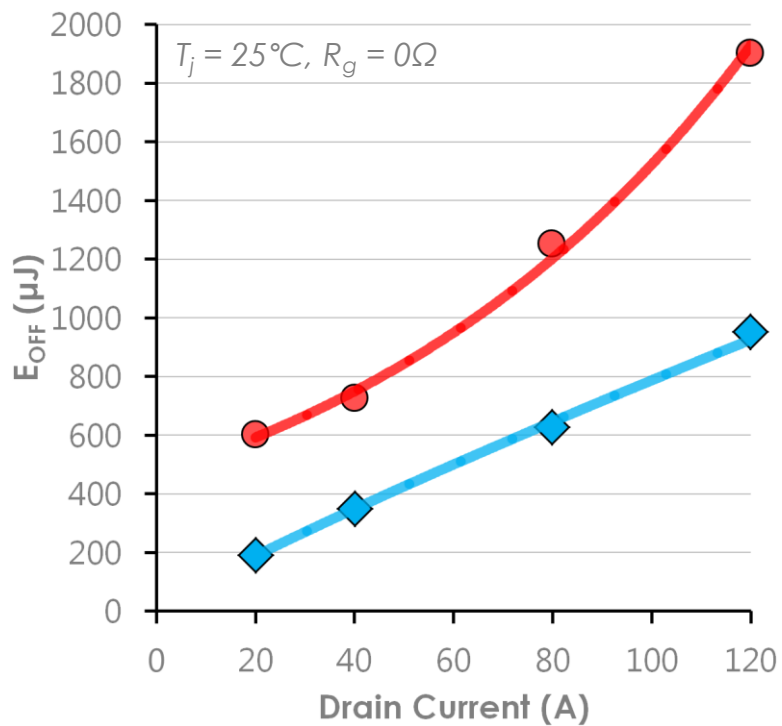
●●●●●●●● 160 A  
————— 80 A

# Switching Energy MOSFET Module

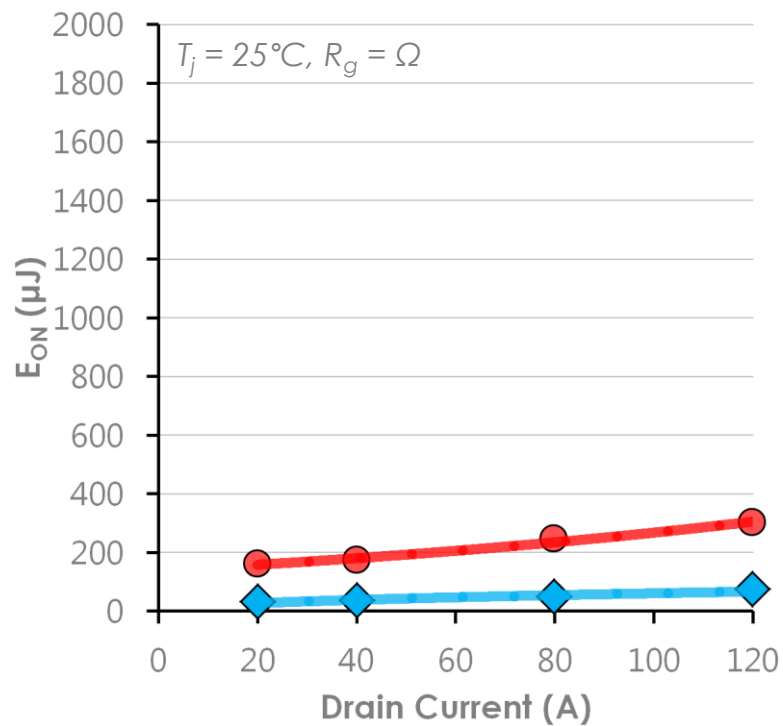
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### Turn Off Loss



### Turn On Loss



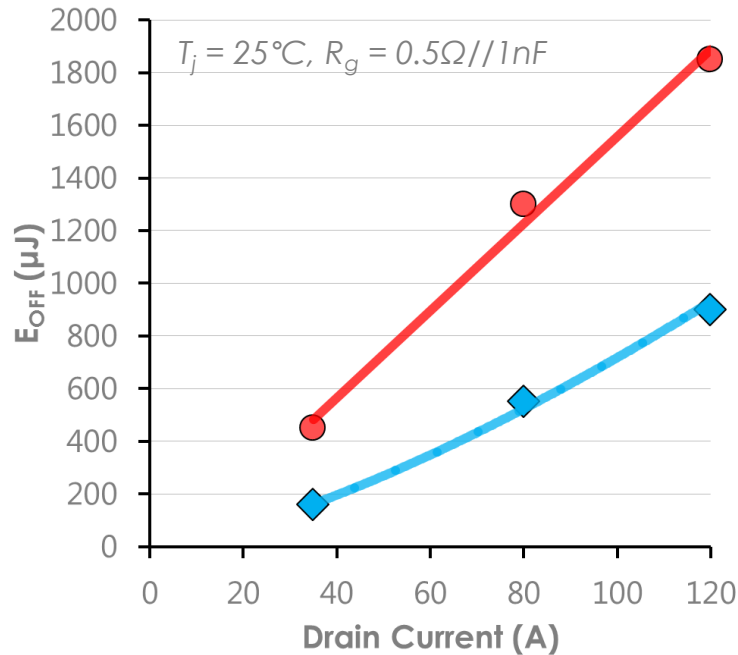
300 V

600 V

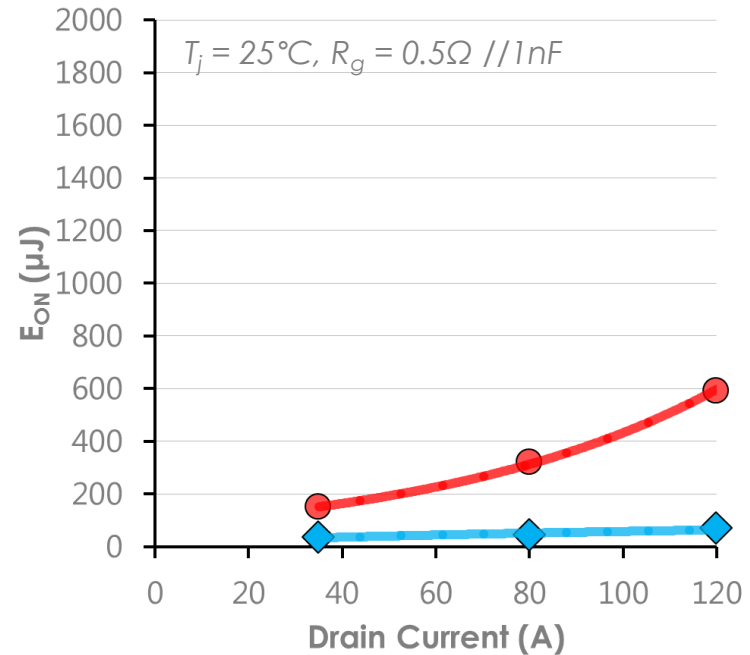
# Switching Energy JFET Module

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### Turn Off Loss



### Turn On Loss



300 V

600 V

**These newly developed high performance SiC power modules can provide substantial system benefits, including:**

**Increased**

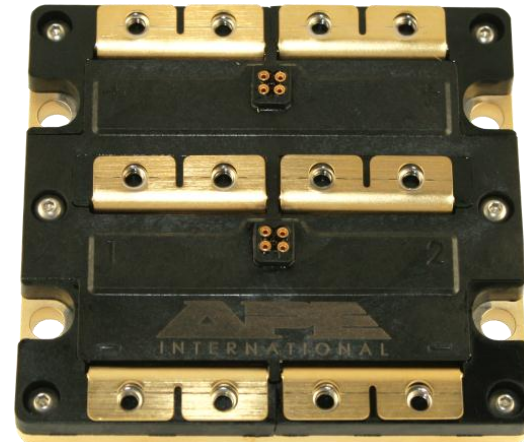
efficiency  
power density

**Reduced**

volume  
weight

**Higher**

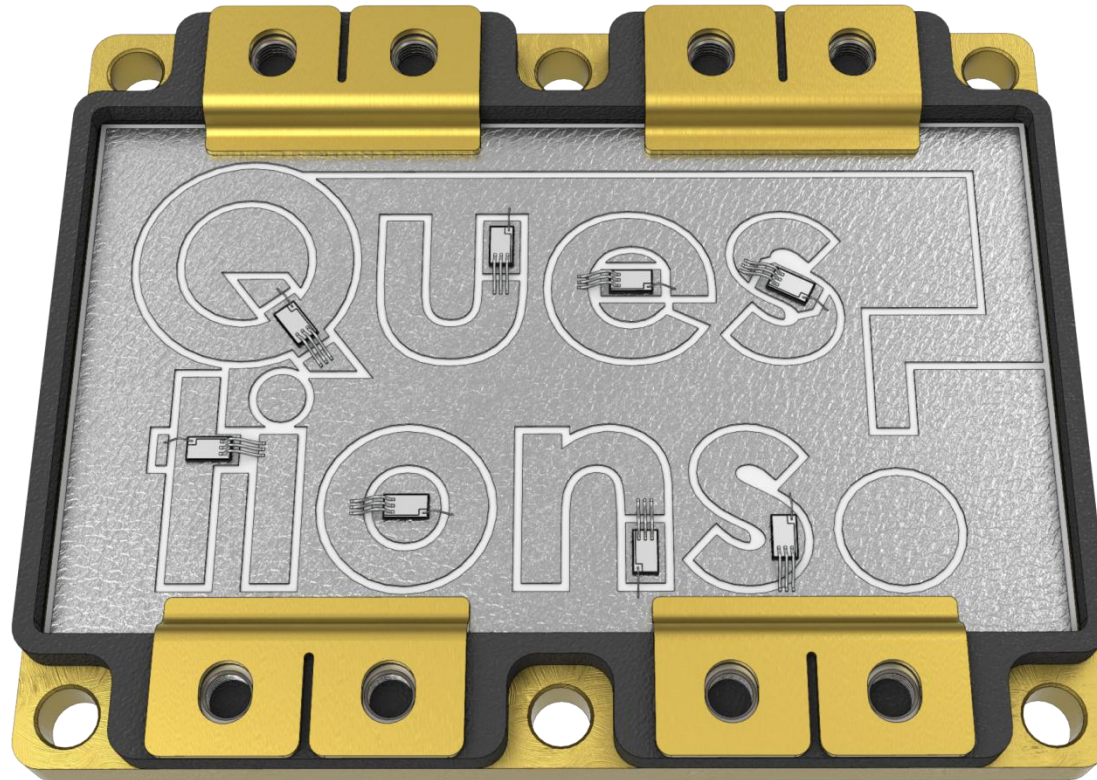
junction temperatures  
ambient temperatures





Thank You!

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## Acknowledgements

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