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OPTICAL BUS EXTENDERS FOR HIGH SPEED COMPUTERS(U)
OPTIVISION INC DAVIS CA A A SAWCHUK 10 FEB 88
N00014-87-C-0869

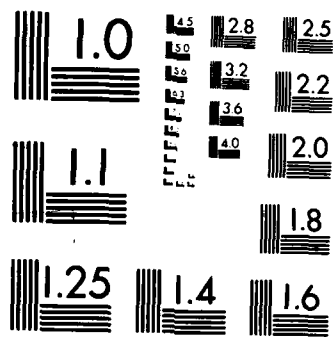
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OPTIVISION, INC.

2655 PORTAGE BAY AVENUE

DAVIS, CALIFORNIA 95616

(916) 756-4429

February 10, 1988

AD-A192 779

SCIENTIFIC OFFICER
Mathematical Sciences Division
Office of Naval Research

Attn: Dr. William Miceli (Code 1111)
800 North Quincy Street
Arlington, Virginia 22217-5000

Ref: Contract N00014-87-C-0869
Deliverable item 0001AA

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Dear Dr. Miceli:

Enclosed please find three copies of the Initial Technical Abstract that was mailed to the SDIO by Dr. Sawchuk, the contract PI on September 30, 1987.

The DCASMA office in San Francisco informs us that apparently the abstract has not reached you. We are enclosing the three copies to you and also distributing it to others on the contract distribution list.

Your acceptance and approval of this item will be most appreciated.

Sincerely,

Anil Jain

Anil Jain
President

Enclosures
AJ:mb

Copy: Mr. Doug Collins

: Director, NRL

: DTIC

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OPTIVISION, INC.

2655 PORTAGE BAY AVENUE

DAVIS, CALIFORNIA 95616

(916) 756-4429

OPTICAL BUS EXTENDERS FOR HIGH SPEED COMPUTERS

Initial Technical Abstract

Contract N00014-87-C-0869

Item 0001AA
(Sept.30,1987)

Delivered to:

1. Dr. William Miceli, (Code 1111)
Scientific Officer,
Mathematical Sciences Division
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TOPIC: 11 - Optical Computing and Optical Signal Processing

Optical Bus Extenders for High Speed Computers

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Mr. Sawchuk
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Abstract

A bus extender is a means for high-speed interconnection of a multitude of individual computing modules comprising a distributed computing system. When the elements to be interconnected are spaced by distances of more than a very few meters, there are significant advantages to the use of optics for providing the interconnects. The feasibility is being examined of using optical bus extenders in a strategic defense system. The advantages and limitations of both free-space communication and fiber communication over short distances are being determined. Critical issues are the bulk and cost of components, and the efficiency with which light can be detected at the end of the various links. Optimization of certain parameters of the extender are being explored with respect to both cost and performance. The general results obtained are being applied to the special case of the VME bus. Protocols to control activity on the extender are being considered and a preferred solution is being proposed. As issues arise that can be resolved only by breadboarding a collection of components or a small subsystem, then such breadboarding is being undertaken. The problem of simultaneously mapping the state of the bus to several remote locations is being examined. Various methods for tapping the optical energy for multiple destinations are being considered and the optical efficiency and cost of these solutions are being determined.

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AASawchuk
9/30/87

SDIO SBIR	
Project No.	11
Phase	1
Topic	Optical Computing and Optical Signal Processing
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