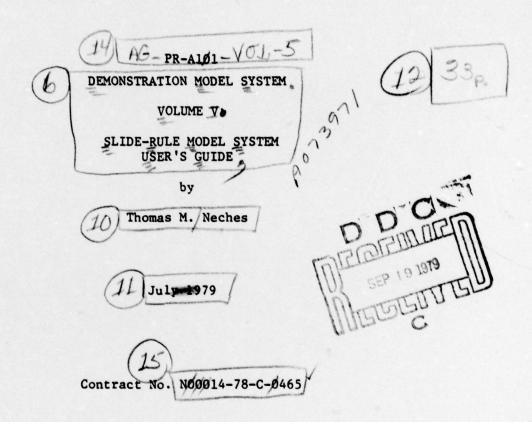


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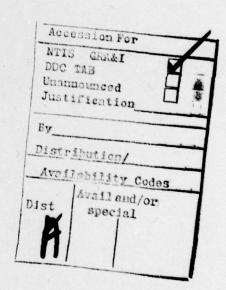
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#### INTRODUCTION

The Level I "Slide-Rule" Cost Model System is implemented on a Texas Instruments TI-59 programmable calculator coupled to a TI-PC-100A Print/Security Cradle.

The model system consists of four linked programs, the Top-Down Model, (TDM), the Lowest Removable Assembly Model (LRAM), the System Aggregation Model (SAM), and the System Confidence Model (SCM). Each program and its data input sets are stored on magnetic cards. The output of each program is used as input to succeeding programs, together with additional input data.

The TI-59 has 120 program/data registers, which can be partitioned as desired between program instruction steps and data memory registers. When the calculator is turned on, 60 memory registers are automatically reserved for data storage. All programs other than the TDM use the default allocation. The TDM, however, uses only 40 registers for data storage; the remainder is used to store the program code. Therefore, when running the TDM, the first step after turning on the calculator will be to repartition the memory registers.

Model output and all cost inputs are given in thousands of dollars. TDM running time is approximately 1 minute; LRAM running time is approximately 40 seconds (slightly greater if the LRA is coded depot repair); the SAM requires approximately 10 seconds per aggregation run; SCM running time is approximately 10 seconds per LRA input.

If program execution is interrupted in the models of a run, it is likely that the calculator will be in the Fix 2 display mode. If this occurs, press [NV] 2nd before reading in any new data cards.

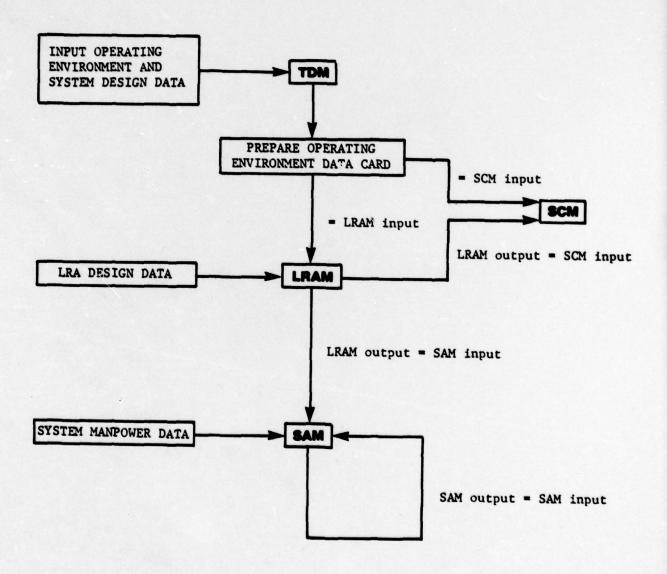
In order to conserve memory register space, several constants describing the operating environment of the system have been incorporated into the program code. A list of these constants and their location in the program is presented for each model. Values of the code constants can be altered by adjusting the program code. The routine for altering the values of the code constants is the following:

- 1. Press GTO.
- 2. Enter Program Location.
- 3. Press LRN .
- 4. Enter New Value.
- 5. Press LRN.

New values of the code constants must have exactly the same numbers of decimal places (including any decimal points) as the original value. (For example, 1.0 may be replaced by 10., .01, 100, 9.5, 000, etc., but not by 1, 1.00, .0, 10.0, 2., etc.) Failure to do this will almost certainly cause program execution errors.

Figure 1 presents the input/output linkage between the four models. Program operation for the TDM, LRAM, SAM and SCM is described in the User's Guide Supplements A, B, C and D. Each subsection is a self-contained unit presenting program operating instructions, data input/output instructions, code constant program locations, and program data register allocation and usage.

Figure 1 Level I Input/Output



USER'S GUIDE SUPPLEMENT
FOR TOP-DOWN MODEL

Figure A-1 TDM Operating Instructions

	Procedure	Press	Display/Printer
1	Repartition memory	4 <b>2nd</b> 17	639.39
2	Load program and input data	(load banks 1, 2, 3, 4)	1, 2, 3, 4
3	Alter input data*  (i.e., set MTBF = 240 hrs.)	240 <b>STO</b> 21	240.
4	Run program  (To suppress printing of C <sub>j</sub> press		C <sub>1</sub> = maintenance wage  C <sub>2</sub> = maintenance training  C <sub>3</sub> = operator wage  C <sub>4</sub> = operator training  C <sub>5</sub> = production and spares  C <sub>6</sub> = support and test eqpt.  C <sub>7</sub> = repair  C <sub>8</sub> = item entry and management  C <sub>9</sub> = documentation  life cycle cost = \( \subseteq \)
5	For new design variant go to Step 3	<b>V</b>	
6	Record input data for future use	3 2nd (load bank 3) 4 2nd (load bank 4)	4.

<sup>\*</sup> See Figure A-3.

Figure A-2 Altering Operating Environment Output Data Card

Step	Procedure	Press	Display/Printer
1	Load Operating Eviron- ment data card*	CLR (load Bank 3)	3.
2	Alter input data** (example: set L = 6.67)	6.67 <b>STO</b> 47	6.67
3	Record op. env. data card for use in LRAM and SCM	3 2nd Arra (load Bank 3)	3.

<sup>\*</sup>Turn calculator off for a few seconds to insure normal partitioning (479.59).
\*\*See Figure 2.5.

Figure A-3 TDM Input Data
(Sample Data Collection Worksheet)

Variable Name	Units	(Sample) Value	Storage Address	
r <sub>1</sub>			02	Fraction LRA's coded local repair
LRT	weeks		03	Average local repair response time
r <sub>2</sub>			06	Fraction LRA's coded depot repair
D	weeks		07	Deployment period
n *			10	Total # LRA's in system
n	-		11	# Unique LRA types in system
s			12	Ratio peak operating hrs. per wk. to av. op. hrs./wk.
N			13	# Ships
AN <sub>m</sub>	men		14	On-board available maintenance personnel
BN m	\$1000		15	Annual billet cost for maintenance personnel
ANo	men		17	On-board available operators
BNo	\$1000		18	Annual billet cost for operators
TC	\$1000		19	"C" School training cost for operators
LC	years		20	System life cycle
MTBF	hours		21	System mean time between failure
UCL	\$1000		22	Estimated unit production cost at lot size LOT
2			23	LOT size used for UC <sub>LOT</sub>
Q			24	# Systems per ship
AHR	hrs/wk		25	Av. system operating hrs./ operating wk.
MTTRS	manhour		26	Mean time to repair system
SM	manhrs/ week		27	Weekly scheduled maintenance manhour requirement

Figure A-3 TDM Input Data (cont'd)

Variable Name	Units	(Sample) Value	Storage Address	
TC <sub>m</sub>	\$*000		28	System level maintenance training requirement
θ	men		29	# Operators/system
STE	\$1000		30	System level support and test equipment purchase cost
COD	\$1000		31	Cost of repair at contractor operated depot
RP	\$1000		32	Repair materials cost for local repair of LRA
DOC	\$1000		33	System level documentation cost

Figure A-4 TDM Code Constant Program Location

Variable Name	Units	Program Location	Current Value	Available Program Steps*	Definition
BG	\$*000	062	10.5	4	Annual billet cost for general labor personnel
TA	\$ 000	090	10.0	4	Average cost of "A" schoo training course
TOR		099	.45	3	Annual personnel turn- over rate
K*		230	.95	3	Confidence level against on-board spare stock-out
DRT/d**	weeks	288	13	2	(Depot response time = 26 wks.)/(# depots = 2)
z <sub>b</sub>		295	1.65	4	Standard deviation for .95 confidence level against LRA stock-out at depot
d**		305	2	1	# Depots
h		334	2	1	# Deployments/year
1-COND		355	.98	3	1 - (Condemnation Rate = .02)
log RRATE/ log 2		374	.15	4	Learning curve reduction factor = log .90/ log 2
p		384	.10	3	Discount rate
Ñ		430	1.0	3	Ratio (MTTR <sub>LRA</sub> ,MTTR <sub>SYS</sub> )
WH <sub>m</sub> •U	hr./wk.	445	53	2	(Maintenance personnel wkly. avail. work hrs. = 67) (Util. Rate = .8)
Ť		456	1.0	3	Ratio (TCLPA/TCSYS)

<sup>\*</sup>Includes decimal point and change-sign operator.
\*\*Altering d requires altering DRT/d as well.

Figure A-4 TDM Code Constant Program Location (cont'd)

Variable Name	Units	Program Location	Current Value	Available Program Steps	Definition
WH <sub>O</sub>	hr./wk.	492	74	2	Operator wkly. avail- able work hrs.
ŝ		529	1.0	3	Ratio (STE <sub>LRA</sub> /STE <sub>SYS</sub> )
m		537	.12	3	Annual support of support eqpt. factor
IEC	\$^000	569	.45	3	Item entry cost
IMC	\$ 000	573	.23	3	Item management cost
рр		583	1.0	3	Av. unique components per LRA
Ď		611	1.0	3	Ratio (DOC /DOC SYS)

Figure A-5 Operating Environment Variable Card\*

Variable Name	Units	(Sample) Value	Storage Address	Definition
Q			30	# Systems per ship
AHR	hr./wk.		31	Av. system operating hr./ operating week
s			32	Ratio peak operating hrs. per wk. to av. operating hrs. per week
LRT	weeks		33	Average local response time
D	week		34	Deployment period
К			35	Desired LRA confidence level**
XD	weeks		36	Lead time if depot stock-out
N			37	# Ships
DRT			38	Depot response time
£			39	Lot size used for unit cost estimations
1-COND			40	1-(condemnation rate)
log RRATE/ log 2			41	Learning curve factor
IEC+IMC.L	\$ *000		42	Item entry and management factor
COD	\$*000		43	Cost of repair at contractor operated depot
AN	men		44	Maintenance manpower available to LRA**
BN	\$*000		45	Annual billet cost for trained maintenance personnel

<sup>\*</sup>This card, prepared by the system designer, is input to the LRAM and SCM.

<sup>\*\*</sup>Value may vary for different LRA's.

Figure A-5 Operating Environment Variable Card (cont'd)

Variable Name	Units	(Sample) Value	Storage Address	Definition
BG	\$1000		46	Annual billet cost for general labor personnel
L	yrs.		47	Discounted life cycle
TA	\$*000		48	"A" School course cost for maintenance personnel
(1+TOR·L)			49	Discounted personnel attri- tion factor
(1+mL)			50	Discounted support and test equipment maintenance factor

Figure A-6 TDM Memory Register Allocation

PROGRAM STEPS	000-239	BANK 1
PROGRAM STEPS	240-479	BANK 2
PROGRAM STEPS DATA REGISTERS		BANK 3
DATA REGISTERS		BANK 4

Figure A-7 TDM Data Register List

BANK 4

BANK 3

00	indirect	
	old, 4, indirect	flg
02	LRT	•
03	s <sub>1</sub> or	•
04	К, А	
05	r <sub>1</sub>	
06	D	•
07	s <sub>2</sub> , s <sup>-</sup>	•
80	K <sub>2</sub> , UC	
09	r <sub>2</sub>	
10	n	•
11	n	•
12	8	•
13	N	•
14	ANm	•
15	B N <sub>m</sub>	•
16	TC <sub>m</sub>	
17	ANo	•
18	BNo	•
19	TCo	•
20	LC	•
21	MTBF	•
22	ucœ	•
23	l -	•
24	Q	•
25	AHR	•
26	MTTRS	•
27	SM	•
28	TS <sub>m</sub>	•
29	θ	•

30	STE	•
31	COD	•
32	RP	•
33	DOC	•
34	SPARES, S	
35	к, в	
36	λ', h•D	
37	λ, LCC .	
38	V	
39	$r_1 + r_2$	

Marks input variable

USER'S GUIDE SUPPLEMENT
FOR LOWEST REMOVABLE ASSEMBLY MODEL

Figure B-1 LRAM Operating Instructions

Step	Procedure	Press	Display/Printer
1	Load program	CLR (load Bank 1)	1.
	× 1	CLR (load Bank 2)	2.
2	Load operating environ-	CLR (load Bank 3)	3.
	ment data card		
3	Load input data	CLR (load Bank 4)	4.
4	Alter input data*	1000 STO 1 4	1000.
5	Run program  (To suppress printing of C <sub>j</sub> , j = 2-8, press  2nd Sin j. To restore printing option press  [INV 2nd Sin j.  Note: there is no C <sub>1</sub> .)	A	C2 WAGE C3 TRN C4 HRDW C5 STE C6 RPR C7 IEMC C8 DOC
6	For new design variant go to Step 4	. 40	
7	Record input/output data	4 2nd Wile	4.
		(load Bank 4)	

<sup>\*</sup>See Figure B-2.

Figure B-2 LRAM Data Input Instructions (Sample Data Collection Worksheet)

Variable Name	Units	(Sample) <sup>*</sup> Value	Storage Address	
"IDEN"	!		11	OP Code for LRA identifier*
q			12	Total # of units in the system
δ			13	Ratio (LRA oper. hrs./system op. hrs.
MTBF	hrs.		14	Mean time between failure
r <sub>1</sub>			15	Local repair LOR switch**
r <sub>2</sub>			16	Depot Repair LOR switch**
UC <sub>&amp;</sub>	\$1000		17	Estimated unit cost at lot size (specified in op. envir. data)
MTTRS	manhour		18	Mean time to repair system due to LRA failure
MTTR	manhour		19	Mean time to repair LRA***
TFI	\$ 1000		20	Specific training cost to remove and replace LRA
TR	\$*000		21	Training cost to repair LRA***
STE	\$1000		22	Purchase cost of system support
STE <sub>rpr</sub>	\$*000		23	Purchase cost of support and test eqpt. to repair LRA***
c			24	Total # of components in LRA
ē			25	# of new components unique to LRA
DOC	\$1000		26	Documentation cost to describe LRA
DOCrpr	\$1000		27	Documentation cost for repair of LRA**

<sup>\*</sup>A four letter identifier used when the LRAM output is input to the SAM.

<sup>\*\*</sup>For local repair set  $r_1 = 1$ ,  $r_2 = 0$ .

For depot repair set  $r_1 = 0$ ,  $r_2 = 1$ .

For discard set r = 0,  $r_2 = 0$ .

\*\*\*If coded local repair.

Figure B-3 LRAM Code Constant Program Location

Variable Name	Units	Program Location	Current Value	Available Program Steps	Definition
d		221 and 228*	2	1	# Depots
h		247	2	1	# Deployments/yr.

<sup>\*</sup>d must be altered in both locations.

Figure B-4 LRAM Memory Register Allocation

PROGRAM STEPS 000-239	BANK 1
PROGRAM STEPS 240-479	BANK 2
DATA REGISTERS 30-59 (OPERATING ENVIRONMENT)	BANK 3
DATA REGISTERS 00-29 (LRA INPUT)	BANK 4

# Figure B-5 LRAM Data Register Listing

BANK 4

01 ind. C <sub>j</sub> 02 LCC, hold 03 TC <sub>Q</sub> ,C <sub>4</sub> , A, X 04 C <sub>5</sub> , S 05 C <sub>6</sub> , S' 06 C <sub>7</sub> , \(\bar{\lambda}\) 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M', hold 10 K(XD) 11 "IDEN" 12 q 13 & 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 \(\bar{c}\) 26 DOC 27 \(\bar{DOC}\) 28 B. K(t)			SOLONIA FINANCIA
01 ind. C <sub>3</sub> 02 LCC, hold 03 TC <sub>0</sub> ,C <sub>4</sub> , A, X 04 C <sub>5</sub> , S 05 C <sub>6</sub> , S' 06 C <sub>7</sub> , \(\bar{\lambda}\) 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M' <sub>m</sub> , hold 10 K(XD) 11 "IDEN" 12 q 13 \(\delta\) 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>\(\ell\)</sub> 18 MTTRS 19 MTTR 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 \(\bar{c}\) 26 DOC 27 \(\bar{DOC}\) 28 B. K(t)	00	ind. 1bl.	
02 LCC, hold 03 TC <sub>0</sub> ,C <sub>4</sub> , A, X 04 C <sub>5</sub> , S 05 C <sub>6</sub> , S' 06 C <sub>7</sub> , λ 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M' <sub>m</sub> , hold 10 K(XD) 11 "IDEN" 12 q 13 δ 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>ξ</sub> 18 MTTRS 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 c 26 DOC 27 DOC <sub>rpr</sub> 28 B. K(t)	01	ind. C,	
04 C <sub>5</sub> , S  05 C <sub>6</sub> , S'  06 C <sub>7</sub> , $\bar{\lambda}$ 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K  08 C <sub>9</sub> , B  09 M', hold  10 K(XD)  11 "IDEN"  12 q  13 δ  14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	02	LCC, hold	
04 C <sub>5</sub> , S 05 C <sub>6</sub> , S' 06 C <sub>7</sub> , $\overline{\lambda}$ 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M', hold 10 K(XD) 11 "IDEN" 12 q 13 δ 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>4</sub> 18 MTTRS 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 c 26 DOC 27 DOC <sub>rpr</sub> 28 B. K(t)	03	TCo,C4, A, X	
05 C <sub>6</sub> , S' 06 C <sub>7</sub> , $\overline{\lambda}$ 07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M', hold 10 K(XD) 11 "IDEN" 12 q 13 δ 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>\(\hat{\chi}\)</sub> 18 MTTRS 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 \(\bar{c}\) 26 DOC 27 \(\bar{DOC}\) 28 B. K(t)	04	C <sub>5</sub> , S	
06 C <sub>7</sub> , \( \lambda \) 07 C <sub>8</sub> , \( r_1 + r_2 \), \( \text{K} \) 08 C <sub>9</sub> , \( \text{B} \) 09 M'_m, \( \text{hold} \) 10 \( \text{K(XD)} \) 11 "IDEN"  12 q  13 \( \text{6} \) 14 \( \text{MTBF} \) 15 \( r_1 \) 16 \( r_2 \) 17 \( \text{UC}_{\text{Q}} \) 18 \( \text{MTTRS} \) 19 \( \text{MTTR} \) 19 \( \text{MTTR} \) 20 \( \text{TS} \) 21 \( \text{TR} \) 22 \( \text{STE} \) 23 \( \text{STE}_{rpr} \) 24 \( \text{c} \) 25 \( \text{c} \) 26 \( \text{DOC} \) 27 \( \text{DOC}_{rpr} \) 28 \( \text{B.} \( \text{K(t)} \)	05	c, s'	
07 C <sub>8</sub> , r <sub>1</sub> + r <sub>2</sub> , K 08 C <sub>9</sub> , B 09 M', hold 10 K(XD) 11 "IDEN" 12 q 13 δ 14 MTBF 15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>ξ</sub> 18 MTTRS 19 MTTR 20 TS 21 TR 22 STE 23 STE <sub>rpr</sub> 24 c 25 c 26 DOC 27 DOC <sub>rpr</sub> 28 B. K(t)	06	67. X	
08 C <sub>9</sub> , B 09 M', hold 10 K(XD) 11 "IDEN"  12 q  13 δ  14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>ξ</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	07	$c_8, r_1 + r_2, K$	
09 M'_, hold 10 K(XD) 11 "IDEN"  12 q  13 8  14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	08	C <sub>9</sub> , B	
10 K(XD)  11 "IDEN"  12 q  13 6  14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>Q</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	09	M <sub>m</sub> , hold	
12 q  13 δ  14 MTBF  15 r  16 r  2	10	K(XD)	
13 6  14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	11	"IDEN"	•
14 MTBF  15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	12	q	•
15 r <sub>1</sub> 16 r <sub>2</sub> 17 UC <sub>Q</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	13	8	•
16 r <sub>2</sub> 17 UC <sub>2</sub> 18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)	14	MTBF	•
17 UC   18 MTTRS   19 MTTR   20 TS   21 TR   22 STE   23 STE <sub>rpr</sub> 24 c   25 c   26 DOC   27 DOC <sub>rpr</sub> 28 B. K(t)	15	rl	•
18 MTTRS  19 MTTR  20 TS  21 TR  22 STE  23 STE  24 C  25 C  26 DOC  27 DOC  27 PPC  28 B. K(t)	16		•
19 MTTR  20 TS  21 TR  22 STE  23 STE <sub>rpr</sub> 24 c  25 c  26 DOC  27 DOC <sub>rpr</sub> 28 B. K(t)		uc	•
20 TS  21 TR  22 STE  23 STE  24 c  25 C  26 DOC  27 DOC  27 PDC  28 B. K(t)	18		•
21 TR	19	MTTR	•
21 TR	20	TS	•
22 STE	21	TR	9
24 c 25 c 26 DOC 27 DOC <sub>rpr</sub> 28 B. K(t)	22	STE	•
24 c 25 c 26 DOC 27 DOC <sub>rpr</sub> 28 B. K(t)	23	STE	•
26 DOC • 27 DOC <sub>rpr</sub> • 28 B. K(t) •	24	c	•
27 DOC <sub>rpr</sub> • 28 B. K(t) •	25	<u>c</u>	•
28 B. K(t)	26		•
		rpr	•
29 λ	28		•
	29	λ	•

LRAM Input/Output Card

• Marks Input Variable

BANK 3

30	Q
31	AHR
32	S
33	LRT
34	D
35	K
36	XD
37	N
38	DRT
39	L
40	1-COND
41	log RRATE/log 2
42	IEC+IMC · L
43	COD
44	AN
45	BN
46	BG
47	L
48	TA
49	(1+TOR·L)
50	(1+mL)
51	"WAGE"
52	"TRN"
53	"HRDW"
54	"STE"
55	"RPR"
56	"IEMC"
57	"DOC"
58	"*LCC"
59	hold UC

Op. Env. Data Card

USER'S GUIDE SUPPLEMENT
FOR SYSTEM AGGREGATION MODEL

Figure C-1 SAM Operating Instructions

Step	Procedure	Press	Display/Printer
1	Load program	CLR (load Bank 1)	1.
		CLR (load Bank 2)	2.
2	Initialize aggregation registers(load initializing card)	CLR (load Bank 3)	3.
3	Load output card for LRA,*	CLR (load Bank 4)	4.
4	Aggregate		
	Add QIPA LRA's	QIPA A	QIPA IDEN
	Remove QIPA LRA's	QIPA B	-QIPA IDEN
5	Repeat Step 3 and 4 for all LRA's in system		
6	Load system manpower cost card	CLR (load Bank 4)	4.
7**	Alter system input data (i.e., set SM = 2 hrs.)	2 <b>STO</b> 13	2.
8**	Record new system data for future use	4 2nd Write (load Bank 4)	4.

<sup>\*</sup>Output can also from SAM. \*\*This step may be skipped.

Figure C-1 SAM Operating Instructions (cont'd)

Step	Procedure	Press	Display/Printer
9	Compute system costs	C	C1 MWGE C2 MTRN C3 DWGE C4 DTRN C5 HRDW C6 STE C7 RPR C8 IEMC C9 D□C
10	For new design variant go to Step 3 or Step 6		
11	Compute system MTBF and MTTR	D	MTBF MTBF MTTR MTTR
12	Record output data for future use (as input to SAM)	4 2nd Wrie (load Bank 4)	4.

Figure C-2 SAM Input Data (Sample Data Collection Worksheet)

Variable Name	Units	(Sample) Value	Storage Address	
S			05	Ratio peak to average weekly operating hours
Q			06	# Systems per ship
BG	\$1000		07	Annual billet cost for general labor personnel
N			08	# Ships
L	yrs.		09	Discounted life cycle
AHR	hrs/wk.		10	Av. weekly operating hrs.
"sys"			11	System identifier*
q			12	# Units per system**
SM	manh./ week		13	Weekly scheduled main- tenance requirement
AN <sub>m</sub>	men		14	Available pool of trained maintenance personnel
BN m	\$1000		15	Annual billet cost of trained maintenance personnel
TA <sub>m</sub>	\$ 000		16	"A" School course cost for maintenance personnel
TC <sub>m</sub>	\$^000		17	System orientation course cost for maintenance personnel
θ	men		18	# Operators/unit
ANo	men		19	Available pool of trained operators
BNo	\$1000		20	Annual billet cost of trained operators

<sup>\*</sup>Used only if SAM output is used on SAM input.
\*\*q = 1 if SAM us used system level.

Figure C-2 SAM Input Data (cont'd)

Variable Name	Units	(Sample) Value	Storage Address		
TAO	\$~000		21	"A" school course cost for operators	
TCm	\$ 1000		22	System orientation course cost for operators	
PT	\$^000		23	Estimated system assembly cost at lot size $\ell$	
e.	-		24	Lot size used for system assembly cost estimate	
log RRATE /log 2			25	Learning curve factor (log reduction rate)/log 2	
DOC	\$*000		26	System level documentation cost	
STE	\$*000		27	System level support and test equipment cost	
(1+mL)			28	Discounted support of support equipment factor	
(1+TOR·L)			29	Discounted personnel attrition factor	

Figure C-3 SAM Memory Register Allocation

PROGRAM STEPS 000-239	BANK 1
PROGRAM STEPS 240-396	BANK 2
DATA REGISTERS 30-59 (Initialization and Accumulation)	BANK 3
DATA REGISTERS 00-29 (LRAM output, System Man- power, SAM output)	BANK 4

Figure C-4 SAM Data Register Listing

BANK 4

BANK 4

BANK 3

03			TO		
04		1	_	WD	20.00
05	s	1	SI	E	
06	Q	1	RF	R	
07	BG			MC	
08	N	/	DO	C	
09	L	. /	M	<u> </u>	
10	AHR				
11	"SYS"	1	115	YS"	
12	q		q		
13	SM				
14	ANm	/	MT	rbf	
15	BN	1	r	<b>-</b> 0	
16	TAm				
17	TCm				
18	6	1	M	TTR	
19	ANo				
20	BN	, /	T	Cm	
21	TAo				
22	TCo				
23	PTL				
24	l				
25	log F	RAT	E ÷	log	2
26	DOC		*		
27	STE				
28	1 + n	nL			
29	1 + 1	or.	L		

Manpower	Input/SAM
Output	

03	$TC_0 = 0$
04	HRDW
05	STE
06	RPR
07	ĪEMC
08	DOC
09	Mí
11	"IDEN"
12	9
14	MTBF
15	r
18	MTTRS
19	MTTR
20	TS
21	TCm
93.4	
11111	
will	
Will.	
delle	

LRAM Output

800	
32	$\Sigma TC_{\mathbf{m}} = 0$
33	ΣTC <sub>o</sub> = 0
34	
35	$\Sigma$ STE = 0
36	$\Sigma RPR = 0$
37	ΣIEMC = 0
38	EDOC = 0
39	$\Sigma M_{\rm m} = 0$
40	LCC = 0
41	ΣQIPA = 0
42	$\Sigma$ MTBF = 0
43	EMTTR = 0
44	n hold
45	QIPA, Mo hold
46	R, A hold
47	"MWGE"
48	"MTRN"
49	"OWG E"
50	"OTRN"
51	"HRDW"
52	"STE"
53	"RPR"
54	"IEMC"
55	"DOC"
56	"LCC"
57	"MTBF"
58	"MTTR"

Initialize/Aggregation Registers

USER'S GUIDE SUPPLEMENT
FOR SYSTEM CONFIDENCE MODEL

Figure D-1 SCM Operating Instructions

Step	Procedure	Press	Printer/Display
1	Load Program	CLR (load Bank 1)	1.
2	Load Operating Environ- ment Card	CLR (load Bank 3)	3.
3	Initialize	A	CONFIDENCE LEVEL (%)
4	Load Output Card for LRA	CLR (load Bank 4)	
5	Compute achieved confidence level, add to system confidence*	В	κ <sub>1</sub> IDEN
6	Repeat Steps 4 and 5 for every LRA in the system		
7	Compute achieved system confidence	Ō	SYSTEM <b>R</b>

<sup>\*</sup>If an LRA is added by mistake in Step 5, load the card in again and press C.

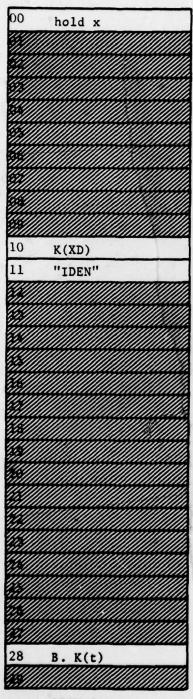
Figure D-2 SCM Memory Register Allocation

PROGRAM STEPS 000-207	BANK 1
	BANK 2
OPERATING ENVIRONMENT	BANK 3
LRAM OUTPUT CARD	BANK 4

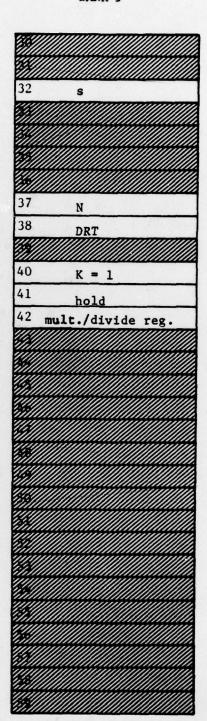
## Figure D-3 SCM Data Register Listing

BANK 4

BANK 3



LRAM Output



Op. Env. Data Card