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TITLE: THIN SOLID FILMS: An International Journal on the Science and Technology of Condensed Matter Films. Volume 412 Nos. 1-2, June 3, 2002. Proceedings of the Workshop on MBE and VPE Growth, Physics, Technology [4th], Held in Warsaw, Poland, on 24-28 September 2001

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Laterally overgrown structures as substrates for lattice mismatched epitaxy

Z.R. Zytikiewicz

Institute of Physics, Polish Academy of Sciences, Al. Lotnikow 32/46, PL-02-668 Warsaw, Poland

Abstract

This article provides a general review of the epitaxial lateral overgrowth (ELO) technology and of application of ELO layers as substrates with adjustable value of lattice constant. In particular, the issues of ELO growth mechanism, substrate defect filtration during ELO procedure and strain in ELO layers will be addressed. Recent literature data on MOVPE ELO growth of GaN on sapphire and our results on lateral overgrowth of GaAs on GaAs and Si substrates by LPE are used as examples. Finally, other lateral overgrowth techniques (growth of lattice mismatched bridge layers and pendeo-epitaxy) will be presented and compared with the conventional ELO technique. © 2002 Elsevier Science B.V. All rights reserved.

Keywords: Lateral overgrowth; Lattice mismatched epitaxy; Epitaxial lateral overgrowth; Pendeo-epitaxy

1. Introduction

Very often there are no suitable substrates for lattice-matched epitaxy of semiconductor structures. In such case a buffer layer with graded or abrupt composition profile is usually deposited on available substrate to obtain the layer with required value of lattice constant. However, despite sophisticated methods of buffer layer engineering, density of dislocations threading to the surface of the buffer from its interface with a lattice mismatched substrate is often too high for device applications. Therefore, the lateral growth techniques have been developed to avoid propagation to the epilayer of dislocations present in the buffer layer. Epitaxial lateral overgrowth (ELO) is a method of epitaxial growth on a partially masked substrate [1]. Prior to the ELO procedure a suitable relaxed buffer layer is grown on the substrate. Then, the structure is covered by a thin masking film and patterned by conventional photolithography to form on its whole area a grating of mask-free seeding windows (Fig. 1a). Finally, an epitaxial layer is deposited on such substrate. The epilayer nucleates selectively on the seeds and the growth proceeds in the direction normal to the substrate. As soon as the crystallisation front exceeds the top surface of the mask, the growth in lateral direction over the masking film

starts (Fig. 1b). The adjacent ELO stripes coalesce to form a continuous epilayer if enough growth time is given. The main advantage of this approach is that the buffer dislocations are blocked by the masking film and can propagate to the ELO layer through a narrow window in the mask, only. Therefore, the laterally grown parts of ELO layers ('wings') should be nearly defect-free despite a high density of threading dislocations (marked as TD in Fig. 1) in the underlying structure. As will be presented below, there are many experimental data showing high efficiency of defect filtration in epitaxial laterally overgrown semiconductor structures. Thus, the ELO method, when combined with well elaborated techniques of buffer layers engineering, is a promising tool to grow high quality epilayers on available lattice mismatched substrates. Then, required value of lattice constant is obtained by controlling composition of the buffer, while dislocations introduced during relaxation of its lattice mismatch with the substrate are filtered during subsequent homoepitaxial lateral overgrowth. In this way new semiconductor substrates with lattice constant different from that of available bulk III–V crystals can be generated allowing for epitaxy of a new class of high quality semiconductor structures required by modern opto- and microelectronics. The recent breakthrough in development of long lifetime GaN/InGaN blue lasers, being due to the high efficiency of defects filtration during lateral growth, is the most

E-mail address: zytkie@ifpan.edu.pl (Z.R. Zytikiewicz).

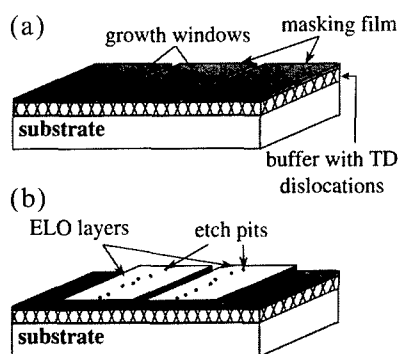


Fig. 1. Principle of ELO procedure: (a) growth of relaxed buffer layer followed by deposition of an amorphous masking film, photolithography and etching of mask-free seeds ('growth windows'); (b) growth of ELO layers starts selectively from the seeds. Then, the ELO layers spread laterally over the masking film. TD are the dislocations threading to the surface of the buffer. The respective etch pits are visible on ELO surface over the seeds, only.

spectacular recent achievement of the ELO technique [2].

Two main fields of interest can be easily distinguished in the published reports on ELO research. These are lateral overgrowth of GaN epilayers on sapphire or SiC substrates and ELO of 'traditional' III–V compounds (e.g. GaAs or InP) on silicon substrates. This choice apparently results from the market demand for novel, low cost semiconductor devices. Therefore, vapour phase growth of ELO GaN and liquid phase epitaxial lateral overgrowth of GaAs will be used as examples in this review to illustrate the basic phenomena that take place in ELO structures. It will be shown that some general rules for growing of thin and wide layers, efficient filtration of defects threading from underlying buffer and impact of this process on structural and optical quality of overgrown material, interaction of ELO layers with the mask leading to downward wings tilt and the ways this tilt can be reduced, relaxation of thermal stress via wing tilting—all these phenomena are similar in both cases despite fundamental differences between the growth techniques used and different properties of materials grown. This clearly means that they are not specific attributes of a particular ELO case but must originate directly from the principle of the ELO technique. Presentation of such general features of epitaxial lateral overgrowth is the aim of this article. In the following sections the issues of ELO growth mechanism, substrate defects filtration during ELO procedure and strain in ELO layers will be addressed. Finally, some other techniques of lateral overgrowth will be presented and compared with the conventional ELO technique.

2. Mechanism of growth of ELO layers

Efficient ELO procedure requires large growth rate anisotropy, i.e. the growth conditions are necessary at

which lateral growth of the layer is much faster than that in the direction normal to the substrate. To benefit from a natural growth anisotropy of various crystal faces the ELO process should proceed at low supersaturation [3]. Therefore, equilibrium growth techniques as liquid phase epitaxy (LPE) or vapour phase epitaxy (VPE) should be chosen, if possible, for lateral overgrowth. Indeed, the ELO layers with the value of the aspect (wing width/thickness) ratio as large as 70 are actually grown from a liquid phase. Solution or melt growth of group III nitrides is extremely complicated due to low solubility of nitrogen in liquid metals. Therefore, metalorganic VPE (MOVPE) or hydride VPE (HVPE) are commonly used nowadays to grow ELO structures of these compounds. However, large supersaturation at the growing face makes control of growth anisotropy difficult, so lateral structures of GaN with an aspect ratio up to 4 are obtained by MOVPE, only. Molecular beam epitaxy (MBE) growth of ELO layers is even more complicated as deposition of polycrystalline material on the mask is hard to avoid [4,5]. Sophisticated systems with molecular beams oriented at low angle to the substrate, low growth rate and precise temperature control [6] or special substrate preparation and growth procedures [7] are necessary to get growth started selectively from the seed. There is no significant gas phase diffusion during MBE growth, so lateral overgrowth must rely on the surface mobility of adatoms, only. Therefore, even if growth selectivity is obtained, the MBE grown ELO layers are usually very narrow which makes their application in devices production difficult.

Much is known already about the mechanisms active during growth of ELO layers (see Nishinaga [3] and Zytkeiwicz [8] for a review). It has been shown that shape of cross-section and the aspect ratio of the layers can be efficiently controlled by choice of seeds orientation. The basic idea is to find such orientation of the seeding lines for which the side walls of the ELO layer are covered by fast-growing planes, while the slowly grown facet is formed on the upper surface. Then, the requirement of large ratio of lateral to vertical growth rates can be fulfilled [3]. For GaN on sapphire ELOs the seeding windows are usually aligned along the $\langle 11-20 \rangle$ or the $\langle 1-100 \rangle$ directions of GaN [9]. For LPE growth of ELO layers on (100) GaAs or InP substrates seeding lines with directions slightly rotated relative to the $\langle 110 \rangle$ are commonly used [3]. Steps on the surface supplied by substrate miscut or dislocations enhance the vertical growth of ELO [8,10]. Therefore, additional restriction for seeds direction appears on off-oriented substrates. Then, from the optimal seed directions the one must be chosen for which density of misorientation steps inside the seeding area is the smallest [10]. For the same reason dislocation density in the buffer must be as low as possible to obtain ELO layers with a large

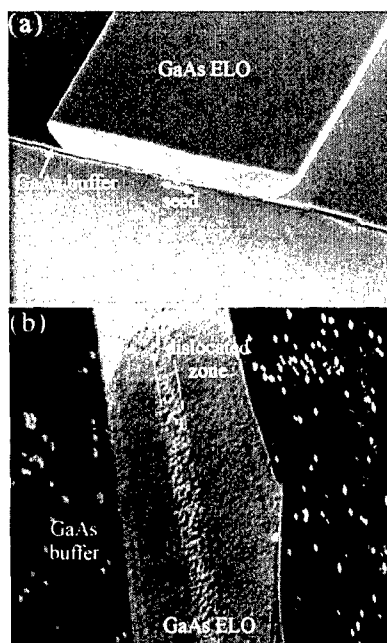


Fig. 2. Scanning electron microscopy image of LPE grown GaAs on Si ELO structure (a) and its plane view after etching in KOH to reveal etch pits distribution (b).

value of the aspect ratio. Doping has been found as an useful way by which the vertical growth rate can be reduced, leading to thin and wide ELO layers. Indeed, we have reported a significant increase of the width/thickness ratio for heavily Si doped GaAs ELO layers grown by LPE [11]. The effect has been explained as being due to impurity induced retardation of surface steps flow on the upper surface of ELO. A similar phenomenon has also been found for Mg-doped GaN ELO layers grown by MOVPE [12].

3. Filtration of substrate defects in ELO structures

Fig. 2a shows a scanning electron microscopy (SEM) image of a cross-section of a GaAs ELO layer deposited on silicon substrate. The structure consists of Si(100) substrate with MBE grown 2- μm -thick GaAs buffer on which the SiO_2 masking film has been sputtered. The GaAs ELO layer was grown by LPE. Its width and thickness are 85 μm and 11 μm , respectively. Other growth details can be found elsewhere [13]. Fig. 2b shows the plane view of the same layer after revealing of etch pits by etching in molten KOH. It is noteworthy that the density of etch pits on the buffer surface is very high ($\sim 10^8 \text{ cm}^{-2}$). On the contrary, in the ELO layer only the dislocations threading from the buffer are observed. As shown in Fig. 2b, these dislocations are confined in a very narrow area above the seed. The rest of the ELO layer is nearly dislocation free, which has been additionally proved by studies of the layers with the use of synchrotron X-ray topography technique [14].

This finding shows that the mechanism of substrate defects filtration active during ELO procedure is very efficient. Similar phenomenon is commonly observed in all laterally overgrown semiconductor structures [3,15–17].

The question arises if all the dislocations originally present in the seeding area do reach surface of ELO layer. Studies of GaN ELO structures by transmission electron microscopy have shown that a large part of dislocations threading from the buffer through opening in the mask change their direction inside the ELO material, so they propagate parallel to the substrate [18]. This change of the propagation direction prevents the dislocations from crossing the film. Consequently, only a part of dislocations from the buffer reach surface of ELO layer. This phenomenon, together with blocking of dislocations by the mask, results in much lower dislocation density in ELO structures as compared with that in planar buffer layers. The mechanism of bending of TD dislocations in ELO layers is not fully understood yet, although there are suggestions that the bending may be closely related to the change of the growth front and appearance of crystal facets at the very beginning of lateral overgrowth [19]. Probably, stress present during growth of the layers may play its role as well. Some of dislocations threading from the buffer propagate through the ELO without any change of their direction, so they create a defected zone on the surface above the seed as that visible on Fig. 2b. In GaN those dislocations are aligned parallel to the c -axis (i.e. perpendicular to the surface), so width of the defected zone does not change significantly with the layer thickness. In many cases, however (e.g. the case of GaAs on Si), TD dislocations are of 60° type. Then, the defected zone width is proportional to the layer thickness and, hence, the lateral growth rate must be increased at expense of vertical growth to obtain a large dislocation-free area of the layer [20].

High crystallographic quality of ELO wings results in their better optical properties than those of planar buffers. As example, Fig. 3 shows a plane view of the GaAs on Si ELO structure similar to that from Fig. 2a, in panchromatic mode of cathodoluminescence (CL).

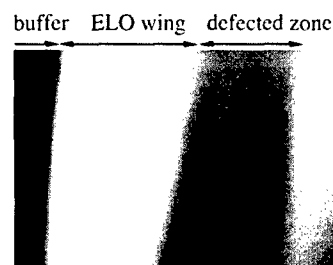


Fig. 3. Cathodoluminescence image of LPE grown GaAs on Si ELO structure from Fig. 2.

Higher CL intensity from the wing as compared to that coming out from the material grown vertically above the seed, and even much higher than that coming out from the buffer, is clearly visible. This is what should be expected, as dislocations are known to behave like centres of effective non-radiative recombination of excited carriers. Similar distribution of cathodoluminescence intensity has also been observed across surface of GaN on sapphire ELO structures [21].

An advantage of using ELO layers for epitaxial device structures was first demonstrated by Nakamura et al., who reported in 1997 a multiquantum well InGaN/GaN laser diode having a life-time longer than 1000 h when deposited on GaN/sapphire ELO substrate [22]. Since then the life-time of these diodes has been significantly increased, so they are commercially available at present. For a comparison, the same InGaN/GaN diodes made on conventional sapphire substrates had worked for 300 h, only [23]. This remarkable improvement was due to significant reduction of the threshold current density of the laser diodes fabricated on low dislocation density ELO substrates. Kozodoy et al. have shown directly that the use of lateral overgrowth to eliminate dislocations leads to better electrical properties of GaN p–n junctions [24]. Reverse-bias leakage current was reduced by three orders of magnitude for the diodes located on low-dislocation density ELO wings. Moreover, large reduction of dark current and sharper cut-off have been found for AlGaIn solar-blind detectors fabricated on ELO substrates [25]. The examples presented above are the best evidence that application of ELO technology leads to significant progress in development of high performance semiconductor devices made of lattice mismatched epitaxial structures.

4. Strain in ELO structures

ELO layers are of much higher quality than the reference planar structures but they are not free of strain. In particular, there is a question about strain induced by the mask itself and/or its possible interaction with the overgrown layer. Moreover, the lattice mismatch and thermal strain induced by different thermal expansion coefficients of subsequent layers and the substrate may result in large deformations of the layers. Although in real heteroepitaxial ELO structures all these phenomena are present together, for clarity of presentation in the next section problem of interaction of ELO layers with the mask underneath will be discussed first. Then, the issue of thermal strain in ELO structures will be addressed.

X-Ray diffraction and topography techniques are commonly employed when studying strain in laterally overgrown structures. Fig. 4 shows the typical geometry used for measurements of X-ray rocking curves of ELO layers. First, the rocking curve is measured for the

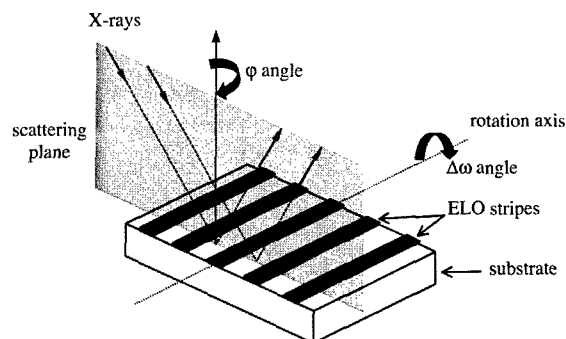


Fig. 4. Schematic of geometry used for measurements of X-ray rocking curves of ELO samples. The scattering plane is defined by incident and diffracted wave vectors. φ is the angle between direction of the seeds and the sample rotation axis during the ω scan; $\varphi=0^\circ$ for the geometry shown.

sample position in which the scattering plane (defined by incident and diffracted wave vectors) is perpendicular to the seeding line direction. This corresponds to the axis of sample rotation during the ω scan being parallel to the seeds ($\varphi=0$). Next, the sample is rotated around the substrate normal and the rocking curve is measured again for the scattering plane parallel to the seeds (i.e. for $\varphi=90^\circ$). As will be shown later, this procedure allows to determine the most characteristic features of the strain field in the samples.

4.1. Mask induced strain in ELO layers

Fig. 5 shows X-ray rocking curves of GaAs layer grown by LPE on SiO_2 masked GaAs substrate (see the source article for growth details) [26]. Both, the as-grown sample and that cut from the same wafer and etched to remove selectively the SiO_2 mask have been studied. The rocking curves presented in Fig. 5a,b have been measured with the $\varphi=0^\circ$ and $\varphi=90^\circ$ sample orientation, respectively. As it can be seen from Fig. 5a the rocking curve of the as-grown sample is very broad. However, it becomes much narrower after the SiO_2 mask has been removed. On the contrary, for $\varphi=90^\circ$ the rocking curve is quite narrow and etching causes only a slight change of its shape (Fig. 5b). It is worth mentioning that during rocking curve measurements the X-ray diffraction is sensitive mainly to the distortion of the (100) crystal planes in the scattering plane. Therefore, the large width of the rocking curve shown in Fig. 5a indicates that a significant deformation of the as-grown ELO takes place in the cross-section plane perpendicular to the seeds. In principle, similar broadening of the rocking curve could also be caused by lattice mismatch and/or composition gradients inside the structure. However, an analysis of reciprocal space maps collected during diffraction experiments of the same sample has ruled out such possibilities in our case [27].

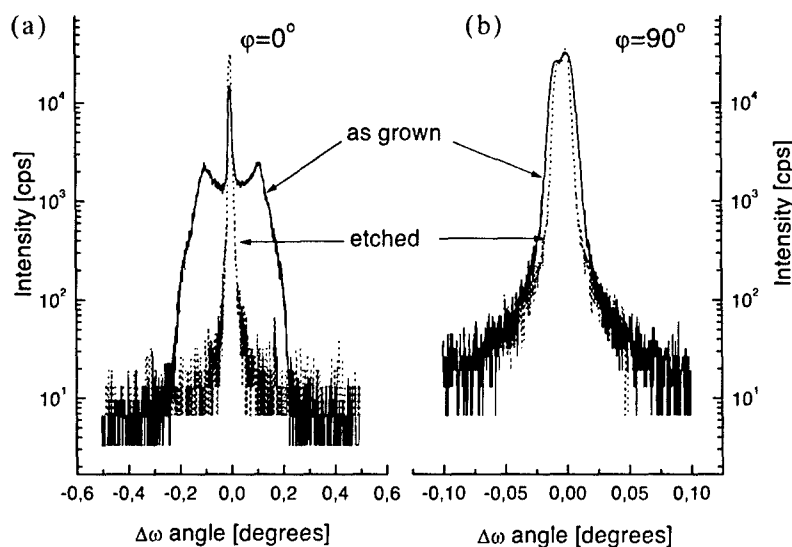


Fig. 5. Rocking curves of (400) $\text{CuK}\alpha_1$ reflection from the as-grown (solid line) and etched (dotted line) GaAs ELO layers on the SiO_2 -covered GaAs substrate measured with the ω axis parallel (a) and perpendicular (b) to the seeding lines.

We have postulated the following picture of strain in GaAs ELO layers grown by LPE on GaAs substrates [26]: the layers are under stress due to interaction of ELO wings with the SiO_2 mask. This stress is below the plastic flow threshold and accompanied strain, responsible for downward tilt of ELO wings visible in Fig. 6a, disappears when the SiO_2 mask is removed by selective etching (Fig. 6b). Let us to point out that from the shape of the rocking curve alone it is impossible to say whether the ELO wings tilt upwards or downwards, so additional experiments are needed to determine unambiguously the tilt direction. In our case, we did a series of diffraction experiments in which a very narrow X-ray beam was precisely moved in small steps across a wide GaAs ELO layer [28]. Thus, measurements of rocking curves from precisely defined regions of the single ELO stripe were possible. By analysing the order in which the side maxima appear while the beam is moved across the stripe we have proved that the wings tilt towards the mask surface. Moreover, the data obtained by scanning X-ray diffraction experiments were used to explain the origin of all the features visible on the rocking curve in Fig. 5a. In particular, we have proved that the side maxima are due to diffraction from the edges of the ELO stripe. Therefore, the misorientation of ELO crystal planes must be the largest there. Consequently, half of the angular separation of the side peaks on the rocking curve can be used as a good measure of the maximum tilt angle $\Delta\Theta_{\text{max}}$ of ELO lattice planes (see Fig. 6a).

We have studied the homoepitaxial GaAs ELO layers by synchrotron X-ray reflection and transmission topography techniques [14,29–31]. The data obtained fully confirm the picture of strain in ELO structures shown in Fig. 6. The topographs together with appropriate

numerical simulations have also been used to determine quantitatively how the misorientation of ELO lattice planes varies with position across the ELO stripe [30]. This procedure has led us to the conclusion that misorientation of the ELO lattice planes increases much faster near the centre of the ELO stripe than at the edges. In other words, the curvature radius of (100) lattice planes increases towards the edges of the ELO layer, which is in very good agreement with the measured shape of the ELO surface. Such analysis of ELO lattice planes curvature has allowed us to postulate that bending of the ELO stripes starts at the very beginning of growth when the laterally overgrown parts are thin and flexible. Then, the bent crystal planes might be reproduced during subsequent growth, although still retaining their shape [30]. As will be shown later, this is exactly the behaviour observed experimentally.

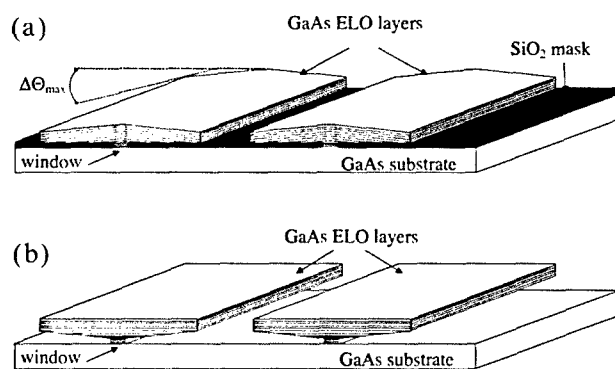


Fig. 6. Schematic drawing of the GaAs on GaAs ELO cross section. The bending is shown in the as-grown state (a). The deformation of crystal planes disappears when the SiO_2 mask is removed by etching (b). $\Delta\Theta_{\text{max}}$ is the tilt of (100) crystal planes between the edge and central part of ELO.

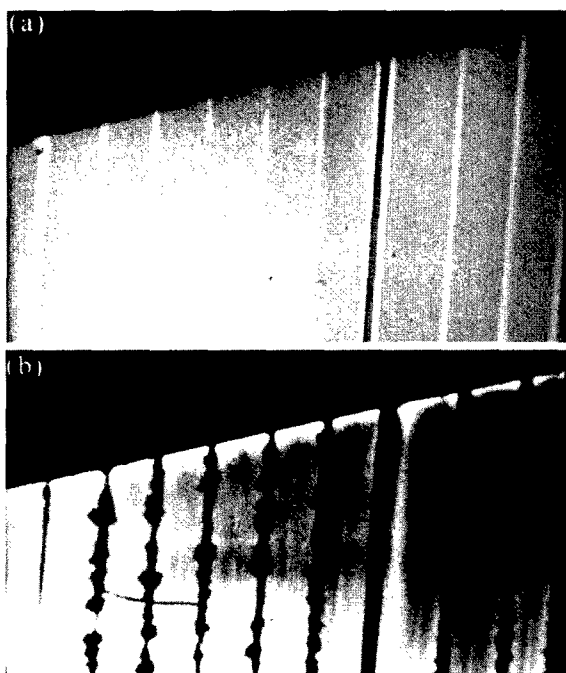


Fig. 7. SEM (a) and CL (b) images of fully overgrown GaAs on GaAs ELO structure. Defects are created at the coalescence fronts where ELO stripes tilted in opposite directions merge.

Very broad X-ray rocking curves similar to that shown in Fig. 5a are very often observed in various ELO systems, which shows that tilting of ELO wings towards the mask is a common problem in laterally overgrown structures. In some cases (e.g. in GaN on sapphire ELO structures) the values of the tilt angle $\Delta\theta_{\max}$ in excess of 2° are reported [32]. So large tilts cannot be accommodated elastically, i.e. by the mechanism we have observed in our GaAs/GaAs samples (see Fig. 5a and Fig. 6). Indeed, cross-sectional transmission electron microscopy studies of GaN on sapphire ELO structures have revealed characteristic defects in the overgrown region, which consisted of arrays of dislocations running along the seed direction [33]. These defects, originating from the bent TD dislocations propagating laterally in the ELO layer, create low-angle tilt boundaries above the edges of the SiO_2 mask.

Tilting of wings caused by their interaction with the mask underneath creates significant problems for coalescence of neighbouring stripes as they tilt in opposite directions (see Fig. 6a). For example, Fig. 7a,b shows, respectively, SEM and CL images of surface of fully overgrown GaAs ELO layer deposited by LPE on SiO_2 masked GaAs substrate. Creation of new defects at the front of coalescence of tilted wings is visible. Similar behaviour has been also observed for GaN on sapphire fully overgrown ELO structures [33]. The image presented in Fig. 7b clearly shows how serious degradation of ELO quality can be caused by tilting of laterally

overgrown parts of the layers. Therefore, there is a widespread discussion in the literature on the origin of tilting of the ELO wings and on the way it can be reduced by controlling the growth parameters.

Fini with co-workers were the first who used X-ray diffraction to observe the time evolution of lattice planes bending in GaN on sapphire ELO layers [34]. The scans in reciprocal space through the (10–13) diffraction peak of GaN obtained in situ, during growth of the structures by MOVPE are shown for various growth times in Fig. 8a. Prior to epitaxy the central peak being due to the diffraction from the GaN buffer is visible, only. Emergence of the side maximum is evident for the growth times ~ 100 s, and by ~ 300 s, distinct side peaks have evolved indicating the wings tilt $\sim 0.9^\circ$. Post growth SEM studies of layer cross-sections have shown that a significant lateral overgrowth started just at that moment. The wing peak narrows during subsequent

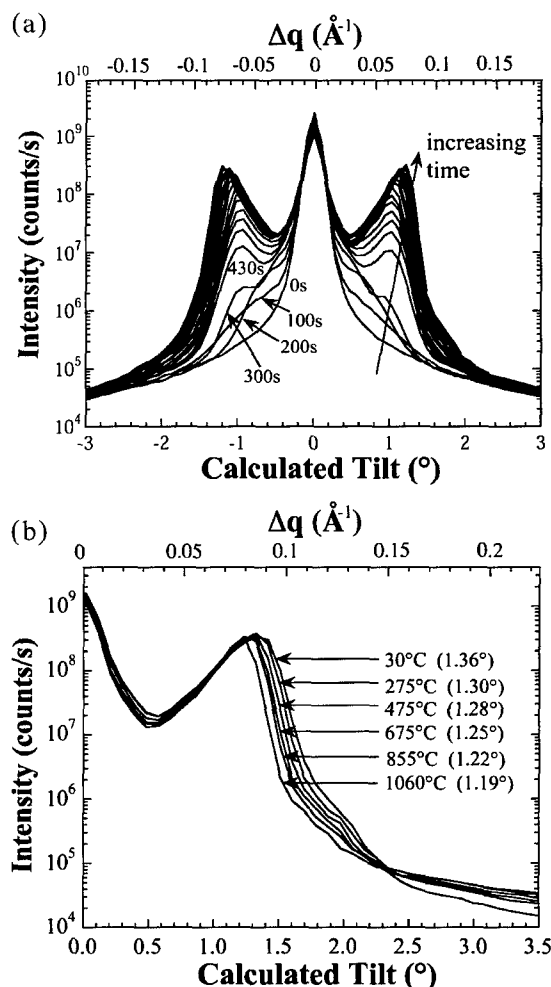


Fig. 8. Scans in reciprocal space through the (10–13) X-ray diffraction peak obtained for various growth times during MOVPE growth of GaN on sapphire ELO structures (a) and on cooling to room temperature (b). The scans in (b) are shown over half of the range measured (from Fini et al. [34]).

growth while its position slightly changes, reaching a tilt of $\sim 1.19^\circ$ after 3600 s of growth. The data collected during this elegant experiment are direct evidence that wing tilt must be due to interaction of the wing with the mask and that it starts at the very beginning of lateral overgrowth, in agreement with our earlier suggestion [30].

Keeping this in mind, the following recipe for the reduction of the mask-induced bowing of ELO layers can be proposed: the initial vertical growth rate of ELO must be increased to start the lateral overgrowth at some microscopic distance from the upper surface of the mask [28]. Then, the chance of ELO wings capture by attractive force and their interaction with the mask should be reduced. We have found several examples that this recipe efficiently works in practice. In particular, we have shown that the GaAs ELO layers grown on SiO_2 coated substrates are strain-free if their wings hang over without any contact with the underlying mask [35]. In the other cases, tilting of the wings can be efficiently tailored by controlling the ratio of vertical to lateral growth rates at the beginning of ELO growth. This has been achieved by growing the GaAs ELO layers on SiO_2 -coated GaAs substrates with increasing density of dislocations. Then, the ratio of vertical to lateral growth rates at the beginning of the growth was increased due to the higher density of surface steps, which in turn led to reduction of the mask-induced tilt of ELO wings [28,35]. In the limiting case of heavily dislocated GaAs substrates, namely on GaAs-coated Si substrates, the vertical growth of GaAs ELO was so fast that air-bridged structures without any interaction with the mask were obtained [13]. Similarly, Fini et al. have shown that the quality of coalescence of adjacent ELO GaN stripes can be improved by forcing vertical development of ELO stripes at the beginning of the growth, followed by change of growth conditions and fast lateral overgrowth of the structure [36]. This is exactly the same recipe we have proposed.

Replacement of commonly used dielectric films by other mask materials seems to be a promising way of reduction of mask induced wing tilt. In particular, we have found that that bending of the GaAs ELO layers grown by LPE is negligible when the SiO_2 mask is replaced by a thin graphite film [26]. We have explained this finding by delayed start of lateral growth caused by the change of the shape of the melt in the corner between the side wall of the ELO layer and the mask when the SiO_2 is replaced by graphite film not wetted by the gallium melt [35]. Similarly, tungsten has been found as promising mask material for MOVPE growth of GaN ELO structures with very low tilt of the wings [37]. In principle, however, it is difficult to predict a priori the mask material that would be the most suitable for each particular ELO case. Therefore, the most natural way to eliminate the mask induced strain would be to

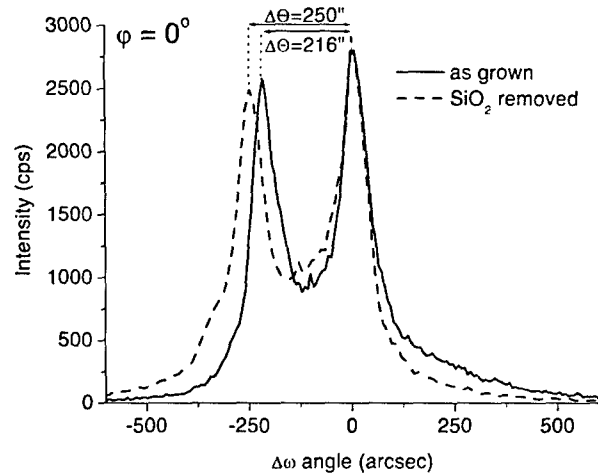


Fig. 9. Rocking curves of (400) $\text{CuK}\alpha_1$ reflection for the as-grown (solid line) and etched (dotted line) GaAs ELO layer on the GaAs-coated Si substrate measured with the ω axis parallel to the seeding lines.

grow the ELO structures with their wings having no direct contact with the mask surface. The GaN air-bridged ELO structures grown by Kidoguch et al. [38] or our GaAs ELO layers with wings hanging over the mask [35] are the best examples that this goal can be achieved. Unfortunately, quite often the growth conditions required for reproducible growth of such structures are difficult to find.

4.2. Thermal strain in ELO structures

Additional deformation of ELO lattice planes may arise when the sample, due to the different thermal expansion coefficients of its components, experiences a large stress upon cooling from the growth to room temperature. This effect is commonly observed in planar heterostructures. As will be shown below, in ELO structures this strain can be relaxed via additional tilting of ELO wings while still preserving their high quality.

Fig. 9 shows the (400) $\text{CuK}\alpha_1$ rocking curve of the GaAs ELO layer on the GaAs-coated Si substrate [13]. The scattering plane was set perpendicular to the seeds in this experiment. As it is seen, the rocking curve consists of two peaks which are separated by $\Delta\theta = 216$ arcsec. The whole rocking curve is so wide that presence of ELO deformation similar to that observed by us for GaAs ELO layer on GaAs substrate could be suspected. For the GaAs on GaAs ELO system we attributed broadening of the rocking curve to the bending strain induced by ELO stripes adhesion to the SiO_2 mask. Then, the tilt of wings disappeared after the mask had been removed. As seen in Fig. 9, the situation is quite the opposite for the ELO GaAs on Si—the separation of the peaks increases ($\Delta\theta = 250$ arcsec) after removal of the mask by selective etching. Moreover, analysis of

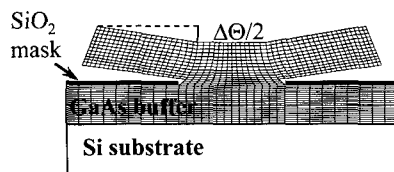


Fig. 10. Schematic drawing of cross-section of the as-grown GaAs ELO layer on Si substrate. The tilt of (100) ELO wings crystal planes equals $\Delta\theta/2$, where $\Delta\theta$ is angular separation of diffraction peaks in Fig. 9.

layer cross-sections with the use of an optical microscope has revealed a microscopic gap between the ELO and the masking film. Thus, the mask induced bending of lattice planes cannot explain the data obtained.

We have performed X-ray diffraction experiments with a narrow X-ray beam moved across the single ELO stripe to show that two peaks visible in Fig. 9 are due to the diffraction from the wings [13]. It is noteworthy that the full width at half maximum of each peak equals to 94 arcsec only, which is much smaller than that of the MBE grown GaAs buffer (435 arcsec). This result, together with an analysis of CL intensity (Fig. 3) and etch pits density (Fig. 2b) distributions, confirms very high crystallographic quality of laterally grown GaAs on Si substrates. By using the X-ray diffraction technique with the beam scanned across the ELO layer we have proved also that the wings tilt upward in the GaAs on Si case [13], which is just the opposite behaviour to that observed in the GaAs/GaAs ELO system. All these results have allowed us to create a model of bending of the GaAs ELO layer on Si substrate as illustrated in Fig. 10. A biaxial tensile strain caused by different thermal contraction of epilayer and substrate is commonly observed in GaAs layers on Si substrates [39]. This strain disappears when the GaAs/Si structure is heated to a temperature of ~ 500 °C [40], which is close to our LPE growth temperature. Therefore, the GaAs ELO layer grows essentially stress free. At room temperature, the basal plane of the ELO should have the same tensile deformation as the upper surface of the buffer GaAs. Since the ELO layer stands free and does not adhere to the mask, unrestricted strain relaxation and free contraction of vertically grown volume of ELO should take place in its upper part. This, in turn, must lead to the upward bending of ELO wings (as shown in Fig. 10). The thermal tensile strain in the seeding area is partially compensated by the mask induced compression [27]. Therefore, deformation of ELO at its base, and consequently the ELO wings tilt angle, increase when the SiO_2 mask is removed. This leads to increase of angular separation of X-ray reflections as shown in Fig. 9. Let us compare the rocking curves of the GaAs ELO layers grown on GaAs and GaAs-coated Si substrates shown in Fig. 5a and Fig. 9, respectively. The

rocking curve from Fig. 5a is very broad which indicates some distribution of strain along the interface between the wing and the mask. On the contrary, in the GaAs on Si structure strain is localised in the seeding area only and the main part of the wings is strain free, so the wing peaks are very sharp. In this way, the different shapes of the rocking curves directly reflect different mechanisms responsible for deformation of ELO lattice planes in both cases.

It is worth noting that our model of thermal strain relaxation in ELO layers predicts that the direction of wing tilt must be correlated with the sign of the strain in the buffer. In particular, downward wings tilt should be observed if the buffer layer is under compressive thermal stress (e.g. in GaN on SiC or sapphire substrates). This is exactly the behaviour presented in Fig. 8b for the GaN on sapphire ELO system. At the growth temperature GaN ELO wings are tilted downward due to their interaction with the mask. Upon cooldown to room temperature position of the wing peak changes from 1.19° to 1.36° , which means that additional downward tilt induced by thermal stress has occurred. Qualitatively, the same phenomenon can be inferred from recent simulations via finite element analysis of strain in ELO structures [41].

5. Related techniques of lateral overgrowth

The most characteristic feature of the ELO process presented so far is the change of the predominant growth direction—from vertical in the growth window to lateral in the regions over the masking film. In principle, however, it is possible to start lateral overgrowth from the seeds oriented perpendicular to the substrate surface, so change of the growth direction is not necessary. If the substrate (bare substrate or substrate covered by a suitable buffer layer) is etched to create on its surface a pattern of ridges or elongated columns oriented similar to that of windows in ELO, conditions are provided such that the side walls of the columns provide the crystallographic template for lateral growth. Fig. 11a shows geometry of silicon substrate used for LPE lateral overgrowth of Si from ridge seeds [42,43]. A thin SiO_2 mask has been deposited in the trenches to avoid direct epitaxy on the substrate. During LPE the growth starts from the side walls of the ridges and proceeds laterally over the mask. The top surface of the ridge is the dislocation-free Si(111) facet, so it contains no step sources. Therefore, under proper growth conditions the vertical growth is completely eliminated and the layer grows laterally without any change of its thickness. As the thickness of the layer is determined by the ridge height, it can be easily controlled. However, very thin layers are difficult to obtain. When the thickness of the layer decreases the radius of curvature of its side wall decreases as well (see Fig. 11a). This leads to increase

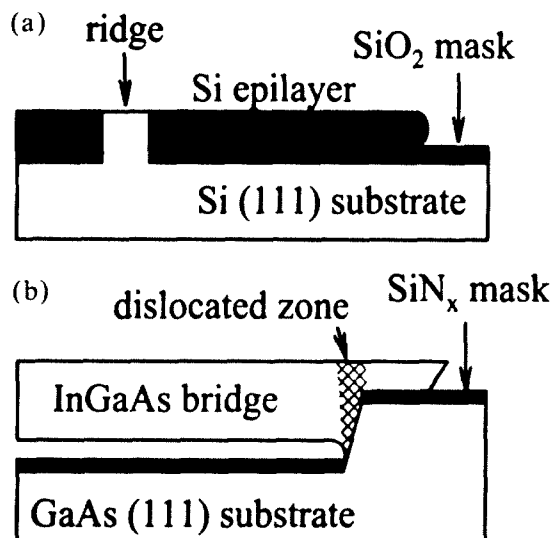


Fig. 11. Schematic illustration of cross sections of Si epilayer grown by LPE on (111) Si substrate from a ridge seed (a) and of InGaAs bridge layer grown by LPE on GaAs substrate (b).

of the equilibrium concentration of the solute around this wall due to the Gibbs–Thomson effect. Therefore, higher melt supersaturation must be used to grow thinner layers. Then, however, vertical growth may be initiated if the supersaturation is large enough for two-dimensional nucleation at the top surface of the ridge. Using this technique as thin as 0.23 μm Si layers with an aspect ratio up to 82 have been grown laterally upon SiO₂-coated Si substrates by LPE [42].

On the contrary to the case of homoepitaxy, in lattice mismatched heterostructures the top surface of the columns is not dislocation-free and must be covered by an additional mask to avoid excessive vertical growth. Fig. 11b shows schematically how this approach has been applied by Iida et al. to grow by LPE InGaAs bridge layers on GaAs substrates [44]. First, the substrate covered by the SiN_x mask has been processed by photolithography and etching to fabricate on its surface a pattern of deep circular trenches. Then, In_xGa_{1-x}As ($x \sim 0.06$) layer has been grown on such substrate by LPE. It is noticing worthy that no InGaAs buffer is used in this case. The layer grew inwards from the side wall of the trench forming a bridge over the trench. Some growth over the mask outside the trench area was observed as well. Analysis of the layer surface revealed presence of dislocated zone with etch pits density $\sim 10^6 \text{ cm}^{-2}$ only above the small area where the layer was in direct contact with the GaAs substrate. The rest of the layer, having the diameter above 1 mm, was of very high quality with dislocation density below 10^4 cm^{-2} , so it could be used as substrate for further deposition of device layers. Unfortunately, the technology presented has been tested only for very low In concentrations (and for very high In content when InAs

instead of GaAs was used as the substrate [45]). Thus, more research is needed to study how this procedure would work for the heteroepitaxial structures with large lattice mismatch. Moreover, the question about quality of coalescence of the layers grown from the opposite walls of the trench seems still to be open. Despite that, the technology of LPE growth of bridge layers over the trenches seems to be very promising due to its simplicity, very high efficiency and high quality of layers produced.

Finally, let us present the method of lateral overgrowth that has received much attention recently, namely the pendeo- (from the Latin: to *hang* or to be *suspended*) epitaxy (PE). To grow PE GaN layers, a thin AlN wetting layer and GaN buffer are grown first on an available substrate as shown in Fig. 12a. Next, trenches deep enough to reach the substrate surface (or even slightly deeper) are etched in the structure. The bottoms of trenches and top surfaces of GaN columns are then

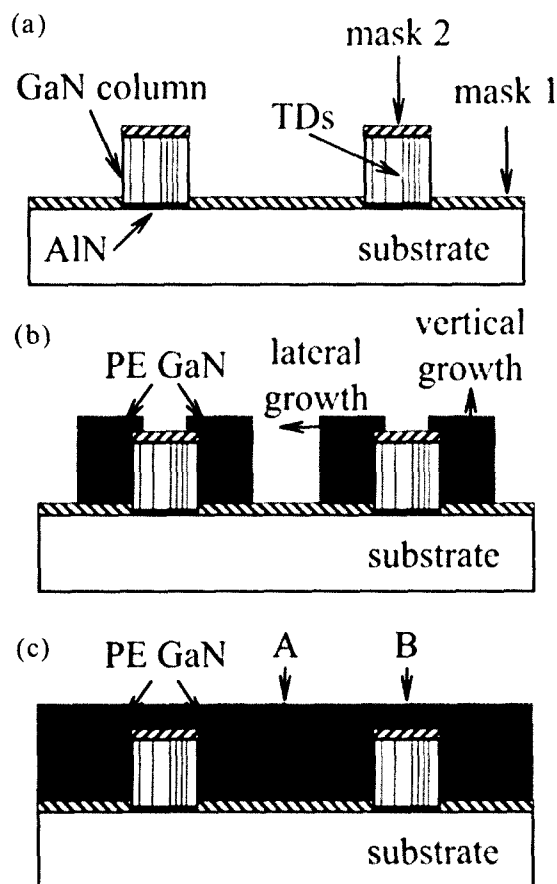


Fig. 12. Schematic of the process steps for pendeo-epitaxy of GaN: (a) etching of elongated columns in GaN buffer layer followed by masking of the substrate (mask 1) and the top column surfaces (mask 2); (b) epitaxial growth of GaN starts from exposed side walls of GaN columns and proceeds laterally to fill the trenches; also some vertical growth occurs; (c) PE layers merge over the trenches (seam A) and over the capped GaN columns (seam B), so continuous PE GaN film is formed. TDs are the dislocations threading to the surface of the GaN column.

covered by a suitable masking film, so GaN column side walls are the only surfaces exposed for subsequent GaN regrowth. During pendeo-epitaxy the GaN layers overflow the trenches growing laterally from the column side walls (see Fig. 12b). Also some vertical growth takes place followed by lateral growth over the mask covering the top of GaN mesas. If long enough growth time is given the PE layers starting from adjacent seeds merge at the fronts marked 'A' and 'B' in Fig. 12c, so a continuous PE GaN layer is formed. As mentioned before, dislocations thread in the GaN buffer mainly along the *c*-axis (i.e. perpendicular to the surface). Thus, only a very small part of them intersect the side wall of GaN columns and propagate into PE GaN layer. Therefore, the regrown material contains four to five orders of magnitude lower density of dislocations than that in the buffer [46]. Moreover, in contrary to the ELO case, also the dislocations originally present in the buffer (marked as TDs in Fig. 12) are prevented from reaching the PE layer if the GaN columns are capped by the mask. Thus, in principle, the density of dislocations should be reduced over the whole area of the substrate and the need to locate devices only in the laterally overgrown parts of the structures (as it was in the case of ELO-see Section 3) could be eliminated.

The PE technique in its version shown in Fig. 12 has been used by Chen et al. to grow by MOVPE GaN layers on SiO₂ masked sapphire substrates [47]. Studies of the layers with the use of transmission electron microscopy have proved that laterally overgrown parts of the layers are nearly dislocation-free. However, new dislocations have been found at the fronts (marked as 'A' and 'B' in Fig. 12) where the layers merge as well as over the edges of masked GaN columns. In the light of what has been written in Section 4.1 above, this finding is not surprising. Most probably, interaction of the laterally overgrown parts of the layers with the SiO₂ mask has occurred leading to their relative misorientation and defects creation at the coalescence fronts. Thus, this is the same phenomenon as we have discussed already and which is illustrated in Fig. 7.

These results, as well as the whole discussion in Section 4 of this review, clearly show that properties of laterally overgrown layers are strongly influenced by the presence of a mask. In particular, the strain introduced by the mask and 'poisoning' by impurities diffusing out from the mask at high temperature have negative impact on quality of the layers. Therefore, the processes are needed in which layers could be grown laterally without any masking film. It is important to note that each mask requires additional technological steps. Thus, a maskless version of selective epitaxy would result in significant simplification of the whole procedure, which is especially important if the growth process under development is to be applied on industrial scale. Zheleva et al. have found that under proper MOVPE conditions neither

GaN nor free Ga do accumulate on 6H-SiC (0001) surface [46]. Thus, the pendeo-epitaxy of GaN without mask between the GaN columns (mask 1 in Fig. 12) became possible by replacement of sapphire by SiC. As before, PE growth started from the side walls of the GaN columns and proceeded laterally at some distance from the SiC surface. Transmission electron microscopy showed that neither tilting nor low angle tilt boundary were present at the coalescence boundary over the trench (seam 'A' in Fig. 12c). However, tilting of 0.2° and defects at the seam 'B' were still found in the portion of the coalesced GaN layer, which interacted with the mask covering the GaN column [48]. This tilt can be further eliminated if completely mask-free PE process (i.e. without masks 1 and 2) is employed as has been reported for GaN [46] and AlGaIn [49] on SiC systems. Then, defect-free coalescence can be obtained at both coalescence fronts [46]. The price paid for that, however, is enhanced vertical growth of the PE layer and free propagation into regrown GaN of TD dislocations originally present in the GaN buffer.

The next step towards commercialisation of pendeo-epitaxy of GaN would be to replace SiC substrates, which are expensive and available with low diameters, by large area silicon wafers. However, direct epitaxy of GaN on Si is difficult and usually results in polycrystalline films, most likely due to the prior formation of the SiN_x film on the Si surface. Therefore, Davis et al. have employed the procedure in which Si(111) substrates are first covered by an epitaxial film of SiC, followed by their transfer to the MOVPE system for deposition of an AlN wetting layer and a GaN buffer [48]. Next, they could make use of the process route elaborated earlier for PE of GaN on bulk SiC, namely, substrate etching to define GaN columns and PE regrowth. The quality of the GaN layers they obtained was quite similar to that on bulk SiC substrates. The problem, however, was the thermal stress arising due to large difference of thermal expansion coefficients between subsequent layers, which resulted in cracking of the structures on cooling if the SiC transition layer was too thin.

6. Summary

Review of the epitaxial lateral overgrowth technology and of application of ELO layers as substrates with an adjustable value of lattice constant is provided. The main idea behind lateral overgrowth techniques is selective epitaxy of a homoepitaxial layer on a relaxed buffer having the required value of lattice parameter. The seeds can be defined by patterning of the mask covering the buffer layer (ELO) or the growth starts from the exposed side walls of ridges etched in the buffer (PE). Selective nucleation of the layer in the narrow seeds allows to pass through information on buffer lattice spacing, while

blocking all (or main part of) defects created due to lattice misfit between the buffer and its substrate. We have shown numerous examples that this procedure works efficiently. Usually, practical realisation of the idea above depends on the system considered. Quite often specific properties of materials and of growth technique used determine the way in which laterally overgrown structures are produced (eg. the maskless pendeo-epitaxy of GaN on SiC). There are, however, some basic phenomena typical for all laterally overgrown structures. These include some general rules that should be followed to obtain wide and thin laterally overgrown layers (i.e. choice of optimal direction of the seeds, high quality of the buffer, low growth rate, use of doping to enhance lateral growth, etc.). Efficient filtration of dislocations originally present in the buffer is well evidenced in all laterally overgrown structures. Tilt of wings caused by their interaction with the mask underneath is commonly observed both in ELO and PE layers independently on the growth technique used. If the tilt angle is small the accompanied strain can be accommodated elastically (eg. in GaAs on GaAs ELOs). Otherwise, arrays of dislocations creating low angle boundaries appear above the mask edges (eg. the case of GaN ELO structures). Tilting of laterally grown parts of the layer leads to generation of new defects at the coalescence front where two layers grown from neighbouring seeds and having opposite tilt directions merge. Growing the layers standing free above the mask seems to be the best way to improve quality of the coalescence front, although there are no general recipes how to grow such structures. Thermal strain being due to different expansion coefficients of subsequent layers is also commonly found in laterally overgrown heterostructures. However, if the layer is attached to the buffer through the narrow seeds only, this strain is usually much smaller than in reference planar structures.

It is difficult to predict how the lateral overgrowth technology will develop in the future. From scientific point of view there are still many questions to be answered. In particular, understanding of microscopic interaction of ELO wings with the mask, identification of mechanisms leading to bending of dislocations threading through the seed in ELO and many other issues require further work. Basic crystal growth research is needed to increase width of laterally overgrown layers, to improve the quality of coalescence fronts, to develop technology of lateral overgrowth of ternary layers on binary substrates and to simplify the whole growth procedure according to industrial standards. Application of laterally overgrown layers will depend on progress of these studies. Growth technology must be significantly simplified for wide application of laterally overgrown substrates. Market demands for novel semiconductor devices will decide if and which techniques of lateral epitaxy find their permanent position in industrial prac-

tice. Nowadays there is a large demand for group III nitrides substrates, so lateral overgrowth of these compounds is mostly studied in many laboratories worldwide. Probably, the importance of lateral overgrowth of 'traditional' III–V ternary substrates will increase in the near future.

Acknowledgments

The author thanks D. Dobosz for her assistance in the LPE growth of the GaAs ELO structures and J. Domagala and M. Godlewski for XRD and CL, respectively, analysis of the GaAs ELO samples. Contributions of T. Tuomi, R. Rantamäki, P.J. McNally, and D. Danilewsky to synchrotron X-ray topography experiments are highly appreciated. Moreover, permission for the use of the data shown in Fig. 8 granted by P. Fini from the University of California in Santa Barbara is highly appreciated. This work is partly supported by the Polish Committee for Scientific Research under the grant No. 7T08A 007 20.

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