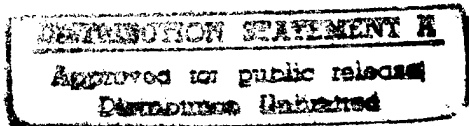


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MULTI-LAYER TILED ARRAY

STATEMENT OF GOVERNMENT INTEREST

BACKGROUND OF THE INVENTION

(1) Field of the Invention

The present invention relates to planar sonar arrays. More particularly, the invention relates to the arrangement of transducer elements in planar sonar arrays.

(2) Description of the Prior Art

Conventional planar sonar array designs typically comprise ceramic or polyvinylidene fluoride (PVDF) transducer elements arranged in a single plane according to a required inter-element spacing. The inter-element spacing is based on the half wavelength dimension of acoustic signals at the desired operating frequency. There are several disadvantages associated with this conventional array design. First, the inter-element spacing places an upper bound on the center to center distance between adjacent elements. If this upper bound is violated, beamformed acoustic energy will be allowed to fold back spatially, resulting

1 in false indications of received energy. This is analogous to
2 frequency domain aliasing where the Nyquist criteria has been
3 violated, but in the case of a beamformer or spatial filter, the
4 independent variable is bearing instead of frequency.

5 Furthermore, because the inter-element spacing constrains
6 the maximum separation distance between elements, there is a
7 corresponding upper bound on element size. That is, as the
8 inter-element spacing is decreased, the area available for the
9 element face decreases as a function of the square of the linear
10 dimension of the element's side. Element signal-to-noise ratio
11 is proportional to the volume of an element and decreases as the
12 elements get smaller. Furthermore, since the PVDF transducer
13 elements used in conventional array construction are essentially
14 flat plate capacitors, the capacitance of any element is directly
15 proportional to the electrode area of the element. As the area
16 available for the element electrode decreases, the capacitance is
17 lowered, thereby increasing electronic noise floor levels.
18 Elevated noise floor levels can mask acoustic signals of
19 interest. Furthermore, the effects of the increased noise floor
20 levels can be exacerbated by the fact that at certain operating
21 frequencies, the deep ocean ambient sound pressure level has a
22 notch making it the quietest region in the usable spectrum. It
23 is at these frequencies that problems resulting from the limited
24 capacitance of the elements are most noticeable.

25 In addition to the sensitivity and detection problems,
26 elevated noise floor levels complicate the fabrication of

1 conventional arrays. To keep the electronic noise floor level as
2 low as possible, the capacitive loading of the elements must be
3 kept at a minimum. The capacitive loading at the elements is
4 kept at a minimum by keeping the lead lengths of any signal
5 conditioners (preamplifiers) as short as possible. However, this
6 requirement complicates fabrication and provides little space to
7 connect the preamplifiers.

8 Two alternatives have been considered to increase the
9 capacitance in a flat plate capacitor without increasing the area
10 of the electrodes. One approach is to decrease the spacing
11 between the two electrodes of the capacitor. The second approach
12 is to increase the dielectric constant of the material between
13 the electrodes. However, neither of these approaches has proven
14 feasible for use in elements within a sonar array. The first
15 approach decreased the element sensitivity greatly and
16 alternative dielectric materials considered in the second
17 approach either decreased the element sensitivity or increased
18 the thermal noise of the array.

19 Thus, what is needed is a planar array with an arrangement
20 for the individual elements that increases the capacitance of the
21 elements by increasing their surface area while maintaining the
22 inter-element spacing required to avoid spatial aliasing in the
23 received signals.

SUMMARY OF THE INVENTION

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Accordingly, it is a general purpose and object of the present invention to provide a sonar array that maximizes the capacitance of the array elements while maintaining the required inter-element spacing.

Another object of the present invention is to increase the distance between the array element preamplifiers and the elements.

A further object of the present invention is to reduce the electronic noise floor levels of the array element and preamplifier combinations.

These and other objects made apparent hereinafter are accomplished with the present invention by providing a multi-layer tiled arrangement for the individual elements that increases the capacitance of the elements by increasing their surface area while maintaining a required inter-element spacing. The array comprises an outer array and an inner array of transducer elements which convert an acoustic pressure to an electrical response. The transducer elements of the outer array are positioned so as to form a grid of isolation spaces separating each one of the transducer elements. Similarly, the inner array also has transducer elements arranged in a two dimensional matrix with a grid of isolation spaces separating each one of the transducer elements. When an acoustic signal reaches the array, a portion of the signal strikes the transducer elements of the outer array and a portion of the signal

1 propagates through the outer array and strikes the transducer
2 elements of the inner array. The electrical response generated
3 by the transducer elements are conditioned by preamplifiers that
4 are coupled to a beamformer. The beamformer combines and post-
5 processes the conditioned signals from the elements of the outer
6 and inner arrays as signals from a single array of transducers.

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BRIEF DESCRIPTION OF THE DRAWINGS

9 A more complete understanding of the invention and many of
10 the attendant advantages thereto will be readily appreciated as
11 the same becomes better understood by reference to the following
12 detailed description when considered in conjunction with the
13 accompanying drawings wherein like reference numerals and symbols
14 designate identical or corresponding parts throughout the several
15 views and wherein:

16 FIG. 1 is a block diagram of a multi-layer tiled array;

17 FIG. 2 is a diagram showing an arrangement of transducer
18 elements for a section of a multi-layer tiled array; and

19 FIG. 3 shows a cross-sectional view of a multi-layer tiled
20 array.

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DESCRIPTION OF THE PREFERRED EMBODIMENT

23 Referring to the drawings, there is shown in FIG. 1 a block
24 diagram of a multi-layer tiled array 10 in accordance with the
25 present invention. Array 10 comprises an outer array 12a of
26 transducer elements 14a and an inner array 12b of transducer

1 elements 14b. Each of the elements 14a and 14b within arrays 12a
2 and 12b, respectively, are electrically independent; thus, each
3 of the elements 14a and 14b are shown separated from their
4 neighboring elements by a space 28. Each of the elements 14a and
5 14b convert acoustic signals incident upon the element to an
6 electrical response such as a change in voltage, a change in
7 current or the like. Transducer elements 14a and 14b can
8 comprise any conventional acoustically transparent material such
9 as polyvinylidene fluoride (PVDF) or the like. The electrical
10 signals generated by transducer elements 14a and 14b are
11 conditioned by preamplifiers 16 and coupled to beamformer 18.
12 Such coupling of preamplifiers 16 to an array 10 is well-known in
13 the art. Using well known techniques, beamformer 18 processes
14 the conditioned signals from preamplifiers 16 to generate an
15 output signal 20. Such techniques can include beamforming, match
16 filter, or the like.

17 Processing only the signals from elements 14a of array 12a,
18 results in a conventional array having an inter-element spacing
19 22 measured from element center to element center. However,
20 processing the signals from elements 14a and 14b as a single
21 array creates a multi-layer array having a reduced inter-element
22 spacing 22' measured between an element 14a of array 12a and a
23 neighboring element 14b of array 12b without reducing the size of
24 the elements.

25 In operation, a sound source 24 which in the present
26 instance may represent either an active source or an echo from a

1 source located elsewhere, generates acoustic signals 26 which
2 propagate through the water to array 10. When signal 26 reaches
3 array 10 it strikes the transducer elements 14a of outer array
4 12a and propagates through transducer elements 14a and spaces 28
5 to strike transducer elements 14b of inner array 12b. The
6 electrical response generated by transducer elements 14a and 14b
7 of outer array 12a and inner array 12b, respectively, are
8 conditioned by preamplifiers 16 that are coupled to beamformer
9 18. The signals generated by elements 14a and 14b of arrays 12a
10 and 12b are processed as a single array by beamformer 18 to
11 produce output signal 20.

12 Referring now to FIG. 2, there is shown a diagram
13 illustrating an arrangement of transducer elements for a section
14 of a multi-layer tiled array 10. A section of array 10
15 comprising outer array 12a represented by the solid lines and
16 inner array 12b represented by the dashed lines is shown. Outer
17 array 12a and inner array 12b each comprise a plurality of
18 electrically independent transducer elements 14a and 14b,
19 respectively, arranged in a two-dimensional matrix. The
20 elements 14a of outer array 12a are arranged such that each
21 element 14a is separated from its neighboring elements within
22 array 12a by a distance of " W_1 "; thereby forming a grid of
23 isolation spaces 28 of width " W_1 " separating each of the elements
24 14a. Similarly, elements 14b of inner array 12b are arranged so
25 as to form a grid of isolation spaces 30 of width " W_2 " separating
26 each of the elements 14b. Widths " W_1 " and " W_2 " are negligible in

1 size in comparison to the size of transducer elements 14a and
2 14b.

3 Transducer elements 14b of inner array 12b are positioned
4 such that the center of each transducer element 14b is aligned
5 with an isolation space 28 of outer array 12a. Preferably,
6 elements 14b are positioned such that the center of each element
7 14b in array 12b is substantially aligned with the intersection
8 of two of the isolation spaces 28 of outer array 12a thereby
9 maximizing the inter-element spacing 22'.

10 Preferably, both arrays 12a and 12b are oriented at angle α
11 of approximately 45 degrees relative to the horizontal to
12 maintain the conventional azimuthal and elevation angle
13 orientation for the inter-element spacing 22'. Because of the
14 square shape of the individual physical elements 14a and 14b and
15 the 45 degree orientation, Bartlett shading is imposed on the
16 individual elements. The elements may take other shapes such as
17 rectangular, circular, or the like; however, the square shape
18 will maximize the element capacitance. Optionally, conventional
19 shading techniques, such as Hamming or the like, can be employed
20 either electronically or computationally to the entire array in
21 addition to any shading technique imposed during fabrication
22 resulting from the shape of the elements.

23 Referring now to FIG. 3, there is shown a cross-sectional
24 view of array 10 according to one embodiment of the present
25 invention. In the embodiment shown in FIG. 3, transducer
26 elements 14a of outer array 12a comprise a layer of PVDF film 32

1 with a top surface 34 and an opposing bottom surface 36 with a
2 plurality of electrically independent electrodes 38 disposed on
3 each of the surfaces 34, 36. Each electrode 38 can be
4 individually placed on surfaces 34 and 36, or each electrode can
5 be formed by depositing a thin layer of conductive material such
6 as copper or the like on surfaces 34, 36 and etching away
7 material to form a grid of isolation spaces 28. Any conventional
8 technique such as spraying or the like can be used to deposit a
9 thin layer of conductive material. Alternatively, array 12a can
10 comprise a single electrode disposed on surface 34 or 36 with a
11 plurality of individual electrodes disposed on the opposing
12 surface. However, in a preferred embodiment array 12a comprises
13 individual electrodes 38 disposed on each surface to reduce
14 coupling between adjacent elements. Similarly inner array 12b
15 can comprise a layer of PVDF film 40 with a top surface 42 and an
16 opposing bottom surface 44 with a plurality of electrically
17 independent electrodes 46 disposed on each of surfaces 42,44.

18 A window 48 of acoustically transparent material such as
19 polyurethane or the like can be disposed to outer surface 50 of
20 outer array 12a. The window 48 allows acoustic signals 26 to
21 propagate to elements 14a, 14b and keeps water away from array
22 10. A non-conductive adhesive 52 can be used to join inner array
23 12a to outer array 12b and to help prevent any misalignment of
24 elements 14a, 14b. A substrate 54 can be disposed to the lower
25 surface 56 of inner array 12b to support arrays 12a and 12b.
26 Additionally, substrate 54 can be comprised of materials to

1 dampen the acoustic signals propagating through the array thereby
2 reducing any reflection of the signals towards array 10. Leads
3 58 receive the electrical signals generated by transducer
4 elements 14a and 14b. The signals are then conditioned by
5 preamplifiers 16. Widths "W1" and "W2" are made large enough to
6 allow for the connection of leads 58 to transducer elements 14a
7 and 14b and small enough to obtain the maximum physical size of
8 transducer elements 14a and 14b.

9 In operation, signal 26 propagates through the water until
10 it reaches array 10. The material used to form transducer
11 elements 14a and 14b is acoustically transparent thereby allowing
12 signal 26 to propagate through transducer elements 14a to
13 transducer elements 14b.

14 The multi-layer tiled array of the present invention
15 provides a novel arrangement for the individual elements that
16 increases the capacitance of the elements by increasing their
17 surface area while maintaining the inter-element spacing required
18 to avoid spatial aliasing in the received signals. The array
19 provides significant advantages over prior art arrays. First,
20 the array increases the maximum allowable distance between array
21 elements. Second, it increases the capacitance of the elements
22 which can be utilized to obtain a lower electrical noise floor
23 level thereby improving the sonar system's performance in
24 detecting low level target signals. Furthermore the arrangement
25 eases the fabrication of the array by allowing the preamplifiers
26 to be located a greater distance from the array.

1 What has thus been described is a sonar sensor array that
2 provides a multi-layer tiled arrangement for the individual array
3 elements. The arrangement increases the capacitance of the array
4 elements while maintaining the required inter-element spacing.

5 It will be understood that various changes in the details,
6 materials, steps and arrangement of parts, which have been herein
7 described and illustrated in order to explain the nature of the
8 invention, may be made by those skilled in the art within the
9 principle and scope of the invention.

10 For example: a different material might be used for the
11 construction of the array to attain better sensitivity or
12 capacitance. Other shading could be implemented readily during
13 fabrication of the inner and outer arrays or additional shading
14 could also be applied after the sensor either electronically or
15 computationally if further improved sidelobe performance of some
16 other shading design is required.

2
3 MULTI-LAYER TILED ARRAY

4 ABSTRACT OF THE DISCLOSURE

5 A sonar sensor array having a multi-layer tiling arrangement
6 for the individual elements that increases surface area available
7 for each element while maintaining the inter-element spacing
8 required to avoid spatial aliasing when the received signals are
9 combined to form a sonar beam. The array comprises outer and
10 inner arrays of transducer elements for converting an acoustic
11 signal to an electrical response. The transducer elements of the
12 outer array are positioned such than a grid of isolation spaces
13 separates each of the transducer elements. The transducer
14 elements of the inner array are also positioned such that a grid
15 of isolation spaces separates each one of the transducer
16 elements. The electrical response generated by the transducer
17 elements of the inner and outer arrays are coupled to a
18 beamformer which processes the responses to produce an output
19 signal.

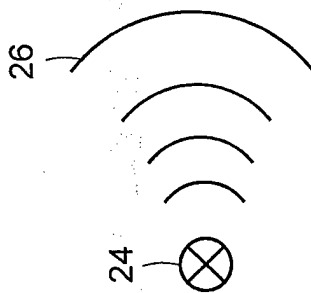
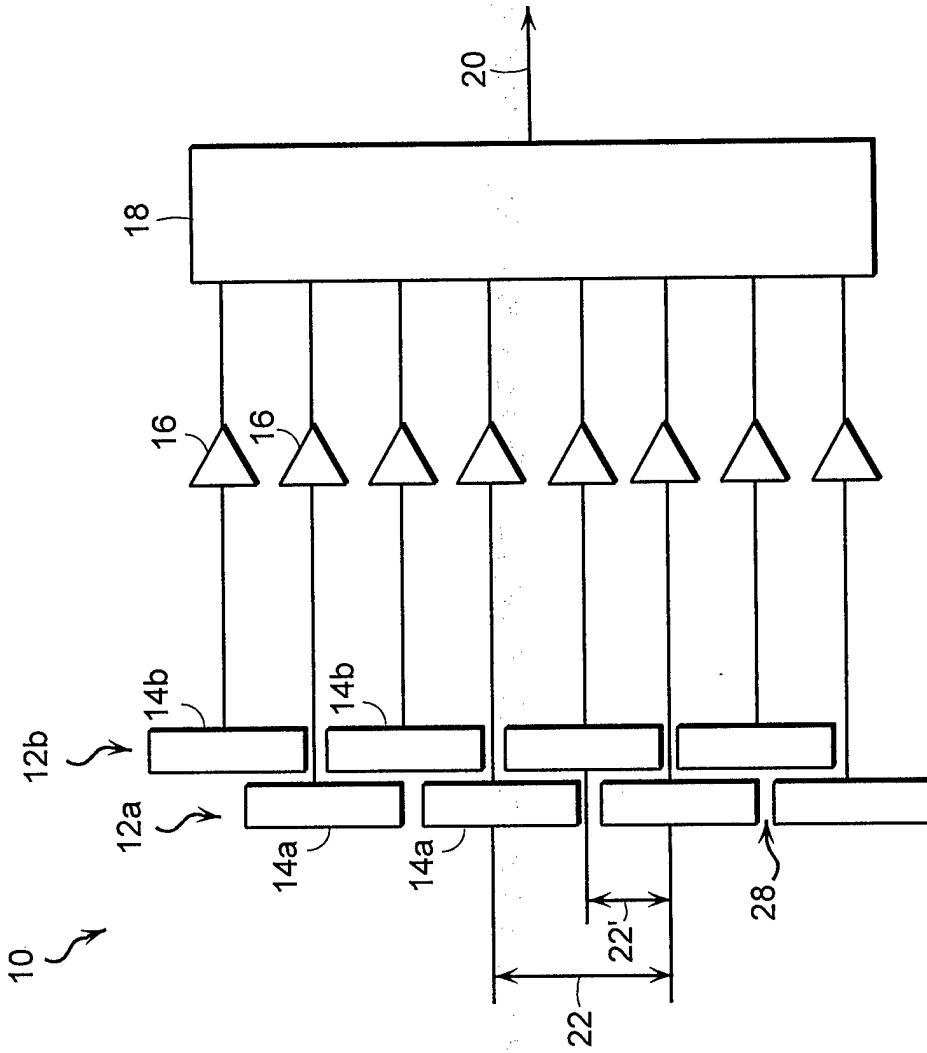


FIG. 1

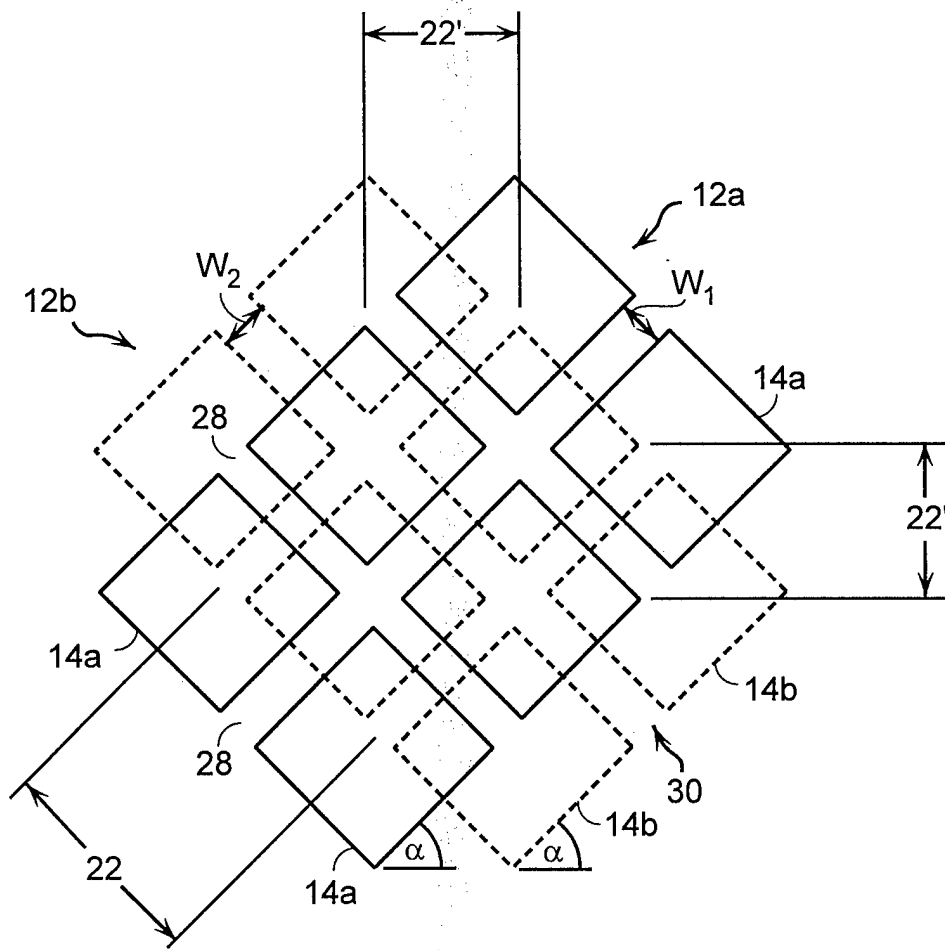


FIG. 2

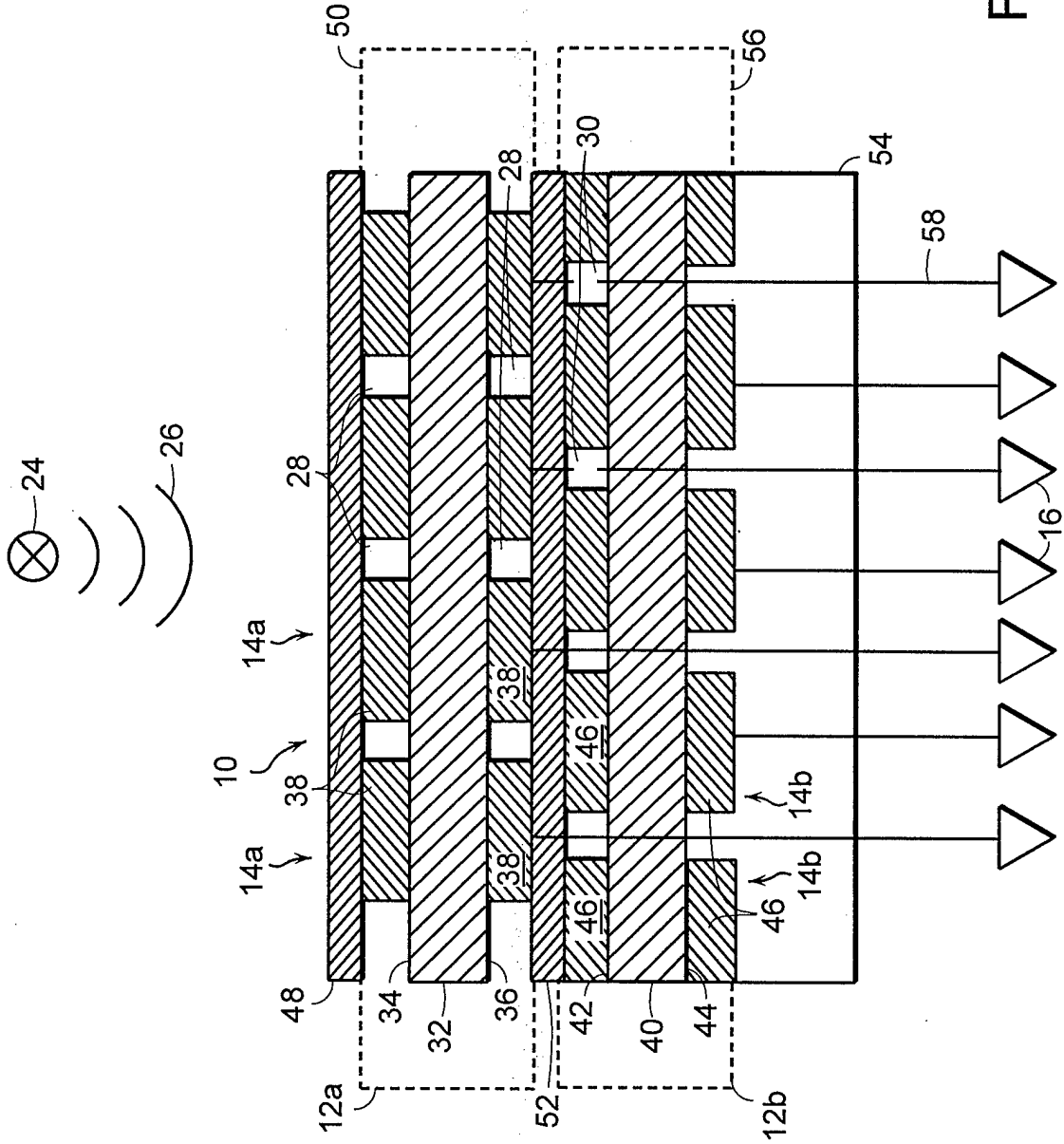


FIG. 3