



TECHNICAL DOCUMENT 3282
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Electrode Placement for Active Tuning of Silicon-on-Insulator (SOI) Ring Resonator Structures Clad in Nematic Liquid Crystals

Joanna Ptasinski

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Advanced Photonics
Technologies Branch

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EXECUTIVE SUMMARY

OBJECTIVE

This technical report analyzes the optimum electrode placement for electrically tunable ring resonators. Combining these resonators with nematic liquid crystals enables easily tunable devices where the tuning is performed through either electrical or optical means. Electrode placement determines the total amount of a resonance shift that can be achieved.

METHOD

Simulation results contained within this report were performed using COMSOL Multiphysics® for a 500-nm wide, 250-nm tall silicon waveguide surrounded by SiO₂ and liquid crystals. The 500-nm wide waveguide confines most of the optical mode to its core.

CONCLUSIONS AND RECOMMENDATIONS

The effect of electrode placement for electrically tunable ring resonators was analyzed. The symmetric ring resonator geometry plays a role in the total amount of achievable resonance shift. Factors governing the overall amount of resonance shift include the perimeter of the ring, the amount of the ring covered by SiO₂, and the waveguide propagation loss influenced by the location of the metal layer above the buried oxide (BOX).

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1. INTRODUCTION

Tunable photonic resonant structures allow for altering of their electromagnetic spectrum and find applications in optical switching, filtering, buffering, lasers, and biosensors. Photonic resonances are essentially optical modes that require strong photon confinement that can be achieved through high index contrast using either dielectric materials or metals (plasmonics). Examples of dielectric resonators include rings, racetracks, disks, waveguide grating resonators, and contra directional grating coupler ring resonators [1–3]. Combining dielectric resonators with nematic liquid crystals (LC) enables easily tunable devices where the tuning is performed through either electrical or optical means [4]. Analyzing the optimum electrode placement for electrically tunable ring resonators is of interest because the device geometry plays a role in the total amount of a resonance shift that can be achieved.

2. ELECTRODE PLACEMENT FOR ACTIVE TUNING OF LIQUID CRYSTALS

The symmetric ring resonator geometry becomes an important factor in the active tuning of liquid crystals, and to observe an appreciable resonance shift, the ring symmetry must be broken; otherwise, the effective index changes stemming from the contributions of the two LC orientations may cancel each other, resulting in a null shift of the resonance [5]. Figure 1 shows a schematic that illustrates this concept.

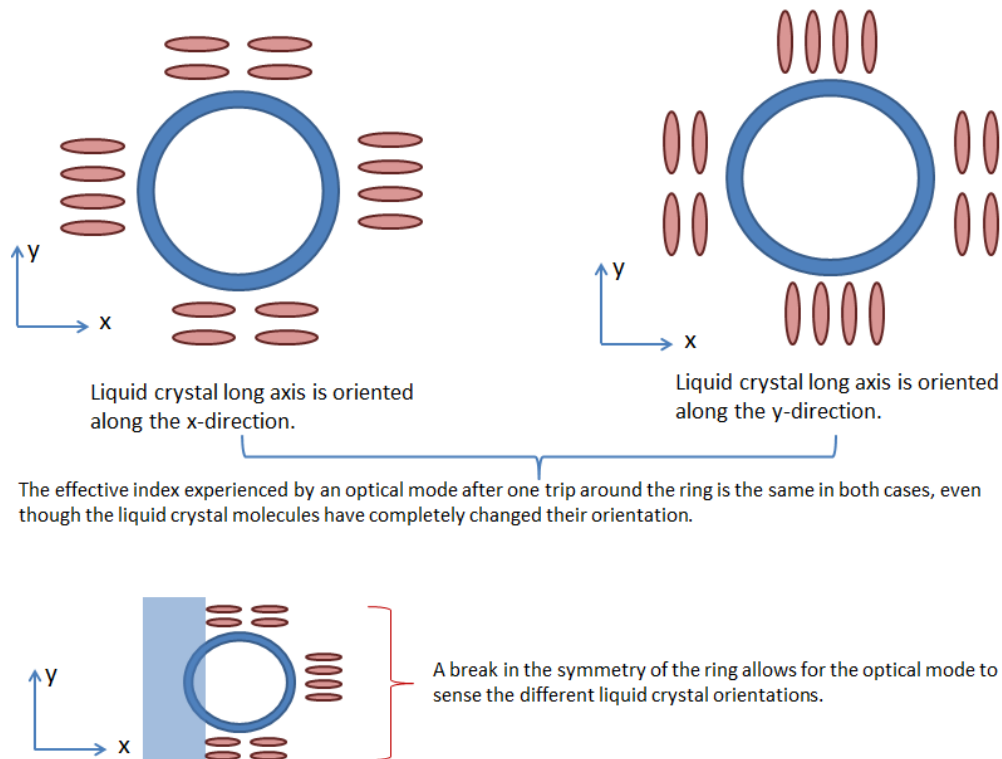


Figure 1. A schematic illustrates why the ring symmetry requires a break.

Additional factors contributing to the overall amount of resonance shift include the perimeter of the ring and specific placement of the electrodes. To simultaneously break the geometry of the ring and also embed electrodes with minimal obstacles to fabrication, a scheme where the electrodes are raised above the silicon structures is preferred. Figure 2 shows a conceptual schematic of the scheme. This configuration relies on tuning of the LC layer by fringe fields formed between the electrodes. The distance between the electrodes and the height of the electrodes above the buried oxide layer impact the strength of the field near the silicon structures. The maximum amount of the refractive index change due to LC tuning is affected by how much of the ring is covered by the SiO₂ layer affects the maximum amount of the refractive change caused by LC tuning, and the location of the metal layer above the buried oxide (BOX) influences the waveguide propagation loss.

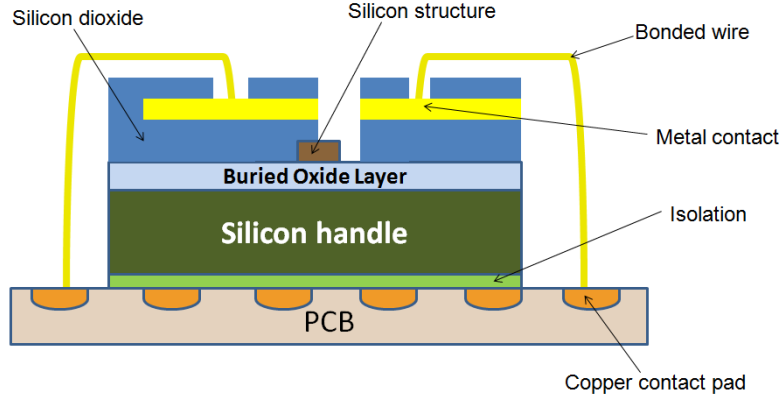


Figure 2. Conceptual schematic of electrode placement for active electrical tuning of liquid crystals.

Figure 3 (A) shows a schematic of a metal electrode layer placed above a silicon waveguide. Figure 3 (B) depicts the result of a COMSOL[®] simulation of the transverse magnetic (TM) mode for a 500-nm wide, 250-nm tall silicon waveguide surrounded by SiO₂, and with a gold electrode placed 100 nm above the silicon waveguide. A large portion of the optical mode is confined to the silicon waveguide, but a fraction of the mode is also confined to the SiO₂ region between the silicon waveguide and the metal electrode. A cavity forms between the two high index regions of Si and Au.

In Table 1, the transverse electric (TE) and TM mode effective indices of a 500-nm wide, 250-nm tall silicon waveguide are shown as a function of the location of the metal layer. Losses are minimized as the metal layer is placed further away from the silicon structure and, as expected, the TM mode loss is greater than that of the TE mode, due to the TM mode electric field vector pointing in the direction of the electrode.

The signal loss over a distance, z , is calculated using the Beer–Lambert law:

$$I(z) = I_0 \exp(-\alpha z), \quad (1)$$

where α is the attenuation coefficient expressed as

$$\alpha = 2k_0 k, \quad (2)$$

and $k_0 = 2\pi / \lambda_0$, where λ_0 is the wavelength. The absorption index, k , can be obtained from the complex refractive index, $N = n + ik$.

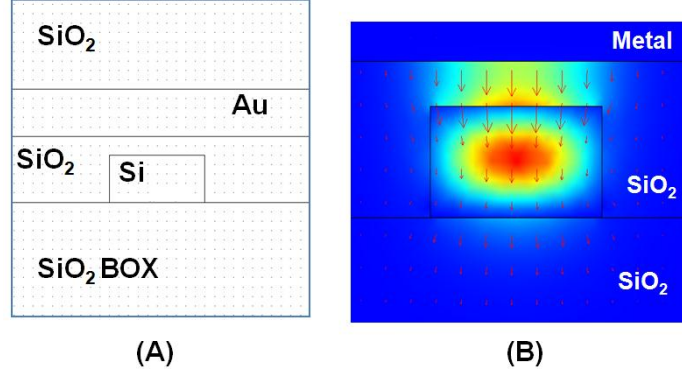


Figure 3. (A) Conceptual diagram of the placement of the metal layer above the silicon waveguide. (B) COMSOL[®] simulation of the TM mode for a 500-nm wide, 250-nm tall silicon waveguide surrounded by SiO₂ and with a metal electrode placed 100 nm above the silicon waveguide.

Table 1. Losses for a 500-nm wide, 250-nm tall silicon waveguide surrounded by SiO₂ and with a metal electrode placed a distance above the waveguide.

Effective Indices		
No metal	TE $n_{eff} = 2.5720$	TM $n_{eff} = 2.064 - 2.509e-10i$
Metal 400nm above BOX	TE $n_{eff} = 2.51-8.31e-4i$	TM $n_{eff} = 2.13 - 1.756e-3i$
Metal 450nm above BOX	TE $n_{eff} = 2.541-1.236e-4i$	TM $n_{eff} = 2.026-5.362e-4i$
Metal 500nm above BOX	TE $n_{eff} = 2.545-4.875e-5i$	TM $n_{eff} = 2.008-3.04e-4i$
Metal 550nm above BOX	TE $n_{eff} = 2.546-1.95e-5i$	TM $n_{eff} = 2.0-1.72e-4i$
Metal 600nm above BOX	TE $n_{eff} = 2.547-7.84e-6i$	TM $n_{eff} = 1.99-9.69e-5i$
Metal 650nm above BOX	TE $n_{eff} = 2.547-3.1e-6i$	TM $n_{eff} = 1.99-5.385e-5i$
Metal 700nm above BOX	TE $n_{eff} = 2.547-1.35e-6i$	TM $n_{eff} = 1.99-3.11e-5i$

The tradeoff between electrode placement and the field required for LC tuning is investigated below using the LIXON[™] ZSM-5970 LC mixture. At 1550 nm, the LIXON[™] indices are $n_o = 1.473$ and $n_e = 1.573$, and its threshold voltage is $V_t = 1.61V$, and saturation voltage $V_{sat} = 2.51 V$. LIXON's clearing point temperature is 123 °C.

Figure 4 depicts a model where the electrodes are placed 0.7 μm above the BOX layer and the SiO₂ layer covers a fifth of the ring. The bus waveguides are spaced 100 nm from the ring. In this model, the potential is varied between 30 to 53 V. Figure 5 shows the strength of the electric field (x-component) for an applied potential of 30 V. The figure shows that field is the strongest near the electrodes.

At 30-V potential, the fringe field allows for LC realignment near the edge of the electrodes. The E-field for most of sample, at a height of 125 nm above the BOX, is 1.43 V/ μm (below the LC threshold voltage) as shown in Figure 6. The strength of the E-field at any point in the region of $x = -6.5 \mu m$ to $x = -4 \mu m$ and $x = 7.5 \mu m$ to $x = 10 \mu m$ can be approximated as a fifth degree polynomial:

$$E_x = -0.12459 \cdot x^5 - 2.8822 \cdot x^4 - 26.573 \cdot x^3 - 121.84 \cdot x^2 - 277.59 \cdot x - 249.79. \quad (3)$$

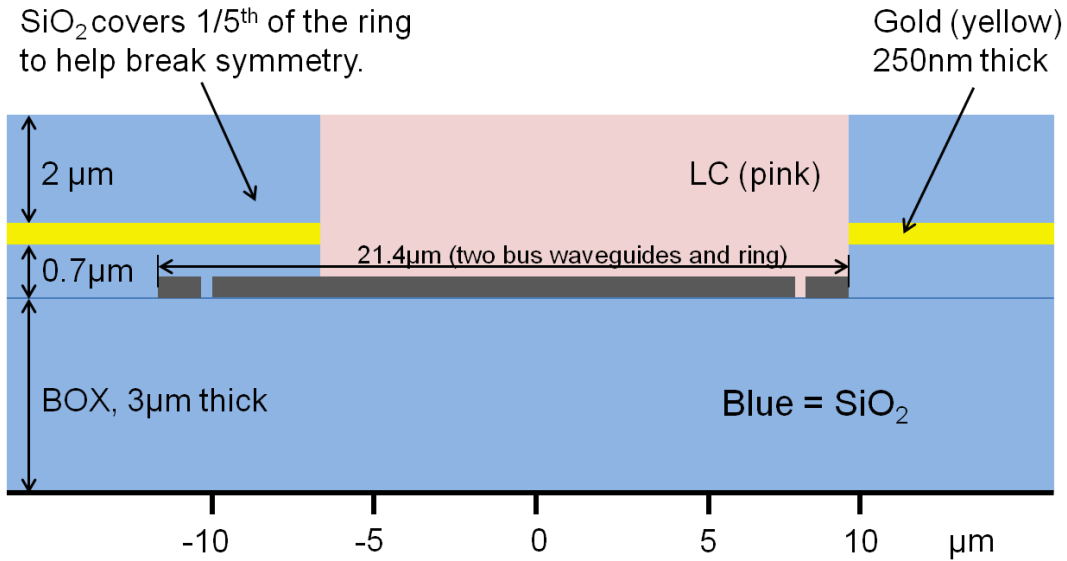


Figure 4. The electrodes are placed 0.7 μm above the BOX layer and a fifth of the ring is covered by SiO₂. The Si ring and the bus waveguides are both 250 nm tall (depicted in gray).

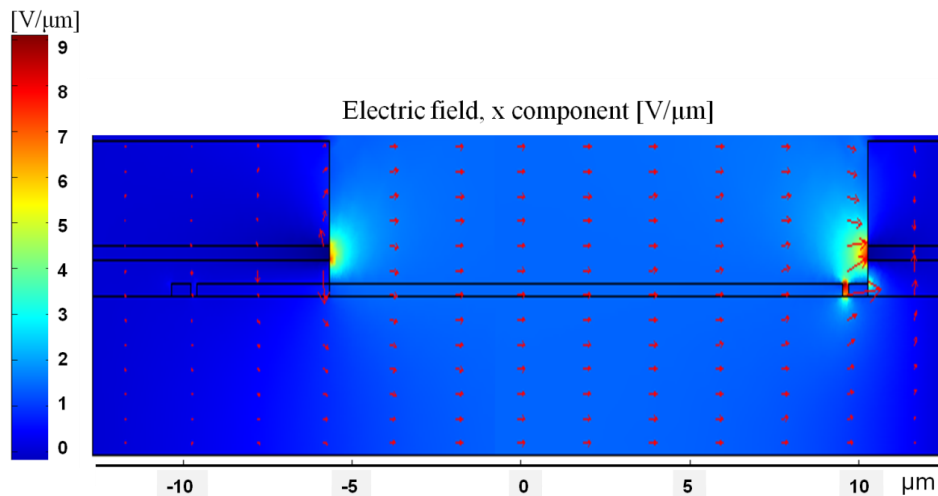


Figure 5. Simulation showing the electric field x-component when a potential is applied to the electrodes.

At a potential of 38 V, the E-field for most of sample is 1.81 V/μm. This is above the threshold voltage of LIXON™, $V_t = 1.61$ V. The electric field x-component for a 38-V applied potential is shown in Figure 7. Figure 8 shows the electric field distribution in the case when a 53-V potential is applied. The field at any point on the sample is equal to or greater than 2.52 V/μm, which is 0.01 V/μm higher than the saturation voltage of LIXON™.

How do the applied fields correspond to the extent of the ring resonator tuning range? Figure 5 shows a simulation result for an applied potential of 0 to 53 V and the resulting electric field x-component, as well as the change in the resonant peak caused by LC reorientation. The maximum ring resonator tuning range is 9 nm. With an applied potential of 30 and 34 V, the fringe field for most of the sample is 1.43 and 1.62 V/μm, respectively. The voltage of 1.62 V/μm is slightly above the threshold voltage of LIXON™. Nevertheless, resonator tuning takes place at these low electric field values. The large fringe fields present near the electrodes are responsible for this effect.

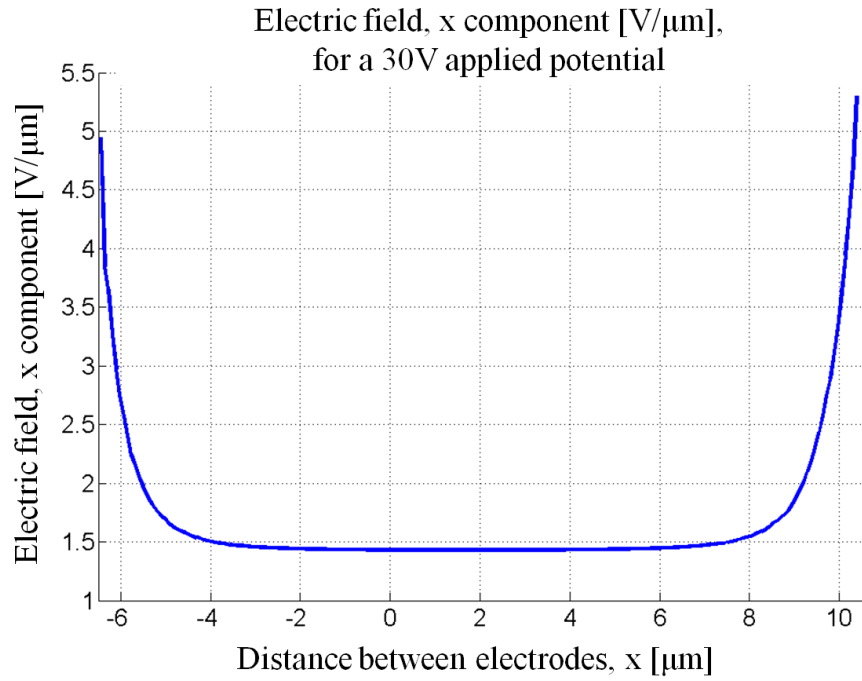


Figure 6. Electric field x-component for a 30-V applied potential and a fifth of the ring covered by SiO_2 .

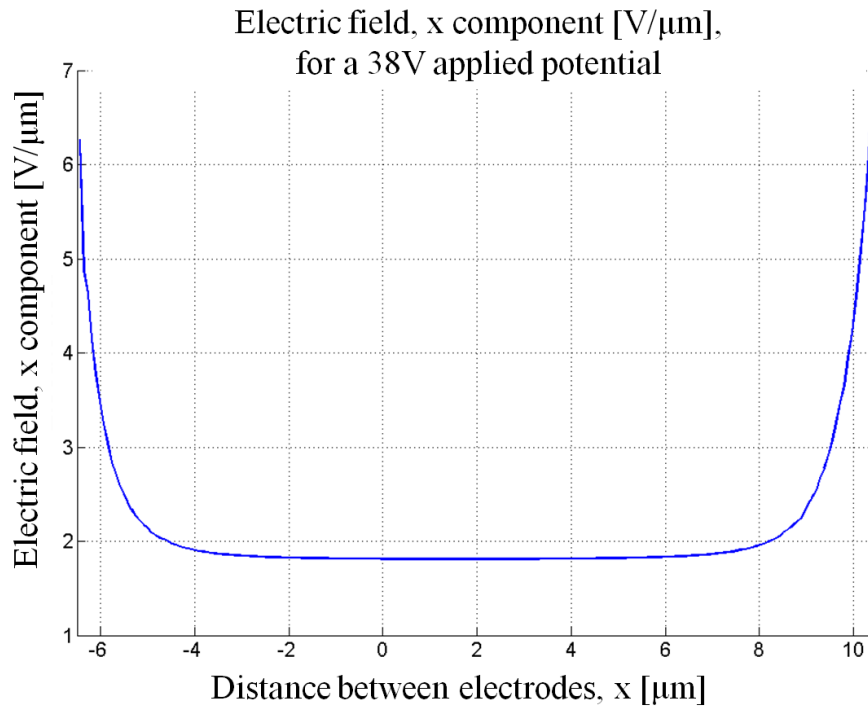


Figure 7. Electric field x-component for a 38-V applied potential and a fifth of the ring covered by SiO_2 .

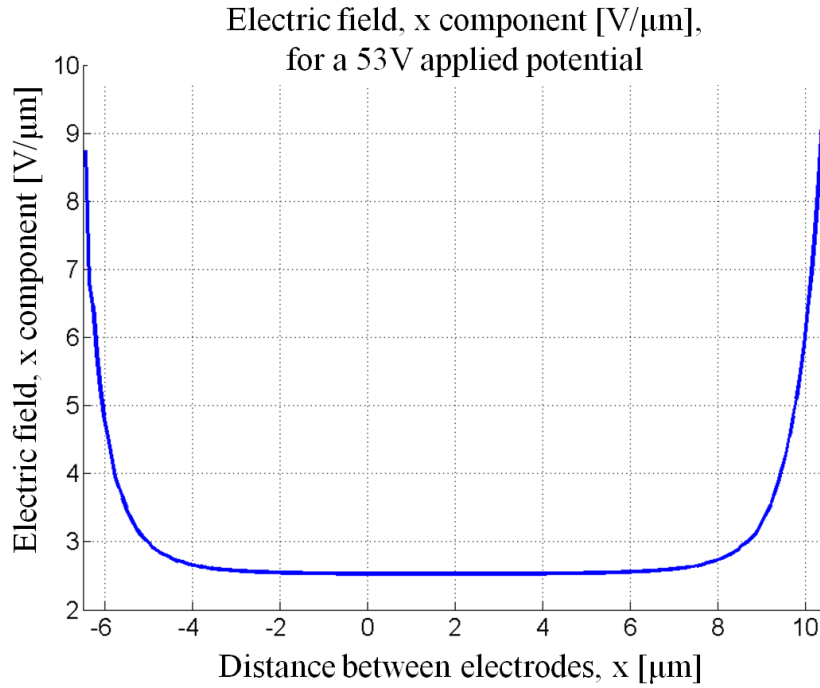


Figure 8. Electric field x-component for a 45-V applied potential and a fifth of the ring covered by SiO₂.

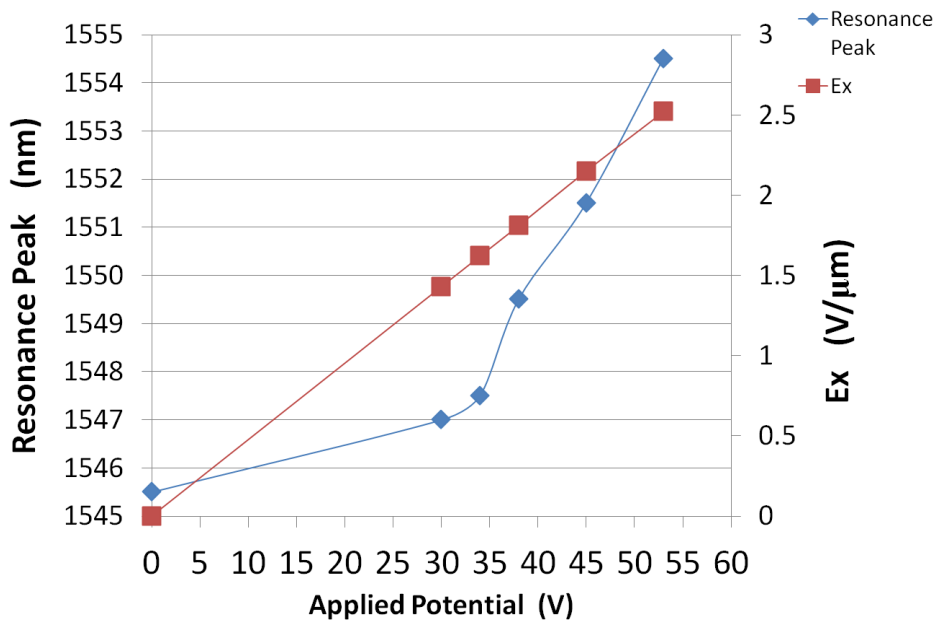


Figure 9. Electric field x-component and resonant peak tuning as a function of applied potential when a fifth of the ring is covered by SiO₂.

If the electrodes are brought closer together by extending the SiO₂ layer halfway through the ring, the potential required to tune the liquid crystals is smaller, but the LIXON™ tuning range is also reduced. To aid in expanding the LIXON™ tuning range without sustaining large propagation losses, the electrodes are brought 50 nm closer to the edge of the BOX layer. Figure 10 shows the geometry

of the model. Figure 11 shows the electric field x-component for a 25-V applied potential. The field for most of the sample is $1.68 \text{ V}/\mu\text{m}$, which is above the threshold voltage of LIXON™. Saturation voltage, $V_{\text{sat}} = 2.51 \text{ V}$, is achieved with an applied field of 38 V, as shown in Figure 12. The potential fields required to achieve tuning are of considerably smaller value in the case when the electrodes are brought closer together. The maximum tuning range achieved with this configuration is 6 nm (Figure 13). In practice, the optimum electrode placement must be chosen dependent on the desired application and power budget.

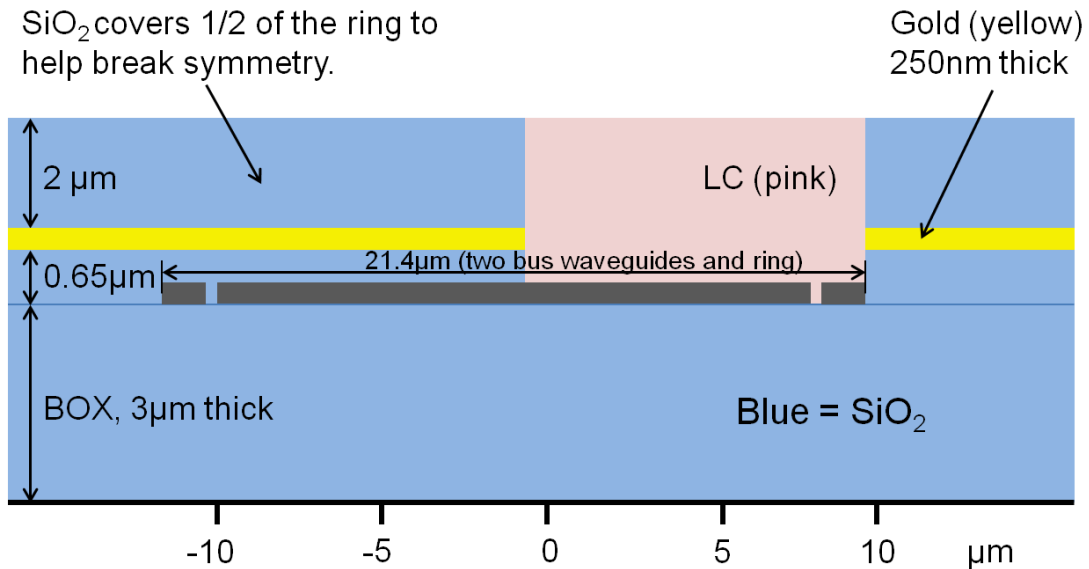


Figure 10. The electrodes are placed $0.65 \mu\text{m}$ above the BOX layer and one-half of the ring is covered by SiO₂. The Si ring and the bus waveguides are both 250-nm tall (depicted in gray).

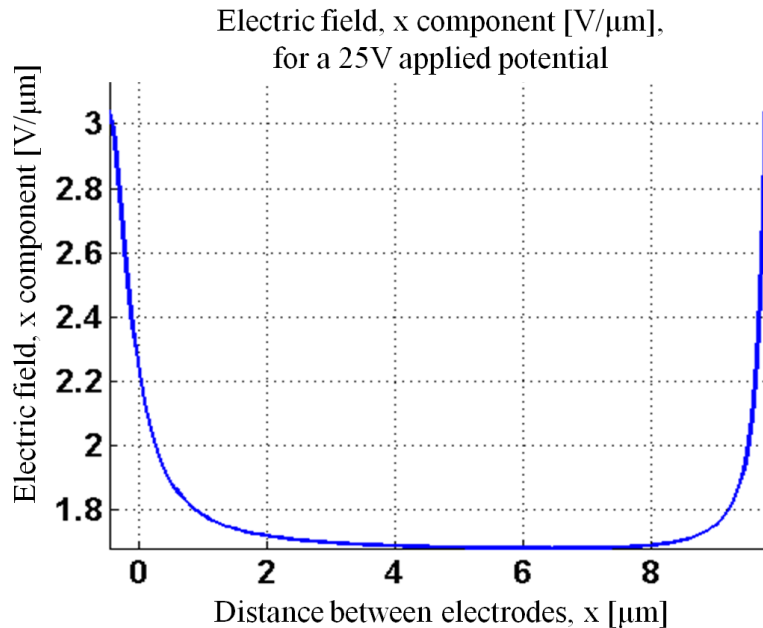


Figure 11. Electric field x-component for a 25-V applied potential and one-half of the ring covered by SiO₂.

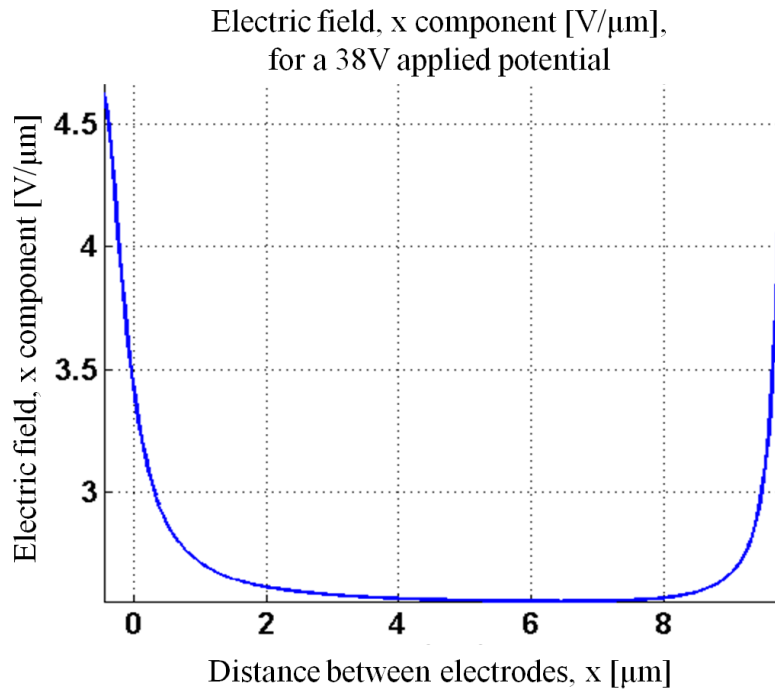


Figure 12. Electric field x-component for a 38-V applied potential and one-half of the ring covered by SiO_2 .

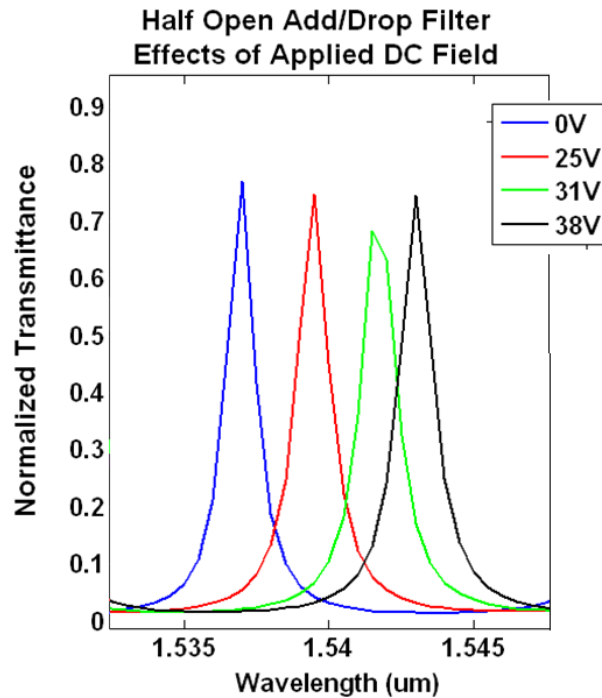


Figure 13. Resonant peak tuning as a function of applied potential when one-half of the ring is covered by SiO_2 .

3. CONCLUSIONS

In conclusion, the effect of electrode placement for electrically tunable ring resonators was analyzed. The symmetric ring resonator geometry plays a role in the total amount of achievable resonance shift. Factors governing the overall amount of resonance shift include the perimeter of the ring, the amount of the ring covered by SiO₂, and the waveguide propagation loss influenced by the location of the metal layer above the BOX.

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