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<b>14. ABSTRACT</b> We developed a SPICE-compatible compact model of TiO2-TiO2-x memristors based on classic ion transportation theory. Our model is shown to simulate important dynamic memristive properties, e.g., the doping front motion and realtime memristance switching, which are critical in memristor-based analog circuit designs. The model, as well as its analytical approximation, is validated with the experimentally obtained data from real devices. Minor deviations of our model from the measured data are also analyzed and discussed.					
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## A compact modeling of TiO<sub>2</sub>-TiO<sub>2-x</sub> memristor

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We developed a SPICE-compatible compact model of TiO<sub>2</sub>-TiO<sub>2-x</sub> memristors based on classic ion transportation theory. Our model is shown to simulate important dynamic memristive properties like real-time memristance switching, which are critical in memristor-based analog circuit designs. The model, as well as its analytical approximation, is validated with the experimentally obtained data from real devices. Minor deviations of our model from the measured data are also analyzed and discussed. © 2013 AIP Publishing LLC [<http://dx.doi.org/10.1063/1.4802206>]

The operational characteristics of memristive devices are typically represented by an I-V curve: the memristance of the device changes with the magnitude and pulse duration of the externally applied excitations.<sup>1</sup> As one of the promising technologies, TiO<sub>2</sub>-TiO<sub>2-x</sub> memristor has recently received significant attention and been widely studied in the solid state device society.<sup>2-10</sup> Some physical models of memristive devices, such as HfO<sub>x</sub> and NiO<sub>x</sub>, proposed a general description on the switching on/off procedure based on the ion/vacancy motion driven by the electric field (or potential gradient), which is known as the filament formation or dissolution.<sup>11-18</sup> The behavior of the TiO<sub>2</sub>-TiO<sub>2-x</sub> device can also be understood by the similar ionic influence:<sup>3,4,19-23</sup> the filament corresponds to the high conductive region while the remaining regions can be treated as low conductive or insulating. Some other models present the understandings on internal state changes under physical factors like electric field and temperature.<sup>11,12,24</sup> Some previous work has proposed to model the memristive switching behavior in oxide devices based on the dynamical interactions between electrons, oxygen vacancies, and oxygen anions.<sup>25</sup> However, due to the high computation cost of complex physical quantities, i.e., transition Hamilton matrix and energy density of states, the model cannot be directly applicable to fast circuit simulation. By combining Monte-Carlo method, electron hopping conduction process can be simulated through the similar technique.<sup>26</sup> Kinetic Monte-Carlo method is also implemented to model the filament formation.<sup>27</sup> However, these models do not explicitly give the static and dynamic electrical switching properties of the memristor device. In some TiO<sub>x</sub> memristor modeling works, the motion of vacancies is simulated through classical molecular dynamics.<sup>28,29</sup> The dynamics of tunnel barrier width can be also obtained by modeling the device as a resistor plus Simmons tunnel barrier.<sup>30</sup> In this work, we model the behavior of memristor device from classical macroscopic viewpoint, which is represented by the electron density distribution inside the device. A compact model of TiO<sub>2</sub>-TiO<sub>2-x</sub> memristor based on classic ion transportation theory is then proposed, targeting the application in

SPICE circuit simulations. Compared to the aforementioned TiO<sub>x</sub> memristor models, our model offers a concrete calculation of the filament dynamics and derives accurate time-varying behaviors of device resistance changing. Our model, as well as its analytical approximation, well matches the measurements of a real TiO<sub>2</sub>-TiO<sub>2-x</sub> device on static I-V curve and dynamic pulse programming.

We divide a TiO<sub>2</sub>-TiO<sub>2-x</sub> memristor into three regions, namely, the conductive region, the transition region, and the insulating region, as shown in Fig. 1. Here, the conductive region corresponds to the region of filaments while the transition region connects the filaments and the remaining insulating region. The formation or dissolution of the filament is simulated as the ion surface motion within the transition region. We use  $w$ ,  $\lambda$ , and  $D$  to denote the lengths of the conductive region, the transition region, and the entire device, respectively. The real-time current density at position  $x$  in the memristor at time  $t$  can be expressed as<sup>31</sup>

$$J_s(x, t) = n_s(x, t)q\mu E_s(x, t) - qDq \frac{dn_s(x, t)}{dx}. \quad (1)$$

Here, the subscript  $s = (c, t, i)$  denotes the parameter of conductive region, transition region, and insulating region,

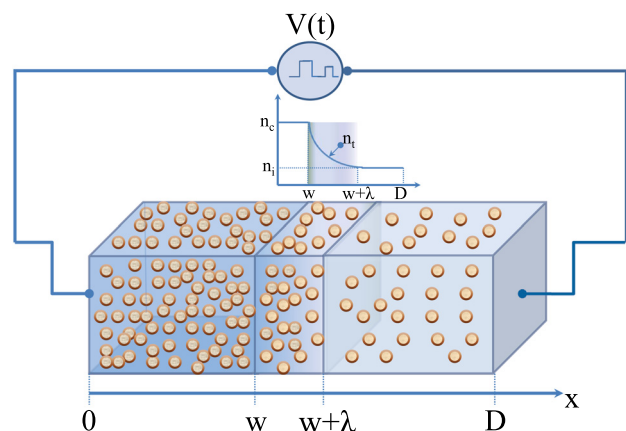


FIG. 1. Region partitioning of TiO<sub>2</sub>-TiO<sub>2-x</sub> memristor.

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respectively.  $E_s$  and  $n_s$  represent the electric field and the electron density, respectively.  $\mu$  is the mobility coefficient.  $q$  is the elementary charge.  $D_q$  is the diffusion constant for electrons. Equation (1) shows that the current is generated mainly from the electron drifting in the electric field and the electron density gradient.  $D_q$  is typically small for electron diffusion current, and the second term in Eq. (1) is ignored in our model. If we ignore the variation of the cross section area and assume the current density is uniform in the memristor (i.e.,  $J_c = J_t = J_i$ ), the relationships between the electric fields and electron densities of the three regions can be summarized as

$$n_c q \mu E_c(t) = n_t(x, t) q \mu E_t(x, t) = n_i(t) q \mu E_i(t). \quad (2)$$

Here, we assume that the  $E_s$  and  $n_s$  are uniform in conductive and insulating regions and are determined only by the real-time applied voltage (except that  $n_c$  is a constant). The equation for the hopping rate of ions in a infinitely large lattice can be expressed as<sup>32</sup>

$$R = f \sinh(QEa/2kT) e^{-U/kT}, \quad (3)$$

where  $R$  is the ion hopping rate,  $f$  is the escape attempt frequency,  $a$  is the lattice periodicity,  $E$  is the electric field,  $Q$  is the charge of ion,  $U$  is the activation energy,  $k$  is the Boltzmann constant, and  $T$  is the local temperature. By considering the fact that the number of generated electrons is proportional to that of the hopping ions inside the thin film, we use the product of a coefficient  $\gamma_i$  and a factor  $(n_c - n_i(t))$  to approximate the electron density generation function under the influence of electric field in the insulating region.  $(n_c - n_i(t))$  also implies that the maximum electron density should not exceed  $n_c$ . Hence, the evolution of the electron density in insulating region under a time-varying electric field  $E_i$  can be calculated by

$$\frac{dn_i(t)}{dt} = \gamma_i (n_c - n_i(t)) E_i(t), \quad (4)$$

where  $\gamma_i$  is the electron density generating coefficient in the insulating region. At a given time  $t$  and position  $x$ , the electric field  $E_t$  can be viewed as a linear function bounded by  $E_c$  and  $E_i$  as

$$E_t(x, t) = \frac{(E_i(t) - E_c(t))}{\lambda} (x - w) + E_c(t). \quad (5)$$

We note that the voltage  $V(t)$  applied on the two ends of the memristor equals the integral of the electric field along the device, or

$$V(t) = E_c(t)w + \int_w^{w+\lambda} E_t(x, t) dx + E_i(t)(D - \lambda - w). \quad (6)$$

Combining Eqs. (5) and (6), we have

$$E_c(t) = \frac{V(t)}{w + \frac{1}{2}\lambda + \frac{n_c}{n_i(t)} \left( D - w - \frac{1}{2}\lambda \right)}. \quad (7)$$

Finally, the transition region length  $\lambda$  reduces when the applied voltage  $V(t)$  increases, which is approximated by

$$\lambda = \lambda_0 e^{-|V(t)|}, \quad (8)$$

in our model.  $\lambda_0$  is the transition region length at  $V(t) = 0$ .

The probability of ion hopping changes with the external electric field.<sup>32</sup> However, the probability is significantly different in the conductive and insulating regions, which leading to the conductivity difference in these two regions. Ideally, every successful ion hopping produces two free electrons and can be viewed as the electron density increase or redistribution. For simplicity, we assume the conductivity in the conductive region (filament) is constant. The motion of ions causes the change of the electron densities in transition and insulating regions, resulting in the formation, growth, and dissolution of the filaments. Since the TiO<sub>2</sub>-TiO<sub>2-x</sub> device is stable when no voltage bias is applied, we can model the ion motion by considering only the external electric field.<sup>32</sup> The internal temperature of the device, which leads to the fluctuations of cycle-to-cycle switching, is not included in our model. Without loss of generality, we assume the endpoint of the filament starts moving from the left end of the device ( $x = 0$ ) at time 0.  $n_t(x, t)$  is the electron density at the position  $x$  in the transition region at time  $t$ . Compared to the electron density  $n_c$  at the boundary between the conductive region and the transition region, the change of the electron density at  $x$  is  $n_c - n_t(x, t)$ , which is mainly due to the oxygen ion vacancy redistribution.<sup>33</sup>

When external voltage is applied, the electron densities are changing with time in both transition and insulating regions (filament growth). The electron density increment in an infinitesimal time interval  $dt$  equals the difference between the electron densities at positions  $x - dx$  and  $x$ , or

$$\begin{aligned} n_t(x - dx, t) - n_t(x, t) &= \frac{dn_t(x, t)}{dx} (-dx) \\ &= -\frac{dn_t(x, t)}{dx} v(x, t) dt \\ &= \gamma_t (n_c - n_t(x, t)) E_t(x, t) dt. \end{aligned} \quad (9)$$

Here,  $\gamma_t$  is the electron generating coefficient in the transition region. Note that the maximum reachable electron density in the memristor device is  $n_c$ , or the region is fully conductive.

Based on Eq. (9), the growth velocity at position  $x$  can be calculated by

$$v(x, t) = -\frac{\gamma_t (n_c - n_t(x, t)) E_t(x, t)}{\frac{dn_t(x, t)}{dx}}, \quad (10)$$

where  $\frac{dn_t(x, t)}{dx}$  can be derived from Eqs. (2) and (5).

The motion of the transition region can be described by the *average* growth velocity, which is defined as

$$\bar{v}(t) = \frac{\int_w^{w+\lambda} v(x, t) dx}{\lambda}. \quad (11)$$

This result shows that the filament growth is triggered by mainly the external electric field and can be viewed as the combined imaginary surfaces within the transition region. In the following sections, we simplify the expressions of  $n_c$ ,  $n_t(x, t)$ , and  $n_i(t)$  as  $n_c$ ,  $n_t$ , and  $n_i$ , which are still the functions of  $x$  and/or  $t$ .

Substituting Eqs. (2) and (5) into Eq. (10), we have

$$v(x, t) = \left( \frac{n_c}{n_t} - 1 \right) n_c E_c(t) \cdot \frac{\gamma_t}{n_t^2} \frac{\lambda n_c n_i}{n_c - n_i}. \quad (12)$$

Substituting Eqs. (2), (5), (7), and (12) into Eq. (11), the filament growth velocity  $\frac{dw(t)}{dt}$  can be approximated by

$$\begin{aligned} \frac{dw(t)}{dt} &\approx \bar{v}(t) \\ &= \gamma_t \lambda \beta \cdot \frac{1}{\frac{n_c}{n_i} - 1} \cdot \frac{V(t)}{w \left( 1 - \frac{n_c}{n_i} \right) + \frac{1}{2} \lambda + \frac{n_c}{n_i} \left( D - \frac{1}{2} \lambda \right)}. \end{aligned} \quad (13)$$

Here,  $\beta$  equals  $\frac{1}{4} \left( \frac{n_c}{n_i} - 1 \right)^3 + \frac{2}{3} \left( \frac{n_c}{n_i} - 1 \right)^2 + \frac{1}{2} \left( \frac{n_c}{n_i} - 1 \right)$ .

The resistance/membrance of the memristor can be calculated by

$$R(t) = \frac{V(t)}{J_c A} = \frac{V(t)}{n_c q \mu E_c(t) A}. \quad (14)$$

Here,  $A$  is the cross section area of the memristor filament. Equations (13) and (14) describe the dynamic changes of memristor device structure and electrical property, respectively.

Assuming  $n_c$  and  $n_i$  are constant during the switching of the memristor (for example, the applied voltage magnitude is low or the programming pulse width is short), we can obtain the general form of  $w$  under a constant applied voltage  $V$  by integrating both sides of Eq. (13) as

$$w(t) = \frac{1}{1 - \frac{n_c}{n_i}} \left\{ \sqrt{\left[ \left( 1 - \frac{n_c}{n_i} \right) w(t_0) + C_0 \right]^2 - 2\gamma_t \beta \lambda V \Delta t} - C_0 \right\}. \quad (15)$$

Here,  $\Delta t = t - t_0$  and  $C_0 = \frac{1}{2} \lambda + \frac{n_c}{n_i} \left( D - \frac{1}{2} \lambda \right)$ . Equation (15) provides an analytical approximation on the growth of the filaments in the memristor. The internal state variable  $w$  is the expression of electron densities and the previous value of  $w_0$ . This result bridges two state variables at two different times under one fixed voltage.  $w$  represents the filament length, or the state of the device and is able to calculate the state variable value or the total resistance. As we shall see later, this analytical result is close to the experimental result of the total device resistance. Based on that, we can quickly estimate the memristor switching time in the following two scenarios:

If the device is switched on, or the filament is formed from the left-end of the memristor to the right-end, at  $t_0 = 0$  (or  $w(t_0) = 0$ ), Eq. (15) can be simplified as

$$w(t) = \frac{1}{1 - \frac{n_c}{n_i}} \left\{ \sqrt{C_0^2 - 2\gamma_t \beta \lambda V t} - C_0 \right\}. \quad (16)$$

The memristor switching time can be estimated by assuming  $C_0^2 - 2\gamma_t \beta \lambda V t = 0$ , or

$$t_{on} = \frac{C_0^2}{2\gamma_t \beta \lambda V}. \quad (17)$$

The corresponding portion of transition region at  $t_0$  is  $w(t_{on}) \approx D - \frac{1}{2} \lambda$  since  $n_i \ll n_c$ . Considering normally  $\lambda \ll D$ , it indicates that the filament is formed completely at the right end of the memristor at  $t = t_{on}$ , or the device is totally switched on.

Similarly, if the filament is in the dissolution process starts from the right-end of the memristor at  $t_0 = 0$  (or  $w(0) = D$ ), Eq. (15) can be simplified as

$$w(t) = \frac{1}{1 - \frac{n_c}{n_i}} \left\{ \sqrt{\left[ \left( 1 - \frac{n_c}{n_i} \right) D + C_0 \right]^2 - 2\gamma_t \beta \lambda V t} - C_0 \right\}. \quad (18)$$

The time required to switch off the memristor ( $t_{off}$ ) can be derived by assuming  $w(t_{off}) = 0$ , or

$$t_{off} = \frac{\left[ \left( 1 - \frac{n_c}{n_i} \right) D + 2C_0 \right] \left( 1 - \frac{n_c}{n_i} \right) D}{2\gamma_t \beta \lambda V}. \quad (19)$$

Table I summarized the three types of the device parameters used in our model, including the geometric parameters, the electrical parameters, and the structural parameters. The electron generating coefficients  $\gamma_t$  and  $\gamma_i$  are derived from the measured data and assumed constant for the different working ranges, as shown in Fig. 2. We compared our model with the experimentally obtained characterized static I-V curve and dynamic pulse programming curve of a TiO<sub>2-x</sub> memristor device.

Fig. 2 shows the measured I-V curve from a real memristor device as well as the experimental data from the numerical simulation and analytical approximation. During the measurement, a sequence of voltage pulses is applied on the memristor device. The magnitude of the voltage pulse grows exponentially, and varies from positive to negative following *sine* function. The numerical simulation well fit the measured data in all four working ranges. The analytical approximation shows slight discrepancy from the measured data when the resistance is high. It is because the simulated

TABLE I. Modeling parameters.

	Parameters	Value	Parameters	Value
Geometric	$D$	35 nm	$A$	$0.25 \mu\text{m}^2$
Electrical	$e$	$1.602 \times 10^{-19} \text{C}$	$n_c$	$8.75 \times 10^{22} \text{m}^{-3}$
Structural	$w_0$	$0.15 D$	$\lambda_0$	$0.05 D$
Working range		Derived parameters		
1 $\rightarrow$ 2	$\gamma_t$	$2.3 \times 10^{-6}$	$\gamma_i$	$1 \times 10^{-6}$
2 $\rightarrow$ 1		$8 \times 10^{-6}$		$1 \times 10^{-8}$
1 $\rightarrow$ 3		$1 \times 10^{-10}$		$1 \times 10^{-9}$
3 $\rightarrow$ 1		$7 \times 10^{-7}$		$2 \times 10^{-7}$

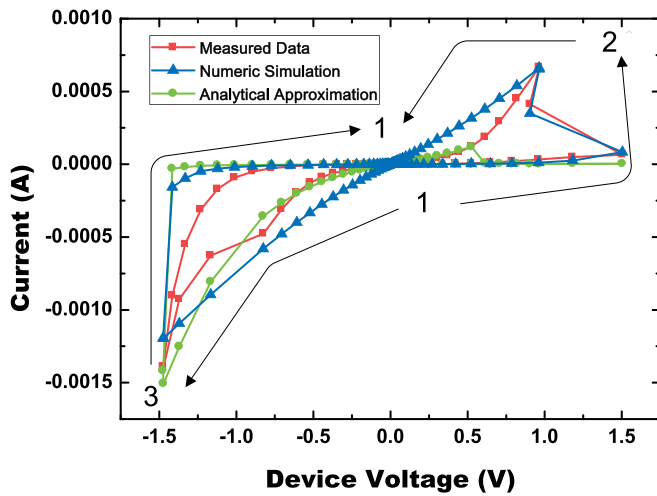


FIG. 2. Model validation with static I-V curve, including numerical simulation and analytical approximation. The magnitude of the applied voltage grows exponentially and follows *sine* function. The memristor is initially set to high resistance state.

filament growth velocity is lower than the actual value when the variation of  $n_i$  over time is ignored.

To prove the capability of our model on simulating the dynamic switching property of the memristor, we plot the resistance changes following the programming pulses in Fig. 3. The resistance of the memristor first decreases when the positive pulses are applied, and then raises when the polarization of the pulses changes to negative. Our numerical simulation matches the measured data very well over most of the plotted points. Small discrepancies show at the high resistance state. One reason for the deviations could be the impact of thermal fluctuations, which become prominent under a relatively low programming voltage. The analytical approximation shows relative large deviation from the measured data at the high-resistance state.

We proposed a compact model to simulate the transition region motion in the  $\text{TiO}_2\text{-TiO}_{2-x}$  memristor based on the classic ion transportation theory. Our model is validated with the measured data from a real  $\text{TiO}_2/\text{TiO}_{2-x}$  memristor device and proved capable of simulating the static and dynamic

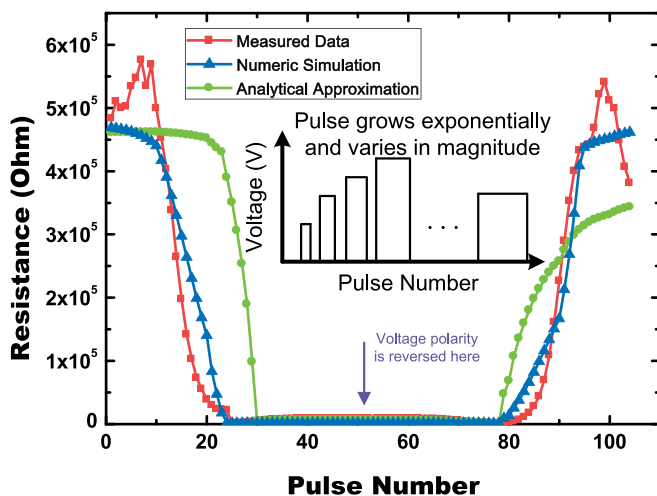


FIG. 3. Model validation with memristor dynamic switching for one single cycle, including numerical simulation and analytical approximation.

switching properties of the device with good accuracy. Our future work will focus on modeling the impact of temperature and 3-D model. We also plan to publish the corresponding Verilog-A model in the public domain in the near future.

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