

LOAN DOCUMENT

PHOTOGRAPH THIS SHEET

DTIC ACCESSION NUMBER

LEVEL

INVENTORY

DOCUMENT IDENTIFICATION

DISTRIBUTION STATEMENT

H
A
N
D
L
E

W
I
T
H

C
A
R
E

ACCESSION FOR	
NTIS	GRAM <input checked="" type="checkbox"/>
DTIC	TRAC <input type="checkbox"/>
UNANNOUNCED	<input type="checkbox"/>
JUSTIFICATION	
BY	
DISTRIBUTION/	
AVAILABILITY CODES	
DISTRIBUTION	AVAILABILITY AND/OR SPECIAL
A-1	

DISTRIBUTION STAMP

DATE ACCESSIONED

DATE RETURNED

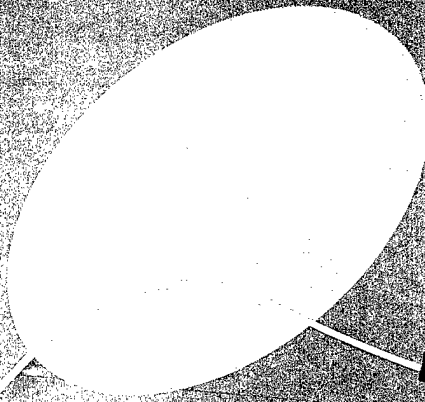
19981223 077

DATE RECEIVED IN DTIC

REGISTERED OR CERTIFIED NUMBER

PHOTOGRAPH THIS SHEET AND RETURN TO DTIC-FDAC

UNCLASSIFIED



NO DISTRIBUTION
STATEMENT

NADC

Info.

NEWS

FINAL SOFTWARE REPORT

DATA ITEM NO. A005

Reproduced From
Best Available Copy

**INTEGRATED ELECTRONIC WARFARE SYSTEM
ADVANCED DEVELOPMENT MODEL (ADM)**

7800987-1

PREPARED FOR:

NAVAL AIR DEVELOPMENT CENTER
WARMINSTER, PENNSYLVANIA

CONTRACT N62269-75-C-0070



ELECTROMAGNETIC
SYSTEMS DIVISION

1 OCTOBER 1977

UNCLASSIFIED

FINAL SOFTWARE REPORT

DATA ITEM A005

**INTEGRATED ELECTRONIC WARFARE SYSTEM (IEWS)
ADVANCED DEVELOPMENT MODEL (ADM)**

Contract No. N62269-75-C-0070

Prepared for:

**Naval Air Development Center
Warminster, Pennsylvania**

Prepared by:

**RAYTHEON COMPANY
Electromagnetic Systems Division
6380 Hollister Avenue
Goleta, California 93017**

1 OCTOBER 1977

INDEX

IEWS FINAL SOFTWARE REPORT

- Tabulation of Available Documentation page 2
- *Appendix 1. Functional Requirement Specification
- Appendix 2. Computer Program Perf. Specification
- Appendix 3. Computer Program Operators Manual (CPOM)
- Appendix 4. Computer Program Design Specification (CPDS)
- Appendix 5. Common Data Base Specification
- Appendix 6. System Controller Exec. Design Specification
- Appendix 7. System Mgmt 2
- Appendix 8. RMP Display & Control Module
- Appendix 9. RMP Resource Mgmt Module
- Appendix 10. Sorter Message Processing
- Appendix 11. Analysis Return, CP Processor
- Appendix 12. Aux. Bus Interface (ABI) Mgmt.
- Appendix 13. Emitter Classification Processing
- Appendix 14. Emitter Library Generation
- Appendix 15. Signal Classification Requirements
- Appendix 16. SC Data Extraction
- Appendix 17. IEWS Data Transfer (XFER)
- Appendix 18. Techniques Gen S/W Des.
- Appendix 19. RP-16 Linking Loader
- Appendix 20. RP-16 Relocatable Object Text
- Appendix 21. Data Recording with STE
- Appendix 22. Data Reduction with STE
- Appendix 23. Signal Sorter Computer Program Perf. Specification (SS CPPS)
- Appendix 24. Signal Sorter Flow Charts
- Appendix 25. Signal Sorter Common Data Base

- Appendix 26. Signal Sorter (SS) Supervisor Design Specification & Flow Diagrams
 - Appendix 27. SS New Emitter Start Unit (NESU) & Flow Charts
 - Appendix 28. SS BIT Software Specification
 - Appendix 29. SC Program Test Plan
 - Appendix 30. System Controller - Sorter Interface
 - Appendix 31. Auxiliary Bus Interface
 - Appendix 32. IFMR - PE Interface
 - Appendix 33. IFMR - TG Interface
 - Appendix 34. IFMR - SC Interface
 - Appendix 35. Test Procedure for System Controller
-
-

***All Appendices are under separate cover.**

IEWS FINAL SOFTWARE REPORT

The IEWS software documentation is listed in Table 1.

Copies of the referenced documents are contained under separate cover in Appendices, 1 through 35, corresponding to items 1 through 35 in Table 1. The paper tape (PT) and floppy disc (FD) code were delivered with the IEWS equipment (Contract Item 0001). The magnetic tape code is currently on file at Raytheon Electromagnetic Systems Division.

TABLE 1. IEWS (ADM) SOFTWARE DOCUMENTATION

Description	Document No.	Source Code	Object Code*	Latest Listing Date
1. Functional Requirement Spec (FRS)	53959-JK-0700	N/A	N/A	N/A
2. Computer Program Perf. Spec (CPPS)	061290529	N/A	N/A	N/A
3. Computer Program Operators Manual (CPOM)	53959-DB-0762	N/A	N/A	N/A
4. Computer Program Design Spec (CPDS)	53959-GT-0750	N/A	N/A	N/A
5. Common Data Base Spec	53959-GT-0751	N/A	N/A	N/A
6. System Controller Exec. Design Spec	53959-GT-0756			
Resource Mgmt. (RM) Exec		MT:332, File 4	FD: TLC 3 File 5 & 6	7/22/77 (patches)
Classification Processor (CP) Exec.		MT:332, File 3	PT: Romex 33 PT: CPEX BB	7/22/77
Auxiliary Processor (AP) Exec.		MT:332, File 0	PT: ECF G. BB FD: TLC 3 File 5&6	7/22/77
7. System Mgmt 2	5395-GT-0757	MT:332, File 4	PT: Romex 33 FD: TLC 3, File 5&6	7/22/77
8. RMP Display & Control Module	53959-GT-0753	MT:356, File 16	PT: DCDR, BB; FD: TLC 3, File 5&6	5/27/77 (patches)
9. RMP Resource Mgmt Module	53959-GT-0752	MT:356, File 16	PT: RMEXTS. BB FD: TLC 3, File 5 & 6	5/25/77 (patches)
10. Sorter Message Processing	53959-GT-0755	MT 332, File 7	PT: ARFG. BB FD: TLC 3, File 5&6	7/22/77 (patches)
11. Analysis Return, CP Processor	53959-GT-0761	MT 332, File 7	PT: ARFG. BB FD: TLC3 File 5&6	7/22/77 (patches)
12. Aux. Bus Interface (ABI) Mgmt.	53959-GT-0754	MT 332, File 7	PT: ARFG. BB FD: TLC 3, File 5&6	7/22/77 (patches)
13. Emitter Classification Processing	53959-GT-0760	MT 332, File 7	PT: ECFG. BB FD: TLC 3, File 5&6	7/22/77

14. Emitter Library Generation	53959-GT-0770	N/A	N/A
15. Signal Classification Requirements	DCC#760844	N/A	N/A
16. SC Data Extraction	53959-GT-0759	MT332, File 5	PT: DE.BB FD: TLC 3, File 5 & 6
17. IEWS Data Transfer (XFER)	53959-TC-0763	MT332, File 5	PT: XFER. BB
18. Techniques Gen S/W Des. Spec Rev 0 (8/5/76), Rev. A (12/2/76), and Rev. B (12/20/76)	53959-HM-0412	MT949, Source Tape 3	FD: No. 1, Files 7 & 8 & 9
19. RP-16 Linking Loader	53959-TC-0773	MT 320, File 2	PT
20. RP-16 Relocatable Object Text	53959-TC-0774	MT 320, File 2	PT
21. Data Recording with STE	53959-DB-0771	FD: File: 1191 Ø DE1.RS XXX Ø DECOM 1194 Ø DE2.RS 1002 Ø COMMON. RS	File: Ø DE 1. BR 1 DECOM. BR 2 DE 2. BR 3 COMMON. BR 3 SYSTEST. BR none
22. Data Reduction with STE	53959-RB-0775	none	FD:XXX File: 2
23. Signal Sorter Computer Program Perf Spec (SS CPPS)	CG-983645-1	MT: 285, File 12	FD: TLC 3, File 2 (with patches)
24. Signal Sorter Flow Charts	CG-983645-2		PT: Supv. BB Part I (2/16/77) Supv. BB Part II (2/16/77) Supv. BR (2/14/77)
25. Signal Sorter Common Data Base	53959-TC-0765		MT: 285, File 1
26. Signal Sorter (SS) Supervisor Design Spec & Flow Diagrams	CG-983645-3		
27. SS New Emitter Start Unit (NESU) & Flow Charts	CG-983645-4	MT: 285, File 13	FD: TLC 3, File 2 (with patches) PT: NESU. BB (2/16/77) NESU. BR (2/14/77) MT: 285, File 2

			(See ref. document)	
28.	SS BIT Software Spec (including flowcharts and listing)	53959-GT-0772		
29.	SC Program Test Plan	53959-GR-0758		
30.	System Controller - Sorter Interface	53959-JK-1002		
31.	Auxiliary Bus Interface	53959-JK-1003		
32.	IFMR - TG Interface	53959-DB-1101		
33.	IFMR - TG Interface	53959-DB-1102		
34.	IFMR - SC Interface	53959-DB-1103		
35.	Test Procedure for System Controller	53959-GR-0301		
36.	IEWS Loader	None	MT 356, File 21	PT: SCLT 1. BQ FD: N/A 7/23/77 SCLT 1A. BB None 4/13/77 SC Loader 1A TLC 3, File 1 3/17/77 SC Loader 2 TLC 3, File 2 3/17/77 SC Loader 3 TLC 4, File 2 4/11/77 SCLT 3. BB TLC 3, File 1 4/15/77 SC Loader 4 TLC 3, File 4 3/17/77
37.	SC Simulator for SS Tests (IEWS SIM)	None	MT: 285, File 7	PT: IEWS SIM. BB (3/1/77) 9/1/76 FD: TLC 3, File 3 MT: 285, File 3 (Rama Rel-catable)
38.	RP160S for CP (Supports IEWS SIM)	None	None	PT: RP160S (11/1/76) RP160S (SC) (3/4/77) FD: TLC 3, File 3
39.	Special Test Equipment (STE) Operating System	None	MT 332, File 8	PT: RP 160S. BQ; FD: OS 4/26/66

*MT: Magnetic Tape
FD: Floppy Disc
PT: Paper Tape