



MONTHLY PROGRESS REPORT

**"HTS Josephson Technology on Silicon with Application to
High Speed Digital Microelectronics"**

Contract No: N00014-94-C-0261

Office of Naval Research, Arlington, VA

— October 27, 1994

Report No.: (0001AA)

Report Period: September 30 - October 29, 1994

Contract Period: September 30, 1994 - September 29, 1995

Principal Investigator: Peter Rosenthal

AFR No: 531001

Accession For	
HTS	TRACI
1	1
Availability Codes	
Dist	Avail and/or Special
A-1	

Summary of Progress

Task 1 - Josephson Junction Fabrication and Testing- Two films of YSZ buffered YBCO films were grown on silicon substrates and sent to Professor Gurvitch's laboratory at SUNY at Stony Brook. The films were tested for resistance vs temperature (see Fig. 1) and displayed excellent (< 2 K) transition widths. Work there is currently underway at Stony Brook to fabricate Josephson junctions.

Task 2- RSFQ Modeling and Design - Exploratory analysis on the high frequency properties of thin, single layer superconducting transmission lines has been commenced. Because of the constraints that film stress place on film thickness, we are presently limited to YBCO films below 70 nm thick. This will likely have a significant impact on transmission line inductance and attenuation.

Task 3 - RSFQ circuit fabrication and Testing - Arrangements have been made with the Cryoelectronics group at the National Institute of Standards and Technology to perform microwave surface resistance measurements on our standard YBCO/silicon films.

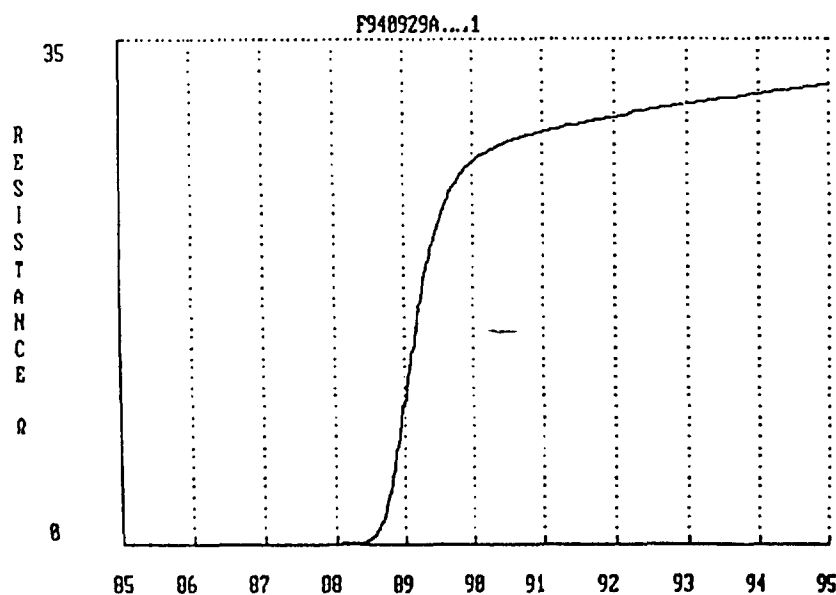


Fig. 1. R vs T of YBCO Film to be processed into junctions.

DTIC QUALITY

94-33837

bias current = .0001 A
89-29-1994 17:34:16

TEMPERATURE (K)

Peter Rosenthal
Peter A. Rosenthal, Senior Physicist