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In Situ Grown Quantum-Wire Lasers

ARO #DAAL03-91-G-0134 - FINAL REPORT by L. A. Coldren: Principal Investigator A.C. Gossard: Co-Principal Investigator J.H. English: Development Engineer

D. Mui: Post Doc S.W. Corzine, S.Y. Hu, D.B. Young, T. Strand, R.

Herrick, S.-L. Lee: Students

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Department of Electrical & Computer Engineering

University of California at Santa Barbara

April 7, 1994

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I. Introduction

This program concentrated on developing techniques to better understand and fabricate quantum-confined structures. The goal was to create the enabling technology for more efficient semiconductor lasers and integrated optoelectronic circuits. Over the contract period, significant advances occurred in the development of quantum-confined lasers, UHV *in-situ* processing technology, and the underlying theory for quantum-confined laser structures. The quantumconfined laser work included both quantum-wire laser and vertical-cavity laser development. This latter effort also required substantial improvements in the MBE growth technology. Much of this technology is now ready for transfer to industry as outlined in Appendix I. In fact, a number of joint projects with industry are underway, as a result of this program.

The work on quantum-wire lasers with 'serpentine superlattice' active regions has generated seminal results demonstrating optical gain anisotropy in in-plane ridge laser structures. This work also resulted in a novel technique of de-embedding the leakage current contribution in ridge laser structures. The vertical-cavity laser effort resulted in an improved theoretical understanding of expected performance as well as record breaking experimental characteristics, including temperature insensitive behavior, high output power, and reduced surface recombination.

The UHV *in-situ* processing work provided a much better understanding of the nature of interface traps that exist at MBE regrown interfaces. Improved etching and regrowth techniques resulted in interface state densities well below 10¹¹/cm² and nearly ideal Schottky diode characteristics for the first time. Also, successful *in-situ* etched mesa overgrowths have been carried out demonstrating low surface recombination and reduced threshold currents in mesa-etched lasers. This work continues under NSF funding in the direction of creating accurate quantum-wire structures using regrowth on etched sidewalls containing multiple quantum-wells.

The theoretical gain modelling work has generated an original theoretical treatment of quantum-confined structures that has been the basis of two book chapters, two invited papers, and a user-friendly highly-efficient software package[1-3, 13]. The package also includes

vertical-cavity laser design programs. This package has been transferred to engineers at Honeywell, AT&T and Hewlett-Packard. The gain software includes quantum-confinement as well as valence-band mixing and strain effects, and numerical analysis short cuts allows it to generate gain curves on a Macintosh in a few minutes.

II. MBE Growth

During the course of the ARO contract, several improvements have been carried out on the Varian Gen II Molecular Beam Epitaxy system used for the growth of the structures under study in this contract. In the pursuit of higher uniformity growths, lower defect levels, and higher system uptime, a high capacity dual-zone gallium furnace was installed in the system. This furnace has a gallium capacity of nearly 3x the previous furnace, and has increased the amount of material that the system is capable of growing between source replenishments. We have also seen a reduction in flux transients at shutter openings due to the hot-lip configuration.

To improve the flexibility of the system, an inverted gallium cell was designed and implemented. Previously we were limited by the geometry of the MBE source configuration, which has only four upward-looking furnace positions. Prior to this source design, liquid sources were required to be in upward-looking positions so that the liquid was contained in the furnace crucible. This new cell design allows a low-capacity, low growth-rate gallium source to be installed in a downward-looking furnace position. This allows the system to be configured with five group III sources: two gallium cells, two aluminum cells, and an indium cell. The two gallium and two aluminum cells allow multiple AlGaAs compositions and growth rates without time-consuming cell temperature changes and without resorting to superlattice approximations of random-alloy AlGaAs and the high number of shutter operations required to achieve them. Vertical Cavity Surface Emitting Lasers (VCSEL's) require very accurate knowledge of the material growth rates in the MBE system. To improve the reproducibility of the growths, extensive work has been carried out to determine the most precise and accurate procedures and conditions for Reflection High Energy Electron Diffraction (RHEED) oscillation growth rate measurements. This work has improved the accuracy of the RHEED oscillation measurements to within $\pm 1\%$. We have also determined conditions to allow accurate measurement of the growth rate of strained InGaAs on GaAs, to give better control of the indium content of the strained quantum well active region of the VCSEL structures. Most recently, an optical monitoring system has been developed that allows measurement of the optical characteristics of the VCSEL

structure at a midpoint in the growth to determine the actual thickness of the structure and to correct for error in the original growth rate measurement. This system involves white-light coupled to a fiber, reflected off the substrate while it is in the growth position in the MBE, collected back in the fiber, and analyzed by a Optical Spectrum Analyzer to give the reflectivity spectrum of the partially-grown device. This reflectivity is compared to calculated reflectivity spectra for the device at that stage of growth and the differences used to determine the actual growth rate of the device. The new growth rates and the optical measurement can then be used to determine the thickness of the cavity as grown and to finish the cavity growth so that the cavity mode is in the proper position. Using this measurement technique, the cavity resonance can be controlled to within 0.2%. This allows the VCSEL structures to be grown repeatably with tight control of the offset between the peak gain of the quantum wells and the lasing mode of the vertical cavity structure, an important parameter in determining the device's thermal characteristics.

III. Vertical Cavity Surface Emitting Laser Research

Beyond the improvements in the growth technology carried out to enhance the reproducibility of the VCSEL growth, several advances in the state of the art in VCSEL devices have been achieved under the ARO program[8, 9]. First are the advances predicted by device modeling in the early stages of the program. Modeling on the thermal behavior of our first VCSEL devices indicated that the major drawbacks in the epitaxial design were in two areas. The first was the cladding material making up the cavity of the VCSEL. This material was seen in modeling to be of too low aluminum content, and that consequently, as the device heated, much of the carrier concentration in the wells was spilling out into the cladding material and recombining nonradiatively. This caused the devices to shut off prematurely. The second major factor was the placement of the peak gain of the quantum wells. The etched-pillar device structure used in our devices exhibits significant heating at the active region during operation. Because the cavity modes are widely spaced (approximately 50 nm apart), the VCSEL does not lase at the peak of the quantum well gain as an in-plane laser does. The lasing mode and the gain peak are independently set during the growth. During operation, by the time the device reaches threshold current, the active region is significantly above room temperature. If the device is grown so that the gain peak and cavity mode line up at room temperature, they will not line up during operation and the threshold current is increased to pump the wells to the gain required to reach threshold at the cavity wavelength. Modeling indicated that to improve the lasers, the cladding material aluminum content should be increased to 50% and the gain be offset to shorter wavelengths by roughly 25 nm to achieve more stable and higher power operation. These enhancements were incorporated in a VCSEL growth and the resulting devices set world records for CW high temperature operation, temperature independent threshold current (as can be seen in Figure 1), and CW power output. For the 8 µm diameter device seen in Figure 1, the threshold was 1.4 mA at RT, decreasing to 1.2 mA at 55°C before increasing again.



Figure 1. Current required to drive an 8µm diameter VCSEL to threshold and various power output levels as a function of temperature.

As can be seen in Figure 1, there is a 70°C range where the current required to achieve 0.5 mW output power varies by less than $\pm 10\%$. A large diameter (70 µm) multimode device was heatsunk and the output power for the device increased to a record 114 mW CW.

More recently, work has been carried out on reducing the threshold current required to drive the VCSELs. An analysis of the current loss in small devices reveals that as the device diameter decreases, the current lost to surface recombination or carrier diffusion (if the quantum well is not etched through) becomes a dominant mechanism defining the device threshold current. Reduction or removal of this nonradiative carrier loss mechanism would result in a substantial reduction in threshold currents and a consequent improvement in power conversion efficiency as well as a reduction in the device heating for a given power output. To test this, VCSEL material similar to that described above was etched through the quantum wells by RIE and then passivated with various sulfide treatments prior to encapsulation with silicon nitride.



Figure 2. Light output vs. drive current for an 8 μ m diameter pillar with no passivation and with three different passivation techniques. The devices all reach a maximum of around 1 mW CW output power, but the figure has been blown up to show the reduction in threshold current for the passivated devices.

Figure 2 shows the threshold current of an 8 μ m diameter device with no passivation and three different passivation techniques. As can be seen, the threshold current for this device was reduced by approximately 33% over the untreated control device.



Figure 3. Threshold current density vs. the perimeter/area ratio of different diameter devices. A perimeter/area ratio of 0 corresponds to the broad-area device threshold current density, and a ratio of 0.5 corresponds to an 8μ m diameter VCSEL.

Figure 3 illustrates the increasing effect of surface recombination on the threshold current density of small VCSEL pillars. The slopes of the curves can be used to extract a surface recombination velocity, and a 25% reduction in surface recombination velocity was observed for the best passivation technique applied to these devices. This treatment resulted in a device with a record combination of low threshold (0.67 mA) and peak CW power output (1.2 mW). The results on the passivation are encouraging, but it is expected that with further refinements in the passivation, more significant reductions in the threshold current can be expected.

IV. Modelling for reduced threshold VCSELs

With the ability to accurately estimate the threshold current densities and differential efficiencies of broad area VCSELs, we feel confident about the theoretical models which have been developed to determine the gain as a function of current density as well as the various cavity losses, both mirror and free carrier absorption losses. Such knowledge has allowed us to optimize the design of VCSEL structures. We have for the most part settled on a three quantum well design with 1% transmission through the output coupler (which provides 99.5% mean mirror reflectivity). These numbers are based upon the internal losses of the cavity which we have attempted to minimize. Further reductions in the internal losses would allow us to reduce the transmission through the output coupler while keeping the differential efficiency constant, leading to lower threshold currents. However, while minimizing internal loss is an important aspect of the design, we have come to the conclusion that a better approach to minimizing the threshold current involves reducing the size of the VCSEL. Thus, this would appear to be a good direction in which to focus.

The challenge is to understand how the laser parameters are modified when the pillar diameter is made smaller than -20 μ m. We believe there are three main effects that must be dealt with: (1) surface recombination at the exposed sidewalls of the active region which increases the threshold current density, (2) optical scattering at the rough sidewalls of the pillar which increases the internal losses of the cavity, and (3) increased thermal impedance of the pillar which increases the temperature of the active region for a given power dissipation, ultimately limiting the maximum CW output power the device can produce. Surface recombination and the thermal impedance are expected to increase as 1/r, where r is the radius of the pillar. Optical scattering on the other hand is expected to increase as $1/r^3$. The experimental dependence of the pulsed threshold current density on radius measured in our lab falls somewhere between 1/r and $1/r^3$ (for VCSELs greater than 5 μ m), suggesting that both surface recombination and optical scattering are important. However, because the optical scattering has a much stronger dependence on r, it is our belief that the key to obtaining good

performance from VCSELs with diameters $< 5 \,\mu$ m must include some method of reducing optical scattering.

Let us first consider surface recombination. Theoretical and experimental broad area VCSELs have threshold current densities in the range of 600-1000 A/cm², which in practical units is equivalent to 0.6-1 mA/(10μ m)². Thus, 10 µm diameter pillars should have threshold currents < 1 mA. Experimental 10 µm diameter devices typically measure in at 1.5-2 mA. Using theoretical numbers for the threshold carrier density and surface recombination velocities typical of InGaAs/GaAs strained quantum wells, it is predicted that half of this threshold current is due to surface recombination. As the diameter is reduced, this fraction increases, thus representing a serious limitation on realizing small diameter VCSELs. In response to this difficulty, we have developed a number of methods to reduce the surface recombination. The surface passivation techniques were outlined above. Also, surface implantation techniques to disorder the quantum wells near the edge of the pillar as well as *in-situ* methods of regrowth to properly terminate the active region at the sidewalls of in-plane lasers are being explored. If successful, these techniques will eventually be applied to VCSELs.

As mentioned earlier, optical scattering also may represent a serious limitation due to its strong dependence on the device size. Unfortunately, quantitative theoretical estimates of the actual loss incurred from rough sidewalls is difficult to estimate. However, the relative dependence is easy to calculate if we assume that the loss is proportional to the power existing at the surfaces. Solving for the cylindrical Bessel function modes of the pillar has allowed us to observe how the losses should vary with radius. At small dimensions, it appears essential to attempt to suppress the fields at the surface. One method we are attempting to implement involves doping the sidewalls to reduce the index there as a result of the plasma effect. Even though these index changes are small, we have found that a 0.5% change in index penetrating 0.5 μ m into the 6 μ m diameter pillar is enough to significantly suppress the power at the surface. Such numbers are attainable using sidewall implantation or diffusion. One other concern was that the doping in the sidewalls would actually introduce more internal loss. Our calculations

reveal that the confinement factor in the lossy areas decreases more rapidly than the losses increase, as the index is changed (assuming free carrier absorption loss increases linearly with carrier density). As a result, the higher the doping, the better is the loss. Furthermore, the expected modal losses for moderate doping is expected to be < 0.02%, a factor of ten smaller than other losses in the cavity. Thus, we are very optimistic about reducing optical scattering losses.

The final concern with small diameter VCSELs involves the thermal impedance. With the 1/r dependence, we anticipate heating to be a major problem. The most direct way to combat this problem is to heat sink the devices either by flip-chip bonding or by creating trenches around the VCSEL which can then be filled with a good thermal conductor. We are currently working on developing the technology necessary to implement such low thermal impedance devices.

We anticipate that with the above solutions to small VCSEL device problems, the threshold currents can be reduced close to the broad area device limit. For example, using the $1 \text{ mA}/(10\mu\text{m})^2$ threshold current density measured in broad area devices, a 5 μ m diameter device should have less than 0.25 mA threshold current. However, we anticipate that devices as small as 2-3 μ m in diameter should be attainable. Thus, we expect threshold currents to eventually drop below 100 μ A, while simultaneously maintaining reasonable differential efficiencies. And with good heat sinking, CW powers greater than 1 mW should also be possible in such devices.

V. Quantum-wire lasers and Characterization

In the quantum wire laser area, a key effort provided optical gain measurements from a serpentine superlattice nanowire array laser sample grown by MBE on a 2°-off (100) GaAs vicinal substrate[**15]. The sample was fabricated into in-plane ridge-waveguide lasers with stripes either parallel or perpendicular to the nanowire arrays. Spontaneous emission spectra were taken at 1.4 K and 77 K for several injection levels. The measured gain spectra at 1.4 K show extraordinary anisotropic characteristics. It is demonstrated that TM mode gain becomes greater than TE mode gain when the optical cavity is placed along the nanowire direction which gives strong evidence that the lateral quantum confinement in the serpentine superlattice is stronger than the vertical quantum confinement. A novel theoretical model has been proposed and applied to the calculation of optical gain spectra in the serpentine superlattice with consideration of both homogeneous and inhomogeneous broadening[4]. The observed polarization-dependent gain spectra are well-explained by this model. A good fit to the data is achieved when the lineshape functions are assumed to have Gaussian distribution with FWHM energy of 6 meV for the homogeneous broadening and 15 meV for the inhomogeneous broadening.

In order to generate the above described data, a self-consistent method for measuring the material gain/current-density characteristics in ridge lasers was developed[*14]. Ridge waveguide lasers were made, with different stripe widths and ridge heights, by *in situ* laser-monitored reactive ion etching. The leakage current, consisting of mainly ohmic-sheet spreading current and lateral diffusion current, is determined from the variation of threshold current versus stripe width and cavity length. It is found that the inclusion of leakage current is crucial to obtain a self-consistent result. By using this method, the gain/current-density curve for a 8 nm-wide $\ln_{0.2}Ga_{0.8}As/AlGaAs$ strained single-quantum-well laser sample is measured to have a transparency current density of 45.4 A/cm².

VI. In-situ etching and regrowth

We have installed an etching/analysis chamber and connected it with two MBE chambers to make an UHV in-situ processing system[16]. The etching capabilities include a broad-area ion beam source and a reactive gas inlet with microwave cracking capability. This allows us to do both low-damage thermal gas etching and anisotropic ion-beam assisted etching. The system base pressure is $5x10^{-8}$ Torr, with oxygen and water partial pressures at $2x10^{-10}$ and $4x10^{-11}$ Torr, respectively. These low partial pressures make possible the smooth etching of aluminumcontaining materials.

Analytical tools include a focused electron beam and secondary electron detector which give us an electron microscope inside the etch chamber and the possibility of in-situ patterning, and an attached RHEED system which allows us to investigate the etched surface roughness. We use laser reflectance to monitor the progression of an etch in real time.

1. Electrical quality of regrowth interface:

We have used in-situ etch and regrowth to achieve interface trap densities as low as 10¹⁰ cm⁻² with gas etching. We have fabricated Schottky diodes on regrown GaAs which display ideal I-V characteristics and no observable current-limiting effect by the regrowth interface. In view of these promising results, we are applying this etch-and-regrowth technique to the fabrication of gain-coupled DFB and buried in-plane lasers.



2. Regrowth over etched structures:

We have demonstrated in-situ regrowth of GaAs and AlAs layers over etched GaAs and AlAs structures. SEM pictures show that the epilayers adhere to the etched sidewalls – an important result for making buried in-plane lasers with low surface recombination currents. Using SiO₂ as an etch mask, we have also demonstrated liftoff of polycrystalline layers deposited on the mask during regrowth, leaving the original device surface unharmed.



SEM micrograph of GaAs (light) and AlAs (dark) regrowth layers over etched GaAs/AlAs mirror stack. (Uneven layers due to excessive stain etching). Inset shows regrowth on sidewall. Etched with IBAE followed by gas etching.



GaAs and AlAs epilayers deposited on a SiO₂ etch mask (top) and their removal by HF liftoff (bottom).

3. Surface morphology:

We have demonstrated smooth Ion Beam Assisted Etching (IBAE) and thermal gas (chlorine) etching of $Al_xGa_{1-x}As$, $In_{0.53}Ga_{0.47}As$, and InP, as measured with Nomarski microscopy. In addition, we have studied the atomic-scale surface of etched GaAs with RHEED and atomic force microscopy. Chlorine gas etching at 200°C results in a surface roughness on the order of 100Å.



RHEED pattern after 200°C chlorine gas etching (left) and after 90Å growth of GaAs (right). Faceting is evident on the etched surface (chevron-shaped diffraction streaks). Regrowth smoothes the surface as evidenced by the streaky 4x pattern.

4. Buried in-plane lasers:

Using the etch and regrowth techniques discussed above we have fabricated buried inplane lasers. The reduction in surface recombination achieved by regrowth of material on the edges of the active region results in reduced threshold current: in a 5 μ m-wide laser stripe the threshold current density was cut in half, due to the reduction in surface recombination velocity (S) from 2x10⁵ cm/s to 9x10⁴ cm/s. Further improvements are expected, as indicated by the electrical results discussed above.

VII. Participating Scientific Personnel

L.A. Coldren	Principal Investigator
A.C. Gossard	Co-Principal Investigator
J.H. English	Development Engineer
D. Mui	Post Doc

Students

S.W. Corzine, S.Y. Hu, D.B. Young, T. Strand, R. Herrick, S.-L. Lee

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VIII. Appendix I - Summary Foils from Technology Transfer workshop, Los Angeles, 2/3/93.

Presenter: Larry A. Coldren

Organization: University of California, Santa Barbara

Title: Materials and Processing Technologies for Quantum Confined Optoelectronic Device Structures

Summary:

The technology to grow and process optoelectronic materials for high efficiency devices has been advanced under this IST/SDIO program. Novel ultra-high vacuum *in situ* processing apparatus has been developed which contains chambers for the epitaxial growth of 'quantumconfined' device structures as well as their lateral patterning by low-damage etching. Using this apparatus, novel optoelectronic structures have been fabricated. The one with the most immediate commercial potential is a vertical-cavity surface-emitting laser. Advanced design and analysis software has also been developed for this and other related semiconductor lasers under the SDI program This device technology and design expertise is being transferred to several companies. Patents have been granted on the low-damage etching apparatus, the processing recipe and some device embodiments.

Materials and Processing Technologies

for

Quantum-Confined Optoelectronic Device Structures

by

Larry A. Coldren University of California, Santa Barbara, CA

February 3, 1993

Sponsored by IST/SDIO via ARO

Introduction

• UCSB has one of the largest university efforts in III-V semiconductor technology

- A dozen leading faculty involved in all aspects
- State-of-the-art growth and processing facilities
- 5 MBEs; In-situ etching/growth; FIB; Liquid MOCVD; E-beam; Clean room
- State-of-the-art characterization facilities
- An NSF sponsored Science and Technology Center on Quantum-Confined

Electronic Structures (QUEST), an NSF-Materials Research Lab, and a DARPA/industry sponsored Optoelectronics Technology Center (OTC)

• IST/SDIO instrumental in setting up and supporting growth and processing facilities as well as device and modelling work

- Integrated UHV system: MBE + in-situ etching by SDIO; a second MBE and FIB from other sources.

- Novel radical-beam ion-beam etching (RBIBE) technique* installed in-situ

- Support of advanced software development for MBE control** as well as optoelectronic device modelling**

- Early support of leading tunable* and vertical-cavity* laser work

*Patented; **copyrighted

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I. Susmune, et al, JOE, 24(8), 1778-1790 (Aug., 1988)



Technology Description

• Novel low-damage, anisotropic etching apparatus and technique

- RBIBE: #4,874,459

- Incorporation into UHV in-situ processing apparatus: model for integrated growth & processing factory

User-friendly software

- MBE control: MBE-MASTER

- Transferred to Sandia, Xerox, yyy, zzz

- Analysis and design of vertical-cavity lasers: MULTILAYER; GAIN; VCSEL
 - Transferred to Honeywell, AT&T, Hewlett-Packard

Novel optoelectronic devices

- Ultra-wide tuning range laser: #4,896,325
- Efficient vertical-cavity laser: #4,873,696

Innovative Elements--1

• RBIBE

- Only technique to cover <u>full range</u> from purely physical to purely chemical dry etching

- Physical etching is independently controlled from the chemical and the chemical effect is stronger

- Anisotropy can be achieved with minimum damage

- Etch rate can be maximized with minimum damage and controlled anisotropy (record reactive sputter yield)

- Desired for many device applications where high-aspect features are needed



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Innovative Elements--2

:

MBE Software

- Most user-friendly and versatile MBE control software available
- Many subprograms for exotic structures

- Communicate in high-level language: thickness and composition not machine settings.

- Improves output of MBE in quality and quanity

• Device Analysis & Design Software

- Very efficient and user-friendly -- MacIntosh based

- Gain calculations done two-orders of magnitude more efficiently than other available programs

- VCSEL designs with minimal input
- Multilayer waveguide and standing-wave analysis
- Desirable for production of optimized devices

VERTICAL-CAVITY SURFACE-EMITTING LASERS (VCSEL:)









Optical Gain in Strained and Unstrained Quantum Wells

:

S.W. Corzine, B. Thibeault, R.S. Geels, C.P. Chao, Radha Nagarajan, and L.A. Coldren

Sponsored by ARO

A detailed theoretical model of gain has been developed to predict effects of strain in both InGaAs/GaAs and InGaAs/InP material systems





SAIN TITES 24

Comparison of the valence subbands with and without strain. The density of states shown at the right indicates the much improved matching between the conduction and valence band in the strained case. (Ideally the ratio should be equal to 1).

Innovative Elements--3

Novel Optoelectronic Devices

- Sampled-grating tunable diode laser

- Unsurpassed in tuning range (>60nm)
- Best combination of wide tuning range with good mode suppression
- Fabrication no more difficult than standard DBR laser--much simpler than competition
 - Needed in high-capacity wavelength division communication systems

- Ready for final development and manufacture

- Vertical-cavity laser

- Gain placement at standing-wave maximum doubles the modal gain
- High-bandgap confining layers and displaced gain design allows operation
- at high temperatures
 - Temperature insensitive operation also demonstrated.
- Wafer-scale fabrication and testing provides first laser manufacturable by IC technology
 - Needed for numerous applications from data links to optical recording
 - Ready for final development and manufacture

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[14]	Patent Number:	4,373,696
[45]	Date of Patent:	Oct. 10. 1989

ABSTRACT (57]

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DIAMOND HEATSINK



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Device structure used in present work



Introduction: the sampled grating concept; proposed LEOS '91

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Gratings with elements periodically removed lead to periodic reflection spectra and wideband "vernier scale" tuning.





Device Tuning Characteristics

•Tuning by <u>primarily changing one mirror current</u>, with occasional small adjustments in the other mirror current to maximize SMSR.

•SMSR maintained over 30 dB for most of tuning range.



· 64N P642 TO 400

SGDBR tuning vs. difference in mirror currents



I. - 30-40-A

Future Developments

· Basic Technology available for licensing

• Although leading the pack, there are still many improvements possible in the tunable laser and verticalcavity laser devices

- Ready for industrial development

- Funding required

Spinoff Applications

• RBIBE

- Applicable to all manner of semiconductor component manufacture--not limited to 111-Vs and certainly not only to military applications

- Payoff because of improved control and speed

• Software

- MBE control software useful in any MBE application

- Gain analysis software useful for any diode laser

- VCSEL analysis and design useful for many classes of VCSELs-applications not limited

- Immediate payoff in growth quality and quantity and design savings

• Devices

- Tunable lasers desired for wavelength division communication; spectroscopy; beam sweeping; holographic optical memory; and display--not limited to military applications

- VCSELs superior to conventional in-plane lasers for most applications: printing; data links; high-frequency modulation; optical storage; display--not limited to military applications.

- Ready for commercial development--demand is there for cost-effective devices

Key Players

• The RBIBE technology is being emulated without licensing in many laboratories. Equipment manufactures have expressed interest, but not takers yet.

• Hewlet-Packard, Honeywell, and Optical Concepts, Inc are developing the VCSEL technology with us. Others have expressed interest.

• Mild interest, but no firm takers for the tunable laser concepts. Our concept looks like a winner, but the market needs to develop.

• The software is being licensed or shared freely.

• The Japanese and Europeans will have competing activities.

Moving Forward

• Small/large companies need incentives to develop these new technologies. R & D capital scarce.

• UCSB needs additional \$ from government sources to continue research, but more \$ should come via industries to provide for technology transfer.

• Companies need incentives to send employees on-campus to acquire new techological advances

IX. Appendix II - Publications resulting from ARO work

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