





NAVAL POSTGRADUATE SCHOOL Monterey, California



THESIS

COMPUTER PROGRAM TO SIMULATE DIGITAL COMPUTER BASED

LONGITUDINAL FLIGHT CONTROL LAWS IN A

HIGH PERFORMANCE AIRCRAFT

by

James Robert Carter

December 1983

Thesis Advisor:

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M.D. Hewett

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Computer Frogram to Simulate Digital Computer Based Longitudinal Flight Control Laws in a High Perfcrmance Aircraft

by

James Robert Carter Lieutenant Commander, United States Navy B.S., U.S. Naval Academy, 1973

Submitted in partial fulfillment of the requirements for the degree of

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Authcr:		James	<u>R.</u>	Canter		
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		\bigcirc			Thesis	Advisor

Chairman, Department of Aeronautical Engineering

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Dean of Science and Engineering

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I. INTRODUCTION

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A SUBSCRIPTION A PARAMETER

The Navy has experienced the loss of numerous aircraft during recent years due to unintentional departure from controlled flight. The increased cost and complexity of modern aircraft utilizing fly-by-wire flight control systems have placed a renewed emphasis on understanding the performance characteristics of these aircraft, especially near the limits of the flight envelope. The purpose of this thesis is to investigate a method of non-real time computer simulation of the longitudinal flight control system of the F/A-18 aircraft. Future thesis researchers at the Naval Postgraduate School will complete corresponding simulations of the lateral and directional control systems. The combination of these simulations with an existing ascodynamic simulation program will yield a complete aircraft stability and control model. The primary purpose of the model is to investigate methods of designing control augmentation systems which actively inhibit or prevent departures from controlled flight. Other uses of this model would include the capabilities to test new programmable memory configurations, to evaluate new components such as optisal observers, to sigulate degraded flight conditions

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(such as a damaged flight control surface) and to recreate flight conditions during post accident investigations.

The method which was chosen for accomplishment of the objectives of this thesis was the Continuous Systems Modeling Program (CSMP), developed by the IBM Company [Ref. 1]. CSMP is a software package designed to simulate dynamic systems described in terms of differential equations and block diagrams normally encountered in systems theory. CSMP allows programming flexibility through the use of thirty-four pre-programmed functional blocks which are similar to FORTRAN subroutines. These blocks provide rapid access to mathematical functions, switching functions, signal sources, logic functions and FORTRAN functions. Since this thesis represented the first attempt at the Naval Postgraduate School to accurately model the flight control system of a modern, highly augmented tactical aircraft it was deemed important to concentrate on the physical systems rather than become involved in the complexities of numerical analysis.

Alternatives to CSMP which were considered included analog programming and FCRTRAN programming. Analog programming was not selected because it is less

representative of the systems to be modeled and it is less accurate than CSMP. The concept of programming directly in FORTRAN was carefully considered. Since FORTRAN is the source language for CSMP the capabilities of CSMP are a subset of the capabilities of FORTRAN itself. Additionally, CSMP has restrictions on the number of allowable statements, constants, variables and other parameters. Unlike FORTRAN, when functional blocks in CSMP are used, the programmer has no direct control of mathematical operations internal to the functions. The primary reasons for which CSMP was selected were its simplified input statements, output statements and program control statements which facilitated rapid program writing and testing. Additionally, the automatic time and amplitude scaling, data formatting, and compatibility with graphic display devices which CSMP provides are well suited for prototype program development.

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II. <u>METHODOLOGY</u>

A. PROGRAM ORGANIZATION AND NOMENCLATURE

A detailed description of the flight control laws of the F/A-18 aircraft is given in the McDonnell Aircraft Company's system design report [Ref. 2]. The computer program developed in this thesis is based upon figure 16.1 of this report, entitled F/A-18 Longitudinal, Mechanical, CAS, and DEL Control Law Mechanization. This figure contains six pages of block diagrams depicting generation of longitudinal control signals which are valid for all aircraft configurations and failure modes. The version of flight control program incorporated in the flight control computer. programmable read only memory (PROM) utilized in this simulation is 8.2.1, dated August 31, 1982.

A brief discussion of the F/A-18 flight control system is necessary to facilitate a discussion of control law modeling. All computations of control laws are accomplished independently by four channels of digital computation. Primary control in the pitch axis is provided by symmetric deflection of the horizontal stabilators. Full span leading edge flaps and trailing edge flaps are scheduled to provide maximum lift to drag ratic during maneuvering, high angle

of attack and cruise configurations. Roll control is accomplished by ailerons, differential stabilators and differential leading and trailing edge flap deflection. Directional control is maintained by dual rudders. The thesis computer program which is contained in Appendix A simulates the cutput of one channel of digital computation and calculates the angular positions of the stabilators, leading edge flaps and trailing edge flaps. This simulation does not calculate rudder or aileron deflection, however, all electrical signals required for cross axis interconnects are provided.

The task of programming the information given in the longitudinal control law mechanization schematic was simplified by two means. First, the program restrictions and assumptions to conditions of flight which are discussed in part B were applied. As a result sections of figure 16.1 which apply to mechanical control laws and spin modes, for example, were deleted. This reduced the number of schematic blocks to be modeled by approximately one third. The second simplification arose through a system of nomenclature in which nine control paths were defined in order to limit the number of input and output signals for any specific path.

When combined, these nine paths form the total longitudinal control law mechanization.

Figure 2.1 is the overall longitudinal control signal block diagram which was used in this simulation. It was derived by applying all program assumptions or restrictions outlined in part B to figure 16.1 of the system design report.

Block diagrams depicting the logic development of the component paths are included in Appendix A. The nomenclature for each control path which is given in Table I is peculiar to this simulation program.

Table II lists nomenclature for control signal groups which are common to both the McDonnell schematic and to this simulation. Signals with common prefixes are numbered consecutively in the feed forward direction. The primary feed forward input to the CSMP simulation is pilot stick force. Feedback signals include pitch rate, roll rate, yaw rate, angle of attack, normal acceleration and differential control surface commands.

B. PROGRAM RESTRICTIONS AND ASSUMPTIONS

The task of obtaining a working computer program to meet the thesis objectives did not require the simulation of all



TABLE I

Control Signal Paths

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SIGNAL PREFIX

Main path (open lcop)	MP
Pilot stick input path	PS
Pitch rate gyro path	PR
Angle of attack sensor path	8 A N 7
Rechard integrator nath	8 T
Stabilator path	ŜŤ
leading edge flap path	ĹĒ
Trailing edge flàr path	ŤĚ

TABLE II

Common Signal Nomenclature

SIGNAL (OR PREFIX)	DESCRIPTION
ALPHAS Alphat P PK PS PV QC	Angle of attack (computed) Angle of attack (true) Function (air data schedule) Air data schedule gain parameter Static pressure Pitch axis storage location Compressible dynamic pressure Pressure guotient (2007/PS)
K I	PIESSULE QUOTIELT (FUC/PS)

possible conditions of flight. Thus, an assumed aircraft configuration and flight condition led to many simplifications in the model. Each major assumption is discussed below. Should future researchars desire to construct a more general model, additional program logic paths could be included in this CSMP simulation without requiring major rewisions to the program.

Assumptions:

1. The aircraft flight control electronics set (FCES) is operating normally. Primary components of this set include flight control computers, pitch, roll and yaw rate gyros, accelerometer assemblies and flight control panels. The FCES contains logic sequences for failure detection and corrective control law implementation which were ommitted from the thesis computer program.

2. The aircraft is under inner loop control. In this mode pilot stick force forms the primary control input. Autopilot functions such as heading hold, roll or pitch attitude hold, and altitude hold are not in operation.

3. The pitch control augmentation system (CAS) is providing longitudinal control. Backup control systems which are not in operation include the mechanical backup mode and the direct electric link (CEL) mode which provides an open loop signal from the pilct stick position sensor to the stabilator servoactuator.

4. The aircraft is operating in the up and away flight phase in the auto flaps up (AFU) configuration. This phase requires that the flap switch in the cockpit be in the AUTO position or that the calibrated airspeed be greater than 243

knots. In the AFU mode gain schedules are optimized for combat maneuvering characteristics in the low to mid dynamic pressure regime. Additionally, a trim integrator controls load factor and, at angles of attack above 22 degrees, proportional nose down commands are introduced.

5. The gain switch is in the normal position. The override position of this switch causes flight computers to use fixed values for air data schedules and a predetermined angle of attack in control law computations. The normal position of the gain switch allows measured values of air data and angle of attack to be used.

6. The aircraft is operating with weight off the wheels, speed brake in, and with no external stores.

7. Anti spin functions are not simulated.

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III. CONTROL LAW MODELING

A. AIR DATA SCHEDULES

Air data schedules are functions of static pressure (PS), compressible dynamic pressure (QC), and other parameters such as normal acceleration, angle of attack or condition of external stores. The longitudinal control laws contain inputs from 13 different air data schedules which are listed in Table III.

TABLE III

Air Data Schedules

FUNCTION

DESCRIPTION (INPUTS)

Pitch forward loop integrator gain schedule (RI,PS) Fader on supersonic compensation (QC) Stall margin on pitch forward loop integrator (AOA) Trailing edge flap schedule (AOA,RI) Trailing edge flap schedule (QC limit) Leading edge flap schedule (QC) Longitudinal inertial gain schedule (QC)

Each schedule controls an output gain for a particular purpose. Function 32A, for example, yields a uniform initial oitch acceleration in response to sharp inputs, with gain decreasing at high values of compressible dynamic pressure. Other functions such as function 290 are used to determine

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an upper or lower limit to an input signal. Simulation of these functions in the CSBF format required that individual computer programs be written to test each air data schedule. In all cases the final result was given in the flight control system design report, usually in graphical form. Conversion to CSBP format was done by extraction of data points from graphs, use of logic flow charts when available and direct employment of those mathematical formulae which were given. Cnce programmed in CSMP, the tabular and graphical output data was compared to data given in the design report for the same test parameters. Thus, the output of each function was independently verified before the function was included in the longitudinal control law simulation.

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The simulation is written to take advantage of CSMP's SORT and NCSORT capabilities where appropriate. The NOSORT option of CSMP is used for conditional logic and branching. This statement allows the user flexibility in creating sections of the program in which ordinary FORTRAN rules can be used. In NOSORT sections intermediate variables were defined, comparisons made and appropriate branching was executed. The program statements were returned to SORT

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format as soon as conditional logic was no longer required. An early problem revealed that if two or more SORT sections are separated by a NOSORT section information will not be passed between the separated SORT sections. To prevent errors resulting from this restriction from occuring the following decisions regarding the order of program statements were made.

1. The number of NOSORI sections should be minimized.

2. NOSORT sections should be located close together, allowing fewer and larger SORT sections.

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3. Macros should be utilized when possible. Macros, which are similar to subroutines, are discussed in part D under frequency averagers.

The task of arranging program statements to minimize the number of NOSORT sections was greatly simplified through the use of the "output variable sequence" tabulation which is produced as part of the CSMP standard output. The final number of NOSORT statements in the computer simulation could have been further reduced by this method, however, this would have required the movement of large program blocks and caused a deviation from the logic path used in program development. For example, the NOSORT sections of functions

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12 and 40 could have been combined. This would have increased difficulty in program debugging and made areas of the program which require combinational logic less apparent to readers.

Appendix C contains documentation for the air data schedules. Each schedule is included in a complete computer program which produces tabular and graphical data to match the characteristics of figures in the design report. In incorporating these air data schedules into the longitudinal control law simulation all schedules except for function 24 were placed before the main computational body of the program. Function 24, the trailing edge flap schedule, requires conditional logic and contains computed angle of attack as an input. Since computed angle of attack is generated in the normal accelerometer path, function 24 was placed immediately before the trailing edge flap path computations.

B. FADERS, ABAIOG TO DIGITAL CONVERTERS, MECHANICAL EREAKOUTS

The purpose of faders is to eliminate large discontinuities in gain, permitting gradual change in output from old values to new new values at a desired sample rate.

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Faders are located in Figure 16.1 at the outputs of air data schedules and gain schedules which are dependent upon aircraft configuration. Discontinuities may arise as a result of a change in physical measurements such as dynamic pressure, change in aircraft configuration such as speed brake extension, or change in mode of flight. The lower limit for signal MP4, for example, changes from a gain of -50.0 to 0.0 as a result of spin entry. Since the thesis computer program assumes that electrical signals vary smoothly and is restricted to up and away controlled flight, faders are modeled in frequency only.

Analog to digital conversion and frequency matching are obtained by a sample and hold process. Sampling times are generated by the CSMP functional block IMPULS which produces a time series of unit impulse functions with a specified start time and period. Since these pulse trains are used in several areas of the program, impulse functions of 20, 40 and 80 hertz are included immediately following the air data schedules. The zero order hold function ZHOLD keeps signal gain constant throughout the pulse period.

A mechanical breakout force of two pounds is modeled in the pilot stick input path. When large stick forces are

applied, the resultant signal is calculated with a reduction in magnitude of two pounds. This modeling is accomplished in CSMP by the deadspace (DEADSP) functional block. Figure 16.1 of the design report depicts electrical signal "deadbands" which are conceptually identical to mechanical breakouts. Appendix D cortains a program which displays deadspace outputs in tabular and graphical form.

C. ALLASING FILTERS AND SIGNAL LIMITERS

The longitudinal control laws of the F/A-18 include five aliasing filters which are modeled as first and second order Laplace transforms. The first order transform is of the type A/(Bs+1) and is in the pilot stick input path. This lag type filter with one real pole is converted to CSMP format by the functional block REALPL. A required initial condition is the value of the output signal when time is zero. This may be determined arbitrarily by the user, but was set to zero for this simulation.

Second order filters are present in the pilot stick input path, pitch rate gyro path, angle of attack sensor bath and normal accelerometer path. Each is of the form $A/(Bs^2+Cs+D)$ and represents an underdamped system. The CSMP functional block for complex poles (CMPXPL) is used.

Initial conditions are the value of the output signal and time rate of change of the output signal when time equals zero. Natural frequencies varied from 4.34 hertz in the pilot stick input path to 33.3 hertz in the angle of attack sensor path. The appendix contains a computer program which demonstrates the CSMP outputs of both first and second order aliasing filters. Unit step inputs were introduced to each filter to generate transient responses. The first order filter produced an exponential rise to steady state with the correct time constant. Characteristics of the second order filters such as rise time, peak time, maximum overshoot, and settling time compared favorably with theoretical results [Ref. 3].

Signal limiters restrict the maximum or minimum values of an output signal. Stabilator surface deflection, for example, is limited to 10.5 degrees trailing edge down and 24 degrees trailing edge up. The CSMP functional block LIMIT allows direct specification of lower and upper signal limits.

D. FREQUENCY AVERAGERS AND RATE LIMITERS

A characteristic of the F/A-18 flight control system is that various signal paths operate at frequencies of 20, 40

or 80 hertz. When signals are combined mathematically, the inputs are first converted to a common frequency. Normal accelerometer path signals are computed at 40 hertz and combined with cutputs of the forward integrator path, which operates at 20 hertz. In this case a 20 to 40 hertz averager is present between the integrator path and the summing junction connected to the normal accelerometer path. The algorithm used for the 20 to 40 hertz averager is based on a procedure given in the Flight Control Electronic System Report [Ref. 4]. The averager was required to generate signal values at twice the rate of the incoming pulses by linear interpolation between amplitudes of the two previous signals at 20 hertz. The formulas used to generate intermediate signals were:

$$Z40 = Z40Z1 + DEL$$
 (3.1)

$$DEI = (Z20 - Z20Z1) / 2.0$$
(3.2)

where:

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Z40 = current value of the 40 hertz signalZ40Z1 = previous value of the 40 hertz signalZ20 = current value of the 20 hertz signalZ20Z1 = previous value of the 20 hertz signal. Conditional logic was used to keep the output signal equal to the input signal at times when the 20 hertz impuse function was equal to one. The algorithm was initialized by letting "previous values" he equal to input values when time equals zero.

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The presence of numerous frequency averagers in the control laws would have required the repetition of many statement blocks without the use of CSMP program MACROS, which are similar to FCFTRAN subroutines. Frequency averagers in the thesis computer simulation were included in MACROS and placed at the beginning of the program. A MACRO may he used several times within a program. Input and output variables are given dummy names, yet the MACRO is invoked with a unique name which is assigned in a function definition statement. A limitation to the use of MACROS is that variables which are defined in MACRO structure statements are not available for output unless they are designated as arguments in the function definition statement. Additionally, certain functional blocks such as REALPL, CMPXPL and INTGRL cannot be used as arguments or as parts of a MACEO structure statement.

The PROCEDURE function of CSMP was used in each MACRO to cause statements to be executed in the order of their appearance. Each structure statement of a MACRO will be individually scrted unless PROCEDURE is specified. All statements contained within a PROCEDURE function are treated as a single block which can be moved but not rearranged by the CSMP translator.

FORTRAN subprograms were not used in this simulation because the size of the longitudinal portion of the program did not approach the maximum limits of CSMP. Inclusion of the lateral and directional control systems in this program will require that measures be taken to remain within the allowable number of structure statements, NOSORT sections and MACROS. The CSMF translator is not used to process FORTRAN subprogram statements. Since the number of subprogram statements is not counted the overall size of the program may be increased. The capabilities of the computer system library may be utilized by subprograms through the use of the CALL statement. The CALL statement must, however, be included in a NOSORT or PROCEDURE section. A method to invoke subprograms exists which does not require NOSORT or PROCEDURE sections, but it is valid only for two or more

output variables and it requires a specific format for arguments. A final restriction to the use of FORTRAN subprograms is that certain CSMP functional blocks such as ZHOLD, IMPULS, and CMFXPL are not allowable.

The longitudinal control laws contain rate limiters which operate at frequencies of 20 hertz and 80 hertz to restrict the speed of leading edge flap movement. The algorithm for these limiters compares the value of each incoming signal with the value of the previous output signal. The magnitude of the difference between these signals is processed by the LINIT functional block which generates the current output signal. The 20 hertz rate limiter, for example, allows a maximum change in output signal value of 0.9 degrees during each period of 0.05 seconds. A listing of the program which was used to test the rate limiter is contained in appendix D. Correct outputs were observed for both increasing and decreasing input signals.

E. EIGITAL FILTERS, DIGITAL TO ANALOG CONVERTERS, SERVOMECHANISMS

Three types of digital filters are used in the longitudinal control laws. Z filter number P2 is a lead-lag

type controller in the pitch rate gyro path which operates at 20 hertz. Z filter number P9, the forward loop integrator, operates at 20 hertz and compares the aircraft response to the maneuver command. The output signal drives the stabilator servoactuator to reduce the maneuver error to zero. This allows the aircraft to to be automatically kept in a hands off condition since the forward integrator eliminates uncommanded normal acceleration. Z filter number P8 is a structural notch filter which operates at 80 hertz. It attenuates aercelastic bending which is detected by the motion sensors.

In the simulation each filter was developed in its most general form for inclusion in a MACRO. The following equation for lead-lag filter number P2 is given in the Schematic Design Report [Bef. 5].

$$\frac{PR4}{PR3} = \frac{(1+FR11*(1-FK12))Z - (PK11+1)*(1-PK12)}{Z - (1-PK12)}$$
(3.3)

It is modeled in the thesis simulation as

$$\frac{POUT}{FIN} = \frac{NZ - B}{Z - C}$$
(3.4)

where A, B and C are constants. The right shifting and linearity properties of the z transform are used to solve explicitly for the variable FOUT [Ref. 6].

$$\frac{POUT}{FIN} = \frac{A - B(Z-1)}{1.0 - C(Z-1)}$$
(3.5)

Cross multiplication and rearrangement yields:

$$POUT = A + FIN - B + FIN(Z-1) + C + POUT(Z-1)$$
 (3.6)

which is described in the simulation as:

$$\mathbf{FOUT} = \mathbf{A} + \mathbf{FIN} - \mathbf{B} + \mathbf{FIN} + \mathbf{C} + \mathbf{FOUTZ} \mathbf{1}$$
(3.7)

This method, which is termed direct realization programming, was also used in the development of the notch filter and the forward loop integrator. The equivalent Laplace transform for each longitudinal flight control filter is listed in the design report. This permitted a cross check of z filter performance which is included in appendix D.

A specific method of integration may be specified in the terminal portion of a CSMF program. In the case of flight control simulation the Runge-Kutta Fixed Step Size (RKSFX) method was utilized to ensure that integrations would only occur at the desired sampling rate. The highest sampling frequency in any axis of the F/A-18 flight control system is 80 hertz, thus the CSMP integration interval DELT was specified as 0.0125 seconds.

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The variable KEEP is used in CSMP to indicate that the end of a valid integration step has been reached. KEEP is set equal to one when this condition is met. During trial or intermediate integration steps KEEP will equal zero. Each MACRC contains conditional logic which allows calculations to be performed only when KEEP equals one.

Conversion of signals from analog to digital form in the F/A-18 occurs as the signal reaches the servomechanism. The guantizer functional block (QNTZ R) is employed to accomplish the analog to digital simulation. The transfer functions used by the stabilator and flap servomechanisms are not published by the manufacturer of the aircraft. In order for the thesis computer program to generate control surface positions the response characteristics of a Parker Hannifin fly-by-wire actuation system were incorporated. The selected actuators were designed for use with all-digital flight controls and are modeled as second order systems. The stabilator transfer function is

$$\frac{x}{Ei} = \frac{1600}{s^2 + 56s + 1600}$$
(3.8)

where X is the actuator position in degrees and Ei is the position command. The transfer function for both leading and trailing edge flaps is

$$\frac{x}{Ei} = \frac{400}{s^2 + 28s + 400}$$
(3.9)

The advantages of using the second order model instead of a first order model are that the faster rise time more closely represents the physical actuators and that the natural frequency and damping ratio may be independently modified.

F. FROGRAM TESTING METHODS AND RESULTS

The thesis computer program was tasted on three levels. The lowest level involved evaluation of the individual signal blocks of figure 16.1 in the system design report. Sections & through E of this chapter describe signal block modeling techniques and appendicies C through G contain testing programs. To obtain verification of proper program operation it was desired to create input signals of realistic value which would produce a time varying output. The rate limiters, for example, were tested with an input signal which rose exponentially to a limiting value, then descreased exponentially. Output signals at the desired frequency were observed for incoming signals of positive or negative slope and for incoming signals which were within or beyond the rate limit.
The second level of program testing involved the nine signal paths which are listed in Table 1. In most cases a step function was used as the input signal. Intermediate signals were observed to determine continuity, frequency, time constants, and conformance with limiting values.

The highest level of program testing required the deneration of control surface deflections for specific combinations of pilot stick force and motion sensor feedback signals. Neither aircraft flight data nor the McDonnell flight control simulation data were available for direct comparison with the outputs of the thesis program. The description of control characteristics given in the design report was used to make a qualitative analysis of program operation.

One specific condition of flight was selected as a basis for comparison of control responses to various combinations of input signals. This "base condition" of flight was defined such that inputs from motion sensors could be superimposed on base condition inputs to enable an investigation of the effects of each parameter. The base condition for program testing was selected to model an



aircraft operating at 20,000 feet and 250 knots. This fixed the values of static and dynamic pressures for each simulation and permitted manual verification of the gains produced by the air data schedules. Additionally, a step function representing six pounds of force in the aft (positive) direction on the control stick was applied at time 0.0 seconds. All motion sensor inputs were held at zero so that their effects could be individually studied. The initial deflections of the stabilator, leading edge flaps and trailing edge flaps were set to zero.



Figure 3.2. Base Condition Leading Edge Flap Response

Figure 3.1 depicts the movement of the stabilator in response to the base flight conditions. A transient oscillation is produced in the first second which results primarily from the second order filter in the pilot stick dynamics path. After this oscillation has decayed the stabilator continues to deflect at a nearly constant rate, since feedback inputs are supressed. The stabilator reaches the limiting value of 24 degrees trailing edge up after 9.6 seconds.



Figure 3.3. Leading Edge Plap Response to AOA Feedback

Figure 3.2 is a plot of leading edge flap deflection (LEFLAP) versus time for the base flight condition. A steady state flap deflection of 4.8 degrees is achieved after 1.5 seconds. Since LEFLAP is a function only of angle of attack, static pressure and dynamic pressure it was desired to observe the variation in LEFLAP with angle of attack. A ramp type increase in angle of attack sensor input (AA1) was superimposed upon the base flight condition beginning at time 1.4 seconds. The resulting schedule of leading edge flap deflection is shown in Figure 3.3 and is consistent with the functional description given in the design report.



Figure 3.4. Base Condition Trailing Edge Flap Response

Trailing edge flap deflection was modeled similarly in Figure 3.4 for the base filght condition and in Figure 3.5 for the condition in which AA1 is a ramp function starting at time 1.4 seconds.

To determine the effect of forward pressure on the pilot stick a step input of -12.0 pounds was superimposed upon the base flight condition at time 4.0 seconds, which simulated an instantaneous reversal of stick force. Figure 3.6 shows that the direction of stabilator deflection changed abruptly upon introduction of the new stick force, and that the



Figure 3.5. Trailing Edge Flap Response to AOA Feedback

magnitude of the steady state rate of stabilator deflection was approximately unchanged. Pilot stick force inputs of larger magnitude were simulated, but the results are not shown. In these cases the rates of stabilator deflection increased while initial oscillatory behavior exhibited characteristics similar to base condition response.

The influence of angle of attack feedback on stabilator position is displayed in Figure 3.7. As described in the design report, angles of attack in excess of 22 degrees will generate a proportional stabilator command causing the nose



Figure 3.6. Stabilator Response to Stick Force Reversal

of the aircraft to pitch down. In this simulation the angle of attack input function AA1 was set equal to 22 degrees plus a ramp type increase of one degree per second starting a- time 4.0 seconds. The resulting stabilator deflection was identical to that for the base condition until time 4.0 seconds due to the action of the -22 degree bias in the AOA feedback path. The nose down pitching effect of AOA feedback is apparent at all later times. In response to the base condition the stabilator had reached its maximum limit of 24 degrees trailing edge down at time 9.5 seconds. The application of AOA feedback restricted stabilator deflection to 20.9 degrees at time 9.5 seconds.



Figure 3.7. Angle of Attack Damping

Normal acceleration damping is shown in Figure 3.8. A ramp increase in normal acceleration equal to 1.0 g's per second was superimposed on the base condition at time 4.0 seconds. The cutput signal varies smoothly due to the fact that normal acceleration path outputs are processed by the forward integrator path. The stabilator reverses its direction of movement within one second of the time feedback is introduced. In this case the stabilator reached its limit of deflection of 10.5 degrees trailing edge down at time 9.0 seconds.



Figure 3.8. Normal Acceleration Damping

According to the design report, the predominant contribution to pitch damping is generated by the pitch rate gyro path. This is because the pitch rate signal PR5 is summed with the main path signal MP9 downstream of the forward integrator as shown in Figure I. Figure 3.9 depicts stabilator position versus time for a ramp increase in pitch rate beginning at time 4.0 seconds. The change in direction of stabilator movement is much more rapid than that produced by angle of attack or normal acceleration damping.



Figure 3.9. Pitch Rate Damping

The thesis computer program used a significant portion of allowable CSMF program size. Table IV, which is extracted from the "translation table" section of CSMP output, lists the areas of the program which most closely approached the size limits of CSMP.

TABLE IV

Program Size Restrictions

bātamētēt	<u>Program</u>	CSMP <u>Maximum</u>
MACRO and statement outputs Statement input work area Parameters-function generators History and memory block names MACRO statement storage SCRT sections Maximum statements in section	206 422 43 21 85 171	600 1900 50 125 600

IV. CONCLUSIONS AND RECOMMENDATIONS

The computer program developed in this thesis simulates the performance of the longitudinal flight control system of the F/A-18 aircraft by generating control surface deflections and cross axis electrical signals. The responses to AOA, normal acceleration and pitch rate feedback correspond to the descriptions given by the aircraft manufacturer.

The use of CSMP simplified the task of system modeling by provision of pre-programmed functional blocks and a flexible format for outputs. The CSMP program size restrictions do not appear to be a factor which would prohibit the addition of the lateral and directional flight control systems to this simulation. It is recommended that

future expansion of this program be done using techniques to conserve program size. FORTRAN subroutines and computer library functions should be utilized due to the limited MACRO and NOSORT capabilities of the CSMP translator.



FLIGHT CONTROL SYSTEM COMPONENT PATHS

PILOT STICK INPUT PATH



46



Sec. Sec.

PITCH RATE GYRO PATH

ANGLE OF ATTACK SENSOR PATH

N93000



48

NORMAL ACCELEROMETER PATH

a.



49





STABILATOR PATH



LEADING EDGE FLAP PATH

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Υ.

TRAILING EDGE FLAP PATH

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ALL CONTRACTS





AFPENDIX E

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FLIGHT CONTROL COMPUTER PROGRAM
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S. 1.1.

******* ****** ** ** ** LONGITUDINAL FLIGHT CONTROL LAW SIMULATION ** ** ** **************** **** ************ * *****INTEGRATING, LAG, AND NOTCH DIGITAL FILTERS***** MACRC 10 ENDMAC MACRO_FOUT=ZLAG(FIN, KA, KB, KC, IMP, FINZ1, FOUTZ1) FROCEDURAL IF(IMP.NE. 1.0) GO IF(KEFP.NE.1.0) GC IF(TIMF.EQ.0.0) GO FOUT=KA*FIN - KB*FI FOUTZ1=FOUT FINZ1=FIN GO TO 10 ŤŎ ŤŎ 10 10 - KB*FINZ1 + KC*FOUTZ1 10 CONTINUE ENDMAC MACRC FOUT=ZNOTCH (FIN,KA,KB,KC,KD,KE,IMP,FINZ1,... PINZ2,FOUT21,FOUTZ2) FROCEDURAL IF (IMF.NE. 1.0) GC TO 10 IF (KEFP.NE. 1.0) GC TO 10 IF (TIME.EC.0.0) GC TO 10 FOUT=KA*FIN+KB*FINZ1+KC*FINZ2-KD*FOUTZ1-KE*FOUTZ2 FOUTZ2 = FCUTZ1 FOUTZ1 = FCUT FINZ2 = FINZ1 FINZ1 = FIN 10 CONTINUE ENDMAC CONTINUE 10 *********** * *****PREQUENCY AVERAGER S** *** * TO Z40=AV2040 (Z20,IMP) FROCEDURAL IF (KEFP.NE.1.0) GO TO 20 IF (TIME.NE.0.0) GO TO 20 Z2021=Z20 Z4021=Z20 DEL=0.0 GO TO 10 IF (IMP.E0.1.0) GO TC 10 Z40=Z4021+DEL GO TC 15 Z40=Z20 DEL=(Z20-Z20Z1)/2.0 Z20Z1=Z20 Z40Z1=Z40 CONTINUE MACRO 5 10 15 20 CONTINUE

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ENDHAC MACRO Z80=AV4080(Z40,IMP) FROCEDURAL IF (KEEP.NE.1.0) GO IO 20 IF (TIHE.NE.0.0) GO TO 5 Z40Z1=Z40 DEL=0.0 GO TC 10 5 IF (IMP.EQ.1.0) GO TC 10 Z80=Z80Z1+DEL GO TO 15 10 Z80=Z40 DEL=(Z40-Z40Z1)/2.0 GO 10 20 GO TO 5 DEL = (240-24021) /2.0 24021=240 28021=240 CONTINUE CONTINUE 15 CONTINUE 20 CONTINUE ENDMAC MACRC 280=AV2080(220, IMP) EPOCEDURAL C Z80=AV208C(Z20, IMP) FROCEDURAI IP (KEEP.NE.1.0) GO TO 20 IF (TIME.NE.0.0) GO TO 5 Z20Z1=Z20 GO TO 10 IF (IMP.EQ.1.0) GO TC 10 Z80=Z80Z1+DEL2 GO TC 15 Z80=Z20 DEL2=(Z2C-Z20Z1)/4.0 CONTINUE Z20Z1=Z80 CONTINUE 5 10 15 20 EN DM AC * * * DYNAMIC SORT * ******** *****INFUTS***** *************** * PS1=6.0*STEP(0.0) PR1=0.0 AA1=22.0+RAMP(4.0) NZ1=0.0 RR1=0.0 YR1=0.0 * * ********** *****FUNCTIONS REQUIRING NOSORT***** *

****** ****FUNCTION 12**** NCSORT IF(.NOT.RI.GT.0.5) F12 = F12T4 GO TO 6 CONTINUE F12 = F12T3 CONTINUE T GO TO 5 5 6 SORT NCSORT (.NOT.RI.GE.0.75) GO TO 20 F40T9=F40T8 * F40T1 GC TO 30 ĪF CONTINUE F40T9=F40T8 20 30 CONTINUE (.NOT.(QC.GE.440.C.AND.RI.GE.0.75)) GO TO 40 F40T10=F40T6 GO TO 50 ĪF CONTINUE F40T10=F40T7 40 CONTINUE 50 (.NOT.PIQ.GT.980.C) GO TO 60 F40=F40T9 GO TO 80 IF CONTINUE IF (.NOI.PIO.LE.500.C) GO TO 70 F40=F40T5 GO TO 80 60 GC TO 80 CONTINUE F4C=F40110 70 80 CONTINUE *********SORTED FUNCTIONS***** ************** ****** SCRT *********** *****PUSCTICNS 22,23***** F22=0.0167*(-800.0+LINIT(800.0,900.0,QC))

F23=3.1435-0.1429*LIMIT (15.0,21.998, ALPHAT) ************* *****PUNCTIONS 25,27,28,290,29L,32A,37**** **** $F25=47.636-0.05106 \times LIMIT(600.0,835.0,QC)$ P27=1.328+ (ALPHAT+7.6584-17.86+LIMIT(0.44,0.63,RI)) P28=44.551-0.04058*LIMIT(260.0,950.0,QC) F29U=87.3825-76.25*11MIT(0.7,1.146.RI) F291=0.0 QKF=LIMIT(200.0,2000.0,QC) F32A=100.0/QKF F37=2.5-0.5*LIMIT(3.C,5.0,NZA)************** *****PUNCTIONS 68,107**** ****** F68=-0.002977*(-480.C+LIMIT(250.0,480.0,QC)) F107T1=((-5.714E-7) *ABS (OC-750.0)) + (8.4E-4) F107=LIEIT (0.0, 10000.0, F107T1) ********* *****INPULSE PUNCTICNS***** IMP 20= IMPULS (0.0, RA TE20) IMP 40= IMPULS (0.0, RA TE40) IMP 80= IMPULS (0.0, RATE80) * * * * * * * * * * * * * * * * * * ***PK FUNCTIONS ***** **************** ********* *****PILOT STICK INFUT PATH***** 士士 PS2=DEADSP(-2.0,2.0,ES1) PS3=CMPXPL(PS3IC1,PS3IC2,0.14,27.3,PS2*106.47) PS4=BEAIPL(PS4IC1,7.9365E-3,PS3) PS5=ZHCID(IMP80,PS4) PS5=2HCLD(1HECC, LC, PS6=PS5 PS7=PS6*(7.0+(0.2*AES(PS6))) PS8=LIMIT(-25.0,50.0,PS7) PV3=PS8 PV3A=ZHOLD(IMP20,PV3) PS9=FV3*F32A *****PITCH RATE GYRC PATH****

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********* *****ANGIE OP ATTACK SENSOR PATH**** AA2=CMPXFL (AA2IC1, AA2IC2, 0.74, 209.0, AA1*43681.0) AA3=2HOID (IMP40, AA2) ALPHAT=0.59*AA3+1.9 ALPHAS=AIPHAT AA6=ALPHAT-22.0 AA7=IIMIT (0.0, 10000.0, AA6) PV1=PK17*AA7 PV1A=2HCID (IMP20 PV1) PVIA=ZHCID (IMP20, PV 1) *****NORBAL ACCELERCHETER PATH***** NZ2=CMPXFL (NZ2IC1, NZ2IC2, 0.89,200.0, NZ1*40000.0) NZ3=ZHOID (IMP40, NZ2) N 23 = 2H01D (1 MP40, N22) N 2A = N23 N 24 = N23 - (RR 1**2)* (6.8529E-6) P5A = (1.0+PK9*(1.0-PK10)) P5B = (1.0+PK9)*(1.0-FK10) P5C = 1.0-PK 10 N 25 = 2LAG (N24, P5A, P5E, P5C, I MP40, N2421, N2521) PV2 = LIMIT (-10.0, 10.0, N25) N 27 = FV2*FK 16 PV2 + ZFW1D (TMP20, PV2) PV2A=2HOLD (IMP20, PV2) *****FORWARD INTEGRATOR PATH***** FI2=MP3
FI3=IIMIT(-10000.0,0.0,PI2)
FI4=LIMIT(0.0,10000.0,FI2)
FI5=(FI3*F23)+FI4
FI6=ZINT(FI5,0.05,1.C,IMP20,FI6Z1) MP11=LIMIT(-25.0,25.C,MP10) MP12=ZNCTCH(MP11,P8A,P8B,P8C,P8D,P8E,INP80,... MP1121,MP1122,NP1221,MP1222) MP13=MF12-F\$9 *********** *****STAEILATOR PATH****

ST2=IIMIT(-24.0,10.5,NP13) ST3=ST2-DS1 ST4=LIMIT(-24.0,10.5,ST3) ST5=CNT2R(0.0125,ST4) STADEF=CMPXPL(TEDIC1,TEDIC2,0.7,40.0,ST5*1600.0) ****** *****LEADING EDGE FLAP PATH**** LE2=ZINT (ALPHAS, 0.0625, 0.9375, IMP80, LE2Z1) LE2T=ZHCLD (IMP20, LE2) LE2T 1A=IE2T*F27 LE2T 1=ZHCLD (IMP20, LE2T1A) LE2T2=LIMIT (0.0, F28, LE2T1) LE3=LIMIT (C.0, F29U, LE2T2) ***** 18 DEG/SEC RATE LIMIT, 20 HZ***** NO SORT)RT IF (TIME.NE.0.0) GO TC 85 LE4Z 1=LE3 CONTINUE DEL 1=LE3-LE4Z 1 DELIIE=IIMIT(-0.9,0.9,DEL1) LE4A=LE4Z 1+DELLIM IF (IMP20.NE.1.0) GC TO 90 LE4Z 1=LE4A CONTINUE LE4=ZHOLD(IMP20,LE4A) ****************** 85 90 SORT LE5=AV2080 (LE4, IMP20) DLE2P1=LE5+3.0 DLE2=DEADSF (-DLE2P1, DLE2P1, DLE1) DLE3=DLE1+DLE2 NOTE: POSITIVE LEADING EDGE FLAP PATH ONLY LE6=LE5+DLE3 LE7=LIMIT(-3.0,33.0,LE6) *****18 DEG/SEC RATE LIMIT, 80 HZ***** NOSORT JAT IF (TIME.NE.0.0) GO TO 110 LE8 Z1=LE7 CONTINUE DEL1=LE7-LE8Z1 DELLIM=LIMIT(-0.225,C.225,DEL1) LE8 A=LE8Z1+DELLIM IF (IMP80.NE.1.0) GO TO 120 LE8 Z1=LE8A CONTINUE 110 CONTINUE 120 LE8=ZHCID (INP80, LE8 A) SORT LE9=LIMIT(-3.0,33.0,1E8) LE10=CNTZR(0.0125,LE9) LEFLAP=CMPXPL(LEDIC1,LEDIC2,1.4,20.0,LE10+400.0) *****NOSCRT FUNCTION 24 ***** F24L1=22.538-20.51*LIMIT(0.27,0.66,RI) F24L2=32.76-36.0*LIMIT(0.66,0.91,RI) F24T1=LIMIT(0.0,10000.0,ALPHAT) F24T2=AIFHAT-(14.8769-7.6923*LIMIT(0.27,0.91,RI)) F24T3=-2.0*LIMIT(0.0,10000.0,F24T2) F24T4=1.4*(F24T1+F24T3) NOSORT

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AFPENDIX C

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AIR DATA SCHEDULES

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LABEI FUNCTION 12
INITIAL
                 PARAMETER PS= (2000.C, 1000.0, 500.0, 200.0)
*
DY NAMIC
SCRT
       SCRT

RI=RAMP(0.0)

F12T1=RI**2*9.625-.025*RI+1.0

F12T2=PS*7.969E-4+0.84

F12MAX=IIMIT(1.0,8.0,F12T2)

F12T3=LIMIT(1.0,F12MAX,F12T1)

F12T5=LIMIT(5,1.35,RI)

F12T6=F12T5*(0.00952*PS+4.04) +(-PS*0.00396-1.18)

F12T4=LIMIT(1.0,8.0,F12T6)

NCSORT

IF(.NOT.RI.GT.0.5) GO TO 5

F12 = F12T4

GO TO 6

CONTINUE
              CONTINUE
P12 = F12T3
CONTINUE
5
6
TERM INAL
              TIMER FINTIM =2.0, CUTDEL=0.05, PRDEL=0.05
PRINT P12T1,F12T2,F12T3,F12T4,F12T5,F12T6,F12MAX,F12
PAGE XYFICT
OUTPUT RI, F12
          END
STOP
Endjcb
LABEL FUNCTION 24
INITIAL
               PARAMETER RI= (0.27,0.66,0.91)
±
DYNAMIC
        SCRT
              ALPHA=RAMP(0.0)

F2411=22.538-20.51*LIMIT(0.27,0.66,RI)

F24L2=32.76-36.0*LIMIT(0.66,0.91,RI)

F24T1=LIMIT(0.0,10000.0,ALPHA)

F24T2=ALPHA-(14.8769-7.6923*LIMIT(0.27,0.91,RI))

F24T3=-2.0*LIMIT(0.0,10000.0,F24T2)

F24T4=1.4*(F24T1+F24T3)
        NOSORT
              IP (.NCT.BI.GT.0.66) GO TO 10
F24L=F24L2
GO TO 15
CONTINUE
P24L=F24L1
CONTINUE
10
15
               P24=IINIT (0.0, P24 L, P24T4)
TERMINAL
              TIMER FINTIM=30.0,00TDEL=0.5,PRDEL=0.5
PRINT F24L1,F24L2,F24T1,F24T2,F24T3,F24T4,F24L,F24
```

STEPH AND IN

```
PAGE XYFICT
OUTPUT ALPHA, F24
END
SIOP
ENDJCB
```

```
LABEL FUNCTION 29
INITIAL
             PARAMETER FS= (1250.0)
1
DYNAMIC
             RI = RAMP(0.0)
             F29U=87.3825-76.25*LIMIT(0.7,1.146,RI)
F29L=0.0
TERMINAL
             TIMER FINTIM=1.5, PRDEL=0.05, OUTDEL=0.05
PRINT RI,F29U,F29L
PAGE XYFLOT
OUTPUT RI,F29U,F29L
          END
         SIÕP
ENDJÖĒ
LABEI FUNCTION 40
INITIAL
             PARAMETER OC= (1700.0,440.0,335.0)
DY NAMIC
SCRT
              PS = 0.0001 + RAMP(0.0)
              RI=0C/PS
F40T1=3.
             RI=QC/PS

P40T1=3.25-3.0*LIMIT(0.75,0.85,RI)

F40T2=0.65625-.0013125*LIMIT(500.0,1800.0,PS)

F40T3=-0.26177+(9.635E-4)*LIMIT(500.0,1800.0,PS)

PIO=LIMIT(0.0,1800.0,PS)

F40T4=LIMIT(0.0,1800.0,PS)

F40T5=(F40T4*(-PIQ*(1.17428E-6)+(1.5238E-3)))...

+0.475-(6.5E-4)*PIQ

F40T6=F40T3+(F40T2*LIMIT(0.75,0.85,RI))

F40T7=F40T4*(5.6746E-4+1.9841E-7*PIQ)...

+(0.16923-3.84615E-5*PIQ)

F40T8=(0.16923-3.86415E-5*PIQ)...

+(F40T4*(1.67247E-3-9.29152E-7*PIQ))
       NCSORT
                     (.NOT.RI.GE.0.75) GO TO 20
F40T9=F40T8*F40T1
GO TO 30
              IP
             CONTINÜE
P4019=P4018
20
30
              CONTINUE
                     (.NOT. (QC. GE.440.C.AND. RI.GE.0.75)) GO TO 40
F40T10=F40T6
GQ TQ 50
              ĬĒ
```

(.NOT.PIQ.GT.980.0) GO TO 60 P40=F40T9

CONTINUE F40T10=F40T7

CONTINUE

IF

the latest

40

GO TO 80 CONTINUE (.NOT.PIO.LE.500.C) GO TO 70 F40=F40T5 60 GO TO 80 CONTINUE 70 F40=F40T10 CONTINUE 80 TERMINAL TIMER FINTIM=2000.0, PRDEL=20.0, OUTDEL=20.0 PRINT PS,QC,RI,F40T7,F40T9,F40T10,F40 PAGE_XYFIOT OUTPUT PS, F40 enď STOP ENDJCB LABEL GENERATED FUNCTION THIS FROGRAM GENERATES QC FUNCTIONS 22,25,28,32A, AND 68 IN IT IAL PARAMETER FS=1000.0 DYNAMIC QC = RAMP(0.0)F22=0.0167* (-800.0+LIMIT(800.0,900.0,QC)) F25=47.636-0.05106*LIMIT(600.0,835.0,QC) P28=44.551-0.04058*LIMIT(260.0,950.0,QC) QKF=LIMIT(200.0,2000.0,QC) F32A=100.0 / QKF F68=-0.002977 * (-480.0+LIMIT(260.0,480.0,QC)) TERMINAL TIMER FINTIM=2500.0, OUTDEL=50.0, PRDEL=50.0 PRINT OC,F22,F25,F28,QKF,F32A,F68 PAGE XYFLOT CC, F22 CC, F25 CC, F28 CC, F324 CC, F68 ÓÜŤĒUŤ OUTPUT OUTFUT OUTPUT ŌŪŦŦŪŤ END SIOP ENDJÕĒ LABEL GENEBATED FUNCTION IN IT IAL PARAMETER RI = (0.44, 0.63)DYNAMIC ALPHA= BANF (0.0) NZA=ALPHA ÄLPHÄT=ÄIPHA P23=3.1435-0.1429*LINIT (15.0,21.998, ALPHAT)

42

```
F27=1.328*(ALPHA+7.8584-17.86*LIMIT(0.44,0.63,RI))

* F37=2.5-0.5*LIMIT(3.0,5.0,NZA)

*

TERMINAL

TIMER FINTIM=30.0, FRDEL=1.0, OUTDEL=1.0

PRINT F23,F27,F37

PAGE XVFIOT

OUTPUT ALPHAT,F23

OUTFUT ALPHA,F27

OUTPUT NZA,F37

END

STOP

ENDJCE
```

AFPENDIX D

COMPUTER PROGRAMS FOR SIGNAL BLOCK TESTING

CEADSPACE FUNCTION AND ALIASING FILTERS

```
*
INITIAL
CONSTANT FS2C1=2.0
*
DY NAMIC
FS1= -5.0 + RAMP(0.0)
FS2=DFADSP(-PS2C1,PS2C1,PS1)
FS3=CMPXPI(0.0,0.0,0.14,27.3,PS2*106.47)
PS4=REALPL(0.0,7.9365E-3,PS3)
*
TERMINAL
TIMER FINTIM=10.0, OUTCEL=0.25, PRDEL=0.25
LABEL STICK DEADSPACE FLOT
FRINT FS1,FS2
OUTPUT TIME, PS1,PS2,FS3,PS4
FAGE XYPLCT
FND
SIOP
EN DJ CE
```

FREQUENCY AVERAGERS MACRO Z80=AV2080(Z20, IMP) FROCEDURAL IF (TIME.NE.0.0) GO IO 52 22021=220 28021=220 Z80Z1=Z20 G0 TC 54 IF (IMP.EQ.1.0) G0 TC 54 Z80=Z80Z1+DEL2 G0 TO 56 Z80=Z20 DEL 2=(Z2C-Z20Z1)/4.0 CONTINUE Z20Z1=Z20 Z80Z1=Z80 52 54 56 EN DM AC INITIAL CONSTANT RATE=0.05DYNAMIC TERMINAL METHOD RKSFX TIMER FINTIM= 1.0, OUTCEL=0.0125, PRDEL=0.0125, DELT=0.0125 LABEL 20 TO 80 AVERAGER PRINT Y2, Y3, Y4 END SIOP SIGF EN DJ OE MACRC Z40=AV2040 (Z20,IMP) FROCEDURAL IF (TIME.NE.0.0) GO TO 5 Z20Z1=Z20 Z40Z1=Z20 DEL=0.0 $\begin{array}{c} 24021=220\\ \text{DEL}=0.0\\ \text{GO TC 10}\\ \text{IF (IMP.EQ.1.0) GO TO 10}\\ 240=24021+\text{DEL}\\ \text{GO TO 15}\\ 240=220\\ \text{DFT}=(720-72071)/2.0\\ \end{array}$ 5 10 $\vec{DEL} = (\vec{Z} \ \vec{2} \ 0 - \vec{Z} \ 2 \ 0 \vec{Z} \ 1) / 2 \cdot 0$ $\vec{Z} \ 0 \ \vec{Z} \ 1 = \vec{Z} \ 0$ $\vec{Z} \ 4 \ 0 \ \vec{Z} \ 1 = \vec{Z} \ 4 \ 0$ CONTINUE 15 EN DH AC INITIAL RATE=0.05CONSTANT DYNAMIC IC Y 1= RAMP (0.0) Y 1A=EXP (Y 1) Y 2= INPULS (0.0, RATE) Y 3= ZHOIE (Y 2, Y 1 A) Y 4= A V 2040 (Y 3, Y 2) TERMINĂL METHOD RKSFX TIMER FINTIM= 1.0, OUTDEL=0.025, PRDEL=0.025, DELT=0.0125 LABEL 20 TC 40 AVERAGER PRINT Y1, Y1A, Y2, Y3, Y4 PAGE XYFLOT ÖÜTPUT TIME,Y1A,Y3, Y4 FND SIOP ENDJÕĒ

RATE LIMITERS

IN IT IAL
$\begin{array}{c} \text{Y1=RAMP}(0.0) \\ \text{Y1=RAMP}(1.0) \end{array}$
$Y_3 = LINIT(C.0, 3.0, Y2)$
Y4= (-1.0+EXP(-TIME+2.0)) *5 TEP (2.0) Y5=Y3+Y4
IMP20=IMFULS(0.0,0.05) IMP40=IMPULS(0.0,0.025)
IMP80=IMFUIS(0.0, 0.0125) Y20=7HOID(IMP20, Y5)
$\dot{Y}40 = ZHCID$ \dot{I} $\dot{I}P40$, $\dot{Y}5$
NOSOBT
LE821=Y20
BU CONTINUE DEL1=Y20-LE8Z1
DELLIM=LIMIT(-0.1,0.1,DEL1) LE8=IE8Z1+DELLIM
IF (IMP20.NE.1.0) GO TO 90 LE821=IF8
90 CONTINUE TERMINAL
METHOD RKSFX
PRINT_Y3, Y4, Y5, DEL1, DELLIM, LE821, LE8, Y20, IMP20
PAGE XYFICT OUTPUT TIME,Y20,LE8
OUTPUT TIME,Y20,LE821 END
ŜĪŌP En djob

. . .
DIGITAL FILTERS

MACRC FOUT=ZINT (FIN, KA, KB, IMP, FOUT21) C FOUT=ZINT(FIN, KA, KB, I FROCEDURAI IF(IMP.NE. 1.0) GO TO 1(IF(KEFP.NE.1.0) GC TO 1(IF(TIME.EQ.0.0) GO TO 1(FCUT=KA*PIN + KB*POUTZ1 FOUTZ1=FOUT CONTINUE 10 10 10 10 ĖŇ DM ĂČ MACRC FOUT=2LAG (FIN, KA, KB, KC, IMP, FINZ 1, FOUTZ 1) FRÒCEDURAL IF (IMP.NE. 1.0) GC TO IF (KEEP.NE. 1.0) GO TO IF (TIME.EQ.0.0) GC TO FOUT=KA*FIN - KB*FINZ1 10 10 10 KC*FOUT21 ÷ FOUTZ1=FOUT FINZ1=FIN CONTINUE 10 EN DH AC MACRC FOUT=ZNCTCH (FIN, KA, KE, KC, KE, KE, IMP, ... FINZ1, FINZ2, FOUIZ1, FO UTZ2) FROCEDURAI IF(IMP.NE. 1.0) GO TO 10 IF(KEFP.NE.1.0) GC TO 10 IF(TIMF.EQ.0.0) GO TO 10 POUT=KA*FIN+KB*PINZ1+KC*FINZ2-KD*FOUTZ1-KE*FOUTZ2 FOUTZ2 = FOUTZ1 FOUTZ1 = FCUT FINZ2 = FINZ1 FINZ1 = PIN CONTINUE 10 CONTINUE ENDMAC IN IT IAL PA RAM INTN=.0125, INTD=1.0, AA11A1=0.0, AA11A=0.0 PARAM PK9=-1.1543, PK 10=0.4647, AA10B1=0.0, AA11B1=0.0 PARAM NA=.69084, NB=-.99068, NC=.66312, ND=-.99068, NE=.35396 PARAM AA 10C1=0.0, AA 10C2=0.0, AA11C1=0.0, AA11C2=0.0 LA=(1.+PK9*(1.-PK10)) LB=(1.+PK9)*(1.-PK10) LC=1.-PK10 DY NAMIC AA9A = STEF(0.0) IM P= IMPULS (0.0, 0.0125) AA 10A = ZHCLD (IMF, AA9A) AA 11A = ZINT (AA10A, IA TN, INTD, IMP, AA11A1) AA 11B=ZLAG (AA10A, LA, LB, LC, IMP, AA10B1, AA11B1) AA 11C = ZNOTCH (AA10A, NA, NB, NC, ND, NE, IMP, AA10C1,... AA10C2, AA11C1, AA11C2) TERMINAL TERMINAL 10 TERMINAL METHCD RKSFX TIMER FINTIN=1.5, OUTDEL=0.0125, DELT=0.0125 PRINI AA10A, AA11A, AA11B, AA11C OUTPUT AA10A(0.0, 1.5), AA11A(0.0, 1.5), ... AA11B(0.0, 1.5), AA11C(0.0, 1.5)

ENDJOB

interiore in the second

 $i \in \mathcal{N}$

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