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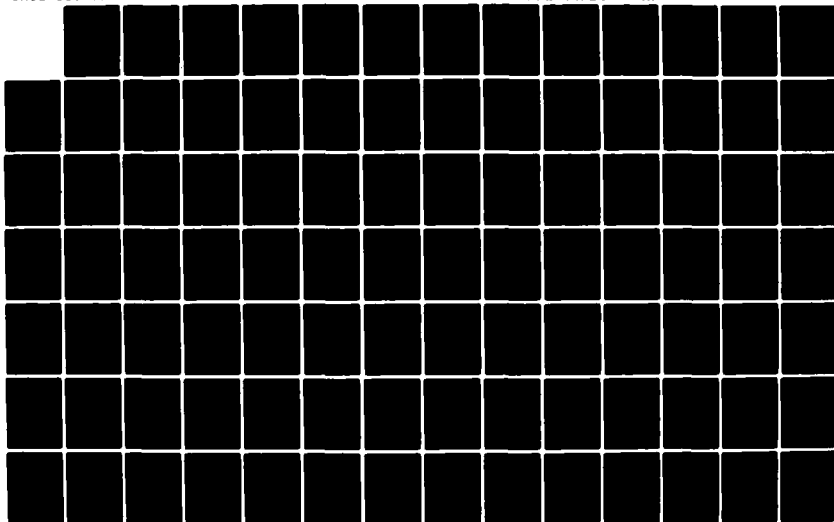
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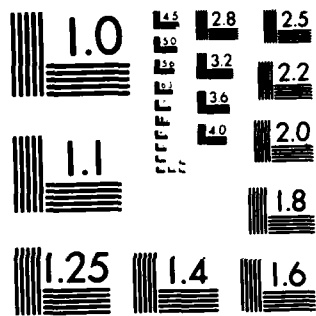
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## A Microcomputer-Based Sampling Digital Voltmeter

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<p>This report describes a sampling digital voltmeter developed at the Underwater Sound Reference Detachment of the Naval Research Laboratory. It consists of a PDP-11/23 microcomputer with up to three channels of analog-to-digital converters capable of making simultaneous measurements on pulsed or continuous sinusoids at frequencies up to 100 kHz. The voltmeter is controlled by ASCII commands on the IEEE-488 bus. Statistical parameters (mean, peak, standard deviation, and second joint moment) are computed using numerical integration. Sinusoidal parameters (amplitude, phase, and harmonic (continued)</p>		

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distortion) are computed using single-frequency discrete Fourier transforms (SFDFT). The digital filtering resulting from the use of the SFDFT can be further enhanced by optional data sequence weighting (multiplying the sequence by a given windowing sequency). Computation times may be reduced by optional subsequence averaging. Complete assembly language listings for the voltmeter program are included and described.

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## A MICROCOMPUTER-BASED SAMPLING DIGITAL VOLTMETER

### INTRODUCTION

Acoustical measurement systems at the Underwater Sound Reference Detachment (USRD) of the Naval Research Laboratory need a specialized multiple-channel voltmeter with the capability to measure voltage, relative phase between channels, and harmonic distortion for pulsed sinusoids at frequencies between 0.1 Hz and 100.0 kHz. To answer this need, a sampling digital voltmeter was developed around an LSI-11/23 microcomputer, using three analog-to-digital (A/D) converters and an IEEE-488 interface to serve as the communications link with any appropriate host computer. Using this voltmeter, a "simple" computer-controlled measurement system can then be configured with the following auxiliary equipment as shown in Fig. 1: synthesizers to generate a sinusoidal test signal and a coherent sampling signal, and programmable amplifiers or attenuators as needed. (The voltmeter has no intrinsic autoranging capability.)

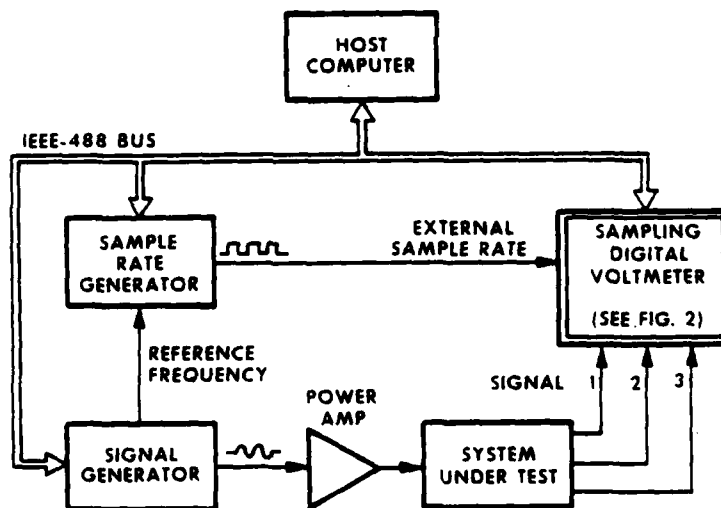


Fig. 1 - Typical measurement system using the microcomputer-based sampling voltmeter.

### APPLICATIONS

The two immediate applications for such a voltmeter were for the USRD's Anechoic Tank Facility (ATF) and Low-Frequency Facility (LFF) measurement systems.

The ATF high-power measurement system requires a three-channel voltmeter to measure simultaneous projector drive voltage, drive current, and receiver output (hydrophone voltage). Measurements of relative phase between voltage and current and the harmonic distortion in all three channels are necessary. The practical frequency range is 2 to 100 kHz; the mode of operation is usually pulsed.

The LFF system requires a two-channel voltmeter to measure the simultaneous voltage outputs of two hydrophones, generally a probe standard and an unknown hydrophone. Relative acoustic phase is required, and harmonic distortion is a useful parameter to monitor. The practical frequency range is 0.1 Hz to 4 kHz; the mode of operation is continuous wave (cw).

The voltmeter was, therefore, conceived to be a device independent of (and transportable between) specific measurement systems at the USRD, with the possible exception being that the A/D converters might vary with application (e.g., a need to operate at higher sampling rates) and are otherwise functionally identical plug-in modules. To get the widest possible range of potential host computers, communication through the IEEE-488 bus was chosen. Another requirement was that all external hardware, including the sample-rate generator, be controlled by the host computer for maximum flexibility. Therefore, all external functions, including automatic ranging of the voltmeter, are controlled by the host.

Since initiating and implementing this signal processing sampling voltmeter, two other similar instruments have been described or released: the low-frequency sampling voltmeter by the National Bureau of Standards [1] and the sampling network analyzer by the Dranetz Company [2]. The three instruments share the sampling ideas but differ in the features and algorithms implemented.

#### **THEORY OF OPERATION**

The voltmeter consists of a microcomputer containing up to three A/D converters with direct memory access (DMA) capability (Fig. 2). This configuration allows the computer to digitize up to three input waveforms simultaneously, sampling them when triggered by a common external sample-rate generator. Where simultaneous measurement of all channels is not desired, separate or delayed sampling signals can be given to individual A/D converters. The present voltmeter is designed to digitize three waveforms of up to 1024 samples each; these buffers could be expanded to 3900 samples (see Appendix F).



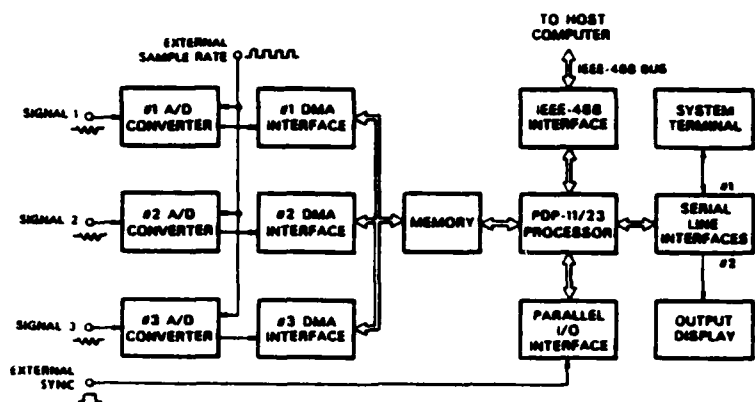


Fig. 2 - Block diagram of the microprocessor-based sampling voltmeter.

The DMA interface loads 12-bit digitized values directly into memory at rates up to 100 kHz per channel. The individual sampled waveforms can be transmitted to the host for processing; alternately, the slave can compute sinusoidal and statistical parameters from these waveforms and return them to the host. Parameters are computed for the fundamental frequency. (Throughout this report "fundamental" frequency refers to the sinusoidal test frequency being measured, no matter how many cycles of this frequency are included in a measured sequence.)

The sinusoidal quantities amplitude, phase, and harmonic distortion are computed for each channel using single frequency discrete Fourier transforms (SFDFT). The statistical quantities average, standard deviation, and the positive and negative extrema are obtained for each channel to characterize noisy or nonsinusoidal signals. In voltmeter terms, these statistical parameters are the dc, true ac rms, and positive and negative peak voltages. To measure the coherence or correlation between channels, the second order joint moment statistic,  $E(xy)$ , can be computed. For the case of the two signals,  $x$  and  $y$ ; proportional to current and voltage, the joint moment is proportional to power. The covariance and cross correlation can be readily obtained from these parameters.

## COMPUTATION PROCEDURES

### Sampling Criteria

In the usual case of interest (pulsed sinusoids), sampling errors are minimized through coherent sampling, which requires an integer number of both test frequency cycles and sampling periods:

$$\frac{m}{f_T} = \frac{n}{f_S} \quad (1)$$

where  $f_T$  is test frequency  
 $f_S$  is sampling frequency  
 $n$  is samples per sequence (integer)  
 $m$  is cycles per sequence (integer).

If the sampling frequency,

$$f_S = \frac{n}{m} f_T \quad (2)$$

is greater than twice the test frequency, the Nyquist theorem is satisfied, and satisfactory results will be obtained. If the necessary sampling rate is too fast for the A/D converter to operate properly, undersampling can be used; by choosing fewer than two samples per cycle while retaining an integer number of samples and cycles per sequence, an effective higher sampling rate is attained. A comparison of the oversampled waveform shown in Fig. 3 with the undersampled waveform in Fig. 4 clearly demonstrates how undersampling can synthesize a waveform (Fig. 5) effectively sampled at a higher rate, although with fewer cycles overall. Undersampling will only work properly on periodic waveforms, however. Computed data in Table 1 demonstrates that peak voltage and harmonic distortion may be affected slightly by undersampling.

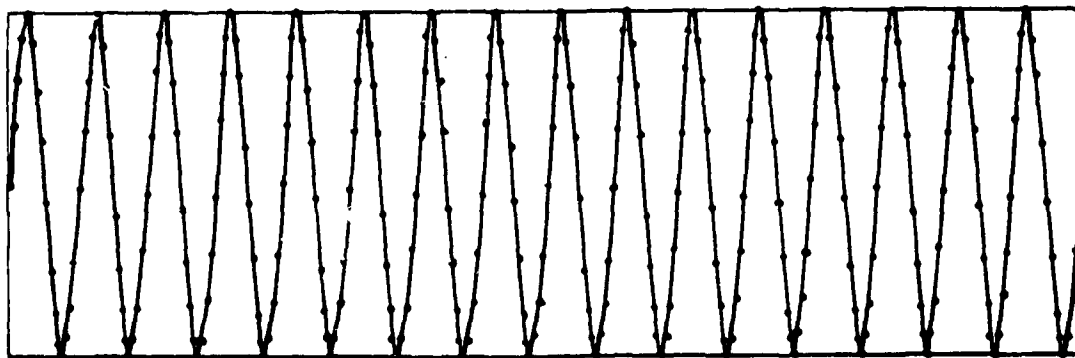


Fig. 3 - Steady-state sinusoidal waveform sampled for 16 cycles at 17 samples per cycle: 272 samples (oversampling).

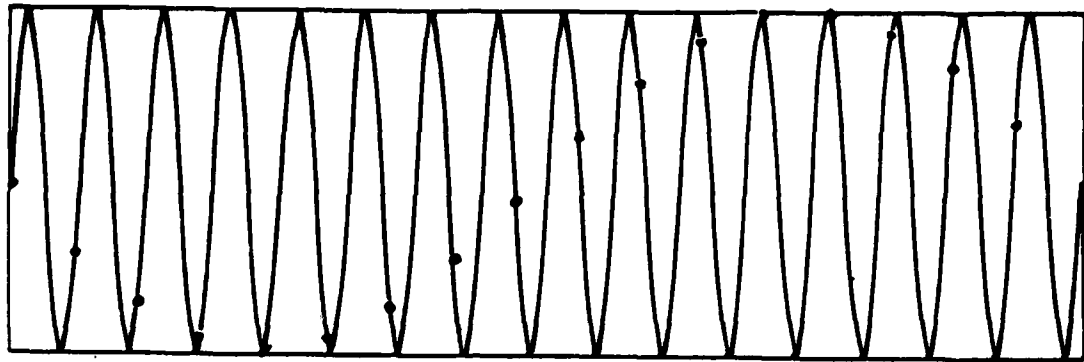


Fig. 4 - Steady-state sinusoidal waveform sampled for 16 cycles at 1.0625 samples per cycle: 17 samples (undersampling).

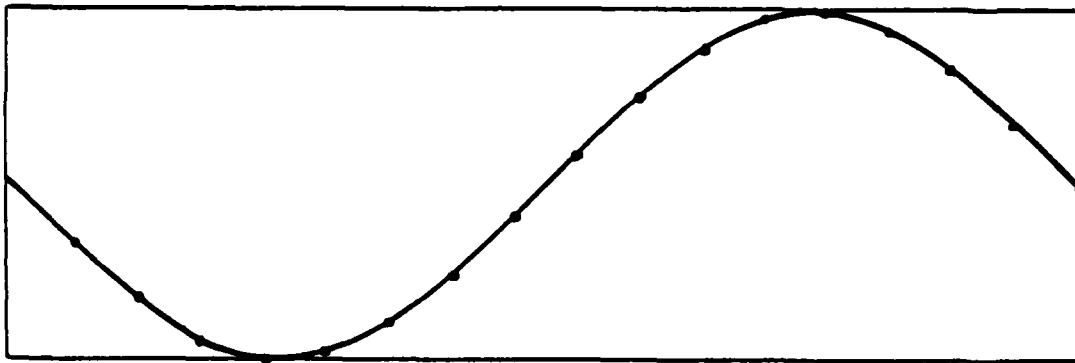


Fig. 5 - Steady-state sinusoidal waveform synthesized from the 17 samples shown in Fig. 5.

Table 1 - Comparison of oversampled and undersampled measurements.

COMPUTED PARAMETER	OVER SAMPLING	UNDER SAMPLING
RMS VOLTAGE (STD DEV)	1.047	1.047
PEAK VOLTAGE	1.485	1.492
DC VOLTAGE (MEAN)	-0.013	-0.013
TOTAL POWER	1.102	1.102
RMS VOLTAGE (DFT)	1.047	1.047
RELATIVE PHASE	0	0
HARMONIC DISTORTION	0.100	0.095

Normal: 2 cycles at 26 samples per cycle (52 samples total).

Undersampled: 25 cycles at 1.04 samples per cycle (52 samples total).

### Sinusoidal Parameters

For sinusoidal testing, the signals are of the form

$$y(t) = \sqrt{2}A_m \cos(2\pi f_T t + \phi_m) + \sum_{k=1}^{\ell} \sqrt{2}A_{km} \cos(2\pi k f_T t + \phi_{km}) + n(t) \quad (3)$$

where the terms are, respectively, the fundamental, the harmonics, and the additive noise.

Least squares estimates of the rms amplitude,  $A_m$ , and phase,  $\phi_m$ , of the fundamental are obtained from

$$\hat{A}_m = \sqrt{2} [\text{Re}^2(Y_m) + \text{Im}^2(Y_m)]^{1/2} \quad (4)$$

and

$$\hat{\phi}_m = \tan^{-1} [\text{Im}(Y_m)/\text{Re}(Y_m)] \quad (5)$$

where

$$Y_m = \frac{1}{n} \sum_{i=0}^{n-1} y_i \exp(-j2\pi im/n) \quad (6)$$

which is the  $m$ th component of the DFT of the  $n$ -point sequence  $y_i$  containing  $m$  cycles of the fundamental; that is, the single frequency discrete Fourier transform (SPDFT). With coherent sampling and in the absence of noise,  $A_m$  and  $\phi_m$  are exact.

Harmonic distortion,  $D$ , is defined in terms of the ratio of power in the harmonics to power in the fundamental.

$$D = \left[ \sum_{k=2}^{\ell} A_{km}^2 / A_m^2 \right]^{1/2} \quad (7)$$

Since the sequences are relatively short and few lines are required, Goertzel's algorithm [3,4] is an efficient implementation of the SFDFIT.

### Digital Filtering

The evaluation of the DFT at a single frequency, as in Eq. (6) can be interpreted as a digital filter [5] with a corresponding enhanced signal-to-noise ratio. However, the filter quality, the skirts and side lobes, can be significantly improved by weighting or windowing the data sequence and then doing the SFDFIT. A particularly effective choice of data weighting sequence is the Kaiser  $I_0$ -sinh window [6]. An example of a weighted data sequence is shown in Fig. 6, and the corresponding digital filter responses are shown in Fig. 7.

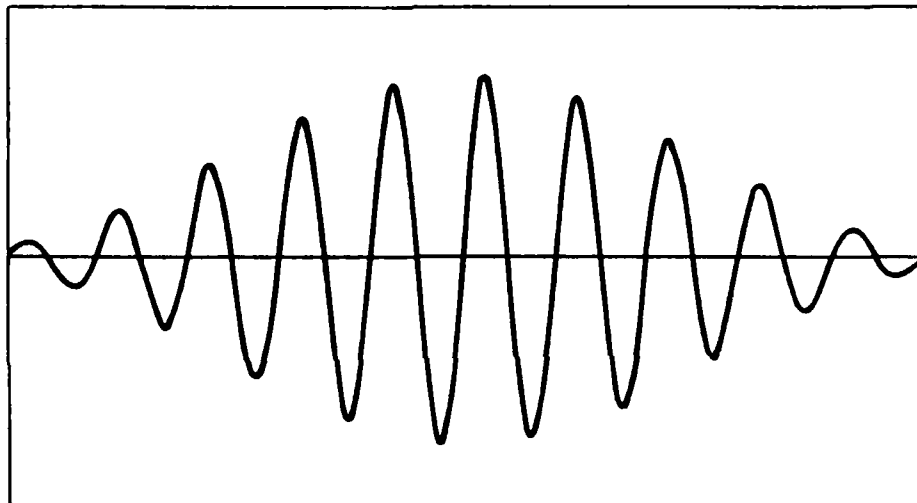


Fig. 6 - Sinusoidal waveform of 10 cycles (product with Kaiser window with 40 dB of spectral side lobe attenuation).

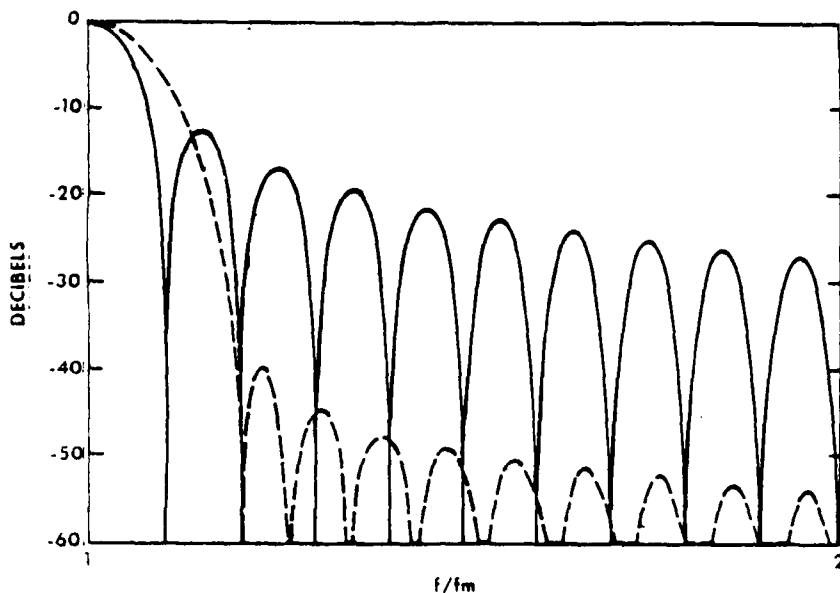


Fig. 7 - Comparison of the filter effect of an unweighted SFDFT (solid line) with that of a SFDFT with the Kaiser window with 40 dB of side lobe attenuation (dashed line). The frequency axis is normalized to the center frequency  $f_m$  which corresponds to the  $m$ th spectral line. The data sequence includes 10 cycles.

The filters are centered on the  $m$ th spectral line or test frequency,  $f_T$ . Following convention, the filter bandwidth is defined as the main lobe width or the distance between the minima adjacent to the  $m$ th line. For the unweighted SFDFT, the fractional bandwidth or main lobe width is

$$\left(\frac{\Delta f}{f_m}\right)_{\text{Rectangular}} = \frac{2}{m}, \quad (8)$$

where  $m$  is the number of cycles of  $f_T$  in the data sequence.

The reduced side lobes of the Kaiser filter are achieved at the expense of an increased main lobe width

$$\left(\frac{\Delta f}{f_m}\right)_{\text{Kaiser}} = \left(\frac{2}{m}\right) \frac{6(R + 12)}{155}, \quad (9)$$

where  $R$  is the attenuation in dB of the first side lobe. Figure 8 illustrates the increase in filter main lobe width as the side lobes are reduced.

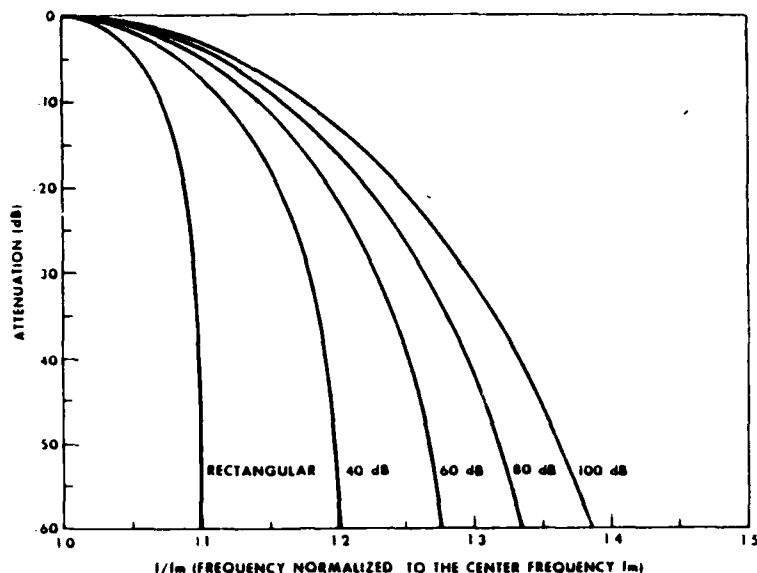


Fig. 8 - Comparison of filter main lobes for unweighted SFDFT and Kaiser-weighted SFDFT with 40 to 100 dB side lobes. The side lobes are not plotted for clarity. The data sequence includes 10 cycles ( $m = 10$ ).

The data window or weighting sequence,  $w_i$ , is computed in the host and transferred to the voltmeter where the window is applied in estimating amplitude and phase using the SFDFT of the weighted sequence

$$Y_m = \frac{1}{n} \sum_{i=0}^{n-1} w_i y_i \exp(-j2\pi im/n) \quad (10)$$

The amplitude estimate,  $A_m$ , is corrected by the window scale factor

$$\bar{w} = \frac{1}{n} \sum_{i=0}^{n-1} w_i \quad (11)$$

Table 2 illustrates that the Kaiser window does not alter or bias the amplitude and phase estimates at the test frequency. However, the harmonic distortion, particularly for low distortion, is biased by the window; larger sidelobe attenuations reduce this bias by reducing the amplitude of the harmonics created by the window.

Table 2 - Amplitude, phase and harmonic distortion estimates with and without a Kaiser window.

WINDOW	MEASURED DATA			SIMULATED DATA		
	$\hat{A}_m$	$\hat{\phi}_m$	100D	100D		
	RMS AMPLITUDE	PHASE DEGREES	PERCENT HARMONIC DISTORTION	PERCENT HARMONIC DISTORTION	0%	0.1%
No Window	1.054	-123.3	0.060	0.000	0.100	1.000
- 30 dB Side Lobe	1.054	-123.3	0.071	0.032	0.073	0.972
- 40 dB Side Lobe	1.054	-123.3	0.077	0.018	0.084	0.984
- 50 dB Side Lobe	1.054	-123.3	0.082	0.009	0.092	0.992
- 60 dB Side Lobe	1.054	-123.3	0.087	0.004	0.097	0.997
- 70 dB Side Lobe	1.054	-123.3	0.097	0.002	0.099	0.999
- 80 dB Side Lobe	1.054	-123.3	0.093	0.001	0.099	0.999
- 90 dB Side Lobe	1.054	-123.3	0.095	0.000	0.100	1.000
-100 dB Side Lobe	1.054	-123.3	0.097	0.000	0.100	1.000

Computations performed on 20 cycles of data at 32 points per cycle. Measured data was the output of a synthesizer; simulated data was a computed sine wave with distortion added to the second harmonic as labeled.

When long sequences or long integration times are used to combat noise, the computation time can be significantly reduced by averaging. This efficiency is possible if the total sequence consists of two or more subsequences with an integer number of samples and cycles per subsequence. The simplest example is a data sequence of  $m$  cycles of the fundamental test sinusoid with each cycle containing  $n$  samples--this data sequence can be compressed from  $m$  cycles to one cycle by averaging. Both the SFDFFT and windowed SFDFFT computations can be speeded up through averaging. (The window is applied before averaging.)

### Statistical Parameters

For signals which may be arbitrary periodic waveforms or noise-like data, statistical parameters are more appropriate in characterizing the data than are the SFDFFT derived amplitude and phase (although naturally these parameters can be computed for sinusoidal signals as well).

The voltage extremes or range of the waveform are obtained by inspection of the data sequence, and both the maximum and minimum voltages are found.

The average or dc voltage is computed with

$$y_{dc} = \bar{y} = \frac{1}{n} \sum_{i=1}^n y_i \quad (12)$$

The standard deviation or true ac rms is obtained from

$$y_{rms} = \sigma_y = \sqrt{\frac{1}{n} \sum_{i=1}^n (y_i - \bar{y})^2} \quad (13)$$



The choice of factor  $1/n$  instead of  $1/(n-1)$  is to satisfy the Parseval relationship so that the rms amplitude of an ideal sine wave is the same whether estimated with the SFDFDT or with the true rms methods.

The second joint moment

$$E(xy) = \frac{1}{n} \sum_{i=1}^n x_i y_i \quad , \quad (14)$$

is analogous to power if the data sequences  $x_i$  and  $y_i$  are proportional to current and voltage. Power measurement was the motivation for including this joint moment estimate. The total ac power,  $P_{ac}$ , is computed using

$$P_{ac} = E(xy) - \bar{x} \bar{y} \quad . \quad (15)$$

All of the summations in Eqs. (10) through (15) can be interpreted as Euler-Maclaurin approximations to integrals of the continuous time functions. For functions that can be represented as a finite number of harmonically-related sinusoids, this simple approximation is exact [7].

#### SOFTWARE FUNCTIONS

The voltmeter software is organized around a monitor routine awaiting input from the host via the IEEE-488 interface (as shown in Fig. 8). The input consists of ASCII strings of variable length--the first two characters being a code for setup or control. Strings are terminated by an end-or-identify (EOI) sent with the last character.

"Setup" codes are accompanied by an encoded byte string specifying the value for a certain parameter, such as the programmable gain for a specific A/D converter. Setup strings tell the program what values are to be used, but do not perform any operation such as physically setting the gain of an A/D converter. No default parameters are present within the program; all parameters must be specifically loaded.

"Control" codes (two-byte strings) command the voltmeter to perform certain functions; i.e., make an A/D conversion, compute certain parameters, transfer data to the host, etc.

The voltmeter can be requested to compute and return various parameters as requested, or to return the original sampled waveform to the host for special processing not supported by the voltmeter (plotting, for example). It is necessary to point out that although commands are transferred to the voltmeter as ASCII byte strings, data is received from the voltmeter in PDP-11 format (two bytes forming the 16-bit integers for sampled data, or four bytes floating point variables for all computed parameters). This was done in the interest of faster data transfer and maintaining original data precision for NRL-USRD applications. This feature prevents full compatibility with non-PDP 11 host processors, although subroutines to encode integer and floating-point variables exist as part of the display functions, and the program could be modified accordingly.

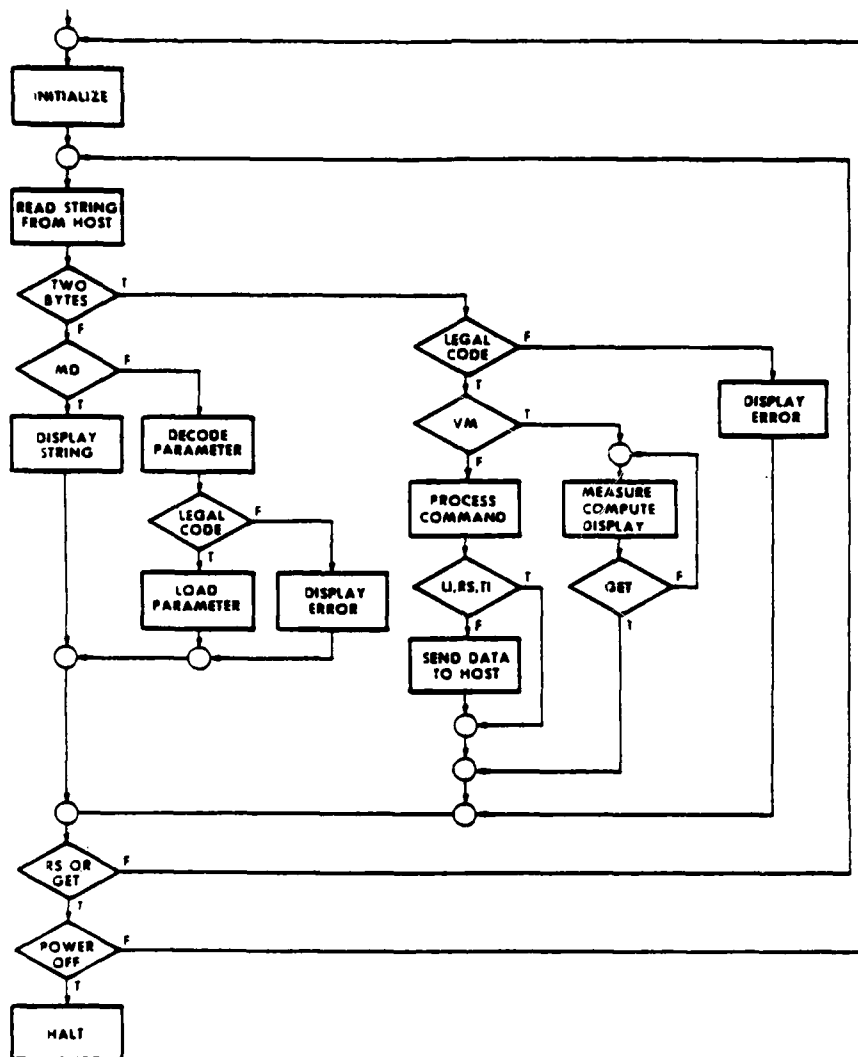


Fig. 9 - Flow chart of the basic monitor logic in the voltmeter software.

A special control code (VM) allows the voltmeter to measure, compute, and display as its own pace without host control; the host regains control by generating a group-execute-trigger (GET) on the IEEE-488 bus. The voltmeter displays data on either the console terminal or a 24-character plasma display. Display styles allowed are: count of A/D conversions and errors; voltage, phase, and distortion of one specific channel; peak voltage of all three channels; or voltages of two channels and the phase between the two.

The computation commands follow this sequence: the buffer of 12-bit digitized integer values is converted to floating-point numbers representing voltages within the A/D converter range ( $\pm 10$  V); a window function is applied, if desired; the data sequence in the buffer is averaged into a smaller subsequence, if desired; the specific computation is made; the results are scaled to account for the A/D converter gain and corrected for amplitude

change due to the window function, if used; and the data are transmitted to the host. The use of windowing and subsequence averaging seems not very useful for the computation of nonsinusoidal parameters.

The four primary commands are: "D" (compute voltage, phase, and distortion from SFDFT); its subset, "C" (the same without distortion), used for more rapid computation where distortion is not needed; "E" (total rms, peak, and dc voltage); and a similar subset, "F" (absolute peak only), a rapid tool for system autoranging or overload detection. Relative computation times are shown in Table 3. Note that these times refer to a PDP-11/23 computer with both the KEF-11 floating-point processor and the new FPF-11 floating-point hardware. The FPF-11 offers as much as a factor of 5 improvement in computation speed.

Table 3 - Sample computations measured for a PDP-11/23 with the KEF-11 and FPF-11 floating-point processors.

COMPUTATION STYLE	COMPUTATION TIME IN SECONDS							
	NO SUBSEQUENCE AVERAGING				WITH SUBSEQUENCE AVERAGING			
	NO WINDOW		WITH WINDOW		NO WINDOW		WITH WINDOW	
	KEF-11	FPF-11	KEF-11	FPF-11	KEF-11	FPF-11	KEF-11	FPF-11
<b>SINUSOIDAL PARAMETERS</b>								
Voltage, Phase	0.494	0.149	0.570	0.172	0.210	0.098	0.293	0.132
Voltage, Phase, Distortion	2.627	0.506	2.632	0.531	0.383	0.133	0.458	0.165
<b>NONSINUSOIDAL PARAMETERS</b>								
rms, Peak, dc Voltage	0.350	0.151	-	-	0.198	0.100	-	-
Peak Voltage	0.189	0.096	-	-	-	-	-	-
Total Power	1.488	0.477	-	-	0.927	0.373	-	-

Average of 100 measurements of 20 cycles of data at 40 points per cycle (800 points). Distortion computed from first 7 harmonics. Subsequence averaging to 1 cycle. Times include data transfer on IEEE-488 bus (about 0.016 second).

Collateral computation commands include the ability to compute the total rms voltage and phase of a specific harmonic and to compute the power parameters second joint moment (ac and dc power) and covariance (ac power). The power parameters are the total power generated by all harmonics, and it is assumed that the host will scale the data properly to account for the calibration of the current measuring circuitry.

Further details of the software are to be found in the appendices and the liberally-commented source code located there. Appendix C lists various error and status messages; Appendix D gives listings for sample host routines to drive the voltmeter; and Appendix E includes complete listings of all software modules loaded into the voltmeter, with external discussion where appropriate.

#### SUMMARY

A multiple-channel microprocessor-based sampling voltmeter has been developed for use in the USRD measurement systems. This voltmeter is capable of measuring amplitude, phase, and distortion, and of using several computational algorithms to derive these results as the application requires. The original voltmeter has been used successfully in the Anechoic

Tank Facility as a high-voltage impedance measurement system, and this more powerful version has been greatly expanded to be used in Low-Frequency Facility, or other measurement systems as needed.

#### ACKNOWLEDGMENTS

The authors would like to acknowledge R.W. Anderson and R.W. Luckey, both of the USRD, and National Instruments, Inc., for their assistance in making this device function on the IEEE-488 interface bus, and C.K. Brown (USRD) for his assistance in incorporating the voltmeter into an operating electronic measurement system. This work was performed in the Acoustic Metrology Program sponsored by the Naval Sea Systems Command (SEA63R12).

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## APPENDIX A

### Voltmeter Hardware

The hardware chosen as the heart of the voltmeter was the Digital Equipment Corporation (DEC) PDP-11/2 microcomputer (later upgraded to the PDP-11/23 with a KEF-11 floating-point processor), because it was a device that the USRD had both the experience and facilities to handle.

For A/D converters, ADAC Corp. models with DMA interfaces were chosen: model 1012, whose maximum sampling rate of 35 kHz was satisfactory for the LFF voltmeter, and model 1012AD, whose maximum sampling rate of 100 kHz was necessary for the ATF voltmeter.

Other interface modules used were:

MXV11-A	(DEC) Multifunction module: 2-serial line units 16K words RAM memory 4K words ROM memory (Intel 2732 EPROMs)
GPIBV11-1	(National Instruments) IEEE-488 bus interface
DRV11	(DEC) 16-bit parallel I/O card

The parallel I/O card is used only in pulsed systems to synchronize the measurements with the beginning of a toneburst.

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## APPENDIX B

### Voltmeter Commands

SETUP COMMANDS (# is channel number): These consist of a 2-byte code and a decimal integer string for the parameter value.

- AE Number of times to automatically retry a measurement if an A/D error occurs
- AI Which A/D-DMA device generates an interrupt when the data transfer is complete (1, 2, 3)
- AN Which A/D converters are to be used; additive, where 1 = #1, 2 = #2, 4 = #3, (7 = all three)
- AV Averaging mode (0 = no averaging, >0 = number of cycles in the averaged subsequence)
- CT Computation type (for future use)
- DD Display device: serial line 1 (console) or 2 (plasma display)
- DS Display style: 0 (conversion count), 1-3 (voltage, phase, distortion of channels 1-3), 4 (peak voltage in all 3 channels), 5 (voltages in channels 1-2 and phase between)
- FC Sample (clock) frequency in Hz
- FS Signal frequency in Hz
- GV Gated system (0 = not gated, 1 = use parallel I/O card to synchronize with toneburst)
- G# Programmable gain for A/D converter (1, 2, 5, 10)
- HA Number of harmonics used to compute distortion
- HC Which harmonic to compute (see B# - CONTROL COMMANDS)
- MD Transfer message to display, a byte string of up to 40 characters
- NC Number of cycles per sequence
- PA Number of points to convert per sequence
- PR Number of points per sequence to return to host

- PS First point to return to host. In practice, the first few converted points are bad, therefore,  $PA = PR + PS$  points are converted and PR points are computed
- P# Port (channel) for A/D Multiplexor (0-15)
- WM Window mode (0 = don't use window, 1 = use window)

CONTROL COMMANDS (2-byte control codes; # is channel number)

- B# Compute and return voltage and phase of the harmonic defined by HC parameter
- CW Compute a sine wave for test purposes
- C# Compute and return voltage and phase of the fundamental
- D# Compute and return voltage, phase, and distortion of the fundamental
- E# Compute and return statistical data (rms, dc, positive and negative voltage extremes)
- F# Compute and return absolute peak voltage
- LI List setup parameters on the display
- ME Perform A/D conversion and return status
- PO Compute and return total power (second joint moment)
- RP Transfer setup parameters to host
- RS Clear conversion and error counters
- R# Read back a data sequence
- RW Read back the average of a window and the weighted sequence
- SJ Compute and return total power (second joint moment) and covariance (ac power)
- TI Measure the time used in data transfer over the IEEE-488 bus
- VM Set to free-running display voltmeter mode
- WI Prepare voltmeter to read a computed window sequence from the host



SPECIAL IEEE-488 COMMANDS

- EOI End or identity - must coincide with last character sent by the host
- GET Group execute trigger - command will interrupt whatever the voltmeter is doing and reinitialize

## APPENDIX C

### Voltmeter Status Messages

Status is returned as an integer variable that is the voltmeter's acknowledgment of a measure command. It refers to either the legality of the command or the success of executing it.

<u>STATUS</u>	<u>MEANING</u>
1	Normal return
-1	A/D error in channel 1
-2	A/D error in channel 2
-4	A/D error in channel 3
-10	DMA error in channel 1
-20	DMA error in channel 2
-40	DMA error in channel 3
-100	A/D setup error
-101	Too many points for program buffer

Errors -1 through -40 are added together in case of multiple errors, and thus cover all numbers between -1 and -77.

The voltmeter processor will halt for the following severe errors:

<u>ADDRESS</u>	<u>REASON FOR HALT</u>
002036	Floating-point exception (can proceed)
002054	Function (sine, atan, sqrt) error (can proceed)
003370	Too many samples to compute (can proceed)
006224	Illegal display mode (can proceed)
013236	IEEE-488 interface illegally strapped (fatal)

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## APPENDIX D

### Voltmeter Control Subroutines

The following describes the protocol necessary and the available general-purpose subroutines for controlling this sampling voltmeter from a host processor. The National Instruments IEEE-488 bus driver and its software interface IBUP are a prerequisite [8].

Listings for the following subroutines (written at NRL-USRD) are included in this appendix.

#### 1. Primitive subroutines

RDL SI      Poll the bus and if successful  
             read a byte string from the voltmeter

WRL SI      Write a byte string to the voltmeter

#### 2. Higher-level I/O subroutines

WRL PA      Send an integer parameter to voltmeter

WRL VM      Send an integer array to voltmeter

WR MEA      Send measurement command to voltmeter,  
             wait an appropriate moment,  
             enable specific clock synthesizer,  
             receive status from voltmeter,  
             disable specific clock synthesizer

#### 3. Example of voltmeter I/O

MET ST      Test routine to command a measurement,  
             read back a data sequence, and  
             convert to volts

```

0001      SUBROUTINE RDLSI (IADR,IBUF,ILEN,ISW)
          C
          C      SUBROUTINE TO READ A BYTE STRING FROM THE LSI VOLTMETER.
          C
          C      ARGUMENTS:
          C          IADR = GPIB BUS ADDRESS
          C          IBUF = BYTE-BUFFER TO LOAD WITH STRING
          C          ILEN = LENGTH OF STRING EXPECTED
          C          ISW = STATUS OF READ PROCESS
          C              ISW = 1 (SUCCESSFUL)
          C
          C      AUTHOR: R. E. SCOTT, JR. (NRL/USRD); VERSION: JANUARY 1982
          C
0002      BYTE IBUF(ILEN)
0003      INTEGER GPIB
0004      C
          C      100      CONTINUE
          C
          C      TEST FOR SRQ (CURRENTLY NOT WORKING WITH LSI)
          C      J=GPIB(13)
          C      IF (J.NE.1) WRITE (6,110) J
          CD110  FORMAT (' RDLSI TEST-SRQ FAILURE: '14)
          C      IF (J.NE.1) GOTO 100
          C
          C      POLL FOR RESPONSE
          C      J=IBUF (6,IADR)
0005      IF (J.EQ.'102) GOTO 200 !SUCCESS
0006      IF (J.EQ.-6) GOTO 230 !TIMEOUT
0007      D
          D120  WRITE (6,120) J
          D      FORMAT (' RDLSI POLL FAILURE: '14)
0008      GOTO 100          !TRY AGAIN
          C
          C      READ THE BYTE STRING
          C      J=IBUF (1,IADR,IBUF,ILEN)
0009      IF (J.EQ.ILEN) ISW=1      !SUCCESS
0010      IF (J.NE.ILEN) ISW=J      !FAILURE
0011      D
          D210  WRITE (6,210) J,ISW
          D      FORMAT (' RDLSI STATUS: J='13' ISW='13)
          D      WRITE (6,220) IBUF
          D220  FORMAT (' RDLSI STRING='1204)
          C
0012      RETURN
          C
0013      C      230      CONTINUE
          D      WRITE (6,240) J
          D240  FORMAT (' RDLSI TIMEOUT: J='13)
0014      RETURN
          C
0015      END
  
```

PROGRAM SECTIONS

Name	Size	Attributes
\$CODE1	000216	71 RW,1,CON,LCL
\$PDATA	000010	4 RW,D,CON,LCL
\$IDATA	000040	16 RW,D,CON,LCL
\$VARS	000004	2 RW,D,CON,LCL

Total Space Allocated = 000272 93

No FPP Instructions Generated

```

0001      SUBROUTINE WRLSI (IADR,STRING,ICNT,ISW)
          C
          C      SUBROUTINE TO WRITE A BYTE STRING TO THE LSI VOLTMETER.
          C
          C      ARGUMENTS:
          C          IADR -- BUS ADDRESS OF DEVICE
          C          STRING-- BYTE ARRAY TO BE SENT
          C          ICNT  -- LENGTH OF ARRAY TO BE SENT
          C          ISW   -- STATUS OF OPERATION
          C                      ISW=1 IS SUCCESSFUL
          C                      ISW=* UNSUCCESSFUL (* = GPIB ERROR)
          C
          C      AUTHOR: R. E. SCOTT, JR. (NRL/USRD); VERSION: JANUARY 1982
          C
0002      BYTE STRING(ICNT)
          C
          C      WRITE THE STRING
0003      J=IBUP(0,IADR,STRING,ICNT)
          C
          C      DID IT WORK? (IF ISW = ICNT, SUCCESS).
          C      IF NOT, ISW IS GPIB ERROR STATUS.
0004      IF (J.NE.ICNT) ISW=J
0005      IF (J.EQ.ICNT) ISW=1
          C
          C      WRITE (0,90) STRING
          D          FORMAT (' WRLSI STRING='<ICNT>A1)
          D          WRITE (6,100) IADR,J,ISW
          D100      FORMAT (' WRLSI STATUS: ADDR='03' J='13' ISW='13)
          C
0006      RETURN
0007      END
  
```

PROGRAM SECTIONS

Name	Size	Attributes
%CODE1	000134	46 RW,I,CON,LCL
%PDATA	000004	2 RW,D,CON,LCL
%IDATA	000032	13 RW,D,CON,LCL
%SVARS	000002	1 RW,D,CON,LCL

Total Space Allocated = 000174 62

No FPP Instructions Generated

```

0001      SUBROUTINE WRLPA (IADR, ICODE, IVALUE, ISW)
          C
          C      SUBROUTINE TO WRITE A CODE AND AN INTEGER VALUE TO
          C      THE LSI-VOLTMETER, AND READ AN ACKNOWLEDGEMENT.
          C      IADR = GPIB BUS ADDRESS OF VOLTMETER
          C      ICODE = TWO-BYTE PARAMETER SETUP CODE
          C      IVALUE = POSITIVE INTEGER PARAMETER TO LOAD
          C      ISW = STATUS OF OPERATION
          C      ISW = 1 (SUCCESSFUL AS ACKNOWLEDGED)
          C      ISW = * (FAILURE * AS ACKNOWLEDGED)
          C
          C      AUTHOR: R. E. SCOTT, JR. (NRL/USRD); VERSION: JANUARY 1982
          C
0002      BYTE STRING(8)
0003      BYTE ICODE(2)
          C
          C      INCORPORATE THE CODE INTO THE STRING
0004      STRING(1)=ICODE(1)
0005      STRING(2)=ICODE(2)
          C
          C      ENCODE PARAMETER INTO SIX-CHARACTER ASCII STRING
0006      ENCODE (6,100,STRING(3)) IVALUE
0007      100      FORMAT (I6)
          C
          C      REPLACE LEADING SPACES WITH ZEROS
0008      DO 110 I=3,8
0009      110      IF (STRING(I).EQ.' ') STRING(I)='0'
          C
0010      NB=8
          C
          C      WRITE THE STRING TO THE GPIB-BUS
          C      WRITE (6,200) IVALUE,STRING
          C      D200      FORMAT (' WRLPA VALUE='I6' STRING='8A1)
0011      CALL WRLSI (IADR,STRING,NB,ISW)
0012      IF (ISW.NE.1) CALL TSTERR (6,ISW)          !ERROR IS ???
          C      IF (ISW.EQ.-6) J=IBUP(10)          !TIMEOUT CLEANUP
          C      D      WRITE (6,300) ISW
          C      D300      FORMAT (' WRLPA (WRITE) STATUS: ISW='I3)
          C
          C      READ REPLY IF GIVEN
          C      CALL RDLIS (IADR,ISTAT,2,ISW)
          C      IF (ISW.NE.1) CALL TSTERR (6,ISW)          !ERROR IS ???
          C      IF (ISW.EQ.-6) J=IBUP(10)          !TIMEOUT CLEANUP
          C      CD      WRITE (6,310) ISW
          C      CD310      FORMAT (' WRLPA (READ) STATUS='I3)
          C
          C      SUBSTITUTE ACKNOWLEDGED STATUS FOR GPIB STATUS
          C      ISW=ISTAT
0013      RETURN
0014      END
    
```

PROGRAM SECTIONS

Name	Size	Attributes
%CODE1	000252	85 RW,I,CON,LCL
%PDATA	000010	4 RW,D,CON,LCL
%IDATA	000032	13 RW,D,CON,LCL
%VARS	000014	6 RW,D,CON,LCL

Total Space Allocated = 000330 108

No FPP Instructions Generated

```

0001      SUBROUTINE WRLVM (IADR,ICODE,IVAL,ICNT,ISW)
          C
          C      SUBROUTINE TO WRITE A BYTE STRING TO THE LSI-VOLTMETER
          C      PREFIXED BY A TWO-BYTE PARAMETER CODE.
          C      IADR = GPIB BUS ADDRESS
          C      ICODE = TWO-BYTE PARAMETER CODE
          C      IVAL = ARRAY TO SEND TO VOLTMETER
          C      ICNT = NUMBER OF INTEGERS TO SEND
          C      ISW = STATUS OF ACKNOWLEDGEMENT
          C      IVAL COULD BE A 4-BYTE REAL VARIABLE (ICNT=2).
          C      IVAL COULD BE NOTHING (ICNT=0; SEND CODE ONLY).
          C
          C      AUTHOR: R. E. SCOTT, JR. (NRL/USDD); VERSION: JANUARY 1982
          C
0002      INTEGER ICODE
0003      INTEGER IVAL(8)
0004      INTEGER STRING(10)
          C
          C      PREFIX CODE INTO STRING
0005      STRING(1)=ICODE
0006      IF (ICNT.EQ.0) GOTO 60
          C
          C      ENTER VALUE INTO STRING, TWO BYTES AT A TIME
0007      DO 50 I=1,ICNT
0008      STRING(I+1)=IVAL(I)
          C
          C      COMPUTE STRING LENGTH
0009      NB=2+2*ICNT
          C
          C      WRITE THE STRING
0010      CALL WRLSI (IADR,STRING,NB,ISW)
0011      IF (ISW.NE.1) CALL TSTERR (6,ISW)
          C      !ERROR IS ???
          C      !TIMEOUT CLEANUP
          C      IF (ISW.EQ.-6) J=IBUP(10)
          C      IF (ISW.NE.1) WRITE (6,100) ISW
          C      !ERROR IS ???
          C      !TIMEOUT CLEANUP
          C      D100  FORMAT (' WRLVM STATUS: ISW='13)
          C
          C      READ REPLY IF GIVEN
          C      CALL RDLGI (IADR,ISTAT,2,ISW)
          C      IF (ISW.NE.1) CALL TSTERR (6,ISW)
          C      IF (ISW.EQ.-6) J=IBUP(10)
          C      !ERROR IS ???
          C      !TIMEOUT CLEANUP
          C      CD  IF (ISW.NE.1) WRITE (6,110) ISW
          C      CD110  FORMAT (' RDLGI STATUS: ISW='13)
          C
          C      EXCHANGE GPIB STATUS FOR ACKNOWLEDGEMENT STATUS
          C      ISW=ISTAT
          C      RETURN
0012
0013      END
  
```

PROGRAM SECTIONS

Name	Size	Attributes
\$CODE1	000234 78	RW,I,CON,LCL
\$PDATA	000034 2	RW,D,CON,LCL
\$IDATA	000032 13	RW,D,CON,LCL
\$VARS	000030 12	RW,D,CON,LCL
\$STEMPS	000002 1	RW,D,CON,LCL

Total Space Allocated = 000324 106

No FPP Instructions Generated



```

0001      SUBROUTINE WRMEA (IADR,ICODE,FREQ,ITIME,ISTAT)
          C
          C      SUBROUTINE TO WRITE A "ME" (MEASURE) TO THE LSI-VOLTMETER,
          C      (WAIT), ENABLE SAMPLING SYNTHESIZER, RECEIVE ACKNOWLEDGEMT,
          C      AND DISABLE SAMPLING SYNTHESIZER.
          C      *** SPECIFICALLY FOR LOW FREQUENCY SYSTEM SYNTHESIZER ***
          C
          C      IADR.....IEEE-488 BUS ADDRESS FOR VOLTMETER.
          C      ICODE....MEASUREMENT CODE (ME).
          C      FREQ.....SAMPLING FREQUENCY IN HZ.
          C      ITIME....TIME TO WAIT BETWEEN MEASURE COMMAND AND ENABLING
          C      SAMPLE-RATE GENERATOR (MSEC).
          C      ISTAT....MEASUREMENT STATUS.
          C
          C      AUTHOR: R. E. SCOTT, JR. (NRL/USRD); VERSION: JANUARY 1982
          C
0002      ISTAT=0          !STATUS: NO MEASUREMENT
          C
          C      COMMAND A MEASUREMENT
0003      CALL WRLSI (IADR,'ME',2,ISW)
0004      IF (ISW.NE.1)CALL TSTERR (6,ISW) !BUS ERROR?
          D
          D100      IF (ISW.NE.1) WRITE (6,100) ISW
          D100      FORMAT (' WRMEA WRITE-"ME" STATUS: '13)
0005      IF (ISW.NE.1) RETURN          !GIVE UP IF BUS ERROR
          C
0006      CALL WAIT (ITIME,1,K)          !WAIT FOR SLAVE TO BE READY
0007      CALL SM102 ('24,FREQ,'E ',JSW,ISW)!ENABLE SYNTHESIZER (CLOCK)
0008      CALL RDLIS (IADR,ISTAT,2,ISW) !GET STATUS FROM SLAVE
0009      IF (ISW.NE.1)CALL TSTERR (6,ISW) !BUS ERROR?
          D
          D110      IF (ISW.NE.1) WRITE (6,110) ISW
          D110      FORMAT (' WRMEA ACKNOWLEDGE STATUS: '13)
0010      CALL SM102 ('24,FREQ,'D ',JSW,ISW)!DISABLE SYNTHESIZER AGAIN
0011      RETURN
          C
0012      END
    
```

PROGRAM SECTIONS

Name	Size	Attributes
%CODE1	000252	85 RW,I,CON,LCL
%PDATA	000042	17 RW,D,CON,LCL
%IDATA	000064	26 RW,D,CON,LCL
%VARS	000006	3 RW,D,CON,LCL

Total Space Allocated = 000406 131

No FPP Instructions Generated

```

0001      SUBROUTINE METST (IAD,IT,MODE)
          C      SUBROUTINE TO TEST THE LEESBURG/RD-11/LSI-11/A22D
          C      HIGH-POWER MEASUREMENT SYSTEM INPUTS.

0002      INCLUDE 'LSPARM.COM'
0003      *      COMMON /LSPARM/SIFREQ,SIAMPL,SAFREQ,SAAMPL,
          *      ICGAIN(3),NAD,NADT,NPAD,NPTR,NPST,NCYC,NHAR,
          *      2IAVG,IDST,ICTYP,IAERS,IGATE,(WINDO,IDLUN,IHARC,
          *      3IPORT(3))
          * C      SIFREQ,SIAMPL-----SIGNAL FREQUENCY AND AMPLITUDE
          * C      SAFREQ,SAAMPL-----SAMPLE FREQUENCY AND AMPLITUDE
          * C      ICGAIN-----A/D PROGRAMMABLE GAIN
          * C      NAD-----WHICH A/D'S USED (1=1,2=2,3=4,ALL=7)
          * C      NADT-----WHICH A/D (DMA) TO INTERRUPT ON
          * C      NPAD-----NUMBER OF POINTS TO CONVERT
          * C      NPTR-----NUMBER OF POINTS TO TRANSFER
          * C      NPST-----FIRST POINT TO TRANSFER
          * C      NCYC-----NUMBER OF CYCLES TO CONVERT
          * C      NHAR-----NUMBER OF HARMONICS USED TO COMPUTE DISTORTION
          * C      IAVG-----NUMBER OF CYCLES TO AVERAGE TO (0=DON'T)
          * C      IDST-----DISPLAY STYLE (0--5)
          * C      ICTYP-----COMPUTATION TYPE
          * C      IAERS-----A/D ERRORS TO RETRY BEFORE QUITTING
          * C      IGATE-----GATED (1) OR NOT (0) ((DRV-11 IN USE))
          * C      (WINDO)-----MULTIPLY DATA BY WINDOW (WDATA)
          * C      IDLUN-----DISPLAY DEVICE
          * C      IHARC-----HARMONIC TO COMPUTE

0004      INCLUDE 'LSDATA.COM'
0005      *      PARAMETER BSIZE=1024                                !MAXIMUM POINTS TO READ
0006      *      PARAMETER NOAD=3                                    !NUMBER OF CHANNELS
0007      *      COMMON /IDAT/IDATA(NOAD*BSIZE)                     !RAW A/D DATA (IN BITS)
0008      *      COMMON /RDAT/RDATA(NOAD*BSIZE)                     !NORMALIZED DATA (IN VOLTS)
0009      *      COMMON /WDAT/WDATA(BSIZE),WATT,WAV                !WINDOW, ATTENUATION, AVERAGE
0010      *      COMMON /SDAT/SDATA(BSIZE)                          !COMPUTATION BUFFER
0011      *      COMMON /RTIM/TIM(NOAD)                              !COMPUTATION TIMES

          C
          C
0012      INTEGER IPAR(15),IAE(6)

0013      IF (IT.EQ.'ME') GOTO 100
0014      IF (IT.EQ.'R1') GOTO 200
0015      IF (IT.EQ.'R2') GOTO 200
0016      IF (IT.EQ.'R3') GOTO 200

          C
          C      GO TO LS. TO PERFORM A/D CONVERSION (ME)
0017      100 CONTINUE
          C      WRITE (6,101)
          C      2101 FORMAT (' DO A/D CONVERSION')
0018      IF (MODE.NE.'LO') CALL WRLVM (IAD,'ME',0.0,ISW)
0019      IF (MODE.EQ.'LO') CALL WRMEA (IAD,'ME',SAFREQ,200,ISW)
0020      IF (ISW.EQ.0) RETURN
0021      WRITE (6,102) ISW
0022      102 FORMAT (' *** A/D CONVERSION ERROR: '13' ***')
0023      IF (ISW.EQ.-100) WRITE (6,110)
0024      IF (ISW.EQ.-101) WRITE (6,120)
0025      110 FORMAT (' A/D SETUP ERROR (A/D'S OR INTERRUPT)')
0026      120 FORMAT (' TOO MANY POINTS REQUESTED')
0027      RETURN

          C
          C      READ A DATA BUFFER FROM THE LSI (R1,R2,R3)
0028      200 CONTINUE
          C      WRITE (6,201)
          C      2201 FORMAT (' READ FROM 11/03')
0029      RDS=SECNDS(0.0)
0030      CALL WRLVM (IAD,IT,0.0,ISW)

```

```

0031      IF (ISW.NE.1) WRITE (6,203) ISW
0032      203      FORMAT (' WRLVM ERROR: '13)
0033      IF (ISW.EQ.-6) J=IBUP(10)
0034      IF (ISW.NE.1) RETURN
0035      IF (IT.EQ.'R1') J=1
0036      IF (IT.EQ.'R2') J=2
0037      IF (IT.EQ.'R3') J=3
0038      K=(J-1)*NPTR+1
0039      CALL RDLIS (IAD,1DATA(K),2*NPTR,ISW)
      C
      0220      WRITE (6,220) (1DATA(I),I=K,K+NPTR)
      C
0040      IF (ISW.NE.1) WRITE (6,230) J,ISW
0041      230      FORMAT (' *** A/D DATA CHANNEL '11' IN ERROR: '13' ***)
0042      IF (ISW.EQ.-6) J=IBUP(10)
0043      IF (ISW.NE.1) RETURN
      C
      C
0044      FIX THE DATA PROPERLY
0045      L1=1+(J-1)*NPTR
0046      L2=L1+NPTR
0047      DO 250 I=L1,L2
0048      IF (IAND('7777',1DATA(I))
0049      (I1.GE.'4000') I1=IOR('170000,I1)
0050      1DATA(I)=10.0*I1/'4000
0051      250      CONTINUE
      C
      D
      D
      0260      TIM(J)=SECNDS(RDS)
      C
0052      IF ((IT.EQ.'R1').OR.(IT.EQ.'R2')) WRITE (6,260) TIM(J)
      D
      D
      0260      IF ((IT.EQ.'R3').OR.(IT.EQ.'RE')) WRITE (6,260) TIM(J)
      C
      0260      FORMAT (' BUFFER READ TIME='F8.3)
      C
0052      RETURN
0053      END

```

PROGRAM SECTIONS

Name	Size	Attributes
\$CODE1	001156	311 RW,I,CON,LCL
\$PDATA	000324	106 RW,D,CON,LCL
\$1DATA	000050	23 RW,D,CON,LCL
\$VARS	000074	30 RW,D,CON,LCL
LSPARM	000072	29 RW,D,OVR,GBL
1EAT	014000	3072 RW,D,OVR,GBL
RDAT	030000	6144 RW,D,OVR,GBL
WDAT	010010	2052 RW,D,OVR,GBL
SDAT	010000	2048 RW,D,OVR,GBL
1TIM	000014	6 RW,D,OVR,GBL

Total Space Allocated = 065772 13821

## APPENDIX E

### Voltmeter Program Listings

The modules used in this program are written in PDP-11 assembly language, divided into pure (ROM) code (modules 1-7) with all dynamic variables (RAM) in the final module.

- (1) LSIVM Command interpreter; measurement, computation, and display driver
- (2) AD2D A/D converter control subroutine
- (3) CONVERT Floating to ASCII encoding subroutine, used to prepare numbers for display or transfer to the host
- (4) BAPD Computation of amplitude, phase, distortion; uses DGZL below to obtain real and imaginary parts of voltage
- (5) DGZL Single frequency DFT subroutine: computes real and imaginary parts of voltage of a given spectral line by Goertzel's algorithm
- (6) GPIOSR IEEE-488 bus input and output subroutines, initialize subroutine, and interrupt service routine
- (7) GRMS Computation by time-integration of rms, dc, positive peak, voltages
- (8) LSIVAR All the dynamic variables used by (1)-(6) above

Also used are modules from DEC's RSX-11M System Library--\$ATAN (arctangent), \$SIN (sine), and \$SQRT (square root).

The subroutines (2)-(7) are written to conform with DEC standards; argument lists are passed through register 5 so that these routines could be used by other programs. Conditional assembly parameters allow dynamic variables to be assembled locally, or in an external global file such as LSIVAR.

- (1) MODULE LSIVM - Main program for controlling the microcomputer-controlled sampling voltmeter

Internal Summary:

<u>(LABEL)</u>	<u>(FUNCTION)</u>	<u>(PAGE)</u>
<u>MONITOR ROUTINES</u>		
START	Initialize system	5
RDCODE	Read string from bus; interpret type of command	5
<u>PARAMETER ROUTINES</u>		
PARAM	Parameter decode and load	6
FREQP	Read frequency data	8
MESDAY	Read and display text string	8
DECOD	Byte string to integer decoding	9
FPISR	Floating-point processor interrupt service routine	9
EXEC	Interpret and direct control codes	10
CERR	Parameter I/O error routine	11
<u>CONTROL ROUTINES</u>		
ABORT	Abort process and reinitialize	11
MESURE	Make measurement	12
WAKE	Counter/display initialization	12
PARLST	Display setup parameters	13
IFWAIT	Delay subroutine	13
PARSND	Send setup parameters to host	14
ADSEND	Send A/D buffer or window to host	14
<u>PRE-PROCESSING ROUTINES</u>		
CMPUT	Computation interpreter	15
CWAVE	Compute test sine wave	17
FLOT	Convert A/D buffer to floating point buffer	18

<u>(LABEL)</u>	<u>(FUNCTION)</u>	<u>(PAGE)</u>
WINDIN	Read sequence window from host; compute average	19
WINDOW	Multiply data buffer by window buffer	19
BAVER	Average data buffer to fewer cycles	20
<u>COMPUTATION ROUTINES</u>		
GDFTD	Compute voltage, phase, distortion (DFT) of fundamental	21
GDFT	Compute voltage, phase (DFT) of fundamental	21
GSL	Compute voltage, phase (DFT) of harmonic	21
TDFTD	Transfer voltage, phase, distortion to host	22
TDFT	Transfer voltage, phase to host; display	22
DSLD	Load DFT values for display	22
GRMSV	Do statistical computation	23
GPEKO	Find absolute peak value	23
TRMSV	Transfer statistical data to host; display	24
TPEK	Transfer peak voltage to host; display	24
DEGAIN	Correct voltage for A/D gain	24
DEWIND	Correct DFT voltage for window average	24
GPWR	Compute total power (second joint moment)	25
GJC	Compute total power and ac power	26
GMN	Get rms voltage for power computation	26
TJC	Transfer total power and ac power to host	26
<u>DISPLAY ROUTINES</u>		
VMDISP	Stand-alone voltmeter executive	27
CHODIS	Display, mode 0 (conversion and error count)	29
CHIDIS	Display, modes 1-3 (voltage, phase, and distortion)	30

CH4DIS	Display, mode 4 (voltages)	32
CH5DIS	Display, mode 5 (voltages and relative phase)	33
WRITE	Terminal output (string)	34
PRINT	Terminal output (integer)	34

```

1  TITLE LS1VH
2  IDENT /06/
3
4
5
6  ;NRL/USRD MICROCOMPUTER SAMPLING VOLTMETER PROGRAM.
7  ;AUTHOR: RICHARD E. SCOTT, JR. (FEBRUARY 1982)
8
9
10
11
12  ;THIS PROGRAM IS DESIGNED TO PERFORM AS A SAMPLING VOLTMETER
13  ;CONTROLLED BY THE GPIB INTERFACE BUS. THE SETUP AND CONTROL
14  ;COMMANDS ARE SENT IN MULTI-BYTE PACKETS. DATA IS RETURNED
15  ;ON COMMAND IN THE FORM OF A WAVEFORM DIGITIZED BY UP TO THREE
16  ;A/D CONVERTERS, AND AS VARIOUS COMPUTED PARAMETERS.
17
18  ;THE "MEASURE" COMMAND IS EFFECTIVELY THE ONLY ONE THAT IS
19  ;ACKNOWLEDGED BY THIS PROGRAM, RETURNING AN INTEGER STATUS.
20
21  ;THE CURRENT VERSION CAN RETURN THE FOLLOWING PARAMETERS
22  ;UNDER HOST CONTROL: RMS, PEAK, AND DC VOLTAGE FROM TIME
23  ;INTEGRATION; RMS VOLTAGE, PHASE, AND DISTORTION FROM A DFT
24  ;CALCULATION. ALSO RETURNED IS TOTAL POWER (POWER IN ALL
25  ;HARMONICS). PREPROCESSING OPTIONS INCLUDE MULTIPLYING THE
26  ;DATA SEQUENCE BY A WINDOW SPECIFIED BY THE HOST, AND
27  ;AVERAGING MANY-CYCLED SEQUENCES DOWN TO FEW CYCLES WHERE
28  ;SUCH IS PERMISSIBLE.
29
30  ;THE PROGRAM CONTAINS SEPARATE, CONTIGUOUS DATA BUFFERS OF A
31  ;GIVEN MAXIMUM SIZE FOR EACH OF THE THREE CHANNELS, HOWEVER,
32  ;DATA IS DIGITIZED INTO MEMORY AS IF THESE ARRAYS WERE CONTIG-
33  ;UOUS, BUT SIZED FOR THE NUMBER OF SAMPLES DIGITIZED. THE
34  ;COMPUTATION IS PERFORMED ON ONE CHANNEL'S DATA AT A TIME, AS
35  ;EACH INTEGER SEQUENCE MUST FIRST BE CONVERTED TO FLOATING-POINT
36  ;VALUES STORED IN THE SINGLE "TEMPORARY" FLOATING ARRAY. TOTAL
37  ;POWER, REQUIRING TWO FLOATING-POINT CHANNELS SIMULTANEOUSLY,
38  ;IS COMPUTED DIFFERENTLY (A SAMPLE AT A TIME).
39
40  ;ALTHOUGH THIS DEVICE CAN ACT LIKE A DISPLAY VOLTMETER
41  ;INDEPENDENT OF COMPUTER CONTROL, IT IS STILL NECESSARY
42  ;TO SET UP THE DEVICE UNDER COMPUTER/BUS CONTROL, BECAUSE
43  ;IT IS FREQUENCY-DEPENDENT, NO DEFAULT VALUES ARE PRACTICAL
44  ;TO ALLOW IT TO POWER-ON INTO A VOLTAGE-READING MODE.
45
46  ;THE VOLTMETER IS CONTROLLED BY COMMANDS FROM THE BUS,
47  ;WHICH CONSIST OF A TWO-BYTE (TWO CHARACTER) CODE, AND
48  ;AN INTEGER ARGUMENT (IF NEEDED) AS A BYTE STRING.
49
50  ;ANY PROCESS CAN BE PERMANENTLY ABORTED FROM THE HOST
51  ;BY TRANSMITTING A DEVICE TRIGGER (GET) ON THE BUS.
52  ;ALL DEVICES AND COUNTERS ARE RESET.

```



```

53 ; SETUP COMMANDS (TWO BYTES PLUS BYTE STRING)-
54 ; FORMAT (TWO-BYTE CODE) (MULTI-BYTE VALUE)
55 ; C: PROGRAMMABLE GAIN FOR A/D CONVERTER # (1-3) (1,2,5,10)
56 ; P: INPUT PORT FOR A/D CONVERTER # (1-3) (0-16)
57 ; AN: WHICH OF A/D'S USED (#1-1, #2-2, #3-4; ALL=7)
58 ; AI: WHICH A/D TO INTERRUPT ON (DMA FINISH)
59 ; AE: HOW MANY A/D ERRORS TO RETRY BEFORE GIVEUP
60 ; PA: NUMBER OF POINTS (CONVERSIONS) PER WAVEFORM SEQUENCE
61 ; PR: NUMBER OF POINTS (CONVERSIONS) TO RETURN TO HOST
62 ; PS: FIRST POINT (CONVERSION) NUMBER TO RETURN TO HOST
63 ; NG: NUMBER OF CYCLES PER SEQUENCE
64 ; HA: NUMBER OF HARMONICS TO USE COMPUTING DISTORTION
65 ; HC: WHICH HARMONIC TO COMPUTE
66 ; DS: DISPLAY TYPE (Q.V.)
67 ; DD: DISPLAY DEVICE (SEE PAGE 3)
68 ; AV: AVERAGING MODE (0=NONE, N=CYCLES RESULTING)
69 ; CT: COMPUTATION TYPE (Q.V.)
70 ; CV: GATED (0=NO, 1=YES)
71 ; WN: WINDOW MODE (0=NONE, 1=USE)
72 ; MD: SEND MESSAGE TO DISPLAY
73
74
75 ; INFORMATIONAL COMMANDS (SIX BYTES)-(OBSOLETE)
76 ; FORMAT (TWO-BYTE CODE) (FOUR-BYTE REAL VALUE)
77 ; FS: SIGNAL FREQUENCY IN HZ
78 ; FC: CLOCK FREQUENCY IN HZ
79
80
81 ; CONTROL COMMANDS (TWO BYTES)-
82 ; FORMAT (TWO-BYTE CODE)
83 ; RS: CLEAR COUNTERS
84 ; NE: PERFORM A/D CONVERSION AS PROGRAMMED
85 ; LI: LIST 17 PARAMETERS ON THE LSIVM CONSOLE
86 ; VM: GO TO VOLTMETER MODE (AUTOMATIC)
87 ; WI: INPUT WINDOW DATA ("PR" REAL VALUES, 4*PR BYTES)
88 ; TI: DUMMY COMMAND FOR I/O TIMING INFORMATION
89 ; CW: COMPUTE SINE WAVEFORM FOR TIMING INFORMATION
90
91
92 ; DATA TRANSFER COMMANDS:
93 ; FORMAT (TWO-BYTE CODE)
94 ; R: TRANSFER WAVEFORM # (1-3) SAMPLES BACK TO HOST
95 ; RW: TRANSFER WINDOW AND AVERAGE BACK TO HOST
96 ; RP: TRANSFER 10 PARAMETERS BACK TO HOST FOR CHECKING
97 ; ALSO.....
98
99 ; COMPUTE COMMANDS (TWO BYTES)-
100 ; FORMAT (TWO-BYTE CODE)
101 ; B: RETURN VOLTAGE AND PHASE OF HCTH HARMONIC, CHANNEL # (1-3)
102 ; C: RETURN VOLTAGE AND PHASE OF FUNDAMENTAL, CHANNEL # (1-3)
103 ; D: RETURN VOLTAGE, PHASE, DISTORTION (FROM DFT), CHANNEL # (1-3)
104 ; E: RETURN RMS, PEAK+, PEAK-, DC VOLTAGES, CHANNEL # (1-3)
105 ; F: RETURN PEAK ONLY, CHANNEL # (1-3)
106 ; P0: RETURN TOTAL POWER FROM CHANNEL #1 (VOLTS) AND #2 (AMPS)
107 ; CJ: RETURN SECOND JOINT MOMENT AND COVARIANCE, CHANNELS #1 AND #2

```

```

110 ;SUBROUTINES REQUIRED:
111 ;
112 ; GPIOSR -- GPIU-488 BUS CONTROL SUBROUTINES
113 ; A32D -- ADAC A/D CONVERTER CONTROL SUBROUTINE
114 ; CONVRT -- READ TO ASCII ENCODING FOR DISPLAY
115 ; GMS -- COMPUTE TONE-SERIES VOLLAGES
116 ; D6ZL -- COMPUTE PREDICTED FOURIER TRANSFORM
117 ; D6ZL -- COMPUTE PREDICTED PHASE AND DISTANCE
118 ; LSIVAR -- GLOBAL DATA VARIABLES
119 ;
120 ;
121 ;
122 ;
123 ;
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129 ;
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155 ;
156 ;
157 ;
158 ;

```

SPECIAL ASSEMBLY INSTRUCTIONS:  
 THIS SYSTEM WAS DESIGNED TO BE LOADED INTO PROM, SO THE  
 DATA VARIABLES ARE CONCENTRATED IN THE MODULE LSIVAR. IT  
 IS NECESSARY TO CHECK THE STATUS OF THE CONDITIONAL  
 ASSEMBLY PARAMETER "PROM" IN THE MAIN ROUTINE LSIVM AND  
 MANY OF THE SUBROUTINES TO INSURE THAT IT IS IN THE RIGHT  
 STATE. IN LSIVM PARAM CONCERNS WHETHER PAGE ZERO (VECTOR  
 SPACE) IS IN PROM (CHOICE ONLY NO); IN A32D PROM CONCERNS  
 WHETHER VECTORS ARE TO BE LOGICALLY LOADED; IN D6ZL AND  
 D6ZL IT CONCERNS WHETHER DATA VARIABLES ARE LOGICALLY ASSEMBLED  
 (AS FOR OTHER USERS), OR LSIVAR (THIS APPLICATION).

SPECIAL TASK-BUILD INSTRUCTIONS: NOTE THAT THIS TASK IS  
 DESIGNED ONLY TO RUN ON A PDP-11 AND WILL NOT  
 EXECUTE UNDER RSN-11E. THIS IS NOT RELOCATABLE.

```

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199 ;
200 ;

```

DEB COMMAND FILE:  
 LSIVM/SQ/-HM/-HD/FP,LSIVM,GP SP=LSIVM  
 GMS,A32D,CONVRT,D6ZL,DTMP  
 GPIOSR  
 U.I.SYSLIB,OBJ,LIB,CATAM:\$SIN:\$DS IN:\$SIC

```

201 ;
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```

ACC=30  
 ACT=21  
 AC2=22

```

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296 ;
297 ;
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299 ;
300 ;

```

000000  
000001  
000002

```

TEXT PRINTING ADDRESSES FOR SERIAL LINE INTERFACES
;CONSOLE (BLUN = 1)
DIAXSR=177564
DIAXBDF=177566
CHXSRI =176504
CHXBDF =176506

;GP1H REGISTERS REQUIRED
;CPACR=160006
;GP1B AUXILIARY COMMAND REGISTER

;SET PROM = 1 IF VECTORS TO BE LOADED IN PROM.
;IF SO ALSO CHANGE TASKBUILD FILE TO PAR=PAR:0....
PROM:

```

LINE	ADDRESS	DATA	COMMENT
160			
161	177564		
162	177566		
163	176504		
164	176506		
165			
166	160006		
167			
168			
169			
170			
171	000001		
172			
173			
174			
175	000167	000774	
176	000000		
177	000004		
178	001000		
179	000024		
180	000026		
181	000030		
182	000034		
183	000036		
184			
185	000040		
186			
187	000100	000000	
188	000132	000340	
189	000134		
190	000150	000000	
191	000152	000340	
192			
193	000154		
194	000170	000000	
195	000172	000340	
196	000174		
197	000210	000000	
198	000212	000340	
199			
200	000214		
201	000230	000000	
202	000232	000340	
203	000234		
204	000244	002026	
205	000246	000340	
206	000250	000000	
207	000252	000340	
208			
209	000254		
210	000270	000000	
211	000272	000340	
212	000274		
213			

.ENDC

```

215 ;START OF PROGRAM
216 ;INITIALIZE WHATEVER IS NECESSARY AND GO INTO "WAIT" STATE.
217 ;NORMAL STATUS IS TO WAIT FOR INPUT FROM THE IEEE-488 BUS;
218 ;WHEN RECEIVED, GO TO EITHER DATA OR COMMAND INTERPRETER.
219
220
221 001006 ;STACK,SP
222 001000 012766 000000C
223 001004 000005
224
225 .IF EQ PROM
226   MOV  #FPISR,@#244
227   MOV  #340,@#246
228   MOV  #FOLSR,@#34
229   MOV  #340,@#36
230 .ENDIF
231 CLR  @#1CA
232 CLR  @#1CE
233 MOV  #CPINA,R5
234 JSR  %C,CPINI
235 MOV  #0,R0
236 HTS  R0
237
238 ;INITIALIZE STACK POINTER
239 ;CLEAR Q-BUS
240 ;LOAD FLOATING-ERROR INTERRUPT VECTOR
241 ;AND PSW
242 ;LOAD FUNCTION-ERROR INTERRUPT VECTOR
243 ;AND PSW
244 ;NUMBER OF A/D CONVERSIONS
245 ;NUMBER OF A/D ERRORS
246 ;INITIALIZE IEEE-488 BUS CARD
247 ;CLEAR PSW
248 ;(ENABLE INTERRUPTS)
249
250 ;THIS PORTION OF THE PROGRAM AWAITS A DATA PACKET FROM THE
251 ;HOST PROCESSOR AND DIRECTS IT TO THE PROPER FUNCTION.
252
253
254 ;DEFAULT STATUS = 1
255 ;READ A CODE FROM THE GPIB
256 ;HOW MANY BYTES?
257 ;IF TWG, EXECUTE A COMMAND
258 ;OTHERWISE INTERPRET DATA
259 ;CODE = "RD"?
260 ;NO
261 ;YES, REQUIRES SPECIAL PROCESSING
262
263
264 ;MAXIMUM INPUT STRING LENGTH
265 ;MAXIMUM DATA BUFFER SIZE

```

```

259
260
261
262
263
264 001122          004767 000634          PC, DECOD          ;DECODE ASCII STRING INTO BINARY INTEGER
265 001122          022737 030507 0000000C      ;CODE = "G1"?
266
267 001126          022737 030507 0000000C      ;G1, @1C
268 001134          001004          108          ;GAIN FOR CHANNEL 1
269 001136          010337 0000000C      R3, @GAIN1
270 001142          000167 177666          RDCODE
271
272 001146          022737 031107 0000000C 108: ;CODE = "G2"?
273 001154          001004          208          ;GAIN FOR CHANNEL 2
274 001156          010337 0000000C      R3, @GAIN2
275 001162          000167 177646          RDCODE
276
277 001166          022737 031507 0000000C 208: ;CODE = "G3"?
278 001174          001004          308          ;GAIN FOR CHANNEL 3
279 001176          010337 0000000C      R3, @GAIN3
280 001202          000167 177626          RDCODE
281
282 001206          022737 047101 0000000C 308: ;AN, @1C
283 001214          001004          408          ;CODE = "AR"?
284 001216          010337 0000000C      R3, @ADS
285 001222          000167 177606          RDCODE
286
287 001226          022737 044501 0000000C 408: ;A1, @1C
288 001234          001004          508          ;CODE = "AI"?
289 001236          010337 0000000C      R3, @AUT
290 001242          000167 177566          RDCODE
291
292 001246          022737 040520 0000000C 508: ;PA, @1C
293 001254          001004          608          ;CODE = "PA"?
294 001256          010337 0000000C      R3, @PAD
295 001262          000167 177546          RDCODE
296
297 001266          022737 051120 0000000C 608: ;PR, @1C
298 001274          001004          708          ;CODE = "PR"?
299 001276          010337 0000000C      R3, @PTR
300 001302          000167 177526          RDCODE
301
302 001306          022737 051520 0000000C 708: ;PS, @1C
303 001314          001004          808          ;CODE = "PS"?
304 001316          010337 0000000C      R3, @STPT
305 001322          000167 177506          RDCODE
306
307 001326          022737 041516 0000000C 808: ;NC, @1C
308 001334          001004          908          ;CODE = "NC"?
309 001336          010337 0000000C      R3, @NCYC
310 001342          000167 177466          RDCODE
311
312 001346          022737 040510 0000000C 908: ;HA, @1C
313 001354          001004          PARX          ;CODE = "HA"?
314 001356          010337 0000000C      R3, @HAR
315 001362          000400          PARX          ;HOW MANY HARMONICS TO PROCESS

```

THIS PORTION OF THE PROGRAM HANDLES THE INPUT OF DATA PARAMETERS. RECEIVED BYTE STRING IS DECODED INTO AN INTEGER VARIABLE IN R3, AND THEN LOADED INTO THE VARIABLE SPECIFIED BY THE CODE WORD IC.

317	001364	022737	053161	0000000	PARX:	CMP	*"AV,@#IC	ICODE = "AV"?
318	001372	001004				BNE	20\$	
319	001374	010337	0500000			MOV	R3,@#AVCCOD	HAVERAGING PROCESS TO FOLLOW
320	001400	000167	177410			JMP	RDCODE	
321								
322	001404	022737	051504	0000000	20\$:	CMP	*"DS,@#IC	ICODE = "DS"?
323	001412	001004				BNE	30\$	
324	001414	010337	0000000			MOV	R3,@#DSTYLE	DISPLAY STYLE
325	001420	000167	177410			JMP	RDCODE	
326								
327	001424	022737	051107	0000000	30\$:	CMP	*"CV,@#IC	ICODE = "CV"?
328	001432	001004				BNE	40\$	
329	001434	010337	0000000			MOV	R3,@#GATED	GETTING FLAG
330	001440	000167	177370			JMP	RDCODE	
331								
332	001444	022737	042501	0000000	40\$:	CMP	*"AE,@#IC	ICODE = "AE"?
333	001452	001004				BNE	50\$	
334	001454	010337	0000000			MOV	R3,@#AERES	RETRY ERRORS COUNTER
335	001460	000167	177350			JMP	RDCODE	
336								
337	001464	022737	052105	0000000	50\$:	CMP	*"CT,@#IC	ICODE = "CT"?
338	001472	001004				BNE	60\$	
339	001474	010337	0000000			MOV	R3,@#CORTYP	COMPUTATION TYPE
340	001500	000167	177330			JMP	RDCODE	
341								
342	001504	022737	042104	0000000	60\$:	CMP	*"DD,@#IC	ICODE = "DD"?
343	001512	001004				BNE	70\$	
344	001514	010337	0000000			MOV	R3,@#DLUN	DISPLAY LUN
345	001520	000167	177310			JMP	RDCODE	
346								
347	001524	022737	046527	0000000	70\$:	CMP	*"WI,@#IC	ICODE = "WI"?
348	001532	001004				BNE	80\$	
349	001534	010337	0000000			MOV	R3,@#MODE	MESSAGE MODE
350	001540	000167	177270			JMP	RDCODE	
351								
352	001544	022737	041510	0000000	80\$:	CMP	*"RC,@#IC	ICODE = "RC"?
353	001552	001004				BNE	90\$	
354	001554	010337	0000000			MOV	R3,@#HTC	HARMONIC TO COMPUTE
355	001560	000167	177250			JMP	RDCODE	
356								
357	001564	022737	030520	0000000	90\$:	CMP	*"P1,@#IC	ICODE = "P1"?
358	001572	001004				BNE	100\$	
359	001574	010337	0000000			MOV	R3,@#AD1C	CHANNEL 1 PORT
360	001600	000167	177230			JMP	RDCODE	
361								
362	001604	022737	031120	0000000	100\$:	CMP	*"P2,@#IC	ICODE = "P2"?
363	001612	001004				BNE	110\$	
364	001614	010337	0000000			MOV	R3,@#AD2C	CHANNEL 2 PORT
365	001620	000167	177210			JMP	RDCODE	
366								
367	001624	022737	031520	0000000	110\$:	CMP	*"P3,@#IC	ICODE = "P3"?
368	001632	001004				BNE	120\$	
369	001634	010337	0000000			MOV	R3,@#AD3C	CHANNEL 3 PORT
370	001640	000167	177170			JMP	RDCODE	
371								

(THIS PORTION WILL READ THE 'REAL' PARAMETERS, SUCH AS  
 ; SIGNAL AND SAMPLE FREQUENCY; THIS IS NO LONGER USED.  
 ; DATA INPUT IS FOUR BYTES WHICH ARE MORE LITERALLY A  
 ; 32-BIT FLOATING VARIABLE IN PDP-11 FORMAT.

```

373
374
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377
378 001644
379 001644 022737 051506 020000C
380 001652 001907
381 001654 013737 000000C 000000C
382 001662 013737 000002C 000002C
383 001670 000115
384
385 001672 022737 041506 000000C 1000
386 001700 001907
387 001702 013737 000000C 000000C
388 001710 013737 000002C 000002C
389 001716 000402
390
391
392 001720 000167 000400 2000
393
394 001724 000167 177104 1000
395
396
397
398
399
400
401 001730
402 001730 012705 001752
403 001734 012737 006412 000000C
404 001742 004767 006222
405 001746 000167 177062
406
407 001752 000002 000000C 000000C MESARG: 2,10,;BLEN,PLAN
    001760 000000C

    ; FS,FS*2 ; IS THE CODE "FS"?
    ; 100$
    ; *IP,FS*2 ; SIGNAL FREQUENCY
    ; *IP+2,FS*2
    ; 100$
    ; *FC,FS*2 ; IS THE CODE "FC"?
    ; 200$
    ; *IP,FS*2 ; SAMPLE FREQUENCY
    ; *IP+2,FS*2
    ; 100$

    ; UNRECOGNIZED; ERROR
    ; RETURN FOR MORE INPUT

    ; ROUTINE FOR HANDLING "MESSAGE OF THE DAY"
    ; HOST MAY SEND UP TO 80-CHARACTER MESSAGE TO SLAVE TO
    ; BE DISPLAYED ON THE VOLTMETER

    ; MESARG,R5
    ; 6412,FS*2 ; REPLACE "R5" WITH CR-LF
    ; PC,WHITE ; OUTPUT STRING
    ; RDCODE
    
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;THIS SECTION WILL DECODE THE BYTE STRING FOLLOWING A
;SETUP COMMAND INTO AN INTEGER STORED IN R3.

DECOD:
MOV @#1BLN,R0 ;LENGTH OF RECEIVED STRING
SUB #2,R0 ;LESS CODE
MOV #1P,R1 ;ADDRESS OF RECEIVED STRING
CLR R3 ;CLEAR ACCUMULATED DATA WORD

;TO BE SAFE
;GET AN ASCII BYTE
;MAKE IT A BINARY DIGIT
;AND ADD TO DATA WORD
;DONE YET?
;FINISHED
;'SHIFT' LEFT
;NEXT DIGIT

10$:
CLR R2
MOV (R1)+,R2
SUB #60,R2
ADD R2,R3
DEC R0
BLE 20$
MUL #10,R3
BR 10$
RTS PC

20$:

;RUDIMENTARY HANDLING OF FLOATING-POINT EXCEPTIONS
;1. E. HALT ON ERROR: CLEAN UP ON CONTINUE (PROCEED)
;FLOATING-POINT INTERRUPT SERVICE

FPI$R:
MOV R0,-(SP)
MOV R1,-(SP)
STFPS R0
STST R0
HALT
BIC #100000,R0
LDFPS R0
MOV (SP)+,R1
MOV (SP)+,R0
RTI

FOISR:
HALT
RTI

;ERROR INTERRUPTS FROM (FOFTRAN) FUNCTIONS
;EC #ATAN2, #SIN, #SQRT

```





```

505
506
507
508 002324 012705 002360
509 002330 004767 005634
510 002334 012705 002350
511 002340 004767 005624
512
513 002344 000167 176464
514
515 002350 000002 000000 CERCL: 2,IC,IBLEN,DLUN
516 002356 000000
517 002360 000002 002406 CERCL: 2,CERPT,CERRL,DLUN
518 002366 000000 CERCL: 2,CERPT,CERRL,DLUN
519 002370 012 015 103 CERPT: .ASCII <12><15>*CODE ERROR: *
520 002376 117 104 105
521 002377 040 105 122
522 002401 117 122
523 002404 072 040
524
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529
530 002410 000016 CERCL: .EVEN
531 002416 000016 CERCL: .WORD 14.
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```

```

;THIS PORTION WILL TYPE OUT ERROR MESSAGES:
;CONDITION? UNRECOGNIZES CODE.

```

```

MOV #CERRA,R5 ;PRINT ERROR MESSAGE
PC,WRITE
MOV #CERRC,R5 ;PRINT ERRONEOUS CODE
PC,WRITE
JMP RDCODE ;RETURN FOR MORE INPUT

```

```
2,IC,IBLEN,DLUN
```

```
2,CERPT,CERRL,DLUN
```

```
.ASCII <12><15>*CODE ERROR: *
```

```
.EVEN
.WORD 14.
```

```

;THIS PORTION WILL HANDLE IRREGULAR INTERRUPTIONS,
;IE, PROCESS ABORTS FROM THE HOST PROCESSOR.
;ACTIVATED BY DEVICE TRIGGER (GET) ON BUS.
;ENTERED FROM GP10SR / INTERRUPT SERVICE ROUTINE

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```

```

MOV #STACK,SP ;INITIALIZE STACK
MOV #GP1NA,R3
JSR PC,GP1NA
MOV #ABO,R0 ;INITIALIZE IEEE-488 BUS CARD
PC,WRITE ;PRINT MESSAGE
MOV #0,R0 ;CLEAR PSW
RDCODE ;(ENABLE INTERRUPTS)
;RETURN FOR INPUT

```

```
2,ABMES,ABLEN,DLUN
```

```
.ASCII <15>*** FUNCTION ABORTED ***
```

```
.EVEN
.WORD 25.
```

```

544      THIS PORTION OF THE PROGRAM CONTROLS THE A/D CONVERSION
545      AND DMA LOADING OF MEMORY WITH DATA.
546
547      CHECK POINTS REQUESTED AGAINST BUFFER SIZE.
548      VERY A/D CONVERSION AND REPEAT IF ERROR OCCURS.
549      INCREMENT CONVERSION (AND ERROR) COUNTER.
550
551      002512      00600000      00000000      00000000
552      002512      00600000      00000000      00000000
553      002520      00600000      00000000      00000000
554      002522      00600000      00000000      00000000
555      002530      00600000      00000000      00000000
556
557      002532      004767      000022      100
558
559      002536      012705      002552      MACK:
560      002542      004767      000000      MACK:
561      002546      000167      176262      MACK:
562      002552      000002      000000      GPACK:
563      002560      013737      000000      DOAD:
564      002566      012705      002636      200:
565      002572      004767      000000      MACK:
566      002576      005237      000000      MACK:
567      002602      005737      000000      MACK:
568      002606      002403      000000      MACK:
569      002610      004767      004122      MACK:
570      002614      000207      000000      MACK:
571      002616      005237      000000      MACK:
572      002622      004767      004110      MACK:
573      002626      005337      000000      MACK:
574      002632      003355      000000      MACK:
575      002634      000207      000000      MACK:
576
577      002636      000006      000000      ADCOM:
578      002644      000000      000000      ADCOM:
579      002652      000000      000000      ADCOM:
580
581
582
583
584      002656      005937      000000      WAKE:
585      002662      005037      000000      WAKE:
586      002666      004767      004044      WAKE:
587      002672      000167      176136      WAKE:
588
589

```

THIS PORTION OF THE PROGRAM WILL LIST THE MAIN SETUP  
PARAMETERS ON THE LSIVM CONSOLE.

```

591
592
593
594 002676
595 002676
596 002704
597 002712
598 002720
599 002724
600 002730
601 002734
602 002740
603 002744
604 002750
605 002754
606 002762
607 002766
608 002772
609 002776
610 003002
611 003006
612 003010
613 003014
614
615 003016
616
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618 003030
619 003032
620 003034
621 003035
622 003036
623 003037
624 003064
625 003072
626 003074
627 003077
628 003100
629 003102
630 003106
631 003110
632 003112
633 003114
634 003116
635 003120
636 003122
637 003122

000025 000000C
012737 000001 000000C
012737 000000C 000000C
012737 000000C 000000C
012705 003042
004767 005240
012705 003022
004767 005320
012705 003064
004767 005220
012705 003032
017737 000000C 000000C
004767 005272
005237 000000C
005237 000000C
005237 000000C
005337 000000C
003403
004767 000066
000741

000167 176012
000002 000000C 000002
000002 000000C 000006
000002 003062
015 012
003055 101
040 050
000010
000002 003074
000000C 003100
051 040
040 075
000004
012700 100000
003106 005300
003110 000240
003112 000240
003114 000240
003116 000240
003120 001372
003122 000207

PARLST:
MOV #21, @LCTR
MOV #1, @LPTR
MOV #DATA, @LADD
MOV #LST1, R5
JSR PC WRITE
MOV #LSTNO, R5
JSR PC PRINT
MOV #LST2, R5
JSR PC WRITE
MOV #LSTDA, R5
MOV @LADD, @LDAT
JSR PC PRINT
INC @LADD
INC @LPTR
DEC @LCTR
BLE 20$
JSR PC, IFWAIT
BR 10$

20$: JMP RDCODE
LSTNO: 2, LPTR, 2, DLUN
LSTDA: 2, LDAT, 6, DLUN
LST1: 2, LST1, LSTL1, DLUN
LST1: .ASCII <15><12>"DATA ("
.EVEN
.WORD LSTL1-LIST1
LSTL1:
LST2: 2, L1ST2, LSTL2, DLUN
LIST2: .ASCII ')' =
.EVEN
.WORD LSTL2-L1ST2
IFWAIT:
MOV #100000, R0
DEC R0
NOP
NOP
NOP
NOP
BNE 10$
RTS PC

;NUMBER TO PRINT
;STARTING AT PARAMETER I
;AT ADDRESS "DATA"
;PRINT INITIAL DESCRIPTION
;PRINT VARIABLE INDEX
;PRINT INTERNAL PUNCTUATION
;PRINT INTEGER VARIABLE
;ANOTHER ONE?
;YES, WAIT TO SEE THIS ONE
;RETURN FOR MORE INPUT
;DELAY ROUTINE (1 SEC??)

```

!THIS PORTION OF THE PROGRAM IS DESIGNED TO SEND THE DATA  
!PARAMETERS BACK TO THE HOST PROCESSOR FOR EXAMINATION.

```

639
640
641 003124
642 012705 003140'
643 003124 004767 000000C
644 003130
645 003134 000167 175674
646 003140 000002 000000C 003146'
647 003146 000052
648
649
650
651
652
653
654
655

```

PARSND:  
PARA: RDCODE  
PSIZE: 42.

!OUTPUT DATA  
!RETURN FOR MORE INPUT

!THIS PORTION OF THE PROGRAM IS DESIGNED TO SEND ONE OF THE  
!THREE DATA BUFFERS BACK TO THE HOST PROCESSOR. THE INPUT  
!CODES ARE R1 (BUFFER 1), R2 (BUFFER 2), R3 (BUFFER 3).  
!THE BUFFERS CONTAIN 12-BIT BINARY WORDS.

!ALSO TO TRANSFER BACK THE WINDOW (RW)--FLOATING WORDS.  
!(PRECEDED BY COMPUTED AVERAGE).

ADSEND:

```

656 003150
657 013737 003356' 000004C
658 003150 023727 000000C 053522
659 003156 001015
660 003164 013702 000000C
661 003166 005202
662 003172 006302
663 003174 006302
664 003176 006302
665 003200 010237 000000C
666 003204 012705 003350'
667 003210 004767 000000C
668 003214 000167 175614
669

```

!LOAD ADDRESS INTO RAM  
!WRITE WINDOW?  
!NO  
!NUMBER OF FLOATING NUMBERS...  
!PLUS ONE FOR AVERAGE  
!... NUMBER OF BYTES  
!OUTPUTTED  
!RETURN FOR MORE INPUT

50:

```

670 003220 012701 000000C
671 003224 023727 000000C 030522
672 003232 001422
673 003234 023727 000000C 031122
674 003242 001412
675 003244 023727 000000C 031522
676 003252 001402 177044
677 003254 000167 000000C
678 003260 063701 000000C
679 003264 063701 000000C
680 003270 063701 000000C
681 003274 063701 000000C
682 003300 063701 000000C
683 003304 063701 000000C
684 003310 010137 000002C
685

```

!STORE ADDRESS  
!WHAT CODE? (BUFFER?)  
!ADD IN OFFSETS FOR  
!.. FOR BUFFERS 2 AND 3  
!AND FOR THE STARTING POINT  
!TWICE--WORD ADDRESS  
!NUMBER OF POINTS TO TRANSFER  
!DOUBLE FOR BYTES  
!OUTPUT TO THE BUS  
!RETURN FOR MORE INPUT

100:

```

686 003314 013702 000000C
687 003320 060202
688 003322 010237 000000C
689 003326 012707 000002 000000C
690 003334 012705 000000C
691 003340 004767 000000C
692
693 003344 000167 175464
694 003350 000002 000000C 000006C ADVA:
695 003356 000000C 000000C RANA:

```

!ADD IN OFFSETS FOR  
!.. FOR BUFFERS 2 AND 3  
!AND FOR THE STARTING POINT  
!TWICE--WORD ADDRESS  
!NUMBER OF POINTS TO TRANSFER  
!DOUBLE FOR BYTES  
!OUTPUT TO THE BUS  
!RETURN FOR MORE INPUT

Line	Code	Address	Comment	Operation	Register	Value	Label
697	COMPUTATION *SNEGATIVE*						
698	11		CONVERT DATA TO REAL, SCALED NUMBERS				
699	12		MULTIPLY SEQUENCE BY WINDOW IF DESIRED				
700	13		AVERAGE SEQUENCE TO FEWER SAMPLES (IF DESIRED)				
701	14		REQUEST SPECIFIC COMPUTATION TYPE				
702	15		A. DATA IS SCALED FOR GAIN IN A/D CONVERTER				
703	16		B. DATA IS CORRECTED FOR WINDOW ATTENUATION IF NECESSARY.				
704	17		TRANSIENT COMPUTED BY HOST.				
705							
706	003366	923737		LD	R0	001120	00000000
707	003369	992003		LD	R0	175436	
708	003366	008000		LD	R0	000000	
709	003379	008000		LD	R0	000000	
710	003372	009107		LD	R0	000000	
711							
712	003376	113700		LD	R0	000000	
713	003402	042700		LD	R0	177774	
714	003406	005000		LD	R0	000000	
715	003410	006100		LD	R0	000000	
716	003412	006100		LD	R0	000000	
717	003414	010037		LD	R0	000000	
718							
719	003420	013737		LD	R0	000000	
720	003426	013737		LD	R0	000000	
721	003434	023727		LD	R0	000000	
722	003442	001510		LD	R0	000000	
723	003444	023727		LD	R0	000000	
724	003452	001512		LD	R0	000000	
725	003454	004767		LD	R0	000000	
726	003460	123727		LD	R0	000000	
727	003466	001470		LD	R0	000000	
728	003470	005737		LD	R0	000000	
729	003474	001402		LD	R0	000000	
730	003476	004767		LD	R0	000654	
731	003502	005737		LD	R0	000000	
732	003506	001402		LD	R0	000000	
733	003510	004767		LD	R0	000704	
734							
735	003514	123727		LD	R0	000000	
736	003522	001422		LD	R0	000000	
737	003524	123727		LD	R0	000000	
738	003532	001424		LD	R0	000000	
739	003534	123727		LD	R0	000000	
740	003542	001426		LD	R0	000000	
741	003544	123727		LD	R0	000000	
742	003552	001430		LD	R0	000000	
743	003554	123727		LD	R0	000000	
744	003562	001432		LD	R0	000000	
745	003564	000167		LD	R0	176534	

```

747      ;COMPUTE AS SPECIFIED.....
748      ;RETURN DATA SPECIFIED.....
749
750 003570 004767 001146      ;COMPUTE AMPLITUDE, PHASE OF HARMONIC
751 003574 004767 001256      ;RETURN DATA TO HOST
752 003600 000167 175230
753
754 003604 004767 001054      ;COMPUTE AMPLITUDE, PHASE OF FUNDAMENTAL
755 003610 004767 001242      ;RETURN DATA TO HOST
756 003614 000167 175214
757
758 003620 004767 000764      ;COMPUTE AMPLITUDE, PHASE, DISTORTION
759 003624 004767 001204      ;RETURN DATA TO HOST
760 003630 000167 175200
761
762 003634 004767 001342      ;COMPUTE TIME-AVERAGED PARAMETERS
763 003640 004767 001514      ;RETURN DATA TO HOST
764 003644 000167 175164
765
766 003650 004767 001420      ;COMPUTE ABSOLUTE PEAK DATA
767 003654 004767 001544      ;RETURN DATA TO HOST
768 003660 000167 175150
769
770 003664 004767 001644      ;GET POWER IN CHANNELS #1-#2
771 003670 004767 002920      ;RETURN DATA TO HOST
772 003674 000167 175134
773
774 003700 004767 002030      ;GET JOINT MOMENT AND COVARIANCE
775 003704 004767 002174      ;RETURN DATA TO THE HOST
776 003710 000167 175120

```

```

778 ;ROUTINE TO COMPUTE A SINUSOIDAL WAVEFORM FOR TESTING.
779 ;V(1)=GAIN(N)*COS((1-1)*2*PI*NCYC/PTR)*2048./10.
780 ;TEST ROUTINE == NOT NECESSARILY NEAT OR EFFICIENT!
781
782 003714      *ADBUF,R0      ;START OF FIRST BUFFER
783 003714      @*STPT,R0     ;TRUE START OF FIRST WAVEFORM
784 003720      ADD           ;(ADD STARTING POINT--TWICE FOR BYTES)
785 003724      @*STPT,R0     ;TRUE END OF FIRST WAVEFORM
786 003730      @*PTR,R0      ;(ALSO TWICE)
787 003734      @*PTR,R0      ;FIND END OF NEXT WAVEFORM
788 005740      @*PAD,R1      ;BY ADDING TWICE POINTS TO A/D
789 003744      @*PAD,R1      ;TRUE END OF SECOND WAVEFORM
790 003750      @*PAD,R2      ;FIND END OF LAST WAVEFORM
791 003752      @*PAD,R2      ;BY ADDING TWICE POINTS TO A/D
792 003756      @*PAD,R2      ;TRUE END OF THIRD WAVEFORM
793 003762      R1,R2
794
795 003764      SETF
796 003766      LDCIF        @*NCYC,AC2
797 003772      LDCIF        @*PTR,AC1
798 003776      DIVF         AC1,AC2
799 004000      MULF         @*TWOPI,AC2
800
801 004004      @*PTR,R3
802 004010      R3,AC0
803 004012      NI,AC0
804 004016      AC2,AC0
805 004020      R0,-(SP)
806 004022      R1,-(SP)
807 004024      R2,-(SP)
808 004026      R3,-(SP)
809 004030      STF         AC2,-(SP)
810 004032      JSR         PC,$$COS
811 004036      @*FFAC,AC0
812 004042      (SP)+,AC2
813 004044      (SP)+,R3
814 004046      (SP)+,R2
815 004050      (SP)+,R1
816 004052      (SP)+,R0
817
818
819 004054      LINCIF       @*GAIN1,AC1
820 004060      MULF        AC0,AC1
821 004062      STCF1      AC1,-(R0)
822
823 004064      LDCIF       @*GAIN2,AC1
824 004070      MULF        AC0,AC1
825 004072      STCF1      AC1,-(R1)
826
827 004074      LDCIF       @*GAIN3,AC1
828 004100      MULF        AC0,AC1
829 004102      STCF1      AC1,-(R2)
830
831 004104      SOB         R3,100
832 004106      JMP         RDCODE
833 004112      .FLT2     6.2631854
834 004116      .FLT2     -1.

```



```

836 ;ROUTINE TO "FLOAT" THE A/D DATA BUFFER FOR PROCESSING
837 ; CONVERT INTEGER TO FLOATING NUMBERS IN VOLTS IN RBUF.
838
839 004122 010046
840 004122 010146
841 004124 010246
842 004126 010246
843 004130 170001
844
845 004132 000000C 000000C
846 004136 123727 000000C 000061
847 004144 001422
848 004146 123727 000000C 000062
849 004154 001412
850 004156 123727 000000C 000063
851 004164 001402
852 004166 000167 176132
853 004172 063701 000000C
854 004176 063701 000000C
855 004202 063701 000000C
856 004206 063701 000000C
857 004212 063701 000000C
858 004216 063701 000000C
859
860 004222 013700 000000C
861 004226 012702 000000C
862 004232 042711 170000
863 004236 021127 004000
864 004242 002402
865 004244 052711 170000
866
867 004250 177021
868 004252 171037 004272
869
870 004256 174022
871 004260 077014
872
873 004262 012602
874 004264 012601
875 004266 012600
876 004270 000207
877
878 004272 036240 000000

```

FLCT:

MOV R0,-(SP)  
MOV R1,-(SP)  
MOV R2,-(SP)  
SETF

SAVE REGISTERS ON STACK

MOV #ADBUF,R1  
CMPB @1CN,\*1  
BEQ 30\$  
CHPD @1CN,\*2  
BEQ 20\$  
CMPB @1CN,\*3  
BEQ 10\$  
JMP CERR

STORE ADDRESS  
STARTING WHERE?

ADD IN OFFSETS FOR

FOR BUFFERS 2 AND 3

AND FOR THE STARTING POINT  
TWICE FOR BYTES

10\$:

20\$:

30\$:

100\$:

110\$:

FFAC:

CLEAR TO TWELVE BITS  
IS IT NEGATIVE?  
NO  
YES--SIGN EXTEND

CONVERT TO FLOATING  
DIVIDE BY HALF FULL SCALE (11 BITS)  
MULTIPLY BY HALF FULL SCALE (10V)

RESTORE REGISTERS FROM STACK

i = 19,2048.

```

880 ROUTINE TO READ THE VALUES OF A SEQUENCE-WINDOW
881 FROM THE HOST.
882 NOTE THAT THE WINDOW MUST MATCH THE ENTIRE SEQUENCE
883 LENGTH (PTR=PTC).
884
885 MOV *CPWIN,R5
886 PC,CPIN
887
888
889
890
891
892
893
894
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928

```

WINDOW

109:

108:

WINDOW:

108:

208:

```

012765 004344
001767 0000000
179409
012700 0000000
013701 0000000
170001
077102
177137 0000000
174401 0000000
173037 0000000
003167 174470
000002 0000000 004354
0000000
0000000
0000000
005737 0000000
001001
000207
012700 0000000
013701 0000000
012702 0000000
170001
172410
172522
171001
174020
077105 000207
000207

```

```

;INITIALIZE SUM
;ADDRESS OF WINDOW DATA
;POINTS TO AVERAGE
;SUM WINDOW
;POINTS USED
;AVERAGED VALUE
;STOPPED
;DESIREDF?
;YES
;BUFFER ADDRESS
;POINTS TO BE WINDOWED
;WINDOW ADDRESS
;DATA WORD
;WINDOW WORD
;PRODUCT
;STORE BACK TO DATA ARRAY
;CONTINUE TILL DONE

```

!SUBROUTINE TO AVERAGE A SEQUENCE OF CYCLES---  
 !NOTE THAT IF THE NUMBER OF POINTS PER SUBSEQUENCE IS INTEGRAL,  
 !THE NCYC CYCLES CAN BE AVERAGED INTO NCYF CYCLE'S WORTH  
 !OF DATA, MAKING SUBSEQUENT COMPUTATION LESS TIME-CONSUMING.

```

930
931
932
933
934
935 004420      005737      000000C      BAVAR:
936 004420      001001
937 004424      000207
938 004426      000207
939 004430      022737      000000C 5$:
940 004436      001001
941 004440      000207
942
943 004442      013701      000000C      10$:
944 004446      006700
945 004450      071037
946 004454      005701
947 004456      001401
948 004460      000207
949
950 004462      022700      0000001      13$:
951 004466      001001
952 004470      000207
953 004472      010037      000000C      14$:
954
955 004476      013701      000000C
956 004502      070137      000000C
957 004506      006700
958 004510      071037      000000C
959 004514      005701
960 004516      001401
961 004520      000207
962 004522      013737      000000C 20$:
963
964
965 004530      010004
966 004532      010037
967 004536      006300
968 004540      006300
969 004542      013701
970 004546      012702      000000C
971 004552      012703      000000C
972 004556      170001
973 004560      177101
974
975 004562      170400      25$:
976
977 004564      172012      30$:
978 004566      060002
979 004570      077103
980 004572      174401
981 004574      174023
982 004576      010302
983 004600      013701      000000C
984 004604      077412
985
986 004606      000207      FC
    
```

```

988
989
990
991
992
993
994
995 004610
996 004610 012705 004642
997 004614 004767 000000C
998 004620 172437 000000C
999 004624 004757 000640
1000 004630 004767 000662
1001 004634 174037 000000C
1002 004640 000207
1003 004642 000007 000000C 000000C AMAG:
004650 000000C 000000C 000000C
004656 000000C 000000C 000000C

1004
1005 004664
1006 004664 012705 004716
1007 004670 004767 000000C
1008 004674 172437 000000C
1009 004700 004767 000564
1010 004704 004767 000606
1011 004710 174037 000000C
1012 004714 000207
1013 004716 000007 000000C 000000C ADFT:
004724 000000C 000000C 000000C
004732 000000C 004740 000000C
004740 000000

1014
1015
1016
1017 004742
1018 004742 013705 000000C
1019 004746 005205
1020 004750 070537 000000C
1021 004754 010537 000000C
1022
1023 004760 012705 005012
1024 004764 004767 000000C
1025 004770 172437 000000C
1026 004774 004767 000470
1027 005000 004767 000512
1028 005004 174037 000000C
1029 005010 000207

1030
1031 005012 000007 000000C 000000C ASL:
005020 000000C 000000C 000000C
005026 000000C 004740 000000C

;COMPUTE THE VOLTAGES VIA DISCRETE FOURIER TRANSFORMS (DFT):
;TYPE "B" -- VOLTAGE AND PHASE OF NTH HARMONIC;
;TYPE "C" -- VOLTAGE AND PHASE OF FUNDAMENTAL;
;TYPE "D" -- VOLTAGE, PHASE, AND DISTORTION OF FUNDAMENTAL.
;VOLTAGES ARE CORRECTED FOR A/D GAIN AND ARE RMS.
;DISTORTION IS IN PERCENTAGE.

MOV *AMAG,R5 ;COMPUTE THE DFT VALUES AND DISTORTION
JSR PC,DAPD ;DCHTZL-TYPE
LDF @*VDFT,AC0 ;CORRECT FOR A/D GAIN
JSR PC,DEGAIN ;AND FOR WINDOW IF USED
STF AC0,@*VDFT
RTS PC

7,RBUF,PTC,NCYF,VDFT,PDFT,DIST,HAR,IERR

GDFT:
MOV *ADFT,R0 ;COMPUTE THE DFT VALUES (NO DISTORTION)
JSR PC,DAPD ;DCHTZL-TYPE
LDF @*VDFT,AC0 ;CORRECT FOR A/D GAIN
JSR PC,DEGAIN ;AND FOR WINDOW IF USED
STF AC0,@*VDFT
RTS PC

7,RBUF,PTC,NCYF,VDFT,PDFT,DIST,NONE,IERR

.WORD 0

;HARMONIC TO COMPUTE
;LINE NUMBER (1 = FUNDAMENTAL)
;NUMBER OF CYCLES OF THIS HARMONIC

MOV @*HTC,R5
INC R5
MUL @*NCYF,R5
MOV R5,@*NCYHA

MOV *ASL,R0
JSR PC,DAPD ;COMPUTE SFDFT
LDF @*VDFT,AC0 ;CORRECT FOR GAIN
JSR PC,DEGAIN ;AND FOR WINDOW IF USED
STF AC0,@*VDFT
RTS PC

7,RBUF,PTC,NCYHA,VDFT,PDFT,DIST,NONE,IERR

```

```

1033
1034
1035
1036 005034
1037 005034 012705 005046'
1038 005040 004767 000000G
1039 005044 000415
1040 005046 000002 000000G 005054'
1041 005054 000014 TDFTA:
1042 N12:
1043 005056 TDFT:
1044 005056 012705 005070'
1045 005062 004767 000000G
1046 005066 000413
1047 005070 000002 000000G 005076'
1048 005076 000010 TDFA:
1049 NB:
1050 005100 172437 000000G
1051 005104 012700 000000G
1052 005110 063700 000000G
1053 005114 174010
1054 005116 172437 000000G
1055 005122 012700 000000G
1056 005126 063700 000000G
1057 005132 174010
1058 005134 172437 000000G
1059 005140 012700 000000G
1060 005144 063700 000000G
1061 005150 174010 000000G
1062 005152 172437 000000G
1063 005156 172537 000000G
1064 005162 173001
1065 005164 174037 000000G
1066
1067 005170 004767 001752
1068 005174 004767 002364
1069 005200 000207

;ROUTINE TO RETURN DATA TO THE HOST.
;ALSO TO SET UP ON-LINE DISPLAY VALUES.

MOV #TDFTA,R5
JSR PC,CPOUT
BR DSLD
2.DFD,N12
.WORD 12.

;OUTPUT VOLTAGE, PHASE, DISTORTION

MOV #TDFA,R5
JSR PC,CPOUT
BR DSL
2.DFD,NB
.WORD 8.

;OUTPUT VOLTAGE, PHASE

LDI #DIST,AC0
MOV #DIST1,R0
ADD #CHNO,R0
STF AC0,(R0)
LDF #VDF1,AC0
MOV #RHSV1,R0
ADD #CHNO,R0
STF AC0,(R0)
LDF #PDFT,AC0
MOV #PHAS1,R0
ADD #CHNO,R0
STF AC0,(R0)
LDF #PHAS1,AC0
LDF #PHAS2,AC1
SUBF AC1,AC0
STF AC0,#RPHAS

;LOAD DATA FOR DISPLAY ROUTINES
;BASE ADDRESS FOR DISTORTIONS
;PARTICULAR ONE AVAILABLE
;LOAD IT
;SAME FOR VOLTAGE

;SAME FOR PHASE

;COMPUTE RELATIVE PHASE

;POSSIBLE---ONE CHANNEL DISPLAY
;POSSIBLE---TWO CHANNEL/RELATIVE PHASE
PC,CH1DIS
PC,CH5DIS
PC

```

```

; COMPUTE THE VOLTAGE PARAMETERS BY RMS TIME-AVERAGE:
; TYPE "E" -- GET RMS, PEAK, AND DC VOLTAGES.
; TYPE "F" -- GET ABS(PEAK) VOLTAGE ONLY.
; VOLTAGES ARE CORRECTED FOR A/D CONVERTER GAIN.
    
```

```

GRMSV:
MOV      *ARMSV,R5          ; CALL RMS COMPUTATION
JSR      PC,CRMS
LDF      @RMSV,AC0         ; CORRECT FOR A/D GAIN (RMSV)
JSR      PC,DECALN
STF      AC0,@RMSV
LDF      @DCV,AC0         ; CORRECT FOR A/D GAIN (DV)
JSR      PC,DECALN
LDF      AC0,@DCV
STF      @PEKVP,AC0       ; CORRECT FOR A/D GAIN (MAXIMUM)
JSR      PC,DECALN
LDF      AC0,@PEKVM,AC0   ; CORRECT FOR A/D GAIN (MINIMUM)
JSR      PC,DECALN
STF      AC0,@PEKVM
RTS      PC
    
```

```

GPEKO:
MOV      *RBUF,R1          ; FIND PEAK ONLY
MOV      @P1C,R0
LDF      (R1)+,AC0
ABSF    AC0
DEC      ...
LDF      (R1)+,AC1
ABSF    AC1
CHPF    AC1,AC0
CFCC
BLE
LDF      AC1,AC0
SOB     R0,100
JSR     PC,DECALN
STF     AC0,@PEKVP
RTS     PC
    
```

```

        100:
        200:
        6, PTC,RBUF,RMSV,DCV,PEKVP,PEKVM
    
```

1071	005292	012705	005342'			
1072	005292	004767	000000C			
1073	005206					
1074						
1075						
1076	005212	172437	000000C			
1077	005216	004767	000246			
1078	005222	174037	000000C			
1079	005226	172437	000000C			
1080	005232	004767	000232			
1081	005236	174037	000000C			
1082	005242	172437	000000C			
1083	005246	004767	000216			
1084	005252	174037	000000C			
1085	005256	172437	000000C			
1086	005262	004767	000262			
1087	005266	174037	000000C			
1088	005272	000267				
1089						
1090						
1091						
1092						
1093						
1094	005274	012701	000000C			
1095	005274	013700	000000C			
1096	005300	172421				
1097	005304	170600				
1098	005306	005300				
1099	005310	172521				
1100	005312	170601				
1101	005314	173401				
1102	005316	170000				
1103	005320	003401				
1104	005322	172401				
1105	005324	077067				
1106	005326	004767	000134			
1107	005330	174037	000000C			
1108	005334	000267				
1109	005340					
1110						
1111	005342	000006	000000C			
1112	005350	000000C	000000C			
1113	005356	000000C				

```

1113
1114
1115
1116 005360 012705 005414'
1117 005360 004767 000000G
1118 005364 004767 000000G
1119 005370 172437 000000G
1120 005374 012700 000000G
1121 005400 063700 000000G
1122 005404 174010
1123 005406 004767
1124 005412 000207
1125 005414 000002
1126 005422 000020
1127
1128 005424
1129 005424 012705 005460'
1130 005430 004767 000000G
1131 005434 172437 000000G
1132 005440 012700 000000G
1133 005444 063700 000000G
1134 005450 174010
1135 005452 004767 002110
1136 005456 000207
1137 005460 000002
1138 005466 000004
1139
1140
1141
1142
1143
1144
1145
1146
1147 005470
1148 005470 113700 000000G
1149 005474 042700 177774
1150 005500 060000
1151 005502 062700 177776G
1152 005506 170001
1153 005510 177110
1154
1155 005512 174401
1156 005514 000207
1157
1158 005516
1159 005516 005737 000000G
1160 005522 001403
1161 005524 172537 000000G
1162 005530 174401
1163 005532 000207

;SUBROUTINES TO TRANSMIT RMS DATA TO THE HOST.
;ALSO PREPARATION FOR REAL-TIME DISPLAYS.

MOV #TMSA,R5
JSR PC,GPOUT
LDF @RMSV,AC0
MOV #RMSV1,R0
ADD @CHNO,R0
STF AC0,(R0)
JSR PC,CH4DIS
RTS PC
2,RMD,N16
.WORD 16.

;TRANSFER THE DATA BACK
;STORE FOR DISPLAYS
;BASE ADDRESS OF RMS DATA
;OFFSET FOR PARTICULAR CHANNEL

TMSV:
MOV #TMSA,R5
JSR PC,GPOUT
LDF @RMSV,AC0
MOV #RMSV1,R0
ADD @CHNO,R0
STF AC0,(R0)
JSR PC,CH4DIS
RTS PC
2,RMD,N16
.WORD 16.

;TRANSFER PEAK VOLTAGE ONLY

TPEK:
MOV #TPEKA,R5
JSR PC,GPOUT
LDF @PEKVP,AC0
MOV #RMSV1,R0
ADD @CHNO,R0
STF AC0,(R0)
JSR PC,CH4DIS
RTS PC
2,PEKVP,R4
.WORD 4.

;SUBROUTINE TO CONVERT THE COMPUTED VOLTAGES TO TRUE VALUES
;BY REMOVING THE GAIN OF THE A/D CONVERTERS.
;ALSO TO CORRECT DATA COMPUTED WITH A KAISER WINDOW, BY
;DIVIDING THE RESULTING MAGNITUDE BY THE WINDOW AVERAGE.

DEGAIN:
MOV @ICN,R0
BIC #177774,R0
ADD R0,R0
ADD #GAIN-2,R0
SETF (R0),AC1
LDCIF (R0),AC1
DIVF AC1,AC0
RTS PC

;SUBROUTINE TO REMOVE CHANNEL GAIN
;EITHER "1", "2", OR "3"
;EITHER 1, 2, OR 3
;OFFSET TIMES TWO
;BASE ADDRESS PLUS OFFSET FOR GAIN

;GET GAIN
;VOLTAGE IN AC0 ALREADY
;VDFT = VDFT / GAIN

;SUBROUTINE TO REMOVE WINDOW FACTOR
;USING A WINDOW?
;NOT THIS TIME
;GET AVERAGE (WAV)
;VDFT = VDFT / WAV

DEWIND:
TST @WMODE
REQ 100
LDF @WAV,AC1
DIVF AC1,AC0
RTS PC
100:

```

```

1165
1166
1167
1168
1169
1170 005534
1171 005534 010046
1172 005536 010146
1173 005540 010246
1174 005542 170001
1175 005544 012701 000000G
1176 005550 063701 000000G
1177 005554 063701 000000G
1178 005560 010102
1179 005562 063702 000000G
1180 005566 063702 000000G
1181 005572 013700 000000G
1182
1183 005576 170402
1184
1185
1186 005600 042711 170000
1187 005604 021127 004000
1188 005610 002402
1189 005612 052711 170000
1190 005616 177021
1191 005620 171037 004272
1192
1193 005624 177137 000000G
1194 005630 174401
1195
1196
1197 005632 042712 170000
1198 005636 021227 004000
1199 005642 002402
1200 005644 052712 170000
1201 005650 177122
1202 005652 171137 004272
1203
1204 005656 171001
1205 005660 177137 000000G
1206 005664 174401
1207 005666 172200
1208 005670 077035
1209
1210 005672 177037 000000G
1211 005676 174600
1212 005700 174237 000000G
1213 005704 012602
1214 005706 012601
1215 005710 012600
1216 005712 000207
1217
1218 005714 012705 005726
1219 005720 004767 000000G
1220 005724 000207
1221 005726 000002 000000G 005466

```

```

;SUBROUTINES TO COMPUTE THE TRUE (PSEUDO) POWER
;ASSUMING VOLTAGE AND CURRENT ARE IN CHANNELS #1-#2
; PWR/K = SUM (V1(J)*V2(J)) / N <FOR J=1..N>
;PSEUDO, FOR TRUE CURRENT IS UNKNOWN. I(J) = K*V2(J).

```

```

GPWR:
MOV R0, -(SP)
MOV R1, -(SP)
MOV R2, -(SP)
SETF
MOV #ADBUF, R1
ADD @STPT, R1
ADD @STPT, R1
MOV R1, R2
ADD @PAD, R2
ADD @PAD, R2
MOV @PTR, R0
; COMPUTE PSEUDO-POWER
; SAVE REGISTERS ON STACK
; STORE ADDRESS OF VOLTAGE
; INCLUDING STARTING POINT
; TIMES TWO (INTEGER ADDRESS)
; STORE ADDRESS OF CURRENT
; OFFSET TO SECOND BUFFER
; ALREADY INCLUDES STARTING POINT OFFSET
; AND POINTS TO COMPUTE

```

```

CLRFB AC2
; INITIALIZE SUM
; FLOAT VOLTAGE.....
; CLEAR TO TWELVE BITS
; IS IT NEGATIVE?
; NO
; YES--SIGN EXTEND
; CONVERT TO FLOATING
; DIVIDE BY HALF FULL SCALE (11 BITS)
; MULTIPLY BY HALF FULL SCALE (10V)
; GET GAIN OF VOLTAGE CHANNEL
; CONVERT TO TRUE VOLTS

```

```

BIC #170000, (R1)
CMP (R1), #4000
BLT 1108
BIS #170000, (R1)
LDCIF (R1)+, AC0
MULF @FFAC, AC0
LDCIF @GAIN1, AC1
DIVF AC1, AC0
; FLOAT CURRENT.....
; CLEAR TO TWELVE BITS
; IS IT NEGATIVE?
; NO
; YES--SIGN EXTEND
; CONVERT TO FLOATING
; DIVIDE BY HALF FULL SCALE (11 BITS)
; MULTIPLY BY HALF FULL SCALE (10V)
; E(I)*I(I)
; GET GAIN OF CURRENT CHANNEL
; CORRECT CURRENT FOR GAIN
; SUM PRODUCTS
; NEXT?

```

```

LDCIF @PTR, AC0
DIVF AC0, AC2
STF AC2, @POWER
MOV (SP)+, R2
MOV (SP)+, R1
MOV (SP)+, R0
RTS PC
; POINTS DONE
; TAKE AVERAGE
; AND SAVE
; RESTORE REGISTERS FROM STACK
; RETURN PSEUDO-POWER

```

```

1008:
1108:
1208:
1308:
TPWR:
TPWRA:

```



```

1223      005734      004767      177574      00000000      GJC:
1224      1227 005734      172437      00000000
1225      1228 005740      174037      00000000
1226      1229 005744
1227      1230
1228      1231 005750      112737      00000000
1229      1232 005756      004767      000050
1230      1233 005762      172437      00000000
1231      1234 005766      174037      00000000
1232      1235 005772      112737      000062      00000000
1233      1236 006000      004767      000026
1234      1237 006004      172437      00000000
1235      1238 006010      172537      00000000
1236      1239 006014      171100
1237      1240 006016      172437      00000000
1238      1241 006022      173001
1239      1242 006024      174037      00000000
1240      1243 006030      000207
1241      1244
1242      1245 006032
1243      1246 006032      013737      00000000      00000000
1244      1247 006040      013737      00000000      00000000
1245      1248 006046      004767      176050
1246      1249 006052      005737      00000000
1247      1250 006056      001402
1248      1251 006060      004767      176272
1249      1252 006064      005737      00000000
1250      1253 006070      001402
1251      1254 006072      004767      176322
1252      1255 006076      004767      177100
1253      1256
1254      1257 006102      000207
1255      1258
1256      1259 006104      012705      006116
1257      1260 006110      004767      00000000
1258      1261 006114      000207
1259      1262 006116      000002      00000000      005076      TJCA:

```

```

;SUBROUTINE TO COMPUTE THE SECOND JOINT MOMENT AND THE
;COVARIANCE (ANALOGOUS TO TOTAL POWER AND AC POWER).

```

```

;GET SECOND JOINT MOMENT (*POWER*)

```

```

;FOR CHANNEL 1
;GET MEAN VALUE

```

```

;FOR CHANNEL 2
;GET MEAN VALUE

```

```

;DC POWER* IN ACI

```

```

;SAVE COVARIANCE

```

```

;FLOAT/AVERAGE/WINDOW/GET RMSV
;POINTS TO COMPUTE ON
;CYCLES TO COMPUTE
;FLOAT THE BUFFER
;MULTIPLY WINDOW?
;NO
;YES
;AVERAGE THE CYCLES?
;NO
;YES
;COMPUTE RMS DATA

```

```

;PTR,*PTC
;NCYC,*NCYT
PC,FLUT
;MODE
100
PC,WINDOW
;AVCCOD
200
PC,BAVER
PC,CRMSV
PC

```

```

;RETURN DATA TO HOST

```

```

1264
1265
1266
1267
1268
1269
1270
1271
1272
1273
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1295
1296
1297
1298
1299
1300
1301
1302
1303
1304
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1318
1319
1320

;VOLTMETER SIMULATION ROUTINE.
;IN THIS STATE THE SLAVE PROCESSOR WILL PRETEND TO BE A
;SAMPLING VOLTMETER AND MEASURE/COMPUTE/DISPLAY AS FAST
;AS IT CAN. PRIOR TO THIS MODE BEING ENTERED, VARIOUS
;PARAMETERS NEED TO BE SET UP (AS IN THE NORMAL COMPUTER-
;CONTROLLED SITUATION). THIS MODE PROCEEDS INDEPENDENT
;OF ANY HOST CONTROL, ALTHOUGH THE HOST CAN ABORT THE MODE
;AND THE SLAVE WILL REVERT TO THE INPUT-AWAITING MODE.

MOV @PTR,@PTC ;POINTS TO COMPUTE ON
MOV @NCYC,@NCYF ;CYCLES TO COMPUTE
JSR PC,DOAD ;DO MEASUREMENT

;NONE
;CHANNEL 1
;CHANNEL 2
;CHANNEL 3
;COMPUTE VIA DFT
;COMPUTE VIA DFT
;COMPUTE VIA DFT
;VOLTAGES
;COMPUTE VIA RMS
;TWO-CHANNEL AND PHASE
;DISPLAY STYLE ERROR

;NO DISPLAY (ALREADY DONE)
;DISPLAY MODES 1,2,3 (SINGLE CHANNEL)
;STYLE = CHANNEL NUMBER
;LOAD AS (1,2,3)
;BUT CONVERT TO (0,1,2)
;THEN (0,2,4)
;THEN (0,4,8)
;CONVERT (1-3) TO ASCII TO FOOL
;INPUT MODE TO EXPECT CHANNEL N
;LOAD DATA
;MULTIPLY BY WINDOW
;AVERAGE DATA
;COMPUTE DATA
;ACCESS VOLTAGE

;ACCESS PHASE

;ACCESS DISTORTION

VMDISP:
MOV @DSTYLE,@0
BEQ 400
CHP @DSTYLE,@1
BEQ 500
CHP @DSTYLE,@2
BEQ 500
CHP @DSTYLE,@3
BEQ 500
CHP @DSTYLE,@4
BEQ 800
CHP @DSTYLE,@5
BEQ 900
HALT
JMP RDCODE

BR VMDISP

400:
MOV @DSTYLE,R0
MOV R0,@CHNO
DEC @CHNO
ROL @CHNO
ROL @CHNO
ADD @R0,R0
MOV @R0,@R1
JSR PC,FLOT
JSR PC,WINDO
JSR PC,BAVER
JSR PC,GDFTD
LDF @VDFT,AC0
MOV @RMSV1,R0
ADD @CHNO,R0
AC0,(R0)
STF @PDFT,AC0
LDF @PHAS1,R0
MOV @CHNO,R0
ADD @R0,(R0)
STF @DIST,AC0
LDF @DIST1,R0
MOV @CHNO,R0
ADD @R0,(R0)
STF PC,CHIDIS
BR 1000

500:
MOV 000000C 0000000C
MOV 013737 013737
MOV 000000C 0000000C
MOV 174414 004767
MOV 000000C 0000000
MOV 023727 023727
MOV 001427 001427
MOV 006154 006154
MOV 001424 001424
MOV 006164 006164
MOV 023727 023727
MOV 001420 001420
MOV 006174 006174
MOV 023727 023727
MOV 001414 001414
MOV 006202 006202
MOV 006204 006204
MOV 006212 006212
MOV 006222 006222
MOV 001556 001556
MOV 006224 006224
MOV 000167 000167
MOV 172602 172602

MOV 013700 013700
MOV 010037 010037
DEC 000000C 0000000C
ROL 006137 006137
ROL 006254 006254
MOV 006260 006260
MOV 006264 006264
MOV 006270 006270
MOV 004767 004767
MOV 006274 006274
MOV 004767 004767
MOV 006304 006304
MOV 006310 006310
MOV 006314 006314
MOV 006320 006320
MOV 006324 006324
MOV 006326 006326
MOV 006332 006332
MOV 006336 006336
MOV 006342 006342
MOV 006344 006344
MOV 006350 006350
MOV 006354 006354
MOV 006360 006360
MOV 004767 004767
MOV 006366 006366
MOV 000560 000560
MOV 000561 000561

```

```

1322      006370 112737 000061 000000C 808:
1323      006376 004767 175320
1324      006402 004767 175750
1325      006406 004767 176006
1326      006412 004767 176564
1327      006416 172437 000000C
1328      006422 174037 000000C
1329      006426 112737 000062 000000C
1330      006434 013737 000000C 000000C
1331      006442 013737 000000C 000000C
1332      006450 004767 175446
1333      006454 004767 175676
1334      006460 004767 175734
1335      006464 004767 176512
1336      006470 172437 000000C
1337      006474 174037 000000C
1338      006500 112737 000063 000000C
1339      006506 013737 000000C 000000C
1340      006514 013737 000000C 000000C
1341      006522 004767 175374
1342      006526 004767 175624
1343      006532 004767 175662
1344      006536 004767 176440
1345      006542 172437 000000C
1346      006546 174037 000000C
1347      006552 004767 001010
1348      006556 000465
1349      006556
1350

```

```

;DISPLAY MODE 4 (THREE CHANNELS)
;CHANNEL 1
;FLOAT DATA
;MULTIPLY BY WINDOW
;AVERAGE DATA
;GET VOLTAGE
;ACCESS VOLTAGE
;CHANNEL 2
;POINTS TO COMPUTE ON
;CYCLES TO COMPUTE
;FLOAT DATA
;MULTIPLY BY WINDOW
;AVERAGE DATA
;GET VOLTAGE
;ACCESS VOLTAGE
;CHANNEL 3
;POINTS TO COMPUTE ON
;CYCLES TO COMPUTE
;FLOAT DATA
;MULTIPLY BY WINDOW
;AVERAGE DATA
;GET VOLTAGE
;ACCESS VOLTAGE
;DISPLAY DATA

```

```

;1,;ICN
PC,FLOT
PC,WINDOW
PC,BAVER
PC,GRHSV
;RMSV,AC0
AC0,;RMSV1
;2,;ICN
;PTR,;PTC
;NCYC,;NCYF
PC,WINDOW
PC,BAVER
PC,GRHSV
;RMSV,AC0
AC0,;RMSV2
;3,;ICN
;PTR,;PTC
;NCYC,;NCYF
PC,FLOT
PC,WINDOW
PC,BAVER
PC,GRHSV
;RMSV,AC0
AC0,;RMSV3
PC,CH4DIS
1008

```

```

1351
1352      006560 112737 000061 000000C 908:
1353      006566 004767 175330
1354      006572 004767 175560
1355      006576 004767 175616
1356      006602 004767 176036
1357      006606 172437 000000C
1358      006612 174037 000000C
1359      006616 172437 000000C
1360      006622 174037 000000C
1361      006626 112737 000062 000000C
1362      006634 013737 000000C 000000C
1363      006642 013737 000000C 000000C
1364      006650 004767 175246
1365      006654 004767 175476
1366      006660 004767 175534
1367      006664 004767 175774
1368      006670 172437 000000C
1369      006674 174037 000000C
1370      006700 172437 000000C
1371      006704 174037 000000C
1372      006710 172437 000000C
1373      006714 172537 000000C
1374      006720 173001
1375      006722 174037 000000C
1376      006726 004767 001032
1377
1378      006732 000167 177166

```

```

;DISPLAY MODE 5 (DUAL CHANNEL)
;FLOAT DATA
;MULTIPLY BY WINDOW
;AVERAGE DATA
;GET VOLTAGE AND PHASE
;ACCESS VOLTAGE
;ACCESS PHASE
;CHANNEL 2
;POINTS TO COMPUTE ON
;CYCLES TO COMPUTE
;MULTIPLY BY WINDOW
;AVERAGE DATA
;GET VOLTAGE AND PHASE
;ACCESS VOLTAGE
;ACCESS PHASE
;COMPUTE RELATIVE PHASE
;DISPLAY DATA
;AND AGAIN
VNDISP
1008:

```

```

;1,;ICN
PC,FLOT
PC,WINDOW
PC,BAVER
PC,GDFT
;VDFT,AC0
AC0,;RMSV1
;PDFT,AC0
AC0,;PHAS1
;2,;ICN
;PTR,;PTC
;NCYC,;NCYF
PC,FLOT
PC,WINDOW
PC,BAVER
PC,GDFT
;VDFT,AC0
;PDFT,AC0
AC0,;PHAS2
;PHAS1,AC0
;PHAS2,AC1
AC1,AC0
AC0,;RPHAS
PC,CH4DIS
VNDISP
1008:

```

```

1380
1381
1382
1383
1384 006736 005737 000000G
1385 006742 001401
1386 006744 006207
1387 006746 012700
1388 006752 012701
1389
1390 006756 112120
1391 006760 001376
1392
1393 006762 013701 000000G
1394 006766 006700
1395 006770 012702 000005
1396 006774 012703 000017G
1397 007000 071027 000012
1398 007004 062701 000060
1399 007010 110143
1400 007012 010001
1401 007014 006700
1402 007016 077210
1403
1404 007020 013701 000000G
1405 007024 006700
1406 007026 012702 000005
1407 007032 012703 000031G
1408 007036 071027 000012
1409 007042 062701 000060
1410 007046 110143
1411 007050 010001
1412 007052 006700
1413 007054 077210
1414
1415 007056 012705 007070'
1416 007062 004767 001102
1417 007066 009207
1418
1419 007070 000002 000000G 007100'
    007076 000000G
1420 007100 000031
1421 007102 000002 000000G 007112'
    007110 000000G
1422 007112 000017
    015 057
1423 007114 104 040
    007117 104 103
    007122 117 126
    007125 075 040
    007130 040 060
    007133 040 105
    007136 122 075
    007141 040 040
    007144 060 000

1424
1425

```

; DISPLAY A/D CONVERSION COUNT AND ERROR COUNT,  
;(IFF DSTYLE.EQ.0):  
; FORMAT: A/D CONV=##### ERR=#####  
; RIGHT DISPLAY STYLE?  
;YES  
;NO  
; LOAD DISPLAY DELIMITERS  
; INTO RAM BUFFER  
; ENCODE CONVERSION COUNT...  
; ... INTO A BYTE STRING...  
; ENCODE ERROR COUNT...  
; ... AND OUTPUT ENTIRE STRING.

```

1427      CR1DIS: 0000000C 0000000C 0000001
1428      CR2DIS:
1429      CR3DIS:
1430      012700 0000000C 0000000C 0000001
1431      012701 007532'
1432      112120
1433      001376
1434      023727
1435      007162 000000C 0000000C 0000001
1436      007170 001020
1437      007172 012705'
1438      007176 004767 0000000C
1439      007202 012705' 007400'
1440      007206 004767 0000000C
1441      007212 012705' 007412'
1442      007216 004767 0000000C
1443      007222 112737 000061 0000001C
1444      007230 000451
1445      007232 023727
1446      007240 001020
1447      007242 012705'
1448      007246 004767 0000000C
1449      007252 012705' 007424'
1450      007256 004767 0000000C
1451      007262 012705' 007436'
1452      007266 004767 0000000C
1453      007272 112737 006062 0000001C
1454      007300 000425
1455      007302 023727
1456      007310 001020
1457      007312 012705'
1458      007316 004767 0000000C
1459      007322 012705' 007474'
1460      007326 004767 0000000C
1461      007332 012705' 007506'
1462      007336 004767 0000000C
1463      007342 112737 000063 0000001C
1464      007350 000401
1465      007352 000207
1466      007354 012705'
1467      007360 004767 000604
1468      007364 000207
1469      1470
1470      1471
1471      1472
1472      1473
1473      1474
1474      1475
1475      1476

```

```

;SUBROUTINE TO DISPLAY DATA ON VOLTMETER
;FORMAT # (1-3) = >V=+***.** P: +****.* D:*.**

```

```

MOV #CHERRY,R0 ;LOAD DISPLAY DELIMITERS
MOV #CH1BLK,R1 ;INTO RAM BUFFER
MOV (R1), (R0)+ ;
BNE 5$

CMP #DSTYLE,#1 ;RIGHT DISPLAY STYLE? (CHANNEL ONE)
BNE 10$ ;YES
MOV #CH1A,R5 ;CONVERT CHANNEL ONE VOLTAGE TO ASCII
PC,CONVRT
MOV #CH1B,R5 ;CONVERT CHANNEL ONE PHASE TO ASCII
PC,CONVRT
MOV #CH1C,R5 ;CONVERT CHANNEL ONE DISTORTION TO ASCII
PC,CONVRT
MOV #'1,@#CHERRY+1 ;
BR 40$

CMP #DSTYLE,#2 ;? (CHANNEL 2)
BNE 20$ ;CONVERT CHANNEL TWO VOLTAGE TO ASCII
MOV #CH2A,R5 ;CONVERT CHANNEL TWO VOLTAGE TO ASCII
PC,CONVRT
MOV #CH2B,R5 ;CONVERT CHANNEL TWO PHASE TO ASCII
PC,CONVRT
MOV #CH2C,R5 ;CONVERT CHANNEL TWO DISTORTION TO ASCII
PC,CONVRT
MOV #'2,@#CHERRY+1 ;
BR 40$

CMP #DSTYLE,#3 ;? (CHANNEL THREE)
BNE 30$ ;CONVERT CHANNEL THREE VOLTAGE TO ASCII
MOV #CH3A,R5 ;CONVERT CHANNEL THREE VOLTAGE TO ASCII
PC,CONVRT
MOV #CH3B,R5 ;CONVERT CHANNEL THREE PHASE TO ASCII
PC,CONVRT
MOV #CH3C,R5 ;CONVERT CHANNEL THREE DISTORTION TO ASCII
PC,CONVRT
MOV #'3,@#CHERRY+1 ;
BR 40$

RTS ;NO
MOV #CH1ER,R5 ;VALUE FILL DISPLAY
PC,WRITE
RTS ;

```

1478	007366	000004	000000C	000005C	CH1A:	4, RMSV1, CHERRY+5., TWO, TWO
	007374	010164'	010164'			
1479	007400	000004	000000C	000016C	CH1B:	4, PHAS1, CHERRY+14., THREE, ONE
	007406	010166'	010162'			
1480	007412	000004	000000C	000027C	CH1C:	4, DIST1, CHERRY+23., ONE, TWO
	007420	010162'	010164'			
1481	007424	000004	000000C	000005C	CH2A:	4, RMSV2, CHERRY+5., TWO, TWO
	007432	010164'	010164'			
1482	007436	000004	000000C	000016C	CH2B:	4, PHAS2, CHERRY+14., THREE, ONE
	007444	010166'	010162'			
1483	007450	000004	000000C	000027C	CH2C:	4, DIST2, CHERRY+23., ONE, TWO
	007456	010162'	010164'			
1484	007462	000004	000000C	000005C	CH3A:	4, RMSV3, CHERRY+5., TWO, TWO
	007470	010164'	010164'			
1485	007474	000004	000000C	000016C	CH3B:	4, PHAS3, CHERRY+14., THREE, ONE
	007502	010166'	010162'			
1486	007506	000004	000000C	000027C	CH3C:	4, DIST3, CHERRY+23., ONE, TWO
	007514	010162'	010164'			
1487	007520	000002	000000C	007530'	CH1ER:	2, CHERRY, CHILA, DLUN
	007526	000000C				
1488	007530	000034			CH1LA:	WORD 28.
1489						
1490	007532	015	061	075	CH1BLK:	.ASCIZ <15>*1=V+##.## P=+###.## D=...##
	007535	126	075	053		
	007540	043	043	056		
	007543	043	043	040		
	007546	120	075	053		
	007551	043	043	043		
	007554	056	043	040		
	007557	104	075	043		
	007562	056	043	043		
1491	007565	000				.EVEN

```

1493
1494
1495
1496 007566 023727 000000C 0000004
1497 007566 001401
1498 007574 000207
1499 007576 000207
1500 007600 012700
1501 007604 012701
1502 007610 112120
1503 007612 001376
1504 007614 012705
1505 007620 004767
1506 007624 012705
1507 007630 004767
1508 007634 012705
1509 007640 004767
1510 007644 012705
1511 007650 004767
1512 007654 000207
1513
1514 007656 000004
1515 007670 000004
1516 007702 000004
1517 007714 000002
1518 007722 000000C
1519
1520 007726 015
007731 075
007734 056
007737 040
007742 075
007745 056
007750 040
007753 075
007756 056
007761 043
1521

```

```

;SUBROUTINE TO DISPLAY DATA ON VOLTMETER
;FORMAT 4: V1=+.** V2=+.** V3=+.** V3=+.** V3=+.**

```

```

CH4DIS:
;RDSTYLE, #4
;RIGHT DISPLAY STYLE?
;YES
;NO
;LOAD DISPLAY DELIMITERS
;INTO RAM BUFFER
;CONVERT CHANNEL ONE VOLTAGE TO ASCII
;CONVERT CHANNEL TWO VOLTAGE TO ASCII
;CONVERT CHANNEL THREE VOLTAGE TO ASCII
;VALUE FILL DISPLAY

```

```

CMP #DSTYLE, #4
BEQ 100
RTS
MOV #CHERRY, R0
MOV #CH4BLK, R1
MOVB (R1), (R0)
BNE 200
MOV #CH4A, R5
PC, CONVRT
MOV #CH4B, R5
PC, CONVRT
MOV #CH4C, R5
PC, CONVRT
JSR #CH4ER, R5
PC, WRITE
RTS

```

```

4, RMSV1, CHERRY+4, .ONE, TWO
4, RMSV2, CHERRY+13, .ONE, TWO
4, RMSV3, CHERRY+22, .ONE, THREE
2, CHERRY, CH4LA, DLUN
.WORD 28.

```

```

CH4BLK: .ASCIZ <15> V1=+.** V2=+.** V3=+.** V3=+.** V3=+.**

```

```

1523
1524
1525
1526 007764
1527 007764 023727
1528 007772 001401
1529 007774 000207
1530 007776 012700
1531 010002 012701
1532 010006 112120
1533 010010 001376
1534 010012 012705
1535 010016 004767
1536 010022 012705
1537 010026 004767
1538 010032 012705
1539 010036 004767
1540 010042 012705
1541 010046 004767
1542 010052 000207
1543
1544 010054
1545 010062 010162
1546 010074 010162
1547 010100 000004
1548 010112 000002
1549 010120 000000G
1550 010124 015
1551 010127 075
1552 010132 056
1553 010135 043
1554 010140 062
1555 010143 043
1556 010146 043
1557 010151 120
1558 010154 043
1559 010157 043
1560 010162 000001
1561 010164 000002
1562 010166 000003
1563 010168 000003
1564 010170 000003
1565 010172 000003
1566 010174 000003
1567 010176 000003
1568 010178 000003
1569 010180 000003
1570 010182 000003
1571 010184 000003
1572 010186 000003
1573 010188 000003
1574 010190 000003
1575 010192 000003
1576 010194 000003
1577 010196 000003
1578 010198 000003
1579 010200 000003
1580 010202 000003
1581 010204 000003
1582 010206 000003
1583 010208 000003
1584 010210 000003
1585 010212 000003
1586 010214 000003
1587 010216 000003
1588 010218 000003
1589 010220 000003
1590 010222 000003
1591 010224 000003
1592 010226 000003
1593 010228 000003
1594 010230 000003
1595 010232 000003
1596 010234 000003
1597 010236 000003
1598 010238 000003
1599 010240 000003
1600 010242 000003
1601 010244 000003
1602 010246 000003
1603 010248 000003
1604 010250 000003
1605 010252 000003
1606 010254 000003
1607 010256 000003
1608 010258 000003
1609 010260 000003
1610 010262 000003
1611 010264 000003
1612 010266 000003
1613 010268 000003
1614 010270 000003
1615 010272 000003
1616 010274 000003
1617 010276 000003
1618 010278 000003
1619 010280 000003
1620 010282 000003
1621 010284 000003
1622 010286 000003
1623 010288 000003
1624 010290 000003
1625 010292 000003
1626 010294 000003
1627 010296 000003
1628 010298 000003
1629 010300 000003
1630 010302 000003
1631 010304 000003
1632 010306 000003
1633 010308 000003
1634 010310 000003
1635 010312 000003
1636 010314 000003
1637 010316 000003
1638 010318 000003
1639 010320 000003
1640 010322 000003
1641 010324 000003
1642 010326 000003
1643 010328 000003
1644 010330 000003
1645 010332 000003
1646 010334 000003
1647 010336 000003
1648 010338 000003
1649 010340 000003
1650 010342 000003
1651 010344 000003
1652 010346 000003
1653 010348 000003
1654 010350 000003
1655 010352 000003
1656 010354 000003
1657 010356 000003
1658 010358 000003
1659 010360 000003
1660 010362 000003
1661 010364 000003
1662 010366 000003
1663 010368 000003
1664 010370 000003
1665 010372 000003
1666 010374 000003
1667 010376 000003
1668 010378 000003
1669 010380 000003
1670 010382 000003
1671 010384 000003
1672 010386 000003
1673 010388 000003
1674 010390 000003
1675 010392 000003
1676 010394 000003
1677 010396 000003
1678 010398 000003
1679 010400 000003
1680 010402 000003
1681 010404 000003
1682 010406 000003
1683 010408 000003
1684 010410 000003
1685 010412 000003
1686 010414 000003
1687 010416 000003
1688 010418 000003
1689 010420 000003
1690 010422 000003
1691 010424 000003
1692 010426 000003
1693 010428 000003
1694 010430 000003
1695 010432 000003
1696 010434 000003
1697 010436 000003
1698 010438 000003
1699 010440 000003
1700 010442 000003
1701 010444 000003
1702 010446 000003
1703 010448 000003
1704 010450 000003
1705 010452 000003
1706 010454 000003
1707 010456 000003
1708 010458 000003
1709 010460 000003
1710 010462 000003
1711 010464 000003
1712 010466 000003
1713 010468 000003
1714 010470 000003
1715 010472 000003
1716 010474 000003
1717 010476 000003
1718 010478 000003
1719 010480 000003
1720 010482 000003
1721 010484 000003
1722 010486 000003
1723 010488 000003
1724 010490 000003
1725 010492 000003
1726 010494 000003
1727 010496 000003
1728 010498 000003
1729 010500 000003
1730 010502 000003
1731 010504 000003
1732 010506 000003
1733 010508 000003
1734 010510 000003
1735 010512 000003
1736 010514 000003
1737 010516 000003
1738 010518 000003
1739 010520 000003
1740 010522 000003
1741 010524 000003
1742 010526 000003
1743 010528 000003
1744 010530 000003
1745 010532 000003
1746 010534 000003
1747 010536 000003
1748 010538 000003
1749 010540 000003
1750 010542 000003
1751 010544 000003
1752 010546 000003
1753 010548 000003
1754 010550 000003
1755 010552 000003
1756 010554 000003
1757 010556 000003
1758 010558 000003
1759 010560 000003
1760 010562 000003
1761 010564 000003
1762 010566 000003
1763 010568 000003
1764 010570 000003
1765 010572 000003
1766 010574 000003
1767 010576 000003
1768 010578 000003
1769 010580 000003
1770 010582 000003
1771 010584 000003
1772 010586 000003
1773 010588 000003
1774 010590 000003
1775 010592 000003
1776 010594 000003
1777 010596 000003
1778 010598 000003
1779 010600 000003
1780 010602 000003
1781 010604 000003
1782 010606 000003
1783 010608 000003
1784 010610 000003
1785 010612 000003
1786 010614 000003
1787 010616 000003
1788 010618 000003
1789 010620 000003
1790 010622 000003
1791 010624 000003
1792 010626 000003
1793 010628 000003
1794 010630 000003
1795 010632 000003
1796 010634 000003
1797 010636 000003
1798 010638 000003
1799 010640 000003
1800 010642 000003
1801 010644 000003
1802 010646 000003
1803 010648 000003
1804 010650 000003
1805 010652 000003
1806 010654 000003
1807 010656 000003
1808 010658 000003
1809 010660 000003
1810 010662 000003
1811 010664 000003
1812 010666 000003
1813 010668 000003
1814 010670 000003
1815 010672 000003
1816 010674 000003
1817 010676 000003
1818 010678 000003
1819 010680 000003
1820 010682 000003
1821 010684 000003
1822 010686 000003
1823 010688 000003
1824 010690 000003
1825 010692 000003
1826 010694 000003
1827 010696 000003
1828 010698 000003
1829 010700 000003
1830 010702 000003
1831 010704 000003
1832 010706 000003
1833 010708 000003
1834 010710 000003
1835 010712 000003
1836 010714 000003
1837 010716 000003
1838 010718 000003
1839 010720 000003
1840 010722 000003
1841 010724 000003
1842 010726 000003
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1886 010814 000003
1887 010816 000003
1888 010818 000003
1889 010820 000003
1890 010822 000003
1891 010824 000003
1892 010826 000003
1893 010828 000003
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1900 010842 000003
1901 010844 000003
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1903 010848 000003
1904 010850 000003
1905 010852 000003
1906 010854 000003
1907 010856 000003
1908 010858 000003
1909 010860 000003
1910 010862 000003
1911 010864 000003
1912 010866 000003
1913 010868 000003
1914 010870 000003
1915 010872 000003
1916 010874 000003
1917 010876 000003
1918 010878 000003
1919 010880 000003
1920 010882 000003
1921 010884 000003
1922 010886 000003
1923 010888 000003
1924 010890 000003
1925 010892 000003
1926 010894 000003
1927 010896 000003
1928 010898 000003
1929 010900 000003
1930 010902 000003
1931 010904 000003
1932 010906 000003
1933 010908 000003
1934 010910 000003
1935 010912 000003
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1953 010948 000003
1954 010950 000003
1955 010952 000003
1956 010954 000003
1957 010956 000003
1958 010958 000003
1959 010960 000003
1960 010962 000003
1961 010964 000003
1962 010966 000003
1963 010968 000003
1964 010970 000003
1965 010972 000003
1966 010974 000003
1967 010976 000003
1968 010978 000003
1969 010980 000003
1970 010982 000003
1971 010984 000003
1972 010986 000003
1973 010988 000003
1974 010990 000003
1975 010992 000003
1976 010994 000003
1977 010996 000003
1978 010998 000003
1979 011000 000003
1980 011002 000003
1981 011004 000003
1982 011006 000003
1983 011008 000003
1984 011010 000003
1985 011012 000003
1986 011014 000003
1987 011016 000003
1988 011018 000003
1989 011020 000003
1990 011022 000003
1991 011024 000003
1992 011026 000003
1993 011028 000003
1994 011030 000003
1995 011032 000003
1996 011034 000003
1997 011036 000003
1998 011038 000003
1999 011040 000003
2000 011042 000003

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1557 !SUBROUTINES TO HANDLE CHARACTER OUTPUT:
1558 !CALL WRITE (IBUFF,ILEN,DLUN)---ASCII STRING
1559 !CALL PRINT (NUMBER,ILEN,DLUN)---DECIMAL NUMBER
1560
1561 010170 016500 000002 WRITE: 2(R5),R0 !SET UP START ADDRESS
1562 010174 017501 000004 MOV @4(R5),R1 !SET UP LENGTH
1563 010200 003404 BLE 30$ !IF NEG OR ZERO, ERROR
1564 010202 112002 !GET NEXT CHARACTER
1565 010204 004707 JSR PC,PUTCHE !LOOP ON CHAR COUNT
1566 010210 077104 SOB R1,10$
1567 010212 000207 RTS PC
1568
1569 010214 022775 CNP *2,06(R5) !WHICH DEVICE?
1570 010222 001407 BEQ CHCHR !CHERRY DISPLAY
1571 010224 032737 BIT *200,@DLVXSR !CHECK XMIT STATUS
1572 010232 001770 BEQ PUTCHE !NOT DONE, YET
1573 010234 110237 JSR R2,@DLVXBF !MOVE IT TO PRINT BUF
1574 010240 000207 RTS PC
1575
1576 010242 032737 CNP *200,@CHXSR !CHECK XMIT STATUS
1577 010250 001774 BEQ CHCHR !NOT DONE, YET
1578 010252 110237 JSR R2,@CHXBF !MOVE IT TO PRINT BUF
1579 010256 000207 RTS PC
1580
1581 !PRINT A DECIMAL INTEGER
1582 010260 012703 PRINT: *STRING,R3 !SETUP OUTPUT BUFFER
1583 010264 016502 MOV 4(R5),R2 !GET ROW MANY DIGITS
1584 010270 017501 MOV @2(R5),R1 !GET NUMBER TO PRINT
1585 010274 003701 TST R1 !MORRY IF NEGATIVE
1586 010276 002002 BGE 5$ !ONE LESS TO PRINT
1587 010300 005401 NEG R1 !AND EXTEND TO 32 BITS
1588 010302 005302 DEC R2
1589 010304 006700 SXT R0
1590
1591
1592 010306 071027 DIV *10.,R0 !DIVIDE BY 10
1593 010312 062701 ADD *60,R1 !CONVERT REMAINDER TO ASCII
1594 010316 010123 MOV R1,(R3)+ !AND STORE IN STRING
1595 010320 010001 MOV R0,R1 !CONVERT QUOTIENT TO DIVIDEND
1596 010322 006700 SXT R0 !AND TO 32 BITS
1597 010324 005302 DEC R2 !CHECK NUMBER OF DIGITS
1598 010326 003367 BGT 10$ !GET ANOTHER
1599
1600 010330 003775 TST @2(R5) !CHECK SIGN
1601 010334 002004 BGE 20$ !NEGATIVE...
1602 010336 012702 MOV *55,R2 !...PRINT MINUS SIGN
1603 010342 004767 JSR PC,PUTCHE
1604
1605 010346 016501 MOV 4(R5),R1 !CHARACTERS TO PRINT
1606 010352 014302 MOV -(R3),R2 !ADDRESS OF STRING (MSD)
1607 010354 004767 JSR PC,PUTCHE !PRINT DIGIT
1608 010360 005301 DEC R1 !DONE YET?
1609 010362 003373 BGT 30$ !NO
1610 010364 000207 RTS PC !YES
1611
1612
1613 .END

```

(2) MODULE A32D - Subroutine to control ADAC A/D converters

Calling Sequence:

CALL A32D (BUFFER, GAINS, NWORDS, ADS, GATED, CHNO, ISW)

where the arguments are:

BUFFER Integer array to receive contiguous sampled data  
GAINS Integer array containing gains of individual A/D converters  
NWORDS Number of samples to digitize per channel  
ADS A/D setup word containing which A/D's in use; which to interrupt from  
GATED Integer variable specifying presence (1) or absence (0) of DRV11/toneburst gate  
CHNO Integer array containing port (channel) numbers for each channel  
ISW Integer variable for returned status

Internal Summary:

<u>(LABEL)</u>	<u>(FUNCTION)</u>	<u>(PAGE)</u>
<u>SETUP ROUTINES</u>		
A32D	Argument transfer; parameter checking	4
SETDMA	Load DMA bus address and word count registers; load pseudo-register for DMA control	4
SETA2D	Load pseudo-register for A/D control; load DMA pseudo-registers into control status registers	6
<u>MEASUREMENT ROUTINE</u>		
READY	Prepare for measurement	7
GO	Check for pulsed-system gate	7
GOCW	Load A/D pseudo registers into actual control status registers; wait for completion (interrupt)	7

ADISR	Dummy interrupt service routine	8
DMISR	Functional interrupt service routine	8

```

1 .TITLE A32D
2 .IDENT /07/
3
4 SUBROUTINE TO LOAD AN INTEGER DATA ARRAY WITH DATA FROM UP
5 TO THREE ADAC-1012 A/D CONVERTERS, RUNNING SIMULTANEOUSLY.
6
7 ;AUTHOR: RICHARD E. SCOTT, JR. (DECEMBER 1981)
8
9 ;FORTRAN CALL--
10 ;CALL A32D (BUFFER,GAINS,NWORDS,ADS,CATED,CHNO,ISW)
11
12 ;"BUFFER" IS AN INTEGER ARRAY TO CONTAIN CONTIGUOUS DATA FROM
13 UP TO THREE CHANNELS OF DMA'ED DATA FROM THE A/D CONVERTERS.
14 EACH DATA WORD IS A 12-BIT INTEGER. FOR N WORDS PER CHANNEL....
15 CHANNEL 1 DATA IN BUFFER(1) TO BUFFER(N)
16 CHANNEL 2 DATA IN BUFFER(N+1) TO BUFFER(2N)
17 CHANNEL 3 DATA IN BUFFER(2N+1) TO BUFFER(3N)
18
19 ;"GAINS" ARE PROGRAMMABLE GAIN CODES (1,2,4,8) OR (1,2,5,10)
20 ;FOR A/D CONVERTERS (AS DEFINED BY HARDWARE STRAPPING).
21 ;GAINS IS AN INTEGER ARRAY.
22
23 ;"NWORDS" IS THE NUMBER OF WORDS PER CHANNEL TO BE DMA'ED
24 INTO MEMORY FROM THE A/D CONVERTERS.
25
26 ;"ADS" IS THE A/D SETUP ARRAY WHERE:
27 ADS(1) = A/D'S USED (#1=1) + (#2=2) + (#3=4) (ALL=7)
28 ADS(2) = DMA TO GET INTERRUPT FROM (1,2,3)
29 AGAIN, WITH ADS(1) = 3 ONE CAN USE TWO A/D CARDS ONLY WITHOUT
30 CONCERN FOR THE SUBROUTINE ADDRESSING THE NONEXISTENT THIRD
31 A/D CARDS.
32
33 ;"CATED" FLAGS THE PRESENCE OF PULSE/GATING MECHANISMS.
34 ;CATED = 1 MEANS USE PARALLEL I/O CARD TO FLAG START OF PULSE.
35 ;CATED = 0 MEANS DON'T.
36 ;THE LATTER CASE MEANS THAT A PARTICULAR BACKPLANE DOES NOT
37 NEED TO HAVE A DRV-11 INSTALLED TO AVOID TRAPS DUE TO ADDRESSING
38 UNNEEDED AND NONEXISTENT CARDS. HOWEVER, ONE MAY FIND OCCASIONAL
39 PHASE ERRORS BECAUSE OF CLOCK PULSES OCCURRING DURING THE SETUP
40 OF THE A/D REGISTERS. ONE TECHNIQUE USED IS TO REQUIRE THAT THE
41 HOST PROCESSOR TURN OFF THE SAMPLING CLOCK UNTIL THE REGISTERS
42 ARE ALL PREPARED.
43
44 ;"CHNO" IS AN ARRAY OF THREE CHANNEL NUMBERS, CHANNEL IN THE SENSE
45 ;OF THE SIXTEEN POSSIBLE MULTIPLEXED PORTS TO EACH A/D CONVERTER.
46
47 ;"ISW" IS STATUS OF TRANSFER (0 = AOK)
48 ;ISW = -1; A/D #1 ERROR (THESE 6 ERRORS ARE ADDITIVE)
49 ;ISW = -2; A/D #2 ERROR (DITTO...)
50 ;ISW = -4; A/D #3 ERROR
51 ;ISW = -10; DMA #1 ERROR
52 ;ISW = -20; DMA #2 ERROR
53 ;ISW = -40; DMA #3 ERROR (...RANGING -1 TO -77)
54 ;ISW = -100; PARAMETER ERROR (FROM THIS PROGRAM: A/D SETUP ERROR)
55 ;ISW = -101; PARAMETER ERROR (FROM CALLING PROGRAM: TOO MANY POINTS)
56
57 ;**** ALL ARGUMENTS ARE INTEGERS ****

```

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;HARDWARE REGISTER DEFINITIONS

;REGISTERS FOR THE FIRST A/D CONVERTER (A):
164040 ADCSRA=164040 ;A/D CONTROL STATUS REGISTER
164042 ADDRRA=164042 ;A/D DATA BUFFER
172410 DMCRA=172410 ;DMA WORD COUNT REGISTER
172412 DMCARA=172412 ;DMA BUS ADDRESS REGISTER
172414 DMCSRA=172414 ;DMA STATUS REGISTER
172416 DMNAXA=172416 ;DMA CHANNEL DATA

;REGISTERS FOR THE SECOND A/D CONVERTER (B):
164050 ADCSRB=164050 ;A/D CONTROL STATUS REGISTER
164052 ADDRRB=164052 ;A/D DATA BUFFER
172420 DMCRB=172420 ;DMA WORD COUNT REGISTER
172422 DMCARB=172422 ;DMA BUS ADDRESS REGISTER
172424 DMCSRB=172424 ;DMA STATUS REGISTER
172426 DMNAXB=172426 ;DMA CHANNEL DATA

;REGISTERS FOR THE THIRD A/D CONVERTER (C):
164060 ADCSRC=164060 ;A/D CONTROL STATUS REGISTER
164062 ADDRRC=164062 ;A/D DATA BUFFER
172430 DMCRC=172430 ;DMA WORD COUNT REGISTER
172432 DMCARC=172432 ;DMA BUS ADDRESS REGISTER
172434 DMCSRC=172434 ;DMA STATUS REGISTER
172436 DMNAXC=172436 ;DMA CHANNEL DATA

;A/D CONTROL STATUS REGISTER (ADCSR*):
;BIT 15: ERROR BIT (SET FOR CONVERSION TIMING ERROR)
;BIT 14: INTERRUPT ON ERROR (0)
;BIT 12-3: NOT USED (EXTENDED CHANNEL ADDRESS)
;BIT 11-8: ADDRESS OF CHANNEL (*DD)
;BIT 7: A/D DONE FLAG (SET AT END OF CONVERSION)
;BIT 6: INTERRUPT ENABLE (0)
;BIT 5: OPTIONAL ENABLE (0)
;BIT 4-3: PROGRAMMABLE GAIN (AS INPUT)
;BIT 2: SEQUENTIAL ENABLE
;BIT 1: EXTERNAL ENABLE (1)
;BIT 0: A/D CONVERSION GO BIT (0)

;DATA REGISTER (ADDR*):
;BITS 12-15: NOT USED (1111)
;BITS 00-11: DATA

;DMA WORD COUNT REGISTER (DMWCR*):
;BITS 12-15: NOT USED (1111)
;BITS 00-11: ONE'S COMPLEMENT OF WORDS

;DMA BUS ADDRESS (DMCAR*):
;BITS 00-11: WORD ADDRESS

```

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110 ;DMA CONTROL STATUS REGISTER (DMCSR*):
111 ;BIT 15: ERROR BIT (SET FOR ANY ERROR)
112 ;BIT 14: NONEXISTENT MEMORY ERROR
113 ;BIT 13: ATTENTION BIT (EXTERNAL)
114 ;BIT 12-8: NOT USED (0)
115 ;BIT 7: READY FLAG (SET WHEN DMA DONE)
116 ;BIT 6: INTERRUPT ENABLE (0)
117 ;BIT 5-4: EXTENDED ADDRESS BITS
118 ;BIT 3-1: NOT USED (0)
119 ;BIT 0: DMA CO BIT (1)
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167770
167772
167774

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;REGISTERS FOR THE DRV-11 USED FOR SYNCHRONIZATION IN PULSED
;SYSTEMS.

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;
;DRCSR=167770
;DROUT=167772
;DRINN=167774

```

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;VECTOR ADDRESSES:

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; 130 - A/D COMPLETION (A) OR ERROR
; 150 - DMA COMPLETION (A)
; 170 - A/D COMPLETION (B) OR ERROR
; 210 - DMA COMPLETION (B)
; 230 - A/D COMPLETION (C) OR ERROR
; 250 - DMA COMPLETION (C)

```

```

;SET PROM = 1 IF VECTOR SPACE IS WITHIN ROM SPACE AND SET
;VECTORS REQUIRED. OTHERWISE, VECTORS ARE LOADED DYNAMICALLY.
;SET PROM = -1 FOR RAM VARIABLES ASSEMBLED LOCALLY.
PROM=1

```

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000001

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142 ;SUBROUTINE INITIALIZATION:
143 ; GRAB THE ARGUMENTS.
144 ; DO SOME CHECKING FOR VALIDITY.
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<pre> A32D:: TST      @12(R5) BEQ      10\$ CLR      @DRCSR MOV      10(R5),R0 MOV      (R0),WAD MOV      2(R0),IWD MOV      2(R5),R0 MOV      @6(R5),R4 COH      R4 MOV      14(R5),R2 CMP      #7,WAD BLT      PERR CMP      #1,WAD BCT      PERR CMP      #3,IWD BLT      PERR CMP      #1,IWD BLE      SETDMA MOV      #-100,@16(R5) RTS      PC </pre>	<pre> ;START OF ROUTINE ;IF HAVE, THEN... ;CLEAR UP THE DRV-11 STATUS REGISTER ;ADDRESS OF ADS ;A/D'S-USED WORD ;DMA INTERRUPT WORD ;ADDRESS OF FIRST DATA BUFFER ;NUMBER OF WORDS ;BUT ONE'S COMPLEMENTED ;ADDRESS OF CHANNEL PORTS ;CHECK FOR ERRORS ;WAD &gt; 7 ;WAD &lt; 1 ;IWD &gt; 3 ;IWD &gt;=1 ;PARAMETER ERROR ;SET UP THE NECESSARY REGISTERS FOR THOSE A/D-DMAS ;THAT ARE TO BE USED. ;FIRST DMA REGISTERS SETUP: </pre>	<pre> BIT      #1,WAD BEQ      20\$ MOV      R4,@DMCRA MOV      R0,@DNCARA MOV      #1,@TI MOV      (R2)+,R1 SHAB      R1,@DMUXA MOV      R1,@ADD ;IF EQ PROM MOV      #ADISR,@#130 MOV      #340,@#132 MOV      #DNISR,@#150 MOV      #340,@#152 .ENDC CHIP      #1,IWD BNE      20\$ MOV      #101,@TI ;IF EQ PROM MOV      #DNISR,@#150 .ENDC </pre>	<pre> ;IS THIS DMA ACTIVE? ;NOPE! ;LOAD WORD COUNT REGISTER ;LOAD ADDRESS REGISTER ;LOAD CSR, GO ONLY ;GET PORT NUMBER ;LOAD DMA MULTIPLEXOR ADDRESS ;SHIFT INTO BITS 8-11 ;THEN STORE FOR LATER USE ;LOAD INTERRUPT VECTOR (A/D) ;AND PROCESSOR STATUS WORD ;LOAD INTERRUPT VECTOR (DMA) ;AND PROCESSOR STATUS WORD ;INTERRUPT OFF THIS ONE? ;NOPE! ;LOAD CSR, IE AND GO ;LOAD ACTIVE ISR VECTOR </pre>
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199
200
201 000176 067500 000006
202 000202 067500 000006
203 000206 032767 000002 000000G
204 000214 001424
205 000216 010437 172420
206 000222 010037 172422
207 000236 012737 000001 000000G
208 000234 012201
209 000236 010137 172426
210 000242 000301
211 000244 010137 000000G
212
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218 000250 022767 000002 000000G
219 000256 001003
220 000260 012737 000101 000000G
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227 000266 067500 000006
228 000272 067500 000006
229 000276 032767 000004 000000G
230 000304 001426
231 000306 010437 172430
232 000312 010037 172432
233 000316 012737 000001 000000G
234 000324 012201
235 000326 010137 172436
236 000332 000301
237 000334 042701 170977
238 000340 010137 000000G
239
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242
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245 000344 022767 000003 000000G
246 000352 001003
247 000354 012737 000101 000000G
248
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250

```

!SECOND DMA REGISTERS SETUP  
 @6(R5),R0 !INCREASE BUFFER ADDRESS  
 @6(R5),R0 !(TWICE FOR BYTES)  
 #2,WAD !IS THIS DMA ACTIVE?  
 30\$ !NOPE!  
 R4,@#DMC RB !LOAD WORD COUNT REGISTER  
 R0,@#DMC ARB !LOAD ADDRESS REGISTER  
 #1,@#T2 !LOAD CSR, GO ONLY  
 (R2)+,R1 !GET PORT NUMBER  
 R1,@#DNM UXB !LOAD DMA MULTIPLEXOR ADDRESS  
 R1,@#BDD !SHIFT INTO BITS 8-11  
 !THEN STORE FOR LATER USE  
 !LOAD INTERRUPT VECTOR (A/D)  
 #340,@#172 !AND PROCESSOR STATUS WORD  
 #DMISR,@#210 !LOAD INTERRUPT VECTOR  
 #340,@#212 !AND PROCESSOR STATUS WORD  
 .ENDC  
 #2,IWD !INTERRUPT OFF THIS ONE?  
 30\$ !NOPE!  
 #101,@#T2 !LOAD CSR, IE AND GO  
 #DMISR,@#210 !LOAD ACTIVE ISR VECTOR  
 .ENDC

!THIRD DMA REGISTERS SETUP  
 @6(R5),R0 !INCREASE BUFFER ADDRESS  
 @6(R5),R0 !(TWICE FOR BYTES)  
 #4,WAD !IS THIS DMA ACTIVE?  
 SETA2D !NOPE!  
 R4,@#DMC RC !LOAD WORD COUNT REGISTER  
 R0,@#DMC ARC !LOAD ADDRESS REGISTER  
 #1,@#T3 !LOAD CSR, GO ONLY  
 (R2)+,R1 !GET PORT NUMBER  
 R1,@#DNM UXC !LOAD DMA MULTIPLEXOR ADDRESS  
 R1 !SHIFT INTO BITS 8-11  
 #170977,R1 !CLEAN UP ACCIDENTS  
 R1,@#CDD !THEN STORE FOR LATER USE  
 !LOAD INTERRUPT VECTOR (A/D)  
 #340,@#232 !AND PROCESSOR STATUS WORD  
 #DMISR,@#250 !LOAD INTERRUPT VECTOR  
 #340,@#252 !AND PROCESSOR STATUS WORD  
 .ENDC  
 #3,IWD !INTERRUPT OFF THIS ONE?  
 30\$ !NOPE!  
 #101,@#T3 !LOAD CSR, IE AND GO  
 #DMISR,@#250 !LOAD ACTIVE ISR VECTOR  
 .ENDC



!PREPARE AND LOAD THE A/D CSR'S WITH GAIN, ETC.--EVERYTHING  
 !EXCEPT THE GO BITS. ALSO LOAD THE DMA CSR'S PREVIOUSLY CHOSEN.  
 !LOAD R0-R3 WITH THE ADDRESSES OF THE ACTIVE A/D CSR'S (OR A  
 !DUMMY ADDRESS IF NOT). THIS WILL ALLOW QUICK SETUP-AND-GO  
 !OF THE A/D'S (NEAR SIMULTANEOUS) WITHOUT THE LOGIC REQUIRED  
 !TO NOT "DO" A/D CSR'S NOT EXISTING.

```

252      259 000362 016503 000004 000000C      SETA2D: MOV      4(R5),R3      !ADDRESS OF GAINS ARRAY
253      260 000366 017567 000010 000000C      MOV      610(R5),WAD      !GET A/D APPLICABILITY WORD
254
255      261      !SET UP A/D CONVERTER NUMBER ONE IF ACTIVE
256      262
257      263
258      264 000374 012700 000000C      MOV      #ADUM,R0      !TARGET ADDRESS IF NOT ACTIVE
259      265 000400 032767 000001 000000C      BIT      #1,WAD      !IS THIS A/D ACTIVE?
260      266 000406 001425      BEQ      30$          !NOPE
261      267 000410 012300      MOV      (R3)+,R0      !GET GAIN CODE OF CHANNEL A (1,2,5,10)
262      268 000412 006200      ASR      R0           !CHANGE TO 0,1,2,5
263      269 000414 022700      CMP      #5,R0       !CHECK IF 5
264      270 000420 003002      BCT      20$        !NOPE
265      271 000422 003300      DEC      R0         !SET 5 TO 4
266      272 000424 005300      DEC      R0         !THEN TO 3
267      273 000426 072027      ASH      #3,R0      !SHIFTED INTO BITS 3-4
268      274 000432 005100      CON      R0         !SET TO 30,20,10,00
269      275 000434 042700      BIC      #17747,R0  !SAFETY CLEANUP
270      276 000440 003700      BIS      @#ADD,R0   !ADD ADDRESS SETTING TO R0
271      277 000444 010037      MOV      R0,@#ADCSRA !SET GAIN (ETC) BITS FIRST
272      278 000450 012700      MOV      #ADCSRA,R0 !TARGET ADDRESS IF ACTIVE
273      279 000454 016737      MOV      T1,@#DMCSRA !ENABLE DMA'S
274
275      281      !SET UP A/D CONVERTER NUMBER TWO IF ACTIVE
276      282
277      283 000462 012701 000000C      MOV      #ADUM,R1      !TARGET ADDRESS IF NOT ACTIVE
278      284 000466 032767 000002 000000C      BIT      #2,WAD      !IS THIS A/D ACTIVE?
279      285 000474 001425      BEQ      50$        !NOPE
280      286 000476 012301      MOV      (R3)+,R1    !GET GAIN CODE OF CHANNEL B (1,2,5,10)
281      287 000500 006201      ASR      R1         !CHANGE TO 0,1,2,5
282      288 000502 022701      CMP      #5,R1     !CHECK IF 5
283      289 000506 003002      BCT      40$      !NOPE
284      290 000510 005301      DEC      R1        !SET 5 TO 3
285      291 000512 005301      DEC      R1        !SHIFTED INTO BITS 3-4
286      292 000514 072127      ASH      #3,R1     !SET TO 30,20,10,00
287      293 000520 005101      CON      R1        !NOPE
288      294 000522 042701      BIC      #17747,R1  !ADD ADDRESS SETTING TO R1
289      295 000526 003701      BIS      @#BDD,R1   !SET GAIN (ETC) BITS FIRST
290      296 000532 010137      MOV      R1,@#ADCSRB !TARGET ADDRESS IF ACTIVE
291      297 000536 012701 164050      MOV      #ADCSRB,R1 !ENABLE DMA'S
292      298 000542 016737 000000C 172424      MOV      T2,@#DMCSRB
    
```

```

300
301 000550 012702 000000C
302 000554 032767 000004 000000C
303 000562 001425
304 000564 012302
305 000566 006202
306 000568 006202
307 000570 022702
308 000574 003002
309 000576 005302
310 000600 005302
311 000602 072227
312 000606 005102
313 000610 042202
314 000614 053702
315 000620 010237
316 000624 012702
317 000630 016737
318 000000C 172434
319
320
321
322
323
324 000636 005067 000000C
325 000642 005075 000016
326
327 000646 000240
328 000650 005775 000012
329 000654 001404
330 000656 032737 000001 167774 CO:
331 000664 001774
332
333
334
335
336
337
338
339
340
341
342 000666 012703 000003
343 000672 050310
344 000674 050311
345 000676 050312
346
347
348 000700 000001
349 000702 005767 000000C
350 000706 000240
351 000710 001773
352
353 000712 000207

;SET UP A/D CONVERTER NUMBER THREE IF ACTIVE
MOV #ADUM,R2
BIT #4,WAD
REQ READY
MOV (R3)+,R2
ASR R2
CNP #5,R2
BGT 60$
DEC R2
DEC R2
ASH #3,R2
CON R2
BIC #177747,R2
BIS @CDD,R2
MOV R2,@ADCSRC
MOV #ADCSRC,R2
MOV T3,@DMCSRC

;TARGET ADDRESS IF NOT ACTIVE
;IS THIS A/D ACTIVE?
;NOPE
;GET GAIN CODE OF CHANNEL C (1,2,5,10)
;CHANGE TO 0,1,2,5
;CHECK IF 5
;NOPE
;SET 5 TO 3
;SHIFTED INTO BITS 3-4
;SET TO 30,20,10,00
;ADD ADDRESS SETTING TO R2
;SET GAIN (ETC) BITS FIRST
;TARGET ADDRESS IF ACTIVE
;ENABLE DMA'S

;IS EVERYBODY READY??
;INCLUDING TRANSMIT GATE TO DLV-11???
CLR ADST
CLR @16(R5)
NOP
TST @12(R6)
REQ COCV
BIT #1,@DRINN
REQ CO

;CLEAR STATUS WORD
;CLEAR ERROR STATUS

;USE DRV-11 FOR PULSE GATE?
;NO
;CHECK FOR BIT 0 SET
;...AND WAIT...

;THE FOLLOWING IS DONE THIS WAY SO THE LOADING IS AS FAST
;AS POSSIBLE, THUS ELIMINATING ANY POSSIBLE PHASE ERRORS
;BETWEEN THE THREE CHANNELS.
;TIME TO LOAD EACH CSR IS ABOUT 4.54 MICROSECONDS.
;NOTE THAT IF ACTIVE, THE DESTINATION REGISTERS CONTAIN THE
;ADDRESSES OF THE A/D CSR'S; IF NOT ACTIVE, THEY CONTAIN A
;DUMMY ADDRESS TO AVOID THE POSSIBILITY OF WRITING TO A
;NON-EXISTENT ADDRESS (AND TRAPPING) IF INACTIVE A/D CONVERTERS
;ARE NOT IN THE BACKPLANE.

MOV #3,R3
BIS R3,(R0)
BIS R3,(R1)
BIS R3,(R2)

COCV:
WAIT
TST ADST
NOP
REQ 20$

20$:
RTS PC

;START A/D CONVERSIONS
;WAIT FOR SPECIFIED INTERRUPT
;...AND WAIT...
;RETURN WITH DATA

```

```
355 ; INTERRUPT SERVICE ROUTINE
356 ; DMISR -- DMA SERVICE ROUTINE
357 ; ADISR -- A/D SERVICE ROUTINE
358 ; WITH SIMULTANEOUS ACTION, ALL SHOULD FINISH AT ONE TIME, THUS
359 ; NO NEED TO GO THROUGH ISR FOR ALL THREE DEVICES.
360 ; (INTERRUPTS DISABLED ON REMAINING DEVICES ANYWAY.)
361
362 000714 000002 ADISR: RTI ;:DO NOTHING
363
364 000716 012767 000001 000000C DMISR: MOV #1,ADST ;:GOOD INTERRUPT: EOR FOR CHANNEL SPECIFIED
365
366 000724 032767 000001 000000C BIT ;:THIS A/D ACTIVE?
367 000732 001414 BEQ ;:NOPE
368 000734 005737 164040 @#ADCSRA ;:ERRORS FOR CHANNEL A?
369 000740 100003 BPL ;:NO
370 000742 062775 # -1,@16(R5) ;:YES: ERROR -1+
371 000750 005737 172414 @#DMCSRA ;:ERRORS FOR CHANNEL A?
372 000754 100003 BPL ;:NO
373 000756 062775 # -10,@16(R5) ;:YES: ERROR -10+
374 000764 032767 000002 000000C 2# #2,WAD ;:IS THIS A/D ACTIVE?
375 000772 001414 BEQ ;:NOPE!
376 000774 005737 164050 @#ADCSRB ;:ERRORS FOR CHANNEL B?
377 001000 100003 BPL ;:NO
378 001002 062775 # -2,@16(R5) ;:YES: ERROR -2+
379 001010 005737 172424 @#DMCSRB ;:ERRORS FOR CHANNEL B?
380 001014 100003 BPL ;:NO
381 001016 062775 # -20,@16(R5) ;:YES: ERROR -20+
382 001024 032767 000004 000000C 4# #4,WAD ;:IS THIS A/D ACTIVE?
383 001032 001414 BEQ ;:NOPE!
384 001034 005737 164060 @#ADCSRC ;:ERRORS FOR CHANNEL C?
385 001040 100003 BPL ;:NO
386 001042 062775 # -4,@16(R5) ;:YES: ERROR -4+
387 001050 005737 172434 @#DMCSRC ;:ERRORS FOR CHANNEL C?
388 001054 100003 BPL ;:NO
389 001056 062775 # -40,@16(R5) ;:YES: ERROR -40+
390 001064 000002 ADD RTI
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
```

.IF LT PROM  
;RAM VARIABLES USED BY A32D

```
WAD: .WORD 0
1WD: .WORD 0
T1: .WORD 0
T2: .WORD 0
T3: .WORD 0
ADUM: .WORD 0
ADST: .WORD 0
ADD: .WORD 0
HDD: .WORD 0
CDD: .WORD 0
.ENDC
```

```
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000002  
000003  
000004  
000005  
000006  
000007  
000008  
000009  
00000A  
00000B  
00000C  
00000D  
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00002A  
00002B  
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000039  
00003A  
00003B  
00003C  
00003D  
00003E  
00003F  
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000044  
000045  
000046
```

(3) MODULE CONVRT - Subroutine to convert a floating-point number to an ASCII string.

Calling Sequence:

```
CALL CONVRT (VALUE, STRING, K, L)
```

where the arguments are:

VALUE	Floating-point variable to be encoded
STRING	Byte array to load encoded variable
K	Encode with K places to left of decimal point
L	Encode with L places to right of decimal point

NOTE: Decimal point (.) and sign (+ or -) are not included in the size of K and L.

```

1  .TITLE CONVRT
2  .IDENT /01/
3
4  ;SUBROUTINE TO CONVERT A REAL NUMBER (FLOATING) TO
5  ;AN ASCII STRING OF FORMAT "KK.LLL" FOR PRINTING
6  ;PURPOSES.
7
8  ;CALL CONVRT (VALUE,STRING,K,L)
9  ;WHERE VALUE BECOMES AS F<K>.<L> (FORTRAN)
10 ;
11 ;AUTHOR: RICHARD E. SCOTT, JR. (NRL/USRD -- AUGUST 1981)
12
13 AC0=%0
14 AC1=%1
15
16 CONVRT:
17   MOV     R0,-(SP)
18   MOV     R1,-(SP)
19   MOV     R2,-(SP)
20   MOV     R3,-(SP)
21   MOV     R4,-(SP)
22
23   SETF   4(R5),R3
24   LDF    62(R5),AC0
25   CFCC
26   BGT    10$
27   BR     20$
28   MOVB  #'-',(R3)+
29   MOVB  #'',(R3)+
30   ABSF   AC0
31   STCFI  AC0,R1
32   LDCIF  R1,AC1
33   MOV    66(R5),R4
34   JSR   PC,DECO
35   MOVB  #'',(R3)+
36   SUBF  AC1,AC0
37   MOV   610(R5),R4
38   MULF  67TEN,AC0
39   DEC   R4
40   BGT   30$
41   ADDF  67R0,AC0
42   STCFI AC0,R1
43   MOV   610(R5),R4
44   JSR  PC,DECO
45
46   MOV    (SP)+,R4
47   MOV    (SP)+,R3
48   MOV    (SP)+,R2
49   MOV    (SP)+,R1
50   MOV    (SP)+,R0
51
52   RTS
53
;SAVE REGISTERS ON STACK
;SET TO FLOATING MODE
;ADDRESS OF OUTPUT STRING
;GET VALUE TO PRINT
;POSITIVE OR NEGATIVE?
;INSERT MINUS SIGN
;INSERT PLUS SIGN
;ABSOLUTE VALUE
;SAVE INTEGER PART (VOLTS?)
;DIGITS LEFT OF DECIMAL
;LOAD THEM IN STRING
;AND DECIMAL POINT
;FIND FRACTION PART (MILLIVOLTS)
;CONVERT TO 10**L INTEGER
;FIX ROUND-OFF
;SAVE AS INTEGER
;DIGITS RIGHT OF DECIMAL
;LOAD THEM IN STRING
;RESTORE REGISTERS ON STACK

```



- (4) MODULE DAPD - Subroutine to compute voltage and phase from the real and imaginary parts of the voltage, and to compute the harmonic distortion from a specific number of harmonics.

Calling Sequence:

```
CALL DAPD (X, N, K, AMP, PHASE, DSTORT, LHARM, IERR)
```

where the arguments are:

X	Floating-point data sequence
N	Length of data sequence
K	Spectral line (number of cycles) to compute
AMP	Resulting total rms voltage
PHASE	Resulting phase in degrees
DSTORT	Harmonic distortion
LHARM	Number of harmonics used to compute distortion
IERR	Execution status

- NOTES:
1. If LHARM = 1, distortion is not computed
  2. If DSTORT = -1, distortion was not computed
  3. If DSTORT = -2, error occurred (AMP = 0)
  4. If IERR = -1, K = N (illegal condition)
  5. If IERR = LHARM, harmonic folds onto dc line

```

1 .TITLE DAPD
2 .IDENT /01/
3
4 ;AHPHIA.JTN 17-JUL-81 JDC GEORGE (ORIGINATOR)
5 ;DAPD .MAC 02-OCT-81 RE SCOTT (TRANSLATOR)
6
7 ;SUBROUTINE DAPD (X,N,K,AMP,PHASE,DSTOIT,LHARM,IERR)
8
9 ;COMPUTE AMPLITUDE, PHASE, & PERCENT HARMONIC DISTORTION
10 ;FOR THE K-TH SPECTRAL LINE OF THE DFT OF X, A REAL SEQUENCE
11 ;OF LENGTH N.
12 ;UNDERSAMPLING OR FOLDING WHERE K CAN EXCEED N/2 IS ALLOWED.
13 ;SUBROUTINES REQUIRED: DGZL (X,N,K,XR,XI)
14
15 ;DATA SEQUENCE
16 ;SEQUENCE LENGTH
17 ;DFT SPECTRAL LINE NUMBER OF INTEREST
18 ;ALSO # CYCLES OF TEST SINUSOID IN N SAMPLES
19 ;I.E. K CYCLES OF FUNDAMENTAL (1ST HARM) IN SEQUENCE;
20 ; 2K CYCLES OF 2ND HARMONIC, ETC.
21 ;IF K=0, COMPUTE DC COMPONENT.
22 ;GIVES MAGNITUDE OF K TH SPECTRAL COMPONENT
23 ;PHASE IN DEGREES OF K-TH SPECTRAL COMPONENT
24 ;MODEL IS 0 DEC COSINE -90 DEC SINE
25 ;% HARMONIC DISTORTION OF K TH COMPONENT
26 ;FOR LINE #'S 2*K THROUGH LHARM*K
27 ;HARMONIC TO INCLUDE IN DISTORTION
28 ;IERR=0 NORMAL
29 ;IERR=-1 IF(MOD(K,H)=0)
30 ;IERR=-L IF HARMONIC FOLDS ON DC LINE
31
32 ;DSTOIT = -1 IF NOT COMPUTED
33 ;DSTOIT = -2 IF ERROR IN COMPUTING
34
35 AC0=%0
36 AC1=%1
37 AC2=%2
38
39 HARLIN = 11. ;MAXIMUM HARMONICS TO USE
40
41 ;EXPERIENCE SEEMS TO SHOW THAT A MINIMUM OF 9 POINTS ARE
42 ;REQUIRED TO COMPUTE ACCURATELY THE AMPLITUDE AND PHASE OF
43 ;A SINUSOID; FOR DISTORTION, 2*LHARM+1 POINTS ARE REQUIRED.
44
45 ;SET PROH = 1 FOR RAM VARIABLES STORED EXTERNALLY
46 ;SET PROH = 0 FOR RAM VARIABLES STORED INTERNALLY (NORMAL)
47
48 FROM=1
49
50 DAPD::
51 000000 R0,-(SP) ;SAVE REGISTERS ON STACK
52 000000 R1,-(SP)
53 000000 R2,-(SP)
54 000000 R3,-(SP)
55 000000 R4,-(SP)
56
57 000012 170601 SETP

```



```

59 ***** COMPUTE AMPLITUDE AND PHASE *****
60 *****
61 *****
62 *****
63 *****
64 *****
65 *****
66 *****
67 *****
68 *****
69 *****
70 *****
71 *****
72 *****
73 *****
74 *****
75 *****
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101 *****
102 *****
103 *****
104 *****
105 *****

```

; IERR=0  
 ; I=MOD(K,N)  
 ; NCYC = CYCLES PER SAMPLE  
 ; NCYC / NSAM  
 ; IF ((1.EQ.0).AND.(K.NE.0)) IERR=-1  
 ; R = I (REMAINDER)  
 ; K  
 ; IERR = -1  
 ; NSAM = SAMPLES PER SEQUENCE  
 ; SAVE FOR DCZL  
 ; NSAM / 2  
 ; IF ((1.LE.N/2) I=1  
 ; IF ((1.LE.N/2) PSIGN=+1  
 ; COMPARE I AND NSAM/2  
 ; I > NSAM/2  
 ; I = < NSAM/2 SO KEEP I  
 ; NO PHASE SIGN-CHANGE  
 ; IF ((1.GT.N/2) I=N-1  
 ; IF ((1.GT.N/2) PSIGN=-1  
 ; GET I - NSAM  
 ; NSAM - I  
 ; USE I = NSAM-I  
 ; PREPARE TO CHANGE-SIGN OF PHASE  
 ; (WAGON WHEEL REVERSAL MODE)  
 ; HARM(1)=1  
 ; GET ADDRESS OF HARMONIC ARRAY  
 ; HARM(1) = 1 (THIS ONE DONE)  
 ; CALL DCZTL (X,N,K,XR,XI)  
 ; PROCESS FUNDAMENTAL  
 ; SAVE R5 (DURING CALL DCZL)  
 ; TRANSFER ADDRESS OF ARRAY  
 ; DO SINGLE-POINT DFT

64	000014	005075	000020	CLJL	@20(R5)	
65	000020	017501	000006	MOV	G6(R5),R1	
66	000024	006700		SXT	R0	
67	000026	071075	000034	DIV	@4(R5),R0	
68	000032	005701		TST	R1	
69	000034	001006		BNE	15\$	
70	000036	005775	000006	TST	@6(R5)	
71	000042	01403		BEQ	15\$	
72	000044	012775	177777	MOV	#-1,@26(R5)	
73	000052	017502	005604	MOV	@4(R5),R2	
74	000056	010237	006006G	MOV	R2,@#NSAM	
75	000062	006202		ASR	1L2	
76	000064	020102		CHP	R1,R2	
77	000066	003006		BGT	20\$	
78	000070	010137	000006G	MOV	R1,@#1	
79	000074	012137	000006G	MOV	#1,@#PSIGN	
80	000074	005410		BR	30\$	
81	000104	167501	000004	SUB	@4(R5),R1	
82	000110	005401		NEG	R1	
83	000112	016137	000006G	MOV	R1,@#1	
84	000116	012137	177777	MOV	#-1,@#PSIGN	
85	000124	012700	000006G	MOV	#HARM,R0	
86	000130	010110		MOV	R1,(R0)	
87	000132	010540		.IF	R5,-(SP)	
88	000134	012705	000654'	PROH		
89	000140	004767	000006G	MOV	2(R5),@#DCARG+2	
90	000140	004767	000006G	.ENDC		
91	000140	004767	000006G	MOV	#DCARG,R5	
92	000140	004767	000006G	JSR	PC,DCZL	

```

107          170001
108 000144 172475 000010
109 000146 174062
110 000152 171060
111 000154 172575 000012
112 000156 171101
113 000162 172001
114 000164 172000
115 000166 172000
116 000170 054762 000000G
117 000174 012605
118 000176 174975 000010
119
120 000202 170502
121 000204 170060
122 000206 091420
123
124 000210 010546
125 000212 012705 000720'
126 000216 004767 000000G
127 000222 172400
128 000224 171037 000670'
129 000230 005767 000000G
130 000234 003001
131 000236 170700
132 000240 012605
133 000242 174075 000012
134 000246 000404
135
136 000250 172637 000700'
137 000254 174275 000012
138

```

```

CVAF:
SETF      010(R5),AC0
LDF       AC0,AC2
STF       AC0,AC0
MULF     012(R5),AC1
LDF       AC1,AC1
MULF     AC1,AC0
ADDF     AC0,AC0
ADDF     PC,@SSQHT
JSR      (SP)+,R5
MOV      AC0,@10(R5)

TSTF     AC2
CFCC
REQ      159

MOV      R5,-(SP)
NOV     @ARCBK,R5
JSR     PC,SATAN2
LDF     R0,AC0
MULF   @*ANCC,AC0
TST    PSICH
BCT    10$
NECF   AC0
NOV    (SP)+,R5
STF   AC0,@12(R5)
BR    CD1S

100:
LDF   @*P90,AC2
STF  AC2,@12(R5)

159:

```

```

;*AMP=SQRT(2*(XR*XR+XI*XI))
; COMPUTE VOLTAGE AND PHASE
;XR
;SAVE FOR TEST
;XR*XR
;XI
;XI*XI
;XR*XR + XI*XI
;2 (XR*XR + XI*XI)
;=VDF
;RESTORE R5

;WATCH FOR ATAN(XI/0)
;ERROR1
;*(IF(XR,NE.0)
;*;PHASE=PSIGN*SATAN2(XI,XI)*180./3.1415927
;SAVE R5 (FOR CALL SATAN2)

;ATAN-1 (XI/XR)
;CONVERT TO DECREES
;WAGON WHEELS?
;FORWARD
;NO--BACKWARD ROTATING
;RESTORE R5
;PDT

;*(IF(XR,EQ.0) PHASE=0.0
;FOR ATAN(XI/0)...
;SET PDT = 90

```

```

140 ***** COMPUTE HARMONIC DISTORTION *****
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1000 ;

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183
189 060412 013701 000000G
190 060416 017502 000000+
191 060422 062000
192 060422 062000
193 060424 020102
194 060426 063003
195 060430 010137 000000G
196 060434 000405
197 060436 167501 000000+
198 060442 005401
199 060444 010137 000000G
200
201 060450 012702 000000
202
203 060454 012700 177776G
204 060460 050300
205 060462 060200
206 060464 021001
207 060466 001437
208
209 060470 005302
210 060472 020203
211 060474 002767
212
213 060476 012700 177776G
214 060502 060300
215 060504 060300
216 060506 010110
217
218
219 060510 010546
220 060512 010446
221 060514 010346
222
223
224
225 060516 012705 000654+
226 060522 004707 000000G
227
228
229 060526 172475 000010
230 060532 171000
231 060534 172575 000012
232 060540 171101
233 060542 172001
234 060544 172000
235 060546 172537 000000G
236 060552 172100
237 060554 174137 000000G
238
239 060560 012603
240 060562 012604
241 060564 012605

```

```

; *IF (1,LE,N/2) I=1
; *IF (1,GT,N/2) I=N-1
; RESTORE I TO R1
; GET NSAM
; NSAM / 2
; COMPARE I AND NSAM/2
; I > NSAM/2
; I < NSAM/2 SO KEEP I
; GET I - NSAM
; NSAM - I
; USE I = NSAM-I
; *DO 20 LL = 1,LL
; LL=1
; *IF (HARM(LL),EQ,1) GOTO 30
; HARM(-1) ADDRESS
; HARM(LL) ADDRESS
; IS IT I(=R1)?
; IS USED
; *20 CONTINUE
; LL=LL+1
; R3 IS STILL "L"
; L < LL
; *HARM(L) = I
; HARM(-1) ADDRESS
; HARM(L) ADDRESS
; I = 1
; *CALL DCITZL (X,N,1,XR,XI)
; SAVE R5
; SAVE R4
; SAVE R3
; *TRANSFER ADDRESS OF ARRAY
; LOAD ARGUMENT LIST
; DFT THE HARMONIC
; *PWR=PWR+2*(XI*XR+XI*XI)
; XR
; XI*XR
; XI
; XI*XI
; XR*XR + XI*XI
; I2 (XR*XR + XI*XI)
; SUB TO PWR
; AND SAVE
; RESTORE R3
; RESTORE R4
; RESTORE R5

```

```

243 000566 005304
244 000570 032263
245
246
247 000572 172475 000010
248 000576 170900
249 000600 003413
250 000602 171000
251 000604 174560
252 000606 172471
253 000610 004767 000000G
254 000614 172537 000674
255 000620 171001
256 000622 174075 000014
257 000626 000404
258 000630 172437 000719
259 000634 174075 000014
260
261 000640
262 000640 012604
263 000642 012603
264 000644 012602
265 000646 012601
266 000650 012600
267 000652 000277
268
269 000654 000005 000000G DCARG: 000000G
270 000670 041545 027346 000000G 000000G
271 000674 041710 000000 ANGC:
272 000700 041664 000000 GENT:
273 000704 140200 000000 P90:
274 000710 140400 000000 M1:
275 000714 140500 000000 M2:
276 000720 000002 000000 M3:
277 000722 000000G ARCBLK:
278 000724 000000G
279
280
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```

```

!*:30 CONTINUE
;DECREMENT COUNTER
;NOT DONE--NEXT HARMONIC
!*;DSTORT=100*SQRT(PWR/(AHP*ANP))
;GET VDFT
;CHECK FOR ZERO
;ERROR!! (POTENTIAL PWR/0)
;VDFT * VDFT
;DISTORTION
;DISTORTION IN PERCENT
;LOAD DISTORTION
;DONE
;FOR ERROR CONDITION,
;DISTORTION = -2
;RESTORE REGISTERS FROM STACK

```

```

R4
I0$
@10(R5),AC0
80$
AC0,AC0
AC0,AC1
AC1,AC0
PC,8*SQRT
@*CENT,AC1
AC1,AC0
AC0,@14(R5)
DONE
@-M2,AC0
AC0,@14(R5)
(SP)+,R4
(SP)+,R3
(SP)+,R2
(SP)+,R1
(SP)+,R0
PC
5, RBUF, NSAN, I, XREAL, XIMAG
.FLT2 57.2950 ;RADIANS-TO-DEGREES CONVERSION FACTOR
.FLT2 100.
.FLT2 90.
.FLT2 -1.
.FLT2 -3.
.FLT2 -3.
.WORD 2
.WORD XIMAG
.WORD XREAL
;IF EQ FROM
;RAM VARIABLES USED BY DAPD
.FLT2 0.0
.FLT2 0.0
.FLT2 0.0
.WORD 0
.WORD 0
.WORD 0
.WORD 0
HARLIN ;ARRAY FILLED AS HARMONICS ARE USED
.WORD 0
.ENDC
.END

```

```

XREAL:
XIMAG:
PWR:
PSIGN:
I:
NSAN:
HARN:
RBUF:

```

(5) MODULE DGZL - Subroutine to perform a discrete Fourier transform on a data sequence using Goertzel's algorithm.

Calling Sequence:

```
CALL DGZL (X, N, K, XR, XI)
```

where the arguments are:

X	Floating-point data sequence
N	Length of sequence
K	Spectral line (number of cycles) to compute
XR	Real part of voltage
XI	Imaginary part of voltage

```

1 .TITLE DCZL
2 .IDENT /01/
3
4 ;SUBROUTINE DCZL (X,N,K,XR,XI)
5
6 ;REAL X(N) ;DATA SEQUENCE
7 ;INTEGER N ;SEQUENCE LENGTH
8 ;INTEGER K ;DFT SPECTRAL LINE NUMBER OF INTEREST
9 ;
10 ;REAL XR ;ALSO # CYCLES OF TEST SINUSOID IN N SAMPLES
11 ;REAL XI ;REAL PART OF VOLTAGE
12 ; ;IMAGINARY PART OF VOLTAGE
13
14 ;DCRTZL.FTN -- JD GEORGE (17-JUL-81) (ORIGINATOR)
15 ;DCZL .MAC -- RE SCOTT (02-OCT-81) (TRANSLATOR)
16
17 ;COERTZEL'S ALGORITHM
18 ;TO COMPUTE K-TH LINE OF N POINT REAL SEQUENCE X(L)
19 ;REFERENCE: G. COERTZEL
20 ; "AN ALGORITHM FOR THE EVALUATION OF FINITE TRIG SERIES"
21 ; AMERICAN MATHEMATICAL MONTHLY, V65, 1958, PP.34-35
22 ; ALSO CHAPTER 24, PP.258-62
23 ; MATHEMATICAL METHODS FOR DIGITAL COMPUTERS VOL. 1
24 ; EDITED BY A. RALSTON & H.S. WILF
25 ; JOHN WILEY & SON, 1967
26
27 ;A DOUBLE PRECISION GOERTZEL ROUTINE WHICH IS MORE ACCURATE AND
28 ; FASTER THAN "SPDFT.FTN"
29 ; 40% RUNNING TIME OF SPDFT.FTN
30 ; UP TO 512 POINTS AMPLITUDE ERROR < 0.0012%
31 ; PHASE ERROR < 0.00001DEG
32
33 ;FLOATING POINT ACCUMULATOR #0
34 ;FLOATING POINT ACCUMULATOR #1
35 ;FLOATING POINT ACCUMULATOR #2
36 ;FLOATING POINT ACCUMULATOR #3
37
38 ;SET PROM = 1 FOR RAM VARIABLES STORED EXTERNALLY
39 ;SET PROM = 0 FOR RAM VARIABLES STORED INTERNALLY. (NORMAL)
40 PROM=1
41
42 DCZL:: NOV R5, -(SP) ;PUSH THE ARGUMENT ADDRESS
43 SETD ;SET FLOATING DOUBLE MODE
44 SETI
45
46 LDC1D @6(R5),AC0 ;DFLOAT(K)
47 LDC1D @4(R5),AC1 ;DFLOAT(N)
48
49 DIVD AC1,AC0 ;A0=2.*PI*(K)/(N)
50 MULD @PI,AC0
51 ADDD AC0,AC0
52 STD AC0,@PIA0
53
54 JSR PC,@SDCOS ;CO=DCOS(A0)
55 STD AC0,@PIC0 ;C1=2.*C0
56 ADDD AC0,AC0
57 STD AC0,@PIC1

```





(6) MODULE GPIOISR - This subroutine controls input and output along the IEEE-488 bus. There are three primary functions:

GPINI Initialize the interface and requisite variables; also set up address to use when group execute trigger (GET) is received.

Calling Sequence:

CALL GPINI (RESET)

where the argument is:

RESET Address of routine to transfer to on GET

GPIN Listen function - read a byte string from the IEEE-488 bus, terminated by a character received with EOI true, or by input buffer overflow.

Calling Sequence:

CALL GPIN (INBUF, MAXCH, NCH)

where the arguments are:

INBUF Byte buffer for input string

MAXCH Maximum number of characters allowed

NCH Number of characters received

GPOUT Talk function - set SRQ and transmit a byte string to the IEEE-488 bus.

Calling Sequence:

CALL GPOUT (OUTBUF, NCH)

where the arguments are:

OUTBUF Byte buffer for output string

NCH Number of characters to output

Also included is an interrupt service routine. The only interrupt always enabled is the group execute trigger (GET) interrupt, which feature is used to effect a program abort or restart function. The bus-in and bus-out interrupts are enabled only when their respective subroutines are called and disabled on completion of transfer.

The stand-alone driver supplied by National Instruments Corp. was not used for two reasons: size (keeping the total program size under 4K) and to implement the special abort function. However, programming this complex interface was not a straightforward exercise.

AD-A126 980

A MICROCOMPUTER-BASED SAMPLING DIGITAL VOLTMETER(U)  
NAVAL RESEARCH LAB WASHINGTON DC R E SCOTT ET AL.  
31 MAR 83 NRL-MR-4872

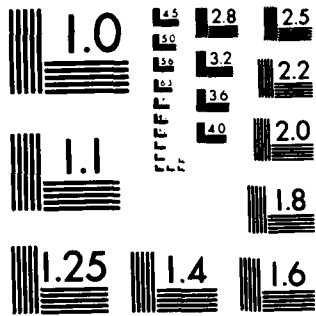
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NATIONAL BUREAU OF STANDARDS-1963-A

```

1  .TITLE GPIOSR
2  .IDENT /08/
3
4  ;SUBROUTINES FOR HANDLING THE GPIBV-11 INTERFACE
5  ; GPIINI -- INITIALIZE BUS
6  ; GPIN -- INPUT DATA FROM BUS (LISTER)
7  ; GPOUT -- OUTPUT DATA TO BUS (TALK)
8  ; ISRCP -- INTERRUPT SERVICE ROUTINE
9
10 ;AUTHOR: R. E. SCOTT, JR. (DECEMBER 1981)--V05
11 ;MODIFIED TO USE EOI ON READ (FEBRUARY 1982)--V06
12 ;MODIFIED TO USE R5 ARGUMENT LIST (FEBRUARY 1982)--V07
13 ;MODIFIED TO USE TRIGGER (NOT "AB") FOR ABORT--V08
14 ;NRL/UNDERWATER SOUND REFERENCE DETACHMENT
15
16 ; INITIALIZE--CALL GPINI (RESET)
17 ; INITIALIZES THE GPIBV-11 INTERFACE.
18 ; LOADS THE INTERRUPT VECTOR AND PSW (IF ALLOWED).
19 ; LOADS THE "RESET" ADDRESS (GO TO IF "GET" RECEIVED),
20 ; AND ENABLES BOTH INPUT AND OUTPUT INTERRUPTS.
21
22 ; INPUT--CALL GPIN (INBUF,MAXCH,NCH)
23 ; ALLOWS A STRING OF VARIABLE LENGTH "NCH" (BUT LESS THAN
24 ; "MAXCH") TO BE LOADED IN "INBUF". EOI MUST COME WITH LAST BYTE.
25
26 ; OUTPUT--CALL GPOUT (OUTBUF,NCH)
27 ; OUTPUTS A STRING "OUTBUF" OF VARIABLE LENGTH "NCH"
28 ; TO THE GPIB BUS.
29
30 ;
31 ; INTERRUPT SERVICE ROUTINE--
32 ; CHECKS FOR INPUT OR OUTPUT INTERRUPTS AND HANDLES WHICHEVER
33 ; IS APPROPRIATE. IT ALSO CHECKS FOR ONE SPECIAL INPUT, THE
34 ; RESET (ABORT) FUNCTION, WHICH REINITIALIZES EVERYTHING.
35
36 ; GPIB REGISTERS REQUIRED:
37 GPVEC = 270 ; INTERRUPT VECTOR ADDRESS
38 GPISR = 164000 ; INTERRUPT SERVICE REGISTER (RO)
39 GPINR = 164000 ; INTERRUPT MASK REGISTER (WO)
40 GPCTSR = 164001 ; CONTROL STATUS REGISTER (RO)
41 GPCSR = 164002 ; COMMAND STATUS REGISTER (RO)
42 GPARR = 164004 ; ADDRESS MODE REGISTER (WO)
43 GPASMR = 164006 ; AUXILIARY CONTROL REGISTER (RW)
44 GPADR = 164010 ; ADDRESS SWITCH REGISTER (RO)
45 GPSPR = 164012 ; ADDRESS REGISTER (WO)
46 GPCCR = 164015 ; SERIAL POLL REGISTER (RW)
47 GPDIR = 164016 ; CONTROL COMMAND REGISTER (WO)
48 GPDOR = 164016 ; DATA IN REGISTER (RO)
49 GPDOR = 164016 ; DATA OUT REGISTER (WO)
50
51 PROM=1 ;SET TO 1 WHEN INTERRUPT VECTORS LOADED INTO PROM;
52 ;OTHERWISE THEY ARE LOADED DYNAMICALLY BY GPINI.
53 ;ALSO, NO LOCAL RAM VARIABLES IF PROM=1.

```

```

54 000000
55 000000 010046
56 000000 000005
57 000002 000005
58 000004 105037 164006
59
60 000010 113700
61 000014 032700 000140
62 000020 001401
63 000022 000000
64 000024 042700
65 000030 110037 177740
66
67
68
69
70 000034 012600
71 000036 016537 000002 000000C
72 000044 005037 000000C
73 000050 112737 000240 164000
74 000056 112737 000010 164015
75 000064 000207
76
77 000066
78
79
80 000066 016537 000002 000000C
81 000074 017537 000004 000000C
82 000102 005037 000000C
83 000106 112737 000241 164000
84 000114 000001 000000C
85 000116 005737
86 000122 001004
87 000124 023737 000000C 000000C
88 000132 003370
89
90 000134 112737 000240 164000 208:
91 000142 005037 000000C
92 000146 013775 000000C 000006
93 000154 000207
94
95
96 000156
97
98
99 000156 016537 000002 000000C
100 000164 017537 000004 000000C
101 000172 112737 000340 164000
102 000200 112737 000102 164012
103 000206 000001
104 000210 005737 000000C
105 000214 003374
106 000216 112737 000240 164000
107 000224 105037 164012
108 000230 000207

;***** GPIB INITIALIZATION SUBROUTINE *****
CPINI:
MOV R0, -(SP)
;SAVE REGISTER 0 ON STACK
;??NEEDED??
RESET
CLR B
MOV #10, @CPACR
;INITIALIZE GPIB AUX REGISTER
MOV @CPASNR, R0
;SET HOLD RFD ON EOI
BIT #140, R0
;GET CONTROLLER ADDRESS
BEQ 58
;TEST CERTAIN SWITCHES ON CONTROLLER
;OKAY; CONTINUE
HALT
;ERROR: SAC AND/OR EXT SET
BIC #177740, R0
;ENABLE TALKING AND LISTENING
MOV R0, @GPADR
;LOAD CONTROLLER ADDRESS
;IF EQ FROM
;DON'T DO IF VECTOR SPACE IS ROM
MOV #1SRGP, @GPVEC
;LOAD INTERRUPT VECTOR
MOV #340, @CPVEC+2
;AND PSW
.ENDC
MOV (SP)+, R0
;RESTORE REGISTER 0
MOV 2(R5), @CTRIG
;LOAD ADDRESS TO GO IF TRIGGER
CLR @GPEOF
;CLEAR EOI FLAG
MOV #240, @CPIMR
;ENABLE GET INTERRUPTS
MOV #10, @CPCCR
;ENABLE MASTER INTERRUPT
RTS
;BUS IS NOW INITIALIZED

; ***** GPIB STRING INPUT SUBROUTINE *****
GPIN:
BISB #100, @CPACR
;RELEASE RFD HANDSHAKE FROM LAST INPUT
MOV 2(R5), @CPBIN
;INPUT BUFFER ADDRESS
MOV #4(R5), @CPMAX
;MAXIMUM LENGTH ALLOWED
CLR @CPNCH
;NUMBER OF CHARACTERS RECEIVED
MOV #241, @CPIMR
;ENABLE BI INTERRUPTS
WAIT
;WAIT FOR "ANY" INTERRUPT
TST @GPEOF
;END-OF-INPUT?
BNE 208
;YES
CHP @CPMAX, @CPNCH
;BUFFER FULL?
BGT 108
;NOT DONE/NOT FULL

MOV #240, @CPIMR
;DISABLE BI INTERRUPTS
CLR @GPEOF
;RETURN NUMBER OF CHARACTERS
MOV @CPNCH, @6(R5)
RTS
;INPUT COMPLETE

;***** GPIB STRING OUTPUT SUBROUTINE *****
GPOUT:
BISB #100, @CPACR
;RELEASE RFD HANDSHAKE FROM LAST INPUT
MOV 2(R5), @CPBOUT
;GET OUTPUT BUFFER ADDRESS
MOV #4(R5), @CPNCH
;GET NUMBER BYTES TO OUTPUT
MOV #340, @CPIMR
;ENABLE BO INTERRUPTS
MOV #102, @CPSPR
;SET SRQ AND READ BIT
WAIT
;WAIT FOR "ANY" INTERRUPT
TST @CPNCH
;ALL CHARACTERS OUTPUT?
BGT 108
;NO--WAIT MORE
MOV #240, @CPIMR
;DISABLE BO INTERRUPTS
CLR @CPSPR
; ** HOST DOESN'T CLEAR *2*
RTS
;OUTPUT COMPLETE

```

```

110          000232
111          032737 001000 164000
112          000232 001407
113          000240 142737 000002 164015 100:
114          000242 132737 000002 164015
115          000250 001371
116          000256
117          000260 132737 000001 164000 200:
118          000266 001013
119          000270 132737 000100 164000
120          000276 001026
121          000300 132737 000040 164000
122          000306 001001
123          000310 000436
124          000312 000177 000000C 300:
125          000316 132737 000002 164000 400:
126          000324 001403
127          000326 012737 000001 000000C 410:
128          000334 113777 164016 000000C 410:
129          000342 005237 000000C
130          000346 005237 000000C
131          000352 000415
132          000354 117737 000000C 164016 500:
133          000362 005237 000000C
134          000366 005337 000000C
135          000372 003401
136          000374 000404
137          000376 132737 000100 164000 600:
138          000404 001774
139          000406 000002 1000:
140          000415
141          000416
142          000417
143          000418
144          000419
145          000420
146          000421
147          000422
148          000423
149          000424
150          000425
151          000426
152          000427
153          000428
154          000429
155          000430
156          000431

```

;\*\*\*\*\* GPIB I/O INTERRUPT SERVICE ROUTINE \*\*\*\*\*  
 ;:TEST IFC BIT  
 ;:OKAY IF NOT SET  
 ;:CLEAR IFC BIT  
 ;:SUCCESSFUL?  
 ;:NO--TRY AGAIN  
 ;:"BI" INTERRUPT SET?  
 ;:YES  
 ;:"BO" INTERRUPT SET?  
 ;:YES  
 ;:"GET" INTERRUPT SET?  
 ;:YES  
 ;:INTERRUPT ERROR; TRY AGAIN  
 ;:RESTART  
 ;:IF DATA RECEIVED FROM BUS  
 ;:END-OF-INPUT?  
 ;:NO--CONTINUE  
 ;:SET EOI FLAG  
 ;:GET A CHARACTER  
 ;:INCREMENT INPUT ADDRESS  
 ;:INCREMENT CHARACTER COUNT  
 ;:IF DATA TO SEND TO BUS  
 ;:SEND OUT DATA BYTE  
 ;:INCREMENT OUTPUT ADDRESS  
 ;:DONE WITH STRING?  
 ;:YES--CLEANUP  
 ;:NO--NEXT?  
 ;:DATA-OUT REGISTER CLEAR?  
 ;:NO  
 ;ASSEMBLE IF VARIABLES NOT FROM  
 ;E-0-I FOUND? FLAG  
 ;INPUT BUFFER ADDRESS  
 ;OUTPUT BUFFER ADDRESS  
 ;MAXIMUM CHARACTERS ON INPUT  
 ;CHARACTER COUNT FOR INPUT OR OUTPUT  
 ;ADDRESS TO GO IF "GET" RECEIVED  
 ;IF WE RAM  
 .WORD 0  
 GPBUF: .WORD 0  
 GPBOUT: .WORD 0  
 GPBMAX: .WORD 0  
 GPBNC: .WORD 0  
 GPCTRIC: .WORD 0  
 .ENDC  
 .END

(7) MODULE GRMS - This subroutine computes parameters from statistical techniques.

Calling Sequence:

CALL GRMS (N, X, RMS, DC, PEAKMX, PEAKMN)

where the arguments are:

N	Length of sequence
X	Floating-point data sequence
RMS	Returned rms voltage (standard deviation)
DC	Returned dc voltage (average)
PEAKMX	Peak voltage (maximum)
PEAKMN	Peak voltage (minimum)

NOTE: This code was originally written by R.W. Anderson and subsequently modified by R.W. Luckey, both of NRL-USRD.



```

1 .TITLE CRMS
2 .IDENT /02/
3
4 ;SUBROUTINE TO COMPUTE TIME-AVERAGE VOLTAGES: RMS, DC,
5 ;MAXIMUM, AND MINIMUM.
6
7 ;
8 ;AUTHOR: RV ANDERSON (SOMETIME IN 1960)
9 ;MODIFIED: RW LUCKEY (FOR BOTH PEAKS; 1981)
10
11 ;
12 ;FORTRAN CALL--
13 ;CALL CRMS (NPTS,VALS,RMS,AVG,PEAKTX,PEAKRN)
14
15 ;
16 ;AC0 - IS THE SUMMING REGISTER
17 ;AC1 - IS THE REGISTER FOR THE SUM OF THE SQUARES
18 ;AC2 -
19 ;AC3 - NUMBER OF POINTS
20 ;AC4 - PEAK MINIMUM - STORAGE - REGISTER NOT GENERALLY USABLE
21 ;AC5 - PEAK MAXIMUM - STORAGE - REGISTER NOT GENERALLY USABLE
22
23 ;
24 ;R0 - LOOP COUNTER - INITIALIZED TO NPTS
25 ;R1 - ADDRESS OF THE VALUES
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53

```

AC0 = %0  
 AC1 = %1  
 AC2 = %2  
 AC3 = %3  
 AC4 = %4  
 AC5 = %5

```

CRMS: SETF 04(R5), AC0
LDF AC0, AC4
STF 04(R5), AC0
LDF AC0, AC5
MOV 02(R5), R0
LDCIF R0, AC3
MOV 4(R5), R1
CLRF AC0
CLRF AC1
LDF (R1), AC2
ADDF AC2, AC0
MULF AC2, AC2
ADDF AC2, AC1
LDF (R1)+, AC2
CHFF AC5, AC2
CFCC
BPL MIN
STF AC2, AC5
CHFF AC4, AC2
CFCC
BHI CONT
STF AC2, AC4
SOB R0, LOOP

```

```

MIN:
CONT:

```

```

; PEAK MINIMUM = X(1)
; STORE IN AC4
; PEAK MAXIMUM = X(1)
; STORE IT IN AC5
; SET UP LOOP COUNTER
; NUMBER OF POINTS (NPTS)
; ADDRESS OF DATA IN REGISTER ONE
; ZERO SUMMING REGISTER
; ZERO SQUARES SUMMING REGISTER
; GET A VALUE
; ADD IT TO SUMMING REGISTER
; SQUARE IT
; ADD IT TO SQUARES SUMMING REGISTER
; GET THE VALUE AGAIN
; SEE IF NEW MAX PEAK
; COPY FUNCTION CODES
; BRANCH IF NOT NEW MAX PEAK
; SAVE NEW MAX PEAK
; SEE IF NEW MIN PEAK
; COPY FUNCTION CODES
; BRANCH IF NOT NEW MIN PEAK
; SAVE NEW MIN PEAK
; LOOP IF NOT FINISHED

```



(8) LSIVAR - The following is an alphabetical list (with definitions) of the variables used in the software. All are used by LSIVM except for those used by the subroutines noted in parentheses.

AO	Exponent factor (DGZL)
AD1C	Input port for channel 1
AD2C	Input port for channel 2
AD3C	Input port for channel 3
ADBUF	A/D buffers (three contiguous arrays)
ADS	Numbers of A/D's used
ADST	Measurement status register (A32D)
ADT	DMA to interrupt on (ignore all others)
ADUM	A/D pseudo-register (A32D)
ADWL	Length in bytes of buffer to send to host
AERRS	Automatic retries when A/D errors occur
AVGCOD	Averaging mode (0 = don't, or cycles per averaged subsequence)
BSIZE	Buffer size allowed per channel
BUF1	12-bit integer values from A/D #1
BUF2	12-bit integer values from A/D #2
BUF3	12-bit integer values from A/D #3
CO	Cosine factor (DGZL)
C1	Cosine factor (DGZL)
CFACT	Current factor ( $I = CFACT * E$ )
CHERRY	Display character buffer
CHNO	Channel number being computed
COMTYP	Computation type (not used)
COV	Covariance (ac power)
DATA	Start of LSIVM input parameter buffer

DCV	Average (dc) voltage from statistical computation
DFD	DFT data array
DIST1	Distortion of channel 1
DIST2	Distortion of channel 2
DIST3	Distortion of channel 3
DIST	Harmonic distortion
DLUN	Display device (1 = terminal, 2 = display)
DSTYLE	Display style (0-5)
ECNT	Counter for A/D error retry
FSAM	Sample frequency (Hz)
FSIG	Signal frequency (Hz)
GAIN1	A/D gain channel 1 (1, 2, 5, 10)
GAIN2	A/D gain channel 2 (1, 2, 5, 10)
GAIN3	A/D gain channel 3 (1, 2, 5, 10)
GAIN	A/D converter gains array
GATED	Gated voltage flag (0 = no, 1 = yes)
GPBIN	Input buffer address (GPIO SR)
GPBOUT	Output buffer address (GPIO SR)
GPEOF	E-O-I found - flag (0 = no, 1 = yes) (GPIO SR)
GPMAX	Maximum characters on input (GPIO SR)
GPNCH	Character count for input or output (GPIO SR)
GTRIG	Address to go to if "GET" received (GPIO SR)
HAR	Number of harmonics to use to compute distortion
HARM	Harmonics - used flag array (DAPD)
HTC	Specific harmonic (line + 1) to compute
I	Line number (DAPD)
IBLEN	Bytes received from IEEE-488 bus

IC	Command byte 1
ICA	Count of A/D conversions
ICE	Count of A/D errors
ICN	Command byte 2 (may be channel number)
IERD	Error status of DFT
INBUF	IEEE-488 bus input packet
IP	Command parameter string
ISW	LSIVM status word
IWD	What DMA to interrupt from (A32D)
LADD	Pointer to displayed parameters
LCTR	Counter for displayed parameters
LDAT	Parameter to display
LPTR	Index for displayed parameters
NCAV	Sequences to average (NCYC/AVGCOD)
NCYC	Cycles per sequence
NCYF	Cycles to compute (AVGCOD); averaging changes
NCYHA	Cycles to compute (for a harmonic)
NSAM	Samples per sequence (DAPD)
PAD	Samples to convert per DMA transfer per channel
PDFT	Phase from DFT
PEKVM	Peak (-) voltage from inspection
PEKVP	Peak (+) voltage from inspection
PHAS1	Phase of channel 1
PHAS2	Phase of channel 2
PHAS3	Phase of channel 3
POWER	Total power (ac and dc)
PSIGN	Phase sign (DAPD)

PTC	Points to compute (PTR/NCYC); averaging changes this
PTR	Samples to transfer to host (samples per sequence)
PWR	Harmonic power (DAPD)
RAA	Argument list for writing buffer to host
RAM	Start of program RAM space
RAN	Address of buffer to send to host
RBUF	One voltage buffer (converted to floating-point)
RMD	Statistical data array
RMSV1	RMS voltage in channel 1
RMSV2	RMS voltage in channel 2
RMSV3	RMS voltage in channel 3
RMSV	RMS voltage from statistical analysis
RPHAS	Phase difference between channels 1 and 2
SO	Sine factor (DGZL)
SJM	Second joint moment (total power)
STACK	Start (top) of program stack
STPT	Number of first sample to transfer to host
STRING	Used by print
T1	DMA #1 pseudo-register (A32D)
T2	DMA #2 pseudo-register (A32D)
T3	DMA #3 pseudo-register (A32D)
VDFT	Total rms voltage from DFT
WAD	What A/D converters used (A32D)
WAV	Average window magnitude
WIND	Window buffer (floating-point)
WMODE	Window mode (0 = no, 1 = yes)
XIMAG	Imaginary voltage part (DAPD)

XREAL      Real voltage part (DAP<sub>0</sub>),

```

1 .TITLE LSIVAR
2 .IDENT /06/
3
4 !GLOBAL VARIABLES (DYNAMIC) FOR LSIVM
5
6 !PSECT $VARL,REL,RO !NOT REALLY RO!! TO FOOL TASKBUILDER!!
7 .BLKB 2452 !SHIFT BEGINNING OF RAM TO 4K BOUNDARY
8
9
10 BSIZE=1024.
11
12 RAM:: .BLKB 1000 !START OF PROGRAM RAM SPACE
13 STACK:: .WORD 0 !START (:OP) OF PROGRAM STACK
14
15 ICA:: .WORD 0 !COUNT OF A/D CONVERSIONS
16 ICE:: .WORD 0 !COUNT OF A/D ERRORS
17
18 !IEEE-488 BUS INPUT PACKET
19 !COMMAND BYTE 1
20 !COMMAND BYTE 2 (MAYBE CHANNEL NUMBER)
21 !COMMAND PARAMETER STRING
22 !LSIVM STATUS WORD
23 !BYTES RECEIVED FROM IEEE-488 BUS
24 !CHANNEL NUMBER BEING COMPUTED
25
26 !E-0-1 FOUND? FLAG (0=NO; 1=YES) (GPIOSR)
27 !INPUT BUFFER ADDRESS (GPIOSR)
28 !OUTPUT BUFFER ADDRESS (GPIOSR)
29 !MAXIMUM CHARACTERS ON INPUT (GPIOSR)
30 !CHARACTER COUNT FOR INPUT OR OUTPUT (GPIOSR)
31 !ADDRESS TO GO TO IF "GET" RECEIVED (GPIOSR)
32
33 !START OF LSIVM INPUT PARAMETER BUFFER
34 !START OF A/D CONVERTER GAINS ARRAY
35 !A/D GAIN CHANNEL 1 (1,2,5,10)
36 !A/D GAIN CHANNEL 2 (1,2,5,10)
37 !A/D GAIN CHANNEL 3 (1,2,5,10)
38 !SAMPLES TO TRANSFER TO HOST (SAMPLES PER SEQUENCE)
39 !NUMBER OF FIRST SAMPLE TO TRANSFER TO HOST
40 !NUMBERS OF A/D'S USED (1=#1,2=#2,4=#3,7=ALL)
41 !DMA TO INTERRUPT ON (IGNORE ALL OTHERS)
42 !SAMPLES TO CONVERT PER DMA TRANSFER PER CHANNEL
43 !CYCLES PER SEQUENCE
44 !NUMBER OF HARMONICS TO USE TO COMPUTE DISTORTION
45 !AVERAGING MODE (0=DON'T; OR CYCLES PER AVERAGE SUBSEQ..)
46 !DISPLAY STYLE (0-6)
47 !COMPUTATION TYPE (NOT USED)
48 !AUTOMATIC RETRIES WHEN A/D ERRORS OCCUR
49 !GATED VOLTAGE FLAG (0=NO; 1=YES)
50 !WINDOW MODE (0=NO; 1=YES)
51 !DISPLAY DEVICE (1=TERMINAL; 2=DISPLAY)
52 !SPECIFIC HARMONIC (LINE+1) TO COMPUTE
53 !INPUT PORT FOR CHANNEL 1
54 !INPUT PORT FOR CHANNEL 2
55 !INPUT PORT FOR CHANNEL 3

```



57	00376	000000	000000	ESIG::	.FLT2	0.0	! SIGNAL FREQUENCY (HZ)
58	003702	000000	000000	FSAM::	.FLT2	0.0	! SAMPLE FREQUENCY (HZ)
59							
60	003706	000000		PTC::	.WORD	0	! POINTS TO COMPUTE (PTR/NCYC); AVERAGING CHANGES
61	003710	000000		NCYF::	.WORD	0	! CYCLES TO COMPUTE (AVCCOD); AVERAGING CHANGES
62	003712	000000		NCYHA::	.WORD	0	! CYCLES TO COMPUTE (FOR A HARMONIC)
63	003714	000000		NCVAV::	.WORD	0	! SEQUENCES TO AVERAGE (NCYC/AVCCOD)
64	003716	000000		ECNT::	.WORD	0	! COUNTER FOR A/D ERROR RETRY
65	003720	000000		ADWL::	.WORD	0	! LENGTH IN BYTES OF BUFFER TO SEND TO HOST
66							
67	003722			DFD::			! START OF DFT DATA ARRAY
68	003722	000000	000000	VDFT::	.FLT2	0.0	! TOTAL RMS VOLTAGE FROM DFT
69	003726	000000	000000	PDFT::	.FLT2	0.0	! PHASE FROM DFT
70	003732	000000	000000	DIST::	.FLT2	0.0	! HARMONIC DISTORTION
71							
72	003736			RMD::			! START OF TIME-INTEGRATION DATA ARRAY
73	003736	000000	000000	RMSV::	.FLT2	0.0	! RMS VOLTAGE FROM TIME INTEGATION
74	003742	000000	000000	PEKVP::	.FLT2	0.0	! PEAK(+) VOLTAGE FROM INSPECTION
75	003746	000000	000000	PEKVM::	.FLT2	0.0	! PEAK(-) VOLTAGE FROM INSPECTION
76	003752	000000	000000	DCV::	.FLT2	0.0	! AVERAGE (DC) VOLTAGE FROM TIME INTEGRATION
77							
78	003756	000000	000000	POWER::	.FLT2	0.0	! TOTAL POWER (SUM OF E*1)
79	003762	000000	000000	CFACT::	.FLT2	0.0	! CURRENT FACTOR (I=CFACT*E)
80							
81	003766	000000	000000	SJM::	.FLT2	0.0	! SECOND JOINT MOMENT
82	003772	000000	000000	COV::	.FLT2	0.0	! COVARIANCE
83							
84	003776	000000	000000	RAA::	0.0.0	0	! ARGUMENT LIST FOR WRITING BUFFER TO HOST
85	004004	000000	000000	RAN::	.WORD	0	! ADDRESS OF BUFFER TO SEND TO HOST
86							! RAM VARIABLES USED BY A32D
87	004006	000000	000000	WAD::	.WORD	0	! WHAT A/D CONVERTERS USED (A32D)
88	004010	000000	000000	WMD::	.WORD	0	! WHAT DMA TO INTERRUPT FROM (A32D)
89	004012	000000	000000	T1::	.WORD	0	! DMA #1 PSEUDO-REGISTER (A32D)
90	004014	000000	000000	T2::	.WORD	0	! DMA #2 PSEUDO-REGISTER (A32D)
91	004016	000000	000000	T3::	.WORD	0	! DMA #3 PSEUDO-REGISTER (A32D)
92	004020	000000	000000	ADUM::	.WORD	0	! A/D PSEUDO REGISTER (A32D)
93	004022	000000	000000	ADST::	.WORD	0	! MEASUREMENT STATUS REGISTER (A32D)
94	004024	000000	000000	ADD::	.WORD	0	! CHANNEL 1 PORT ADDRESS * 400
95	004026	000000	000000	BDD::	.WORD	0	! CHANNEL 2 PORT ADDRESS * 400
96	004030	000000	000000	CDD::	.WORD	0	! CHANNEL 3 PORT ADDRESS * 400
97							
98	004032	000000	000000	LCTR::	.WORD	0	! COUNTER FOR DISPLAYED PARAMETERS
99	004034	000000	000000	LPTR::	.WORD	0	! INDEX FOR DISPLAYED PARAMETERS
100	004036	000000	000000	LADD::	.WORD	0	! POINTER TO DISPLAYED PARAMETERS
101	004040	000000	000000	LDAT::	.WORD	0	! PARAMETER TO DISPLAY
102	004042			STRLNG::	.BLKW	6	! USED BY PRINT
103							

```

105      000000      000000      000000      .FLT2      0.0
106 004056      000000      000000      .FLT2      0.0
107 004062      000000      000000      .FLT2      0.0
108 004066      000000      000000      .WORD      0
109 004072      000000      000000      .WORD      0
110 004074      000000      000000      .WORD      0
111 004076      000000      000000      .BLKW      20
112 004100
113
114 004150      000000      000000      .WORD      0
    
```

```

;RAM VARIABLES USED BY DAPD
;REAL VOLTAGE PART (DAPD)
;IMAGINARY VOLTAGE PART (DAPD)
;HARMONIC POWER (DAPD)
;PHASE SIGN (DAPD)
;LINE NUMBER (DAPD)
;SAMPLES PER SEQUENCE (DAPD)
;HARMONICS-USED FLAG ARRAY (DAPD)
;ERROR STATUS OF DFT
    
```

```

116      004152      000000      000000      000000      A0::      .FLT4      0.0
117 004160      000000      000000      000000      C0::      .FLT4      0.0
118 004162      000000      000000      000000      C1::      .FLT4      0.0
119 004170      000000      000000      000000      S0::      .FLT4      0.0
120 004200      000000      000000      000000
121 004210      000000
    
```

```

;RAM VARIABLES USED BY DCZL
;FACTOR (DCZL)
;COSINE FACTOR (DCZL)
;COSINE FACTOR (DCZL)
;SINE FACTOR (DCZL)
    
```

```

122 004312      000000      000000      000000      000000      000000      000000      000000      000000      000000
123 004326      000000      000000      000000      000000      000000      000000      000000      000000      000000
124 004262      000000      000000      000000      000000      000000      000000      000000      000000      000000
125 004266      000000      000000      000000      000000      000000      000000      000000      000000      000000
126 004272      000000      000000      000000      000000      000000      000000      000000      000000      000000
127 004276      000000      000000      000000      000000      000000      000000      000000      000000      000000
128 004302      000000      000000      000000      000000      000000      000000      000000      000000      000000
129 004306      000000      000000      000000      000000      000000      000000      000000      000000      000000
130 004312      000000      000000      000000      000000      000000      000000      000000      000000      000000
131 004316      000000      000000      000000      000000      000000      000000      000000      000000      000000
132 004322      000000      000000      000000      000000      000000      000000      000000      000000      000000
133
134 004326      000000      000000      000000      000000      000000      000000      000000      000000      000000
135 004326      000000      000000      000000      000000      000000      000000      000000      000000      000000
136 010326      000000      000000      000000      000000      000000      000000      000000      000000      000000
137 014326
138
139 020326      000000      000000      000000      000000      000000      000000      000000      000000      000000
140 030326      000000      000000      000000      000000      000000      000000      000000      000000      000000
141 030332
142
143      000001
    
```

```

;CHERRY DISPLAY CHARACTER BUFFER
;RMS VOLTAGE IN CHANNEL 1
;RMS VOLTAGE IN CHANNEL 2
;RMS VOLTAGE IN CHANNEL 3
;PHASE OF CHANNEL 1
;PHASE OF CHANNEL 2
;PHASE OF CHANNEL 3
;DISTORTION OF CHANNEL 1
;DISTORTION OF CHANNEL 2
;DISTORTION OF CHANNEL 3
;PHASE DIFFERENCE BETWEEN CHANNELS 1 AND 2
;A/D BUFFERS (THREE CONTIGUOUS ARRAYS)
;12-BIT INTEGER VALUES FROM A/D #1
;12-BIT INTEGER VALUES FROM A/D #2
;12-BIT INTEGER VALUES FROM A/D #3
;ONE VOLTAGE BUFFER (CONVERTED TO FLOATING-POINT)
;AVERAGE WINDOW MAGNITUDE
;WINDOW BUFFER (FLOATING-POINT)
    
```

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## APPENDIX F

### Voltmeter Memory Map

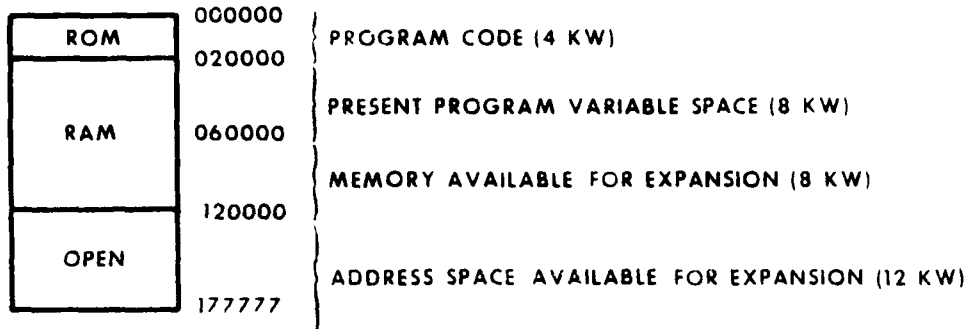


Fig. F1 - Voltmeter memory map.

The ROM code used by this version of the voltmeter occupies approximately 15300 (octal) bytes of memory, so there is room for expansion.

The program variable space allows, at present, data buffers of 1024 samples, but this can be expanded by modifying the parameter BSIZE in LSIVAR when the program is rebuilt. Note that for each sample a buffer is expanded, memory is used as follows:

- 6 bytes for the sampled A/D waveform (2 bytes per channel),
- 4 bytes for the integer to floating-point conversion buffer,
- 4 bytes for the floating-point data window.

This requires approximately 7K words of RAM memory for each additional 1024 samples.