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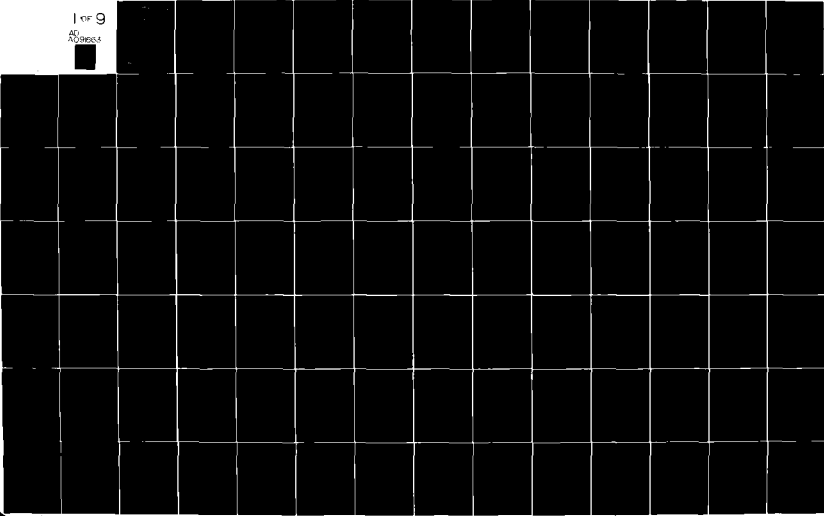
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FINAL REPORT

# SPEECH OPTIMIZATION AT 9600 BITS/SECOND

VOLUME 2

## REAL-TIME SOFTWARE AND HARDWARE

SUBMITTED TO  
DEFENSE COMMUNICATIONS AGENCY

SEPTEMBER 30, 1980

**Sylvania Systems Group**  
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were specification and transmission of the side-band information, accuracy of the pitch and voicing decisions, and error-protection of the important transmission parameters. Also included is the system design, detailed documentation, and program listings of the MAP-300 real-time implementation of the optimized ATC speech coder. Finally, the report includes a description of analog equipment GTE built to interface the MAP-300 to telephone handsets and tape recorders and a description of digital circuits (RS 423 compatible) to interface the MAP-300 to a modem.

This report is bound in two volumes. Volume I contains a description of the ATC system and the results of the FORTRAN simulations. Volume II contains all the information on the real-time system including documentation for implementing the ATC system on the MAP, listing of the MAP software, and documentation for the hardware built by GTE.

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## Chapter 3

### Real-Time Implementation

The GTE Sylvania Speech Processing System functions as a real-time, full duplex terminal of a digital voice communications system. As such, the GTE system accepts and digitizes analog voice input, processes it using an Adaptive Transform Coding (ATC) algorithm, and transmits the resulting serial bit stream at 9600 bits/second across a digital channel to a similar terminal. Because of the full duplex requirement, the system must simultaneously accept a similar bit stream, decode it to produce synthetic speech, and finally convert this synthetic speech to an analog waveform which it then plays out.

This chapter describes the design and operation of the GTE system and the hardware and software components used to construct it.

#### 3.1 System Components

The GTE ATC system is implemented on a MAP-300 processor manufactured by CSP, Inc., Billerica, Mass. The MAP architecture consists of several independent processors, capable of parallel operation. Some of these processors are the Arithmetic Processor, used for high speed floating point operations; the Input/Output Scroll processors, of which there are three in the system, used for the Analog In, Analog Out, and Digital In/Out functions; and the CSPU, which is used to control the other processors and also to perform general computation chores.

Attached to the MAP is a GTE-manufactured Speech Processing Interface (SPI). The SPI includes a handset with earphone and high-quality microphone, amplifiers, and band-limiting filters. The SPI interfaces to the ADAM and AOM scrolls. The Full Duplex Interface (FDI) is also

attached to the MAP. The FDI resides physically on the I/O scroll board and provides 9600 bps digital ports and their interfaces to the I/O scroll. Two additional SPI's and FDI's to be used on other MAP systems have been supplied by GTE.

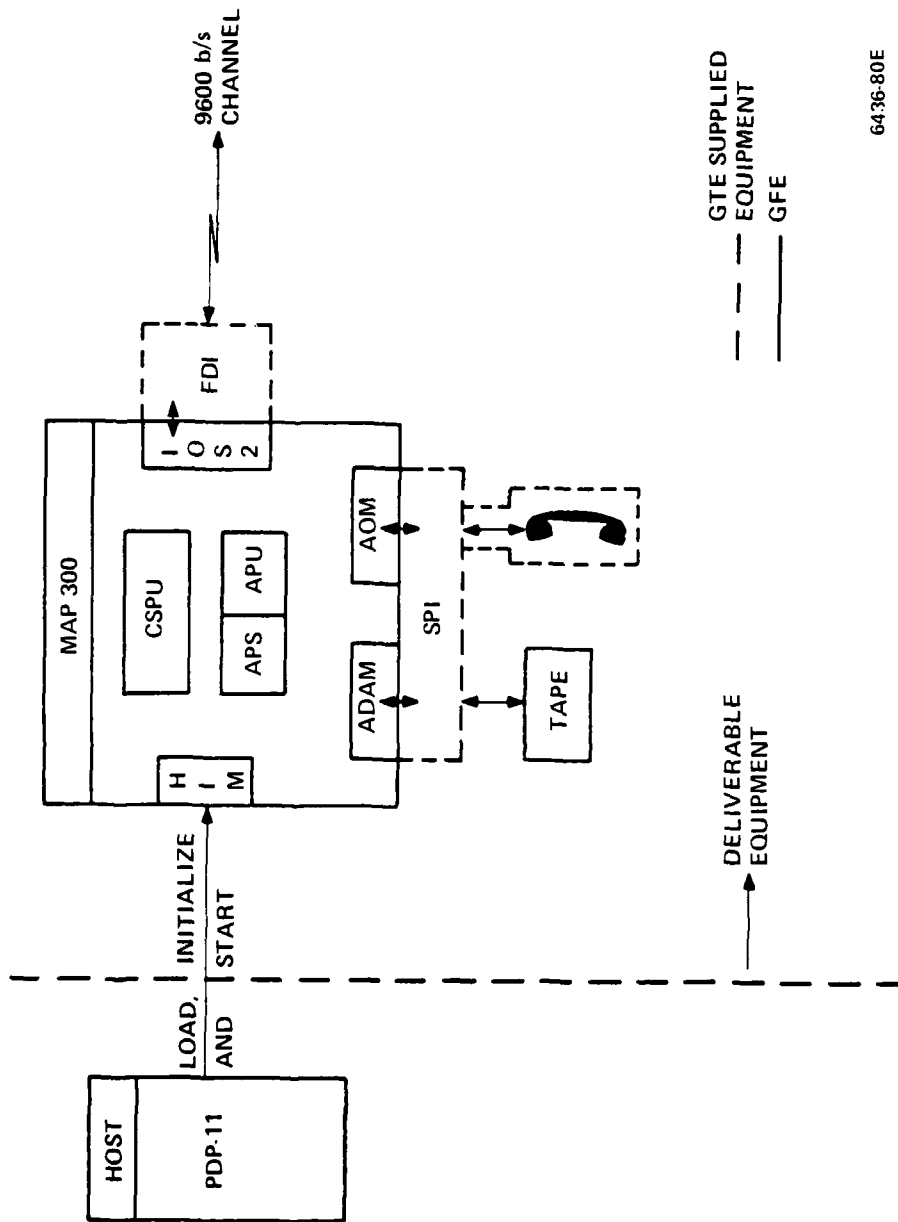
The MAP is attached to a host PDP-11 which is used to load the MAP programs and to initialize and start them. Once the MAP has been started, it runs independently of the host. A block diagram of the system is shown in Figure 3-1.

The software components of the ATC system include modules supplied by CSP, Inc., as well as those written by GTE. Three categories of software are used in the ATC system. The first, the Fortran Control and Support programs, resides in the PDP-11 and is used for communication between the user and the MAP. The second category, Executive software, consists of the body of code that runs in the CSPU. This software is used to control and schedule all the processes in the MAP. The third category, MAP Functions, consists of the program modules that are loaded into the Arithmetic Processor and I/O Scroll processors. The scrolls require only one program each, but the Arithmetic Processor, because of limited memory, must be loaded with successive program modules and started for each. The CSPU Executive software performs this task.

### 3.2 System Design and Operation

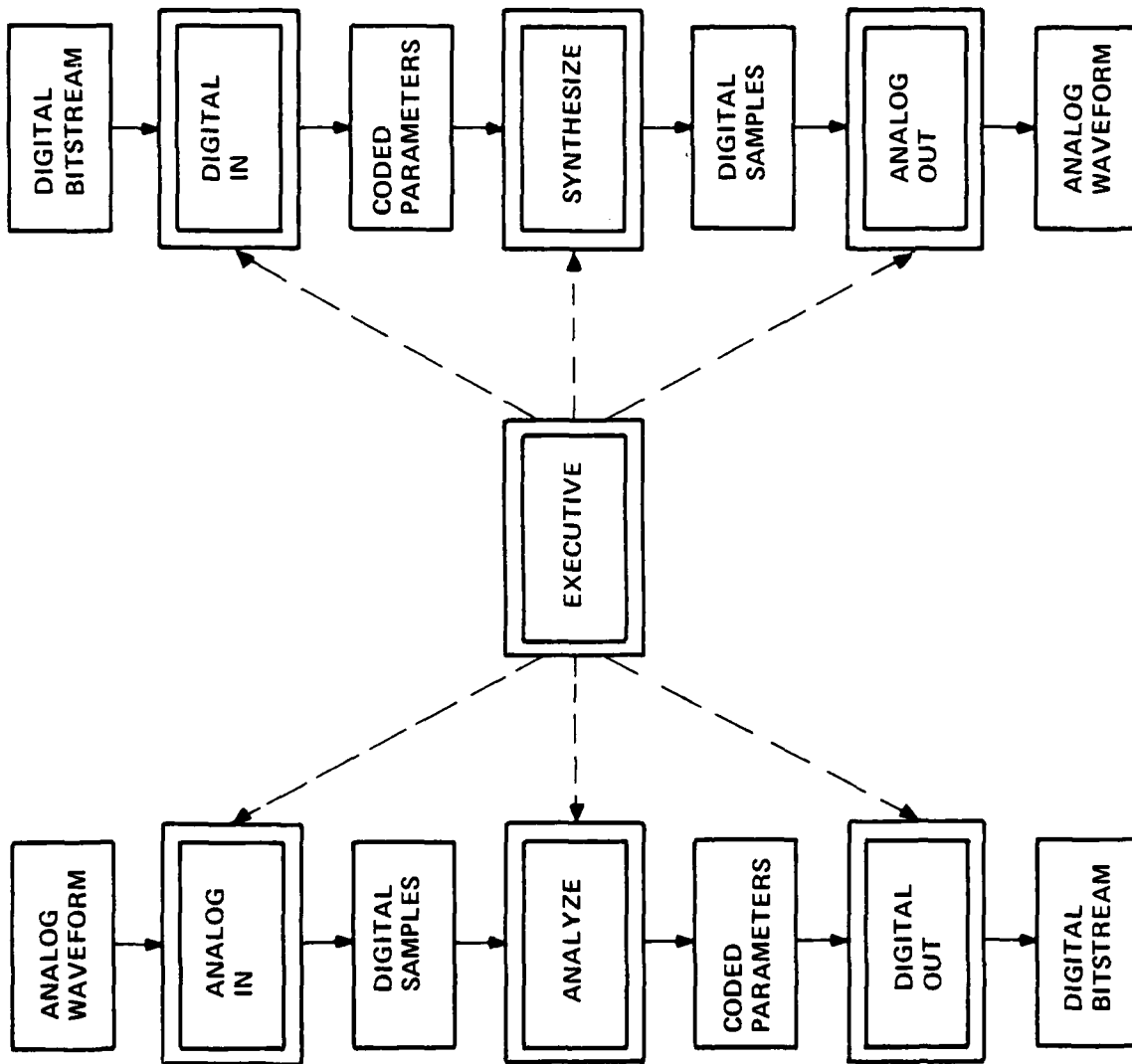
The ATC system is composed of seven distinct processes. These are the Analog In process, the Analog Out process, the Digital In process, the Digital Out process, the Analysis process, the Synthesis process, and the Executive process. Figure 3-2 shows the function of and the relationship between the various processes. Conceptually, all of these





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FIGURE 3-1: ATC SYSTEM COMPONENTS



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FIGURE 3-2: BASIC PROCESSES OF ATC SYSTEM

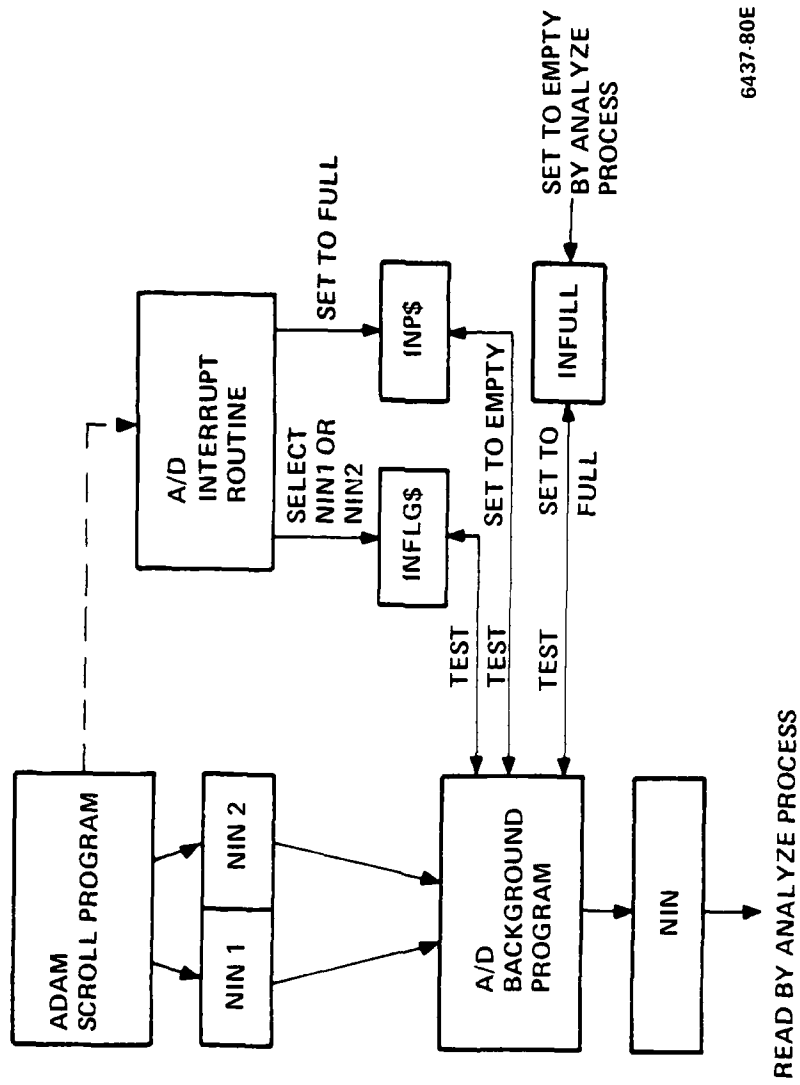
processes operate simultaneously, however, since at least portions of all of them must be executed by the CSPU or the Arithmetic Processor, both of which are sequential machines, the scheduling and synchronization of these processes is at the heart of the system design. Since the four I/O processes are essentially similar, they will be discussed first. The Analyze and Synthesize processes, which depend on their inputs on the Analog and Digital In processes, will be discussed next. Finally, the Executive process and the scheduling of the other processes will be presented.

### 3.2.1 Analog In Process

The Analog In process controls the A/D converter and makes the digitized samples available to the Analysis process. The Analog In process consists of three separate elements: the scroll program, the interrupt routine, and the background program. Figure 3-3 shows the components of the Analog In process.

#### 3.2.1.1 A/D Scroll Program

This program runs in the ADAM scroll. It causes the A/D converter to operate at a pre-selectable sampling rate, which is 6400 samples/second for the purposes of the ATC system. As each sample is digitized, it is placed in the next (empty) location in the current buffer. Two buffers are used so that one can be filling while the data already in the other is used. Each buffer is 246 samples, or 37.5 milliseconds long. When a buffer is filled, an interrupt to the CSPU is generated by the scroll program, and the other buffer becomes the current buffer.



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FIGURE 3-3: ANALOG IN PROCESS

This program is used as supplied by CSP, Inc., and as described in their documentation. The program used is ADMSD. The parameters supplied to it are ADAMPM (=63), the BID of the buffer containing the ADAM program; two identical sampling rates, each 6400 Hz; a control word which selects the internal clock, fixed point sampling, internal triggering, and no slow sampling; channel 1 select; and the double buffering pair where the input will be stored, NIN1 and NIN2.

#### 3.2.1.2 A/D Interrupt Routine

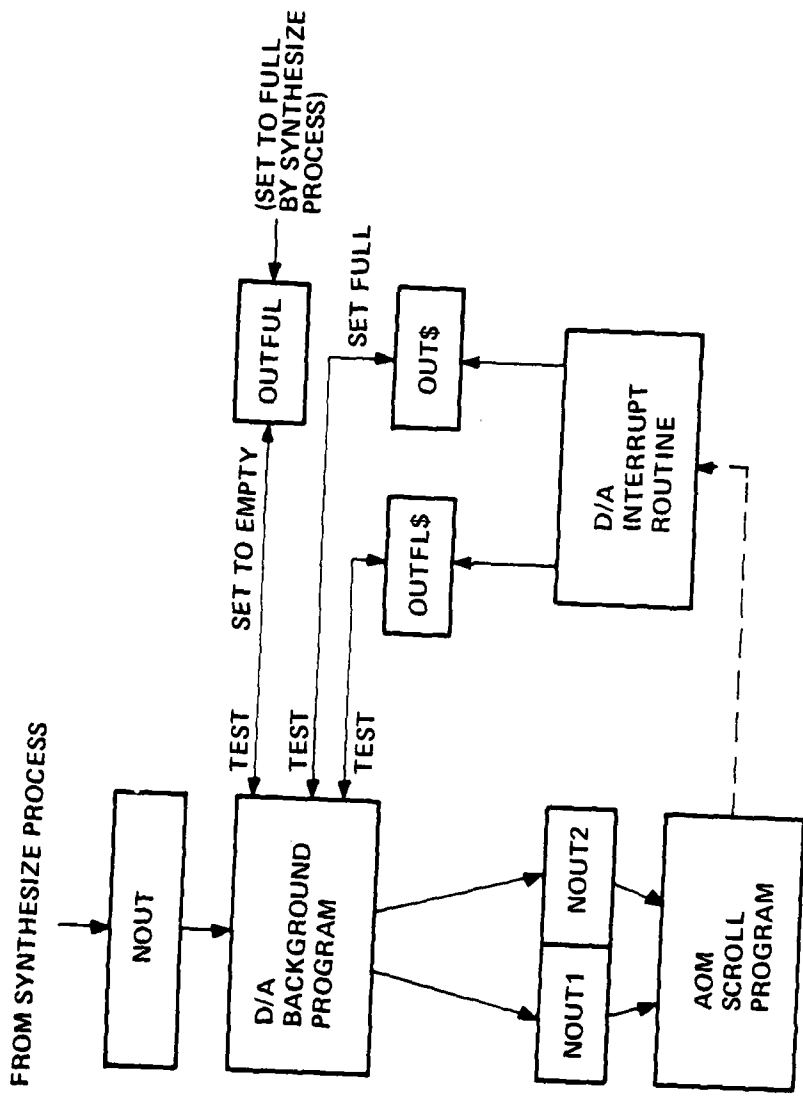
The A/D interrupt routine runs in the CSPU at interrupt level 10. It clears INPS (integer scalar 106) to indicate that a new frame of samples is available, and it also clears or sets INFLGS (integer scalar 107) to indicate that buffer NIN1 or buffer NIN2, respectively, contains the newest data.

#### 3.2.1.3 A/D Background Program

The A/D background program runs in the CSPU. It is enabled by the combination of INPS clear and INFULL (integer scalar 110 ) clear, which indicates that the previous data has been used by the Analysis process. The background process copies the new data from NIN1 or NIN2, depending on the state of INFLGS, into the analysis buffer, NIN. Finally, it sets INFULL and exists.

#### 3.2.2 Analog Out Process

The Analog Out process is similar to the Analog In process except that the data flow is reversed. The Analog Out process consists of the D/A Scroll program, the D/A interrupt routine, and the D/A background program. Figure 3-4 shows the components of the Analog Out process.



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FIGURE 3-4: ANALOG OUT PROCESS

#### 3.2.2.1 D/A Scroll Program

This program runs in the AOM scroll. It controls the D/A converter and uses a double buffering scheme similar to that used by the A/D scroll program. As each of the two buffers is emptied, the program generates a Device 22 interrupt to the CSPU. This program is used as supplied by CSP, Inc., and as described in their documentation. The program module used is AOMID. The parameters supplied to it are AOMPM, (=62), the BID where the AOM program is stored; a sampling frequency divider which is 1; a control word which selects fixed point output, single channel mode, and external triggering; the double buffering pair which stores the output, NOUT1 and NOUT2; and an offset, which is 2. The AOM program produces two channels of output, one of which is the data from the MAP and one of which is a ramp from 0 to 491.

#### 3.2.2.2 D/A Interrupt Routine

The D/A interrupt routine runs in the CSPU at level 9. It clears OUTS (integer scalar 104) to indicate that a buffer has been emptied. It also clears and sets the flag OUTFLS (integer scalar 105) to indicate that buffer NOUT1 or buffer NOUT2, respectively, is the buffer most recently emptied and, hence, available.

#### 3.2.2.3 D/A Background Program

The D/A background program module, called LOBUF, runs in the CSPU and is enabled by the combination of OUTS clear and OUTFUL (integer scalar 111) set, indicating that the Synthesis process has made new data available for output. If synchronization has been acquired, the

background program copies the new data from buffer NOUT into either buffer NOUT1 or buffer NOUT2, depending on the state of OUTFLS. If the system is not in synchronization, the relevant output buffer is set to zero. Finally, the background program clears OUTFUL, indicating that NOUT is available for filling by the Synthesis process.

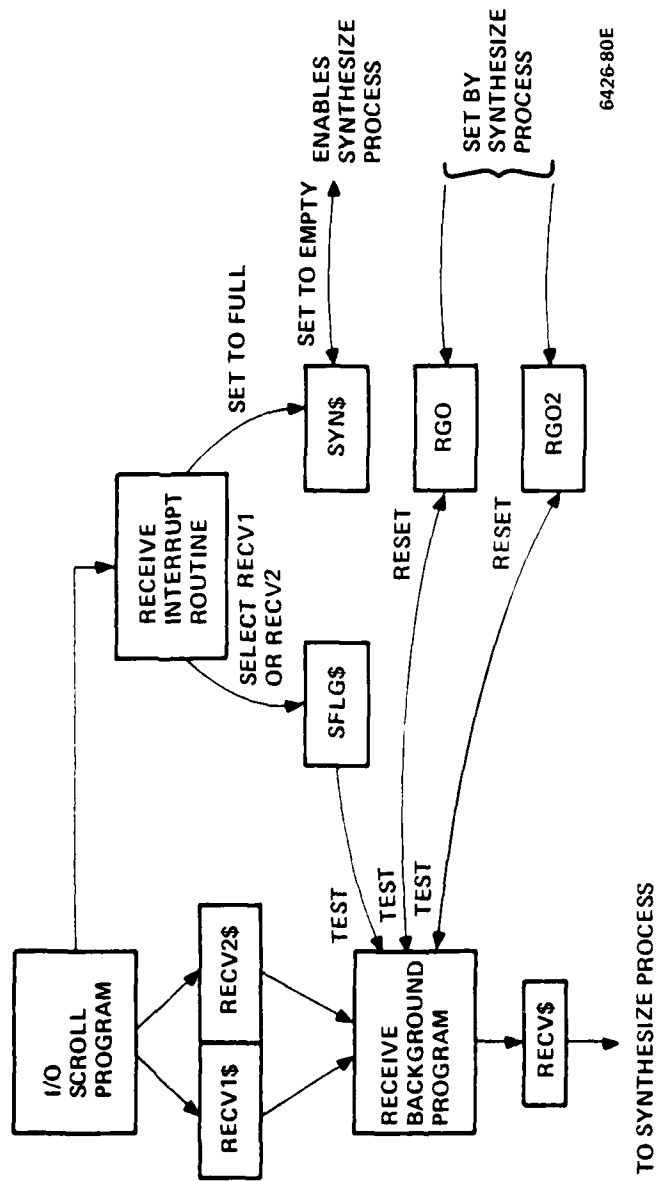
### 3.2.3 Digital In Process

The Digital In process controls the digital input from the I/O scroll, examines the digital data to determine or confirm synchronization, and makes a frame of digital data available to the Synthesis process. The Digital In process consists of three components: the I/O scroll program, the Receiver interrupt routine, and the Receiver background program. Figure 3-5 shows the components of the Digital In process.

#### 3.2.3.1 I/O Scroll Program

This program, which runs in the IOS2 Scroll, both transmits and receives digital data, and, thus, is a component of both the Digital In and the Digital Out process. This GTE written program is called LINE. It uses two sets of double buffers. One set, RECV1 and RECV2, stores the incoming bits from the modem. The other set, SEN1 and SEN2, store the bits to be output to the modem. As either of the receive buffers is filled, an interrupt from Device 16, line 2 to the CSPU is generated, and the other receive buffer begins to fill. Similarly, as either of the send buffers is emptied, an interrupt from Device 16, line 1 to the CSPU is generated, and the next bit to be output is taken from the other send buffer. All four of these buffers are 369 bits long and 16 bits wide. However, only the low order bit from each 16-bit word contains





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FIGURE 3-5: DIGITAL IN PROCESS

the digital data. The reader is referred to section 3.4 for the meanings of the other bits.

#### 3.2.3.2 Receive Interrupt Routine

The Receive Interrupt routine runs in the CSPU at level 7. It has two functions, first, to keep track of which receive buffer has the newest data and to transmit this information to other processes, and, second, to confirm that synchronization is still valid.

When an interrupt from the receive portion of the I/O Scroll program occurs, the Receive interrupt routine clears SYNS (integer scalar 102) and clears or sets flag SFLGS (integer scalar 103) to indicate that buffer RECV1 or buffer RECV2, respectively, has been filled.

When the ATC system has achieved synchronization (integer scalar 118, RSYN, greater than zero), the bit assumed to be the sync bit for the new data is compared to the sync bit from the previous buffer. If these bits are opposite, synchronization is assumed to be still good. If they are the same, and it is the fourth non-alternating sync bit without two consecutive correctly alternating sync bits intervening, synchronization is declared lost (RSYN is cleared). The location of the sync bit in the data buffer is stored in SYNCS (integer scalar 117).

#### 3.2.3.3 Receive Background Program

The Receive background program runs in the CSPU and is enabled by flag RGO (integer scalar 114) being set. This flag is set by the Synthesis process which is in turn enabled by SYNS being clear. The Receive background program sets SYNS to one on entry.

The Receive background program performs several functions. First, it must collect a new frame of data. This consists of finding the newest sync bit location, and using the 369 previous received bits as the frame. This is implemented by having the background program copy the new data into another set of contiguous buffers, BF1, BF2, and BF3. If RECV2 contains the new data, it is copied into BF2. If RECV1 contains the new data, it is copied into both BF1 and BF3. Then, the 369 data bits prior to the sync bit location are copied into buffer RECV. This double copying of RECV1 assures that, regardless of the position of the synchronization bit in the new frame, the 369 data bits previous to it are contained (contiguously) in the triple length buffer.

Second, the background program must acquire synchronization. That is, it must determine the position of the sync bit in the frame of data. The background program builds a histogram containing 369 bins, one for each bit in the frame. When it receives a new frame of data, the new frame is compared bit for bit against the previous frame. If a given bit alternates in the two frames, the corresponding bin in the histogram is incremented. If it does not alternate, the bin is cleared. This process continues until one bin both exceeds the acquired threshold (10.) and is a unique maximum in the histogram. When this occurs, the bit position corresponding to the maximum bin is declared the Sync position and is stored in SYNCSC (integer scalar 117) and RSYN (integer scalar 118) is set to one to indicate that synchronization has been acquired. Until synchronization is acquired, the Receive background process terminates at this point.

Once synchronization has been acquired, the background process must present an actual frame of data, one that has the sync bit at the beginning of the frame, for further processing. To do this, it copies the 369 bits previous to the sync bit of the newest frame from the triple length buffer (BF1, BF2, and BF3) into the receive processing buffer RECVS. This procedure is shown in Figure 3-6. Reasons of efficiency require that the new actual frame be present as a contiguous block within the triple length buffer, in order that a "block move" instruction can be used for the copying and no "corner turning" test is needed.

Once a new frame of data has been copied into RECVS, transmission bit errors are detected and corrected if possible. A (63, 45) BCH code is used to protect 45 bits of the sideband parameters with 18 protection bits. These 63 bits are decoded using a table-look-up procedure for efficiency. For each of the 63 transmitted bits, there are three associated values (Table 3-1). Three residues  $S_1$ ,  $S_3$ , and  $S_5$  are computed by exclusive-or'ing the appropriate value for each bit that is set into each sum. If each of these residues is zero, no transmission errors are detected, and the 45 data bits are used as received.

If the residues are not all zero, the number of errors is determined by computing three syndromes  $\sigma_1$ ,  $\sigma_2$ , and  $\sigma_3$ , and a determinant, which is

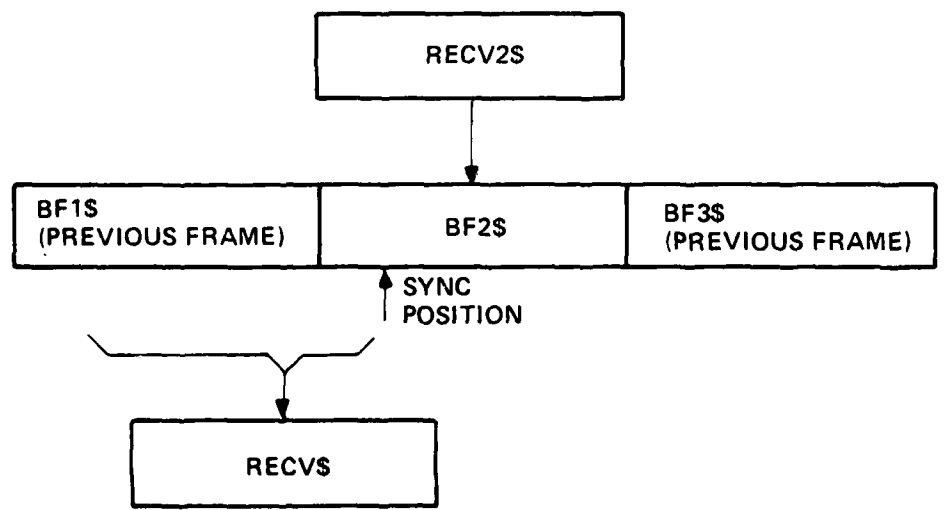
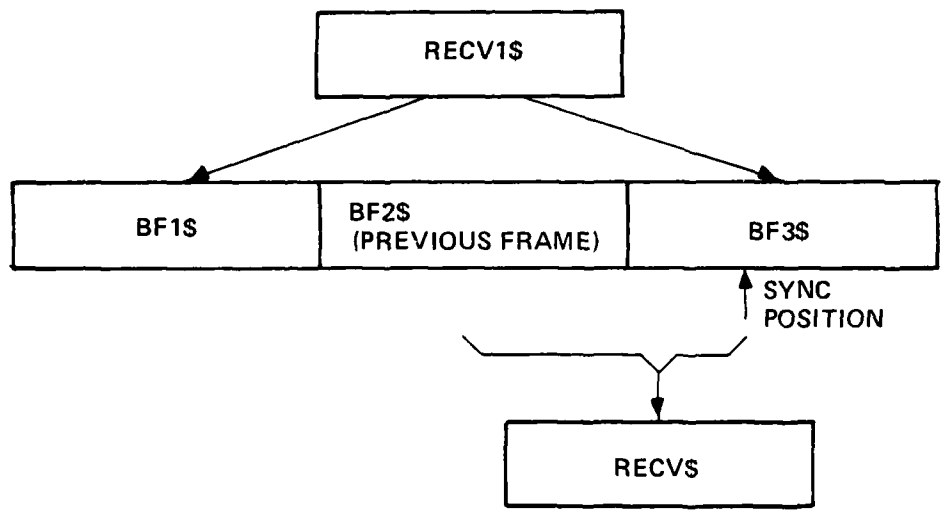
$$S_1^3 + S_3$$

If this determinant is zero, one error has occurred.  $\sigma_1$  is set to one and  $\sigma_2$  and  $\sigma_3$  are zeroed. Otherwise,

$$\sigma_1 = S_1$$

$$\sigma_2 = S_1^2 S_3 + S_5 / (S_1^3 + S_3)$$

$$\sigma_3 = (S_1^3 + S_3) + S_1 \sigma_2$$



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FIGURE3-6: FRAME COLLECTION

Bit Position	S <sub>1</sub> (decimal)	S <sub>3</sub>	S <sub>5</sub>	Bit Position	S <sub>1</sub> (decimal)	S <sub>3</sub>	S <sub>5</sub>
0	33	57	63	33	56	17	30
1	49	62	42	34	28	59	20
2	57	23	13	35	14	15	24
3	61	43	55	36	7	40	16
4	63	13	27	37	34	5	33
5	62	25	18	38	17	24	62
6	31	58	28	39	41	3	21
7	46	54	41	40	53	8	39
8	23	22	15	41	59	1	58
9	42	18	10	42	60	57	44
10	21	51	12	43	30	62	9
11	43	14	8	44	15	23	14
12	52	17	49	45	38	43	53
13	26	59	31	46	19	13	38
14	13	15	43	47	40	25	5
15	39	40	50	48	20	58	6
16	50	5	29	49	10	54	4
17	25	24	22	50	5	22	57
18	45	3	37	51	35	18	46
19	55	8	7	52	48	51	52
20	58	1	59	53	24	14	25
21	29	57	19	54	12	17	47
22	47	62	35	55	6	59	11
23	54	23	3	56	3	15	51
24	27	43	2	57	32	40	34
25	44	13	61	58	16	5	60
26	22	25	23	59	8	24	40
27	11	58	26	60	4	3	48
28	36	54	45	61	2	8	32
29	18	22	54	62	1	1	1
30	9	18	36				
31	37	51	56				
32	51	14	17				

TABLE 3-1: RESIDUE TABLE TBPSM

The powers are formed using two tables TBRPC and TBPRC, shown in Table 3-2. These are essentially logarithm and anti-logarithm tables for Galois field arithmetic. For example,  $S_1^2$  is found by using  $S_1$  as an index to TBRPC, doubling the value found in the table (modulo 63) and using the result as an index into TBPRC. The division is performed similarly.

The Chien search procedure (refer to Section 2.4 ) is used to determine the position of the errors. This procedure uses three additional tables, TBAL1, TBAL2, and TBAL3, shown in Table 3-3, to look up the products  $\sigma_1\alpha$ ,  $\sigma_2\alpha^2$ , and  $\sigma_3\alpha^3$  which replace  $\sigma_1$ ,  $\sigma_2$ , and  $\sigma_3$ . For each bit position,  $\sigma_1$ ,  $\sigma_2$ , and  $\sigma_3$  are exclusive or'ed together. If the result is unity, that bit position is in error and is complemented.

When transmission errors have been corrected, the background process must deserialize the bitstream into code words of various lengths. This deserialization procedure is performed in two parts. The first part deserializes the sideband parameters and makes them available to the Synthesis process. Figure 3-7 shows a map of the data frame and the bits allocated to each of the sideband parameters. The sideband code words are stored in buffer RQPBS, which is later read by the Synthesize process.

Before the second part of the deserialization procedure, deserialization of the Discrete Cosine Transform (DCT) coefficients can be executed, the word lengths of the DCT's must be obtained from the Synthesize process, which has determined these word lengths from the sideband parameters. To avoid delays and the consequent buffering, the transmitted data consists of the current frame of sideband information and the

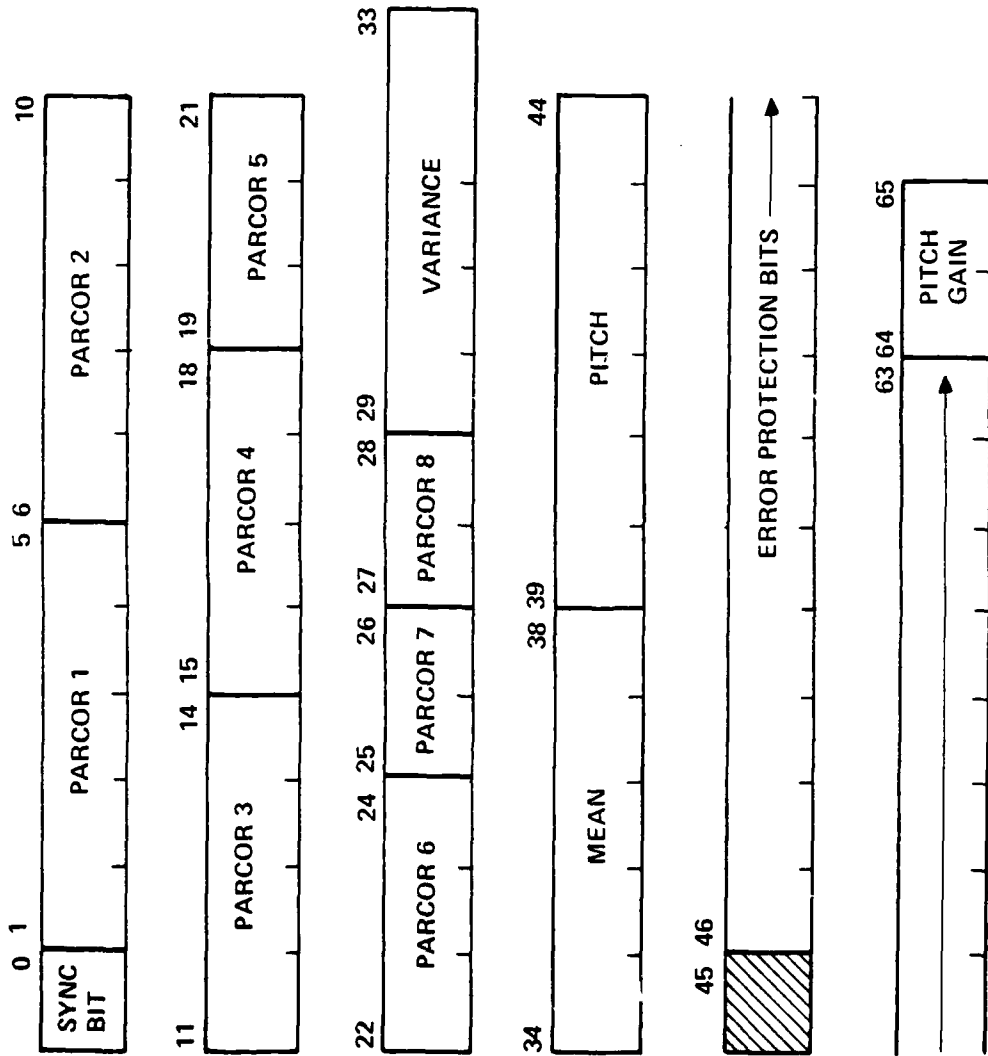
TBRPC				TBPRC			
S	Index	S	Index	Index	S	Index	S
0	0	32	5	0	1	32	9
1	0	33	62	1	2	33	18
2	1	34	25	2	4	34	36
3	6	35	11	3	8	35	11
4	2	36	34	4	16	36	22
5	12	37	31	5	32	37	44
6	7	38	17	6	3	38	27
7	26	39	47	7	6	39	54
8	3	40	15	8	12	40	47
9	32	41	23	9	24	41	29
10	13	42	53	10	48	42	58
11	35	43	51	11	35	43	55
12	8	44	37	12	5	44	45
13	48	45	44	13	10	45	25
14	27	46	55	14	20	46	50
15	18	47	40	15	40	47	39
16	4	48	10	16	19	48	13
17	24	49	61	17	38	49	26
18	33	50	46	18	15	50	52
19	16	51	30	19	30	51	43
20	14	52	50	20	60	52	21
21	52	53	22	21	59	53	42
22	36	54	39	22	53	54	23
23	54	55	43	23	41	55	46
24	9	56	29	24	17	56	31
25	45	57	60	25	34	57	62
26	49	58	42	26	7	58	63
27	38	59	21	27	14	59	61
28	28	60	20	28	28	60	57
29	41	61	59	29	56	61	49
30	19	62	57	30	51	62	33
31	56			31	37		

TABLE 3-2: TBRPC AND TBPRC TABLES



$\sigma$	$\sigma_1\alpha$ TBAL1	$\sigma_2\alpha^2$ TBAL2	$\sigma_3\alpha^3$ TBAL3	$\sigma$	$\sigma_1\alpha$ TBAL1	$\sigma_2\alpha^2$ TBAL2	$\sigma_3\alpha^3$ TBAL3
0	0	0	0	32	3	6	12
1	2	4	8	33	1	2	4
2	4	8	16	34	7	14	28
3	6	12	24	35	5	10	20
4	8	16	32	36	11	22	44
5	10	20	40	37	9	18	36
6	12	24	48	38	15	30	60
7	14	28	56	39	13	26	52
8	16	32	3	40	19	38	15
9	18	36	11	41	17	34	7
10	20	40	19	42	23	46	31
11	22	44	27	43	21	42	23
12	24	48	35	44	27	54	47
13	26	52	43	45	25	50	39
14	28	56	51	46	31	62	63
15	30	60	59	47	39	58	55
16	32	3	6	48	35	5	10
17	34	7	14	49	33	1	2
18	36	11	22	50	39	13	26
19	38	15	30	51	37	9	18
20	40	19	38	52	43	21	42
21	42	23	46	53	41	17	34
22	44	27	54	54	47	29	58
23	46	31	62	55	45	25	50
24	48	35	5	56	51	37	9
25	50	39	13	57	49	33	1
26	52	43	21	58	55	45	25
27	54	47	29	59	53	41	17
28	56	51	37	60	59	53	41
29	58	55	45	61	57	49	33
30	60	59	53	62	63	61	57
31	62	63	61	63	61	57	49

TABLE 3-3: CHIEN SEARCH TABLES



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FIGURE 3-7: PARAMETER FRAME MAP

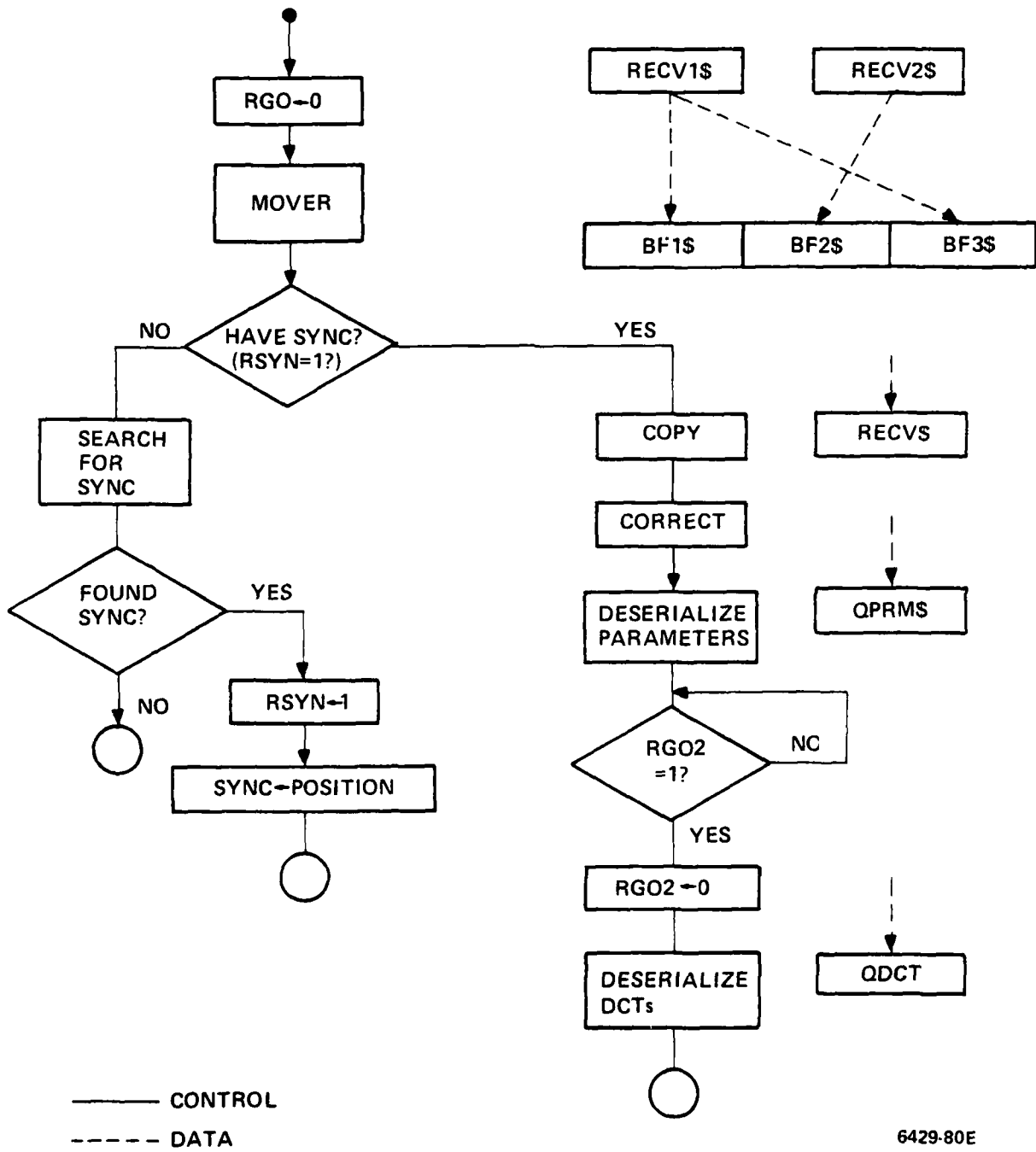
previous frame of DCT coefficients. Thus, the Synthesize process determines the DCT code word lengths for a given frame based on sideband information received in the previous frame. It passes this information to the Receive background program in buffer RIBITS and sets RG02 (integer scalar 115) to 1 to indicate that new data is present. The Receive background program waits for RG02 to be set before it proceeds with the DCT deserialization and clears RG02. The deserialized DCT coefficients are stored in buffer RQTDCT. The Receive background program then terminates. Figure 3-8 shows the organization of the complete Receive background program.

#### 3.2.4 Digital Out Process

The Digital Out process serializes and protects the data to be transmitted and controls the digital output to the I/O scroll. The Digital Out process consists of three components: the I/O scroll program, the Transmit interrupt routine, and the Transmit background program. Figure 3-9 shows the components of the Digital Out process.

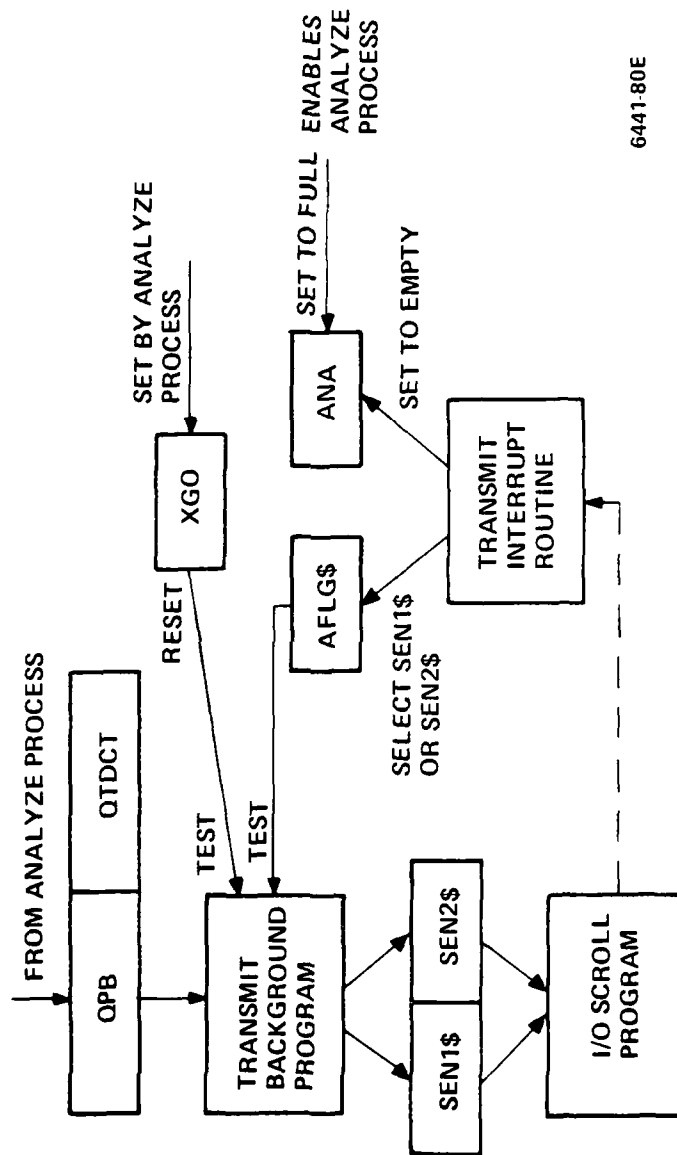
##### 3.2.4.1 I/O Scroll Program

The transmitter function of the I/O scroll program is described in subsection 3.2.3.1. This program puts out the low order bit of each half-word in the double buffer pair SEN1 and SEN2 and generates an interrupt when either buffer is emptied.



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FIGURE 3-8: RECEIVE BACKGROUND PROCESS



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FIGURE 3-9: DIGITAL OUT PROCESS

#### 3.2.4.2 Transmit Interrupt Routine

The Transmit interrupt routine runs in the CSPU at level 7. Its function is to keep track of which transmit buffer, SEN1 or SEN2, has been most recently emptied, and to transmit this information to the Transmit background program.

When an interrupt from the transmit portion of the I/O scroll program occurs, the Transmit interrupt routine clears ANAS (integer scalar 100) to indicate that a transmit buffer has become empty and clears or sets AFLG (integer scalar 101) to indicate that buffer SEN1 or SEN2, respectively, has been most recently emptied.

#### 3.2.4.3 Transmit Background Program

The Transmit background program runs in the CSPU and is enabled by flag XG0 (integer scalar 115) being set. This flag is set by the Analysis process, which is in turn enabled by ANAS being clear. The Transmit background process sets ANAS to one on entry.

The Transmit background program first transforms the quantized sideband parameters from buffer QPBS and the quantized DCT coefficients from buffer QDCTS into a bitstream which is built up in buffer SENS. The SENS buffer contains 369 half-words, the low order bits of which form the bitstream. The function of the higher order bits is described in Section 3.4.

The serialization procedure is performed in two parts. The first part serializes the sideband information according to the frame map in Figure 3-7. This part also skips 18 words in SENS to allow for later

insertion of protection bits. The second part of the serialization procedure serializes the DCT coefficients. These coefficients are those obtained from the previous frame. That is, the DCT coefficients are delayed in the Analysis process by one frame before being serialized, while the sideband parameters are those from the current frame and experience no additional delay. The quantized DCT coefficients are to be transmitted as code words of variable lengths. The background program obtains the length for each coefficient from buffer IBITS. The DCT coefficients in buffer QTDCTS are ordered by word length. These word lengths are constrained to be monotonically decreasing, so that the word length of each DCT coefficient must be equal to or less than that of the previous coefficient.

When all parameters and coefficients have been serialized, the background program generates BCH error protection bits for the resulting bit stream. The protection bits are found using a table look-up procedure. Table 3-4 shows the protection table, TBENC. For each of the 45 bits that is set, the bit position is used as an index into this table. The 18-bit values found from the table are exclusive or'ed together to form the protection bits. Each of the 18-bit values occupies two half words, the 15 least significant bits in one half word and the remaining 3 bits in an adjacent half word.

When all error correction bits have been inserted into buffer SENS, the background program copies this buffer into either SEN1S or SEN2S, depending on AFLG being clear or set. The Transmit background program then terminates.

Bit Position	Value		Bit Position	Value	
	Low Order	High Order		Low Order	High Order
0	30764	7	23	21534	7
1	17466	4	24	21027	4
2	8733	2	25	10513	6
3	4336	5	26	5256	7
4	28843	5	27	29288	4
5	16505	1	28	14644	2
6	22544	3	29	7322	1
7	21540	6	30	30305	7
8	10770	3	31	17180	0
9	27941	6	32	8590	0
10	13970	7	33	4295	0
11	25445	4	34	2147	4
12	12722	6	35	1073	6
13	6361	3	36	536	7
14	29760	2	37	31008	4
15	14880	1	38	15504	2
16	25916	7	39	7752	1
17	19122	4	40	30472	7
18	9561	2	41	17320	4
19	4780	5	42	8660	2
20	29050	5	43	4330	1
21	16529	5	44	28761	7
22	22628	1			

TABLE 3-4: ERROR PROTECTION ENCODING TABLE TBENC



### 3.2.5 Analyze Process

The Analyze process performs the Adaptive Transform Coding algorithm on the digital speech waveforms supplied to it. The result of each instance of this process is a frame of quantized and coded sideband parameters and Discrete Cosine Transform coefficients.

The Analyze process is composed of six functional procedures, each of which consists of one or more array functions. The entire Analyze process runs in the Arithmetic Processor portion of the MAP. The six functional procedures are: Cycle Delay Lines, Compute DCT Coefficients, Compute and Quantize Sideband Parameters, Determine Basis Spectrum, Bit Allocation, and DCT Coefficient Quantization. The relationships of these procedures are shown in Figure 3-10.

#### 3.2.5.1 Cycle Delay Lines Procedure (FCB 237, PBFCB 172)

The Cycle Delay Lines procedure partially implements the buffering between processes required by the ATC system. It consists of the single array function VMOV1, as shown in Figure 3-11. The buffer delays implemented in VMOV1 are shown in Figure 3-12.

VMOV1 first moves the quantized parameters (QPRM) computed in the previous instance of the Analyze process to the buffer (AQPB) used as input by the Transmit background program. Then, it cycles two buffer delay lines, one for the DCT coefficients (QTDCT1) and one for the corresponding bit allocations (MIBIT4). The outputs of these delay lines are moved to input buffers (AQTDC and AIBIT4) used by the Transmit background program. The delay of one frame in the DCT coefficients and bit assignments is required for the proper operation of the Synthesize process. VMOV1 also sets flag XG0 (integer scalar 115) which enables the Transmit background program to run.

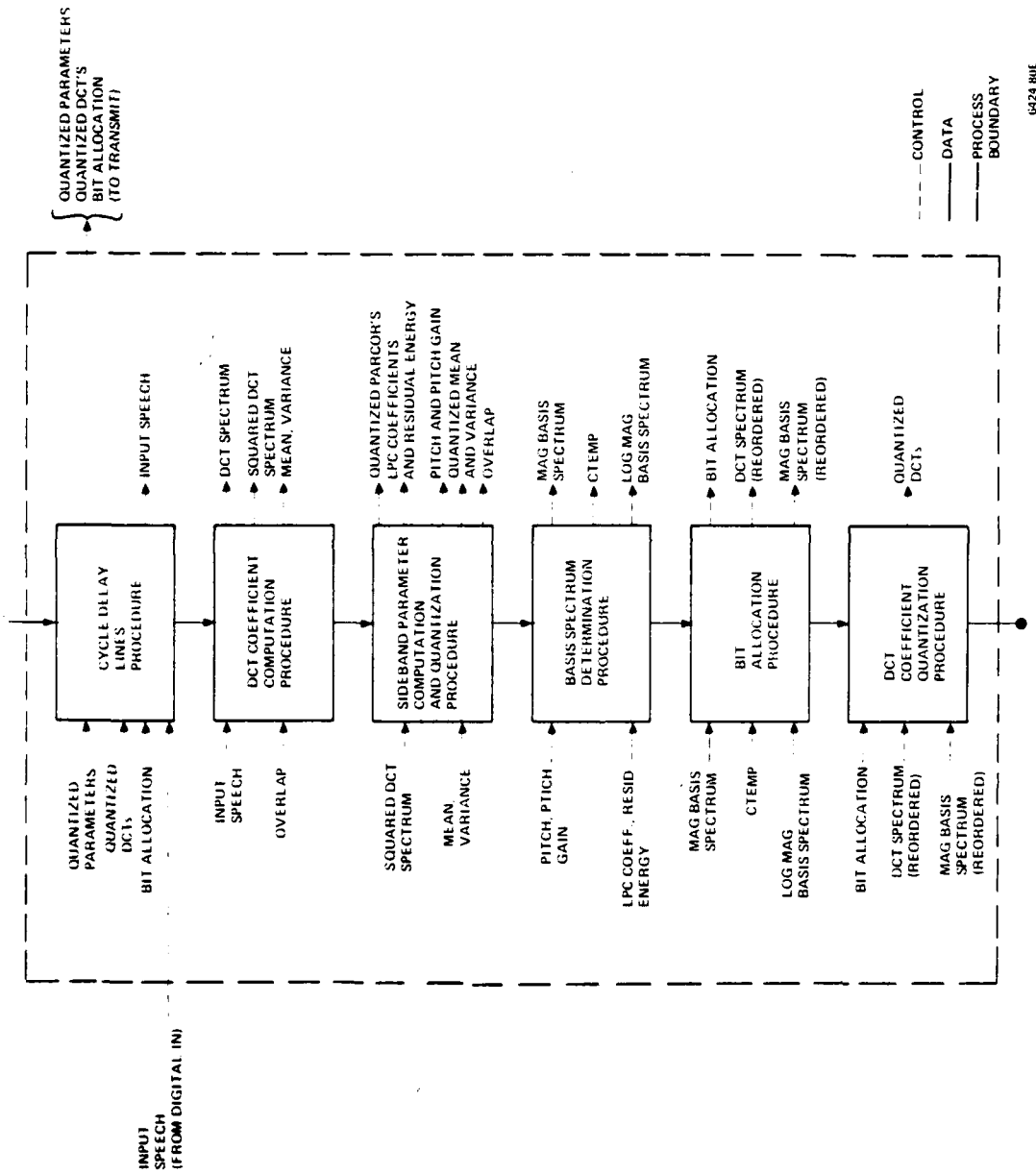


FIGURE 3-10: ANALYZE PROCESS

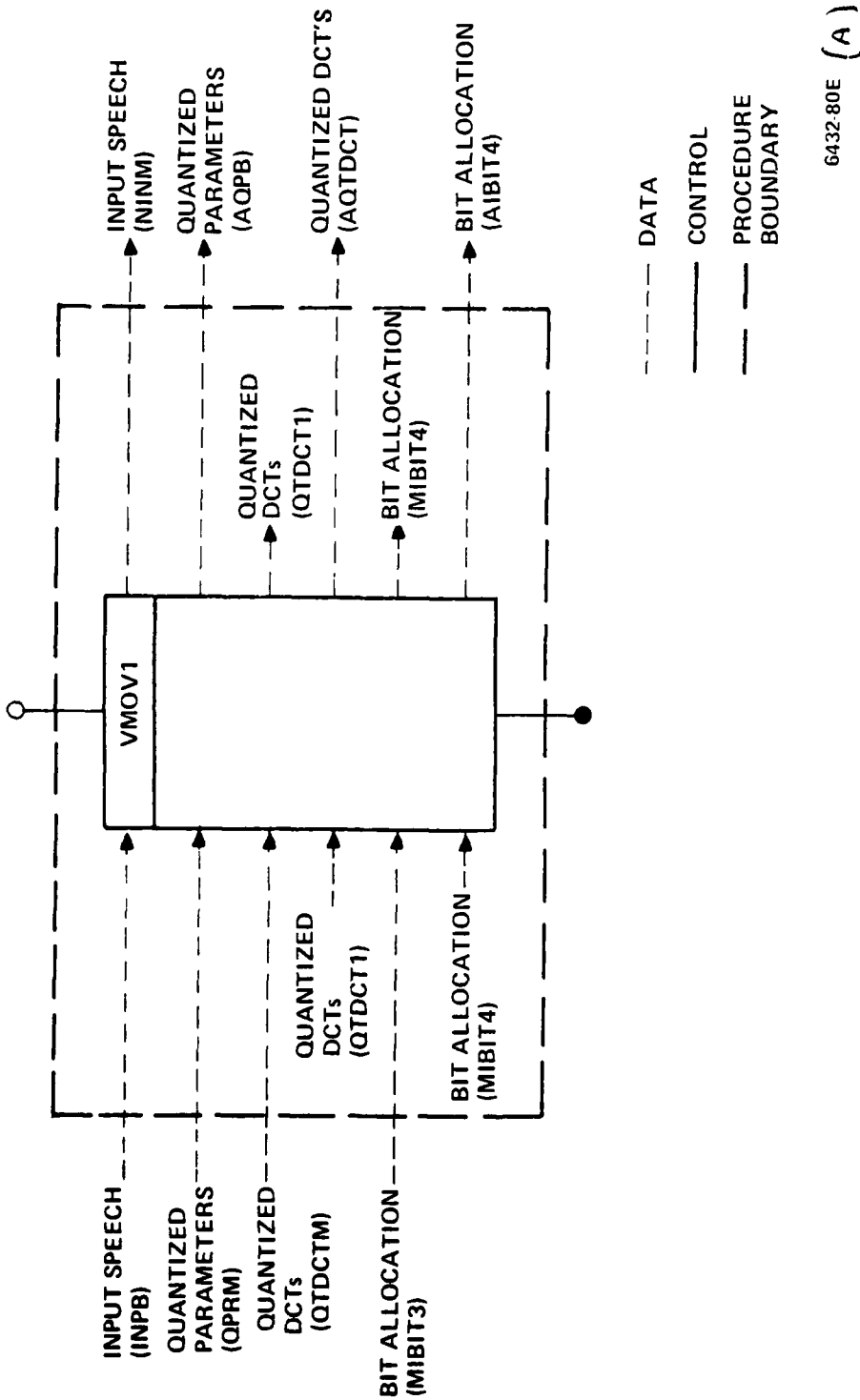
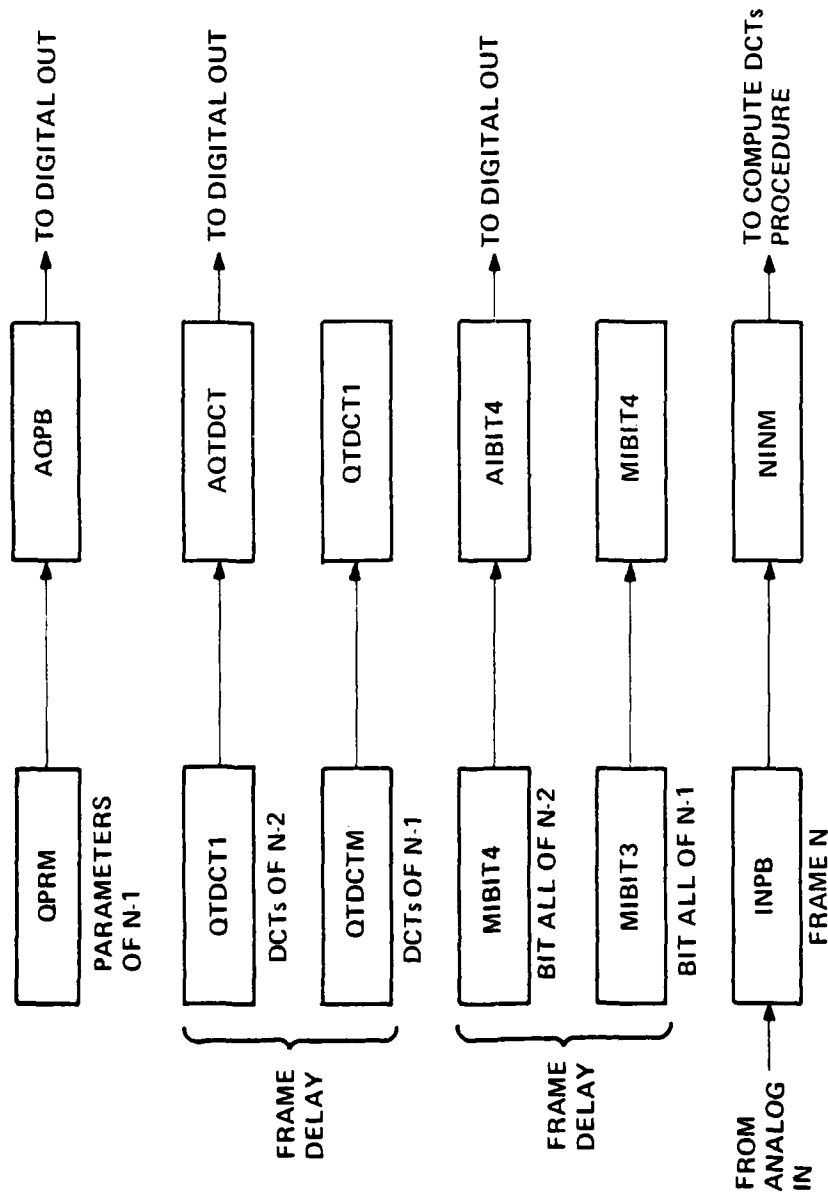


FIGURE 3-11: CYCLE DELAY LINES (ANALYZE)



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FIGURE 3-12: VMOV1 OPERATION FOR FRAME N

### 3.2.5.2 Compute DCT Coefficients Procedure

The Compute DCT Coefficients procedure is used to find the Discrete Cosine Transform representation of the current input frame. This procedure performs this transformation by using the appropriate pre-and post-processing around a Fast Fourier Transform. In addition, it performs the reformatting of the input data and the input frame overlapping.

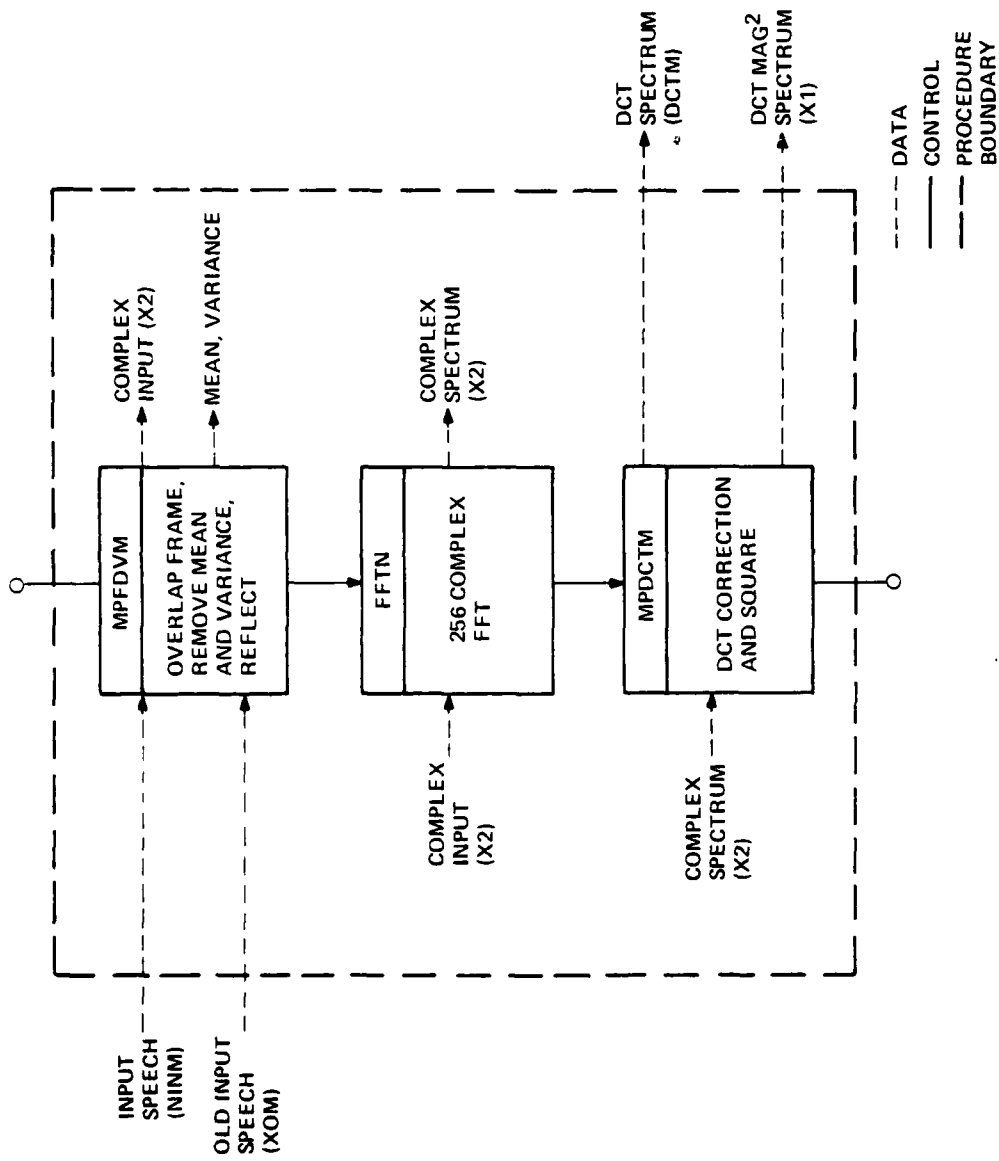
This procedure consists of three array functions: MPFDVM, FFTN (supplied by CSPI), and MPDCTM. The relationships between these array functions are shown in Figure 3-13.

#### 3.2.5.2.1 MPFDVM (FCB240, PBFCB 153)

MPFDVM converts the data in NINM from fixed point to floating point, concatenates these 246 samples with the last 10 input samples of the previous frame, computes and removes the mean and variance of the frame of 256 samples, and reorders the frame in preparation for the following FFT. The reordering algorithm, given by Makhoul<sup>1</sup>, consists of generating a 256 point complex buffer, of which the imaginary points are all zero, the first half of the real points are the even numbered points of the original, and the second half are the reversed odd-numbered points of the original buffer. This array function stores the variance (in dB) in scalar 55 and stores the mean divided by the variance in scalar 52. The re-ordered input is stored in buffer X2.

#### 3.2.5.2.2 FFTN (FCB 204, PBFCB 168)

The FFTN array function is supplied by CSPI. As used in the Compute DCT Coefficients procedure, it performs a 256 complex-to-complex FFT not in place. To do so, it makes use of a cosine table (VSHRT) generated during initialization. The output is stored in buffer X2.



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FIGURE 3-13: COMPUTE DCT COEFFICIENTS PROCEDURE

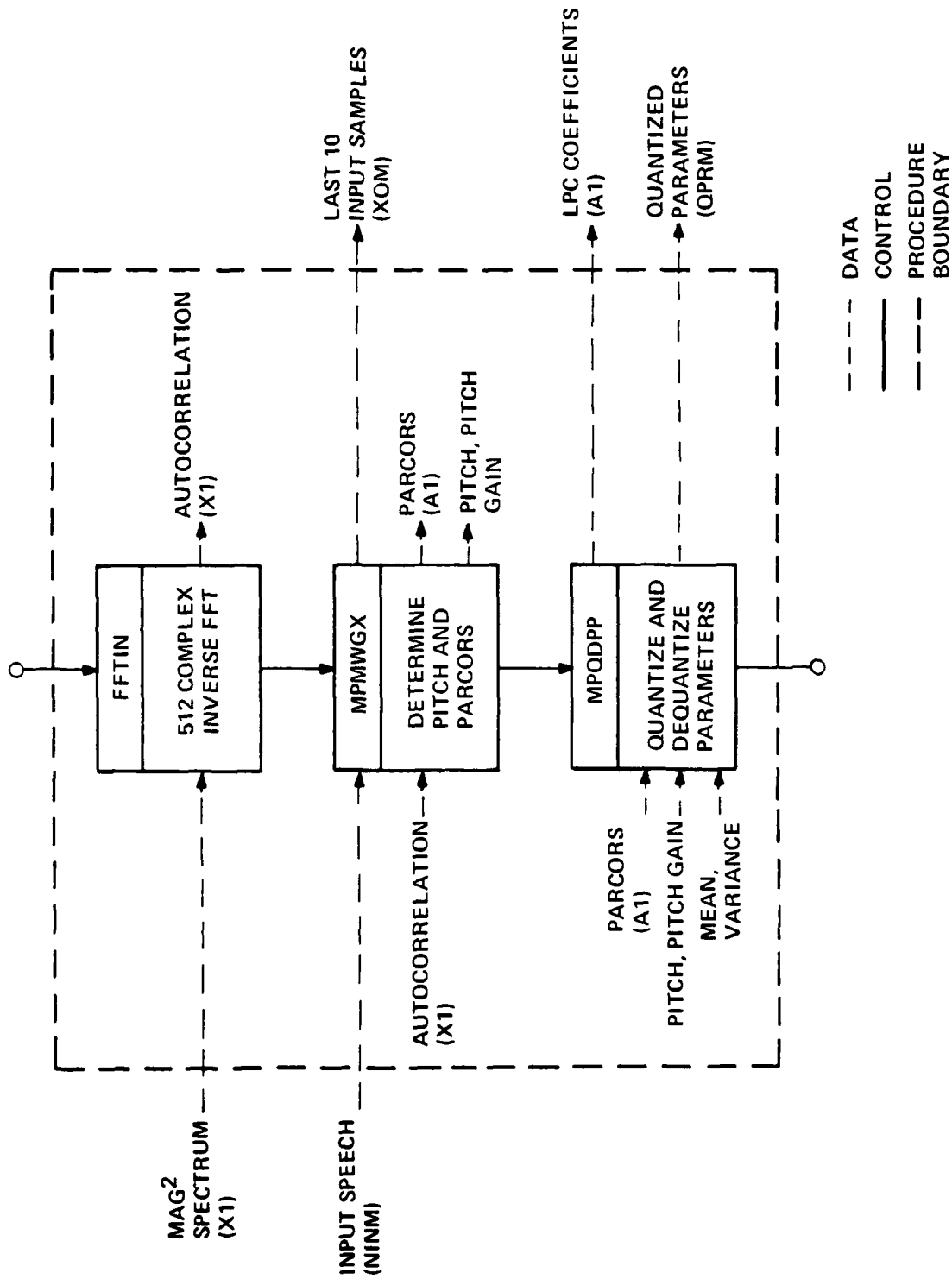
#### 3.2.5.2.3 MPDCTM (FCB 241, PBFCB 154)

MPDCTM forms the 256 unique, real Discrete Cosine Transform coefficients from the 256 complex frequency samples generated by FFTN. It also forms the squared magnitudes of the DCT coefficients. These will be used later by the Compute and Quantize Sideband Parameters procedure for the calculation of an autocorrelation function.

The transformation from Complex Fourier coefficients is given by Makhoul<sup>3</sup>. Since the Fourier coefficients result from the FFT of a real sequence, they are conjugate symmetric and only the first half (128) of them need be used. These 128 Fourier coefficients are multiplied by one eighth of a cycle of a complex exponential. The real parts of the result form the first 128 DCT coefficients and the imaginary parts reversed form the negative of the remaining 128 DCT's. These 256 DCT coefficients are stored in buffer DCTM. The squares of the DCT's are also formed and are used to generate the real parts of 256 complex samples, which are used later. The imaginary parts of these 256 samples are zeroed. These 256 complex samples are then reflected to form a 512 point conjugate symmetric spectrum. The magnitude-squared spectrum is stored in buffer X2.

#### 3.2.5.3 Compute and Quantize Sideband Parameters Procedure

This procedure consists of three array functions, as shown in Figure 3-14: FFTIN, MPMWGX, and MPQDPP. Its function is to compute the LPC spectrum and the pitch which are later used to form the bit allocation basis spectrum.



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FIGURE 3-14: COMPIL AND QUANTIZE SIDEBAND PARAMETERS PROCEDURE



#### 3.2.5.3.1 FFTIN (FCB 206, PBFCB 169)

FFTIN is a CSPI supplied array function that performs an inverse Fast Fourier Transform, not in place. As used in Sideband Parameter procedure, it performs a 512 point complex transformation. Since the input to this function is a symmetric magnitude spectrum, the result is an autocorrelation function consisting of 256 unique real points.

#### 3.2.5.3.2 MPMWGX (FCB 244, PBFCB 155)

The MPMWGX function determines 8 parcor coefficients and an estimate of the pitch and pitch gain from the autocorrelation input. In addition, it also copies the last 10 samples from the current input buffer to an area in the front of the input buffer. These samples will form the first 10 samples of the next frame.

The parcor coefficients are found using the Weiner-Levinson-Durbin algorithm as implemented by CSPI in the MWLD array function.

The pitch is estimated by finding the maximum autocorrelation value that lies between sample indices 15 and 94. The position of the maximum is used as the pitch period  $M$ , and the ratio of the maximum value to the zeroth autocorrelation is used as the pitch gain. The pitch is stored as a floating point number in scalar 61, and the pitch gain is stored in scalar 62.

#### 3.2.5.3.3 MPQDPP (FCB 243, PBFCB 156)

This array function quantizes the 8 parcors determined earlier, as well as the mean, variance, pitch, and pitch gain. It also computes the associated dequantized values, in order that the basis spectrum generated from them later will be the same as that found by the receiver, assuming

no uncorrectable channel errors. The quantization/dequantization tables used are shown in Table 3-5.

Once the parcors have been quantized and dequantized, they are used to find the corresponding Linear Prediction (LPC) coefficients. The following recursion is used to find the first through eighth LPC coefficients:

$$A_m(m) = -P(m)$$

$$A_m(L) = A_{m-1}(L) - P_m * A_{m-1}(M-L) \quad 1 \leq m \leq 8$$
$$1 \leq L < m$$

where  $A_i(j)$  is the  $j$ th LPC coefficient during the  $i$ th iteration and  $P(j)$  is the  $j$ th parcor coefficient. These LPC coefficients are used later in the Compute Basis Spectrum procedure. The residual energy of the LPC coefficients is also computed and stored as ENG in scalar 60.

The quantized, coded parameters are collected in buffer QPRM with the parcor coefficients first, followed by the mean, variance, pitch, and pitch gain.

#### 3.2.5.4 Determine Basis Spectrum Procedure

This procedure, shown in Figure 3-15 computes the basis spectrum from the quantized pitch value and the LPC coefficients. It consists of three array functions: MPFSTV, FF2R, and MPBASP.

##### 3.2.5.4.1 MPFSTV (FCB 245, PBFCB 157)

This array function forms two time-domain functions, one from the quantized pitch and one from the LPC coefficients, and stores them as real and imaginary parts of a complex buffer. The time domain function

Parameter	Threshold	Code	Dequantized Value	Parameter	Threshold	Code	Dequantized Value
PARCOR 1	1.933	31	1.997	PARCOR 2	1.962	31	0.087
	1.789	30	1.869		1.926	30	0.263
	1.644	29	1.703		1.890	29	0.369
	1.541	28	1.531		1.867	28	0.462
	1.444	27	1.502		1.836	27	0.556
	1.343	26	1.385		10804	26	0.624
	1.240	25	1.300		1.768	25	0.681
	1.122	24	1.181		1.732	24	0.736
	1.019	23	1.063		1.695	23	1.714
	0.930	22	0.975		1.653	22	1.676
	0.843	21	0.885		1.602	21	1.630
	0.771	20	0.802		1.547	20	1.574
	0.696	19	0.740		1.492	19	1.520
	0.615	18	0.652		1.436	18	1.464
	0.543	17	0.577		1.373	17	1.407
	0.477	16	0.509		1.306	16	1.338
	0.418	15	0.445		1.238	15	1.274
	0.368	14	0.392		1.162	14	1.203
	0.324	13	0.344		1.085	13	1.121
	0.286	12	0.304		1.012	12	1.048
	0.251	11	0.268		0.942	11	0.977
	0.219	10	0.235		0.876	10	0.907
	0.190	9	0.204		0.819	9	0.846
	0.162	8	0.176		0.764	8	0.792
	0.137	7	0.150		0.709	7	0.736
	0.112	6	0.124		0.653	6	0.681
	0.090	5	0.100		0.590	5	0.624
	0.070	4	0.080		0.509	4	0.556
	0.051	3	0.060		0.415	3	0.462
	0.033	2	0.042		0.316	2	0.369
	0.014	1	0.025		0.175	1	0.263
		0	0.008			0	0.087

TABLE 3-5: PARAMETER QUANTIZATION AND DEQUANTIZATION TABLE

Parameter	Threshold	Code	Dequantized Value	Parameter	Threshold	Code	Dequantized Value
PARCOR 3	1.750	15	1.844	PARCOR 4	1.882	15	1.950
	1.585	14	1.655		1.754	14	1.183
	1.462	13	1.516		1.642	13	1.695
	1.358	12	1.408		1.539	12	1.589
	1.261	11	1.309		1.441	11	1.489
	1.168	10	1.213		1.346	10	1.393
	1.080	9	1.123		1.255	9	1.300
	0.995	8	1.037		1.165	8	1.209
	0.911	7	0.953		1.076	7	1.121
	0.829	6	0.870		0.988	6	1.032
	0.745	5	0.787		0.897	5	0.943
	0.658	4	0.703		0.801	4	0.851
	0.562	3	0.613		0.696	3	0.752
	0.452	2	0.512		0.572	2	0.640
	0.315	1	0.392		0.409	1	0.504
		0	0.238			0	0.314

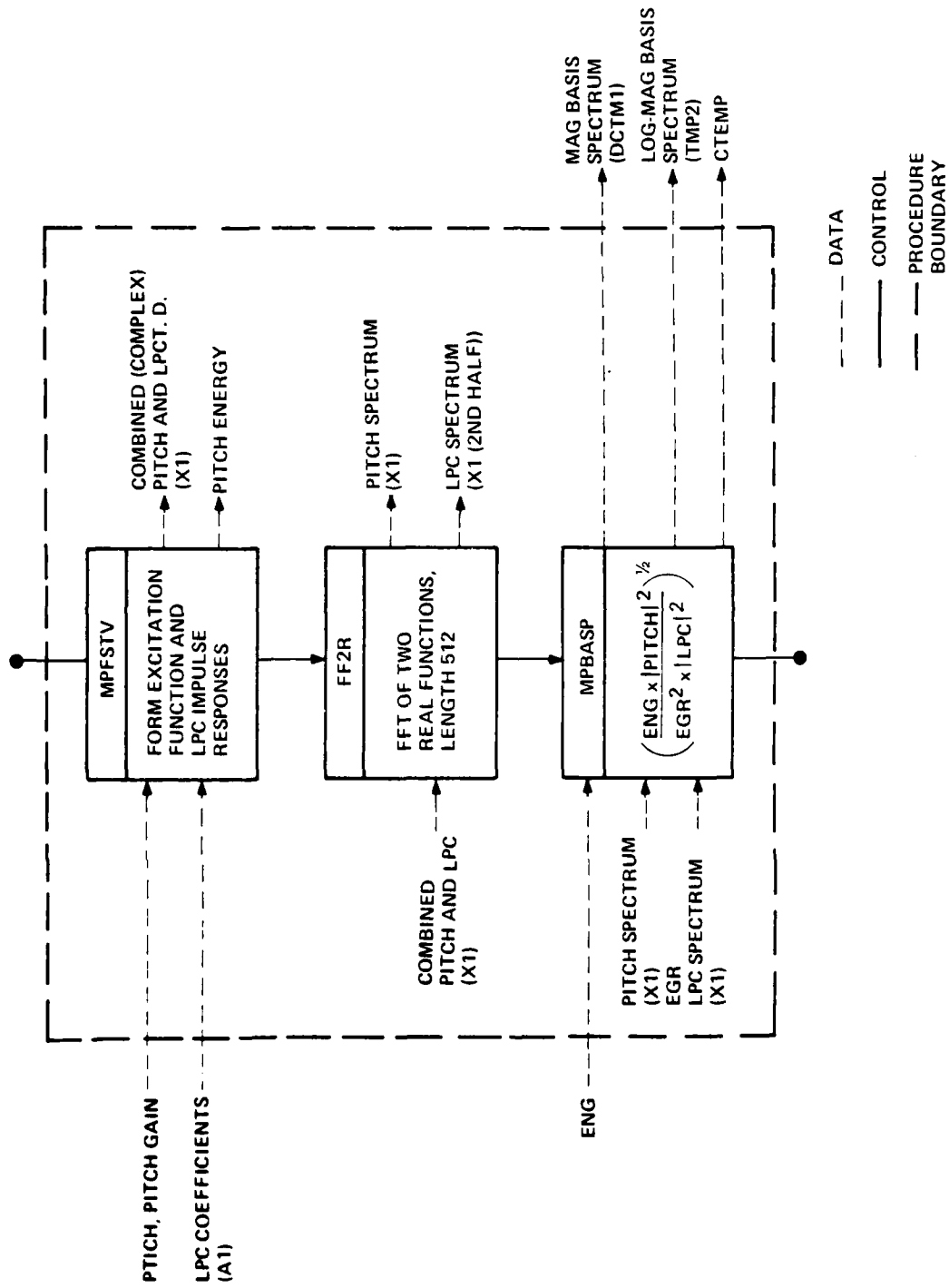
TABLE 3-5: PARAMETER QUANTIZATION AND DEQUANTIZATION TABLE (CONT'D)

Parameter	Threshold	Code	Dequantized Value	Parameter	Threshold	Code	Dequantized Value
PARCOR 5	1.482	7	1.591	PARCOR 6	1.509	7	1.621
	1.297	6	1.374		1.319	6	1.398
	1.153	5	1.219		1.173	5	1.240
	1.022	4	1.086		1.041	4	1.105
	0.891	3	0.958		0.909.	3	0.976
	0.747	2	0.825		0.763	2	0.842
	0.557	1	0.669		0.573	1	0.685
		0	0.445			0	0.462

PARCOR 7	1.167	3	1.320	PARCOR 8	1.243	3	1.382
	0.875	2	1.013		0.985	2	1.105
	0.568	1	0.736		0.722	1	0.865
		0	0.397			0	0.879

Mean			Variance			
2.854	15	3.132	59.760	31	60.709	
2.394	14	2.576	58.016	30	58.810	
2.072	13	2.212	56.491	29	57.223	
1.816	12	1.932	55.072	28	55.758	
1.598	11	1.699	53.709	27	54.385	
1.403	10	1.496	52.334	26	53.034	
1.231	9	1.314	50.900	25	51.633	
1.070	8	1.148	49.431	24	50.167	
	7	0.992	47.960	23	48.695	
	6	0.846	46.483	22	47.224	
	5	0.706	44.982	21	45.741	
	4	0.572	43.443	20	44.222	
	3	0.441	41.864	19	42.664	
	2	0.313	40.204	18	41.065	
	1	0.187	38.386	17	39.343	
	0	0.062	36.477	16	37.430	
			34.633	15	35.525	
Pitcngain	3	0.898	32.888	14	33.741	
0.824	2	0.750	31.179	13	32.035	
0.667	1	0.584	29.409	12	30.322	
0.488	0	0.392	27.476	11	28.495	
			25.415	10	26.458	
			23.405	9	24.372	
			21.426	8	22.439	
			19.276	7	20.412	
			16.994	6	18.140	
			14.548	5	15.849	
			12.057	4	13.242	
			9.831	3	10.868	
			7.798	2	8.794	
			5.631	1	6.802	
				0	4.460	

Pitch (M)	Code	Decoded Pitch	Pitch	Code	Decoded Pitch	Pitch	Code	Decoded Pitch
0	0	15	32	17	32	64	48	63
1	0	15	33	18	33	65	49	65
2	0	15	34	19	34	66	49	65
3	0	15	35	20	35	67	50	67
4	0	15	36	21	36	68	50	67
5	0	15	37	22	37	69	51	69
6	0	15	38	23	38	70	51	69
7	0	15	39	24	39	71	52	71
8	0	15	40	25	40	72	52	71
9	0	15	41	26	41	73	53	73
10	0	15	42	27	42	74	53	73
11	0	15	43	28	43	75	54	75
12	0	15	44	29	44	76	54	75
13	0	15	45	30	45	77	55	77
14	0	15	46	31	46	78	55	77
15	0	15	47	32	47	79	56	79
16	1	16	48	33	48	80	56	79
17	2	17	49	34	49	81	57	81
18	3	18	50	35	50	82	57	81
19	4	19	51	36	51	38	58	83
20	5	20	52	37	52	48	58	83
21	6	21	53	38	53	85	59	85
22	7	22	54	39	54	86	59	85
23	8	23	55	40	55	87	60	87
24	9	24	56	41	56	88	60	87
25	10	25	57	42	57	89	61	89
26	11	26	58	43	58	90	16	89
27	12	27	59	44	59	91	62	91
28	13	28	60	45	60	92	62	91
29	14	29	61	46	61	93	63	93
30	15	30	62	47	62	94	63	93
31	16	31	63	48	63			



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FIGURE 3-15: BASIS SPECTRUM DETERMINATION PROCEDURE



formed from the pitch represents the excitation function. It is an exponentially decaying pulse train with a period equal to the pitch and contains zeroes except at multiples of the pitch period. The values at the pulse points are the positive powers of the pitch gain. That is:

$$XR(K*M) = PG^{K+1} \quad \text{for } 0 \leq K*M < 256$$

where M is the pitch period, PG the pitch gain, and XR is the output. The excitation is stored in the real part of the 512 point complex output buffer. The sum of the pitch pulses is accumulated and stored in scalar 73.

The other time domain function is the vocal tract filter function. It is formed from the impulse response of the LPC filter, which is simply 1, -A(1), -A(2), ... -A(8). The remaining 503 imaginary values are zeroed.

The two time domain functions, though both real, are stored in a complex buffer to facilitate the use of an efficient FFT algorithm, FF2R.

#### 3.2.5.4.2 FF2R (FCB 214, PBFCB 170)

FF2R is a CSPI supplied array function used to transform two real signals simultaneously. One input function is stored in the real part of a complex buffer, and its transform appears in the first half of the complex buffer output. Similarly, the second function is stored in the imaginary parts of the input buffer, and its transform appears in the second half of the output buffer. As used in the ATC system, FF2R uses 512 point complex buffers as input and output. The output buffer is divided in half into two 256 point complex buffers. The first contains

the frequency spectrum of the pitch excitation function. The second contains the frequency response of the vocal tract inverse filter.

#### 3.2.5.4.3 MPBASP (FCB 253, PBFCB 158)

MPBASP forms the magnitude basis spectrum from the frequency spectrum and the inverse filter response. It also forms the base 2 logarithm of this basis spectrum to be used later in bit allocation.

This routine first calculates the magnitude squared of the basis spectrum by the following formula:

$$|BASIS|^2 = \frac{ENG * |Pitch|^2}{EGR^2 * |Inverse Filter|^2}$$

where ENG is the residual energy of the LPC spectrum as calculated in MPQDPP and stored in scalar 60, and EGR is the sum of the excitation pulses as determined by MPFSTV and stored in scalar 73. Then one half the base 2 logarithm of the result is calculated. This is equivalent to the base 2 log-magnitude basis spectrum. For reasons of efficiency, the single MAP instructions LGS and RCP, which approximate the base 16 log and the reciprocal, are used. The DC component of the log magnitude basis spectrum is forced to be very small.

The magnitude basis spectrum is found as follows:

$$|BASIS| = 2^{\left( \frac{\log |Basis|^2}{2} \right)}$$

This exponentiation is performed using the same technique as the CSPI supplied array function VEXP. This approach was found to be faster than finding the square root directly.

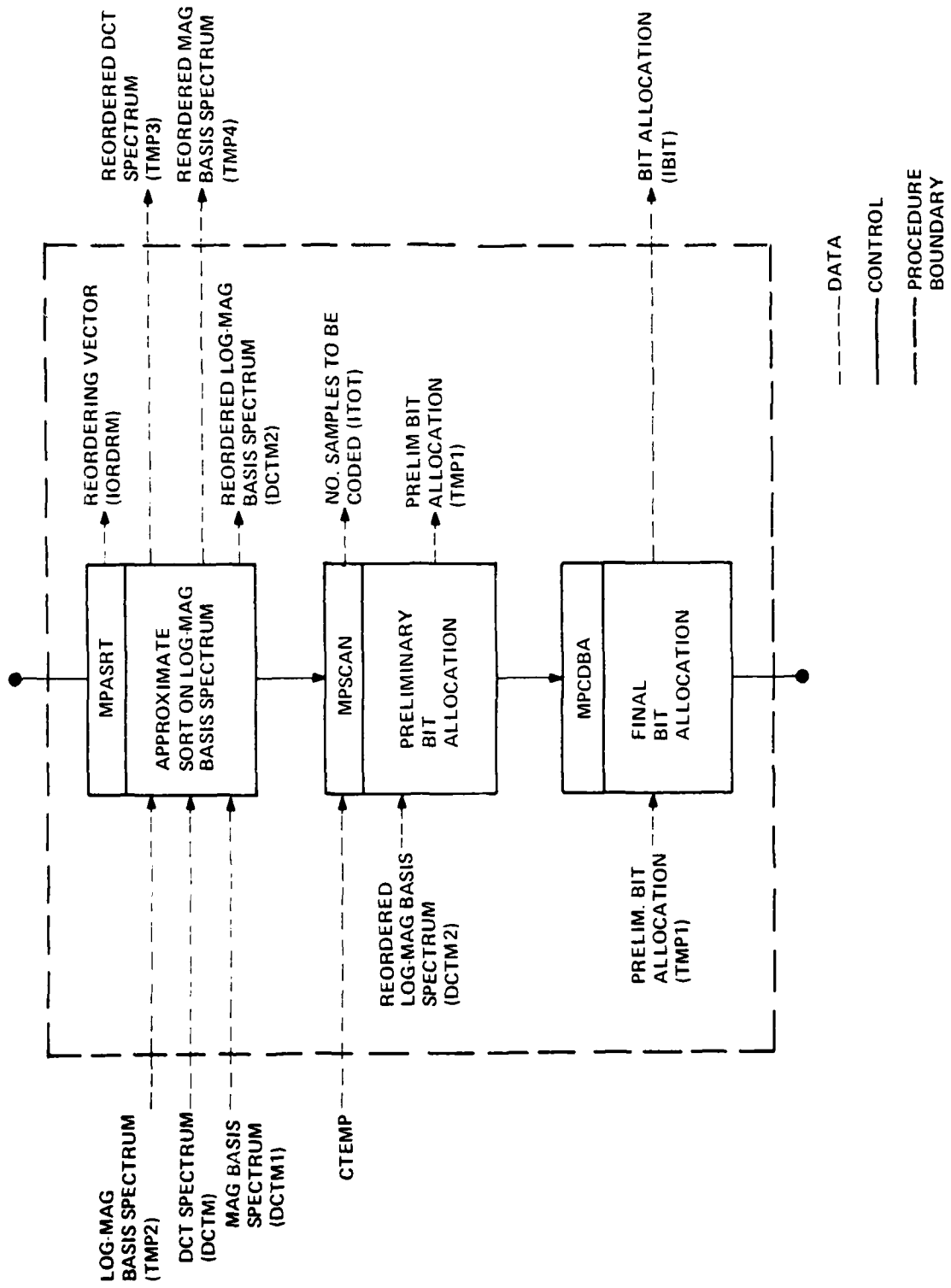
The log base 2 magnitude basis spectrum output is stored in buffer TMP2 (256 real points). The magnitude basis spectrum output is stored in buffer DCTM1 (also 256 real points). Also, scalar 73, EGR, is modified to become  $1.0/EGR$ . Finally, the sum of the log magnitudes is subtracted from the number of bits to be allocated, the difference is divided by 256, and the resultant average bit allocation is stored in CTEMP, scalar 80.

#### 3.2.5.5 Bit Allocation Procedure

The bit allocation procedure determines how many bits are to be used to code each DCT coefficient for transmission under four constraints. The four constraints are: 1) each DCT must be coded with an integral number of bits; 2) that the sum of the bits allocated to the DCT coefficients must equal 267; 3) that the maximum number of bits allocated to any DCT coefficient is three; and 4) that the numbers of bits allocated to the DCT coefficients approximate the log-magnitude basis spectrum.

Satisfying these constraints exactly is a difficult, time consuming task. An approximate solution is found by reordering the log magnitude basis spectrum coefficients (and the corresponding DCT coefficients and magnitude spectrum coefficients) by size. The bit allocation for the reordered DCT coefficients will then be monotonically decreasing. Monotonically decreasing bit allocations are useful for horizontal encoding and protection.

The bit allocation procedure consists of three array functions, as shown in Figure 3-16. They are MPASRT, which performs the reordering, MPSCAN, which determines the preliminary bit allocation, and MPCDBA, which performs a correction on the preliminary bit allocation to produce a final bit allocation.



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FIGURE 3-16: BIT ALLOCATION PROCEDURE

#### 3.2.5.5.1 MPASRT (FCB 249, PBFCB 159)

This array function performs an approximate sort on the log-magnitude basis spectrum coefficients in buffer TMP2. It determines a vector of indices expressing the sorted order, and reorders the DCT coefficients (in buffer DCTM) and the magnitude basis spectrum coefficients (in DCTM1) accordingly.

The sort is performed by assigning each log-magnitude basis spectrum coefficient to one of four bins, depending on size. The index, the position in the buffer of each coefficient, is added to an ordering list corresponding to the bin. The thresholds for the four bins are shown in Table 3-6. When all indices have been added to one of the four lists, the lists are concatenated to form a complete ordering vector in buffer IORDR.

Once IORDR has been obtained, the three input buffers DCTM, (DCT coefficients), DCTM1 (magnitude basis spectrum), and TMP2 (log-magnitude basis spectrum) are reordered by using IORDR as indirect addresses into each of the input buffers and forming sequential outputs. That is,

$$\text{OUTPUT}(I) = \text{INPUT}(\text{IORDRM}(I))$$

The three output buffers are TMP3, TMP4, and DCTM2, respectively.

The required indirect addressing is achieved by performing a "scatter write" into APS program memory to modify the addresses used in the input instructions.

#### 3.2.5.5.2 MPSCAN (FCB 254, PBFCB 160)

This array function proceeds in two passes to form a preliminary bit allocation. The first calculates a new log magnitude spectrum whose

Bin #	Threshold
3	2.907
2	1.322
1	-0.488
0	

TABLE 3-6: BIT ALLOCATION THRESHOLDS

mean is equal to the average number of bits per sample to be allocated. It also determines the last sample of this new spectrum to be coded. That is, the samples which follow will be allocated zero bits. It also keeps a running sum of the samples of the new spectrum to be coded.

The second pass generates another log magnitude spectrum whose mean is the average number of bits available per sample to be coded and whose trailing samples are zero. The coefficients of this spectrum are then rounded and fixed to form the preliminary bit allocation.

Inputs to MPSCAN are buffer DCTM2, the log-magnitude basis spectrum, and CTEMP, the average bit offset to be applied in pass 1, computed in MPBASP and stored in scalar 80. The output is stored as floating point numbers in buffer TMP1. In addition, ITOT, the number of samples to be coded, is stored in scalar 83.

#### 3.2.5.5.3 MPCDBA (FCB 255, PBFCB 161)

This array function consists of at least four passes. The first pass forces the preliminary bit allocations to be monotonically decreasing and sums them. If this sum ever exceeds the available number of bits, BTLTH = 267, the remaining bit allocations in the input buffer are zeroed. The second pass clips the bit allocations at 3, and updates the total bits allocated sum accordingly. Pass 3 allocates the leftover bits, beginning at the front of the buffer, but never raising the bit allocation of any coefficient over 3.0. If some bits remain to be allocated at the end of this pass, the pass is repeated. Finally, the bit allocations are changed from floating point integers to fixed point numbers and stored.

The input to MPCDBA is buffer TMP1, the preliminary bit assignment. The output is stored in IBIT.

#### 3.2.5.6 DCT Coefficient Quantization Procedure (FCB 250, RBFCB 162)

This procedure, shown in Figure 3-17, consists of one array function, MPFSTQ. From the bit allocations in IBIT, this array function determines the number of quantization levels and the associated thresholds for each (reordered) DCT coefficient. The thresholds are multiplied by the corresponding magnitude basis spectrum value and then used to quantize and code the DCT coefficients into code words of various lengths. The thresholds, quantization levels, and resultant code words are shown in Table 3-7.

The inputs to MPFSTQ are IBIT; TMP3, the reordered DCT coefficients; and TMP4, the reordered magnitude basis spectrum. The output of MPFSTQ is stored as fixed point numbers in buffer QTDCTM.

#### 3.2.6 Synthesize Process

The Synthesize process performs the Adaptive Transform Decoding algorithm to obtain digital speech waveforms. The input to this process is coded, quantized sideband parameters and coded, quantized DCT coefficients.

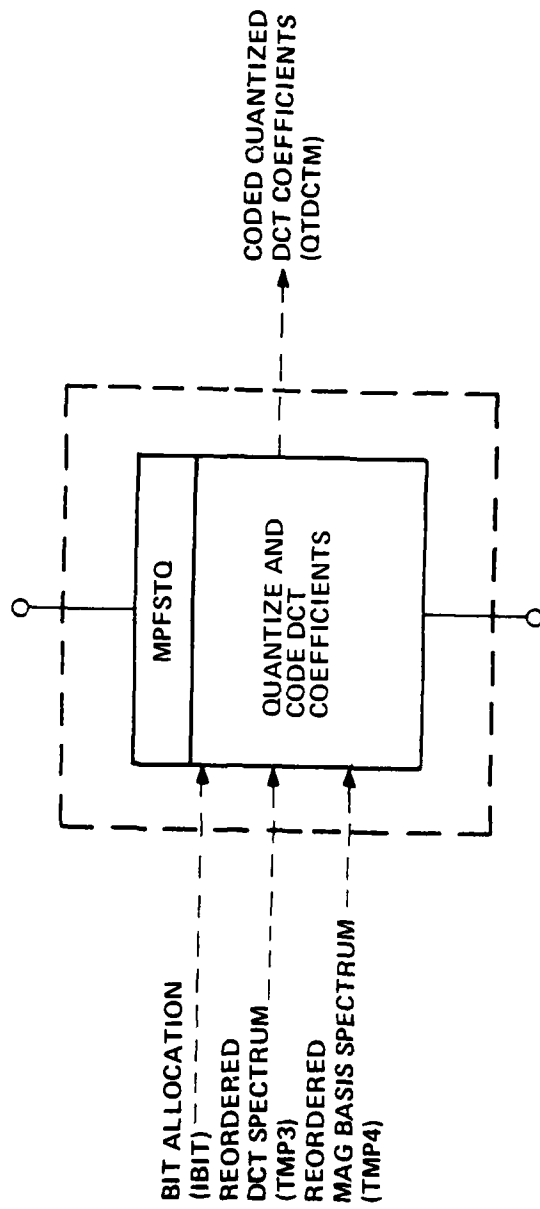
The Synthesize process is composed of seven functional procedures: Cycle Delay Lines, Dequantize Sideband Parameters, Basis Spectrum Determination, Bit Allocation, Copy Temporary Buffers, DCT Coefficients Dequantization, and Inverse DCT Computation. The relationships between these procedures are shown in Figure 3-18. Each procedure consists of one or more array functions. The entire Synthesize process runs in the Arithmetic Processor portion of the MAP.



Code	3 Bit Threshold	2 Bit Threshold	1 Bit Threshold
3	2.3796	-	-
2	1.2327	-	-
1	0.5332	1.1269	-
0	0	0	0

Note: Negative values are quantized similarly,  
 sign bit of code is set

TABLE 3-7: DCT COEFFICIENT QUANTIZATION



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FIGURE 3-17: DCT QUANTIZATION PROCEDURE

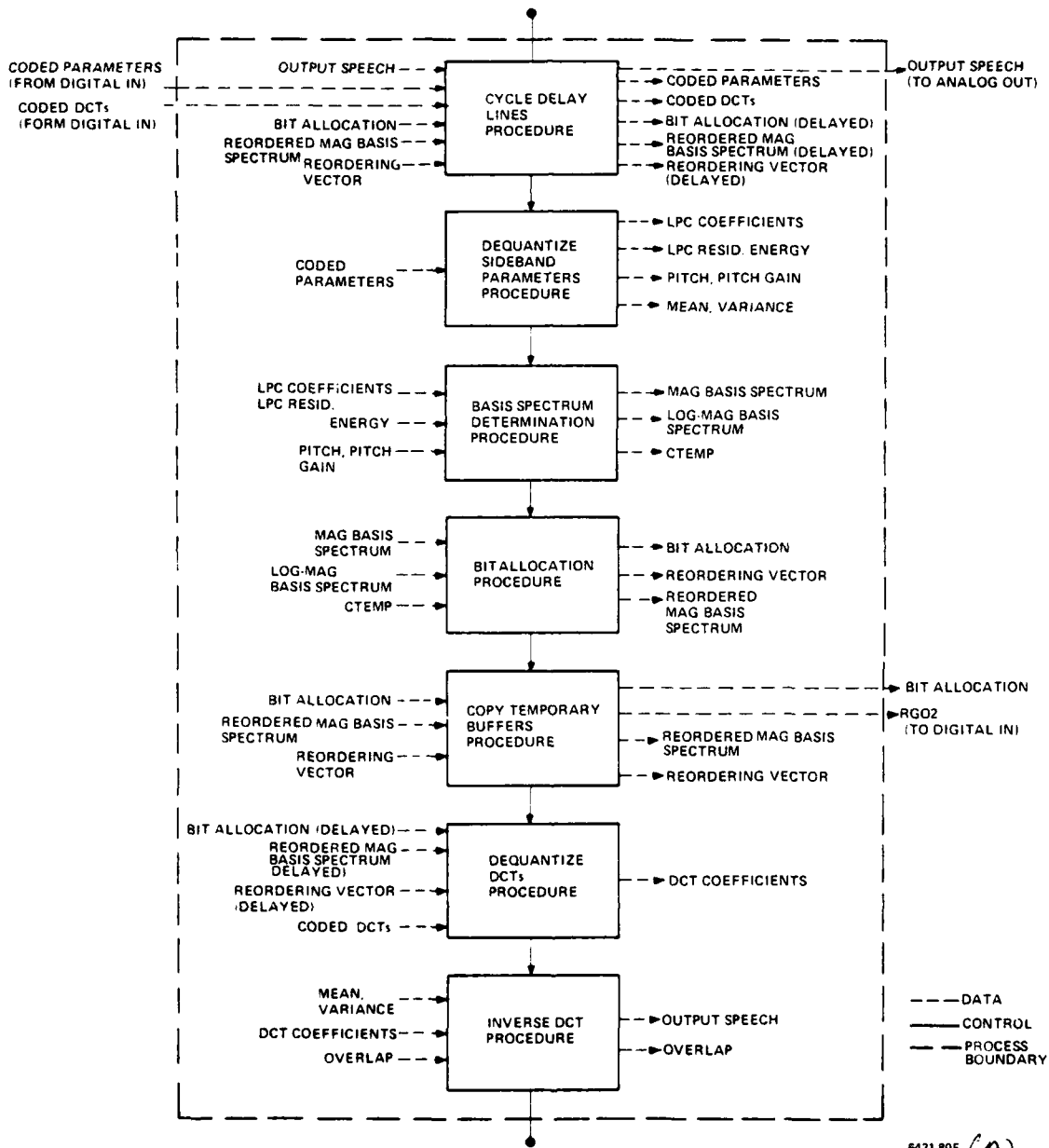


FIGURE 3-18: SYNTHESIZE PROCESS

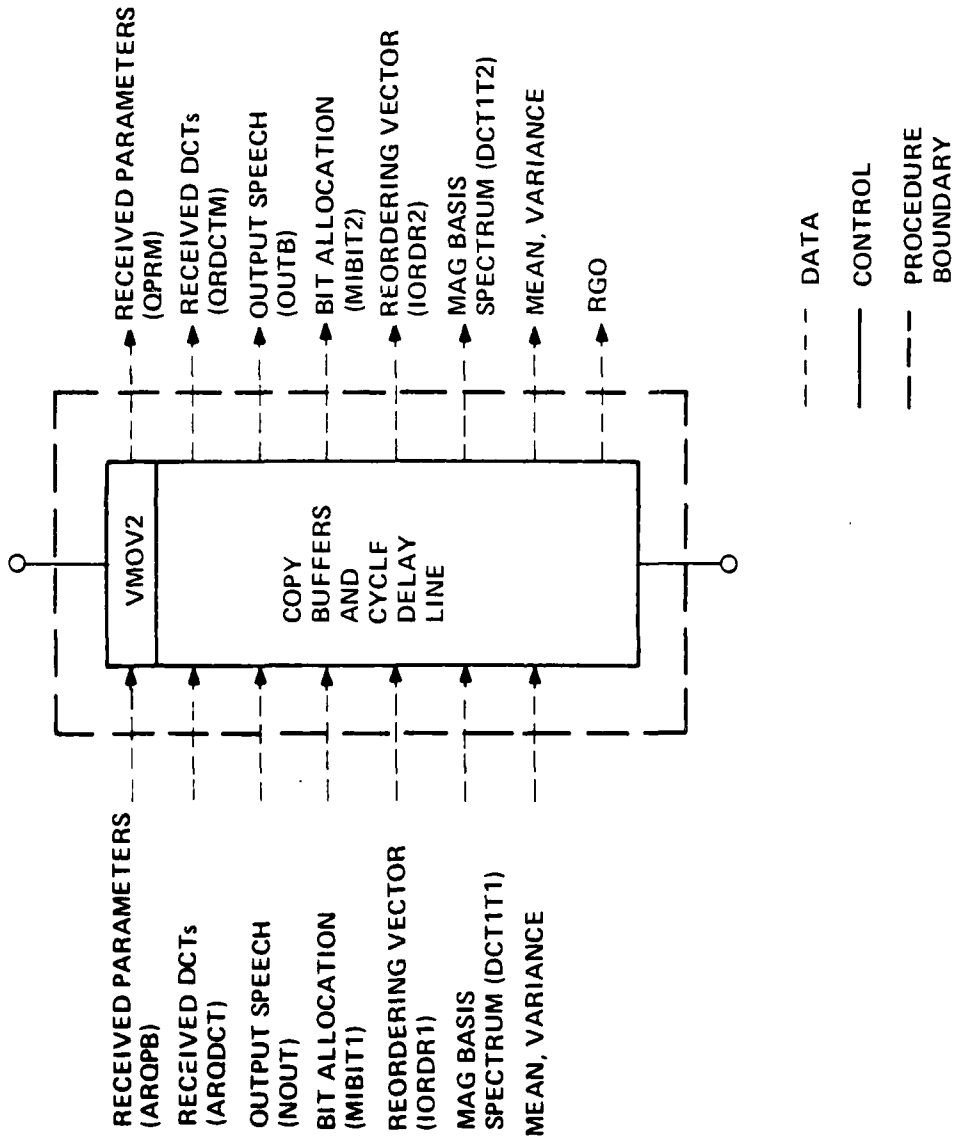
### 3.2.6.1 Cycle Delay Lines Procedure (FCB 238, PBFCB 173)

The Cycle Delay Lines procedure consists of a single array function, VMOV2. It provides data to the Analog Out process and accepts data from the Digital In process, enables the Receive background program, and provides a single frame delay. This procedure is shown in Figure 3-19.

Output speech data from the previous instance of the Synthesize process is stored in buffer NOUT. VMOV2 copies this data to buffer OUTB, which will be used by the Analog Out process. VMOV2 sets the flag OUTFUL (integer scalar 111) to indicate that new data is present, thus enabling the A/D background program.

Similarly, VMOV2 copies data from two buffers, ARQPB and ARQDCT. These buffers have been filled by the Receive background program and contain the de-serialized sideband parameters and DCT coefficients. VMOV2 copies these buffers into QPRM and QRDCTM and sets flag RGO to indicate that the input buffers are empty, enabling the Receive background program to execute and fill them again.

The input sideband parameters in QPRM do not represent the same frame of speech as the DCT coefficients in QRDCTM. Because the bit allocation, which is derived from the sideband parameters, must be obtained before the DCT coefficients can be deserialized, the transmitted DCT coefficients lag the transmitted sideband parameters by one frame. This means that the bit allocation, basis spectrum, and reordering vector determined in one instance of the Synthesize process must be saved to be used in the next instance of the process. VMOV2 implements this delay, as shown in Figure 3-20. The bit allocation, basis spectrum, and reordering vector have been stored in buffers MIBIT1, DCT1T1, and IORDR1, by the



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FIGURE 3-19: CYCLE DELAY LINE'S PROCEDURE (SYNTHESIS/STAGE)

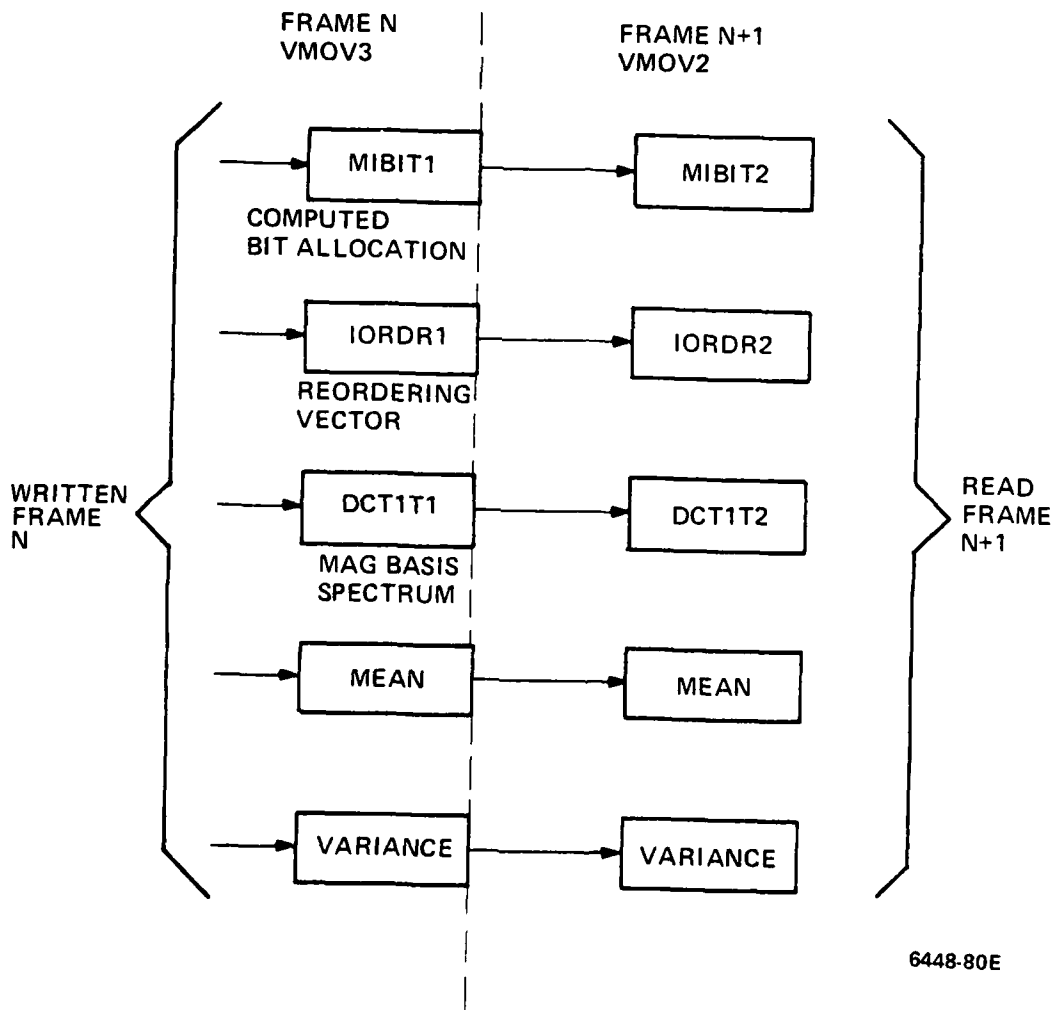


FIGURE 3-20: SYNTHESIZE DELAY LINES

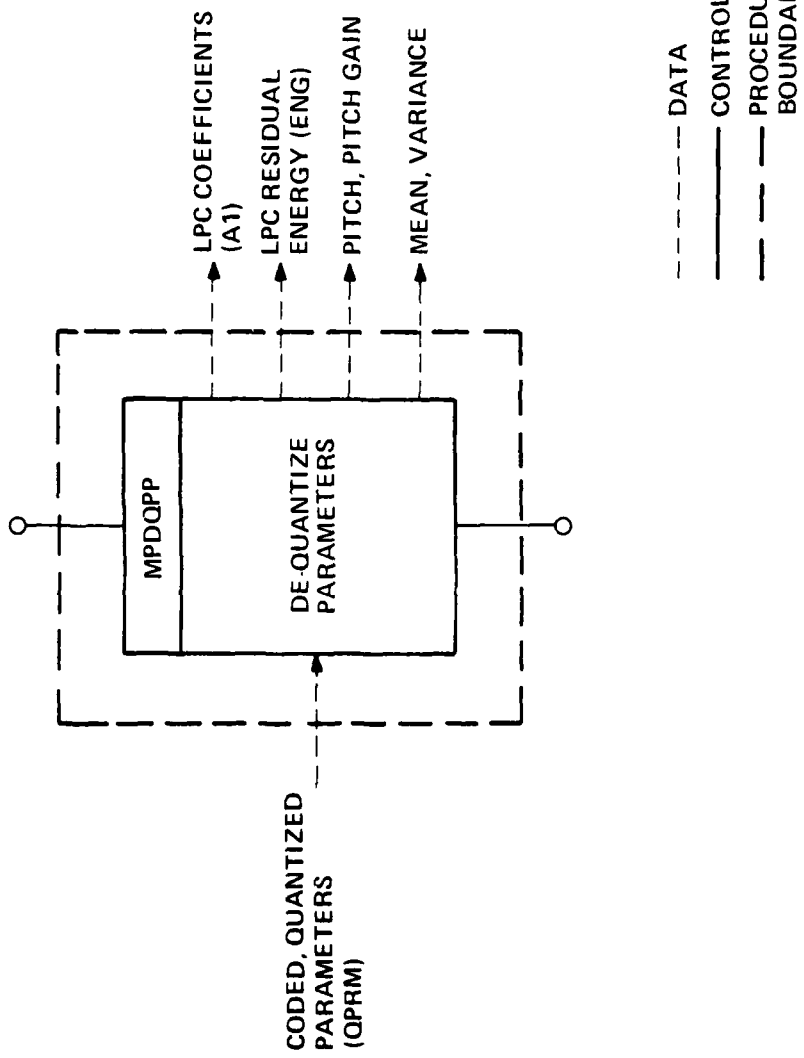
previous instance of the process, and are moved by VMOV2 to MIBIT2, DCT1T2, and IORDR2, from which they will be used in the DCT dequantization procedure. The mean and variance have been stored in scalars 102 and 103 and are moved to scalars 100 and 101 to implement a similar frame delay on these parameters.

#### 3.2.6.2 Sideband Parameter Dequantization Procedure (FCB 244, PBFCB 163)

This procedure, shown in Figure 3-21, consists of a single array function, MPDQPP. MPDQPP decodes 12 sideband parameters: 8 parcor coefficients, pitch, pitch gain, mean, and variance using the same tables used by MPQDPP (subsection 3.2.5.3.3). The input parameters are fixed point integers and are contained in buffer QPRM. These integers are used as indices into the appropriate decoding tables. The dequantized parcor coefficients are transformed to LPC coefficients as in MPQDPP, which are then output to buffer A1. The residual energy of the LPC coefficients (ENG) is stored in scalar 60. The dequantized pitch is output to scalar 91, the pitch gain to scalar 90, the variance to scalar 89, and the mean to scalar 88.

#### 3.2.6.3 Basis Spectrum Determination Procedure

This procedure is identical to the Basis Spectrum Determination performed in the Analyze process (subsection 3.2.5.4, Figure 3-15). This procedure uses MPFSTV, FF2R, and MPBASP to determine the log-magnitude basis spectrum, stored in TMP2, and the magnitude basis spectrum, stored in DCTM1. If there are no uncorrectable transmission errors, these spectra will be identical to those determined in the Analyze process.



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FIGURE 3-21: PARAMETER DEQUANTIZATION PROCEDURE



#### 3.2.6.4 Bit Allocation Procedure

This procedure determines the number of bits used to code each of the DCT coefficients. It is very similar to the bit allocation procedure used in the Analyze process (subsection 3.2.5.5) except that the sorting function is slightly changed.

The bit allocation procedure in the Synthesize process also consists of three array functions: MPSSRT, MPSCAN, and MPCDBA. This procedure is shown in Figure 3-22.

##### 3.2.6.4.1 MPSSRT (FCB 248, PBFCB 167)

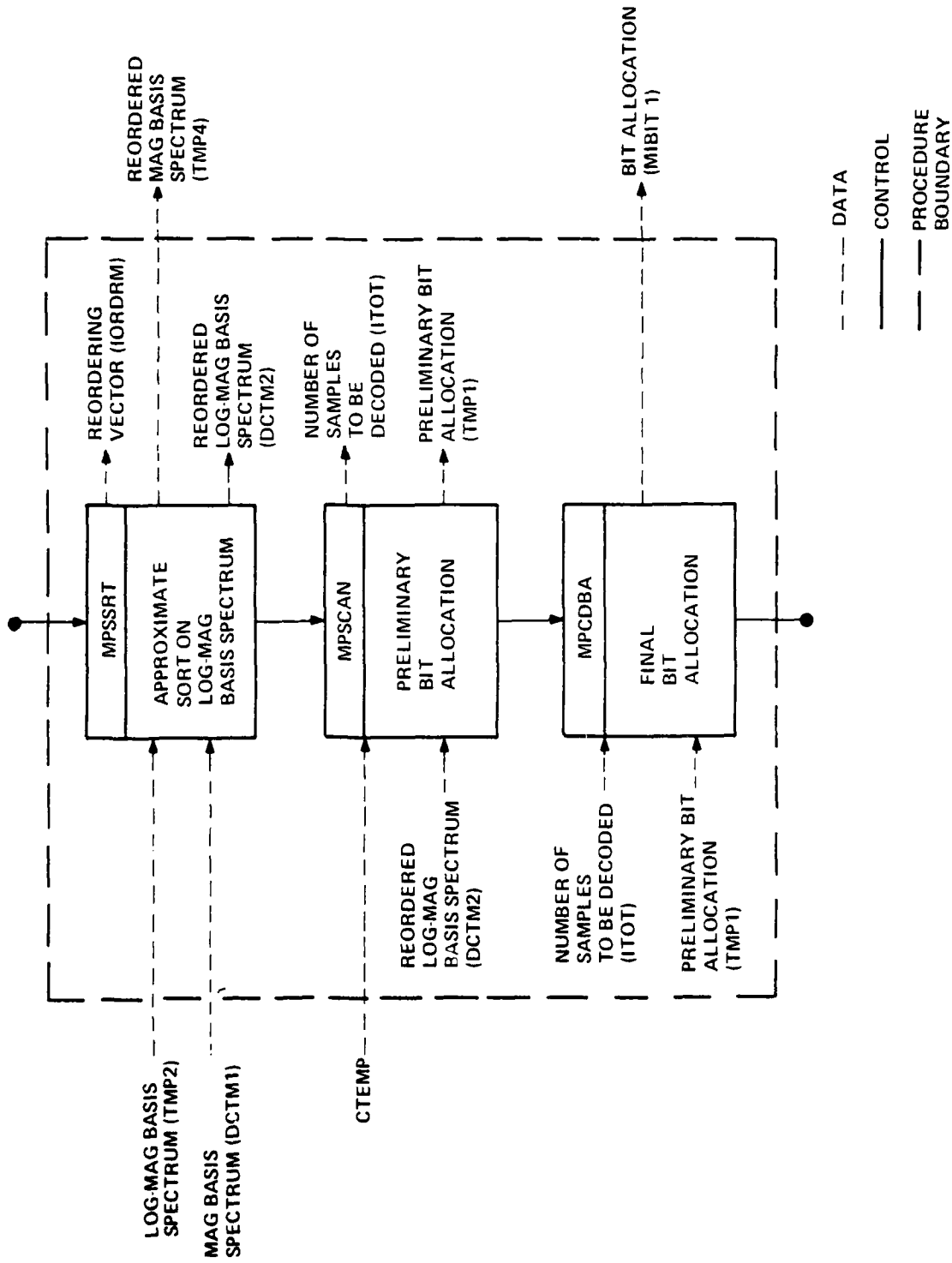
This array function is similar to MPASRT (subsection 3.2.5.5.1) in that it reorders the magnitude and log-magnitude basis spectra contained in buffers DCT1 and TMP2, respectively, and stores the reordering information in a vector of indices, IORDRM. However, MPSSRT performs no reordering on the (coded) DCT coefficients. The coded DCT coefficients are transmitted in sorted order. They will be reordered to normal order in the dequantization procedure (subsection 3.2.6.6) later.

##### 3.2.6.4.2 MPSCAN (FCB 254, PBFCB 160)

This is the same array function used in the Analyze process and described in subsection 3.2.5.5.2. It uses the same input and output buffers and is invoked using the identical prebound FCB.

##### 3.2.6.4.3 MPCDBA (FCB 255, PBFCB 175)

This is the same array function used in the Analyze process and described in subsection 3.2.5.5.3. It is the identical FCB, but prebound differently. As used in the Synthesize process, it is prebound FCB



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FIGURE 3-22: BIT ALLOCATION PROCEDURE (SYNTHESIS)

175. The only difference made by this alternate prebinding is that the output buffer, which stores the bit allocations, is MIBIT1 instead of MIBIT2, as is used in the Analyze process.

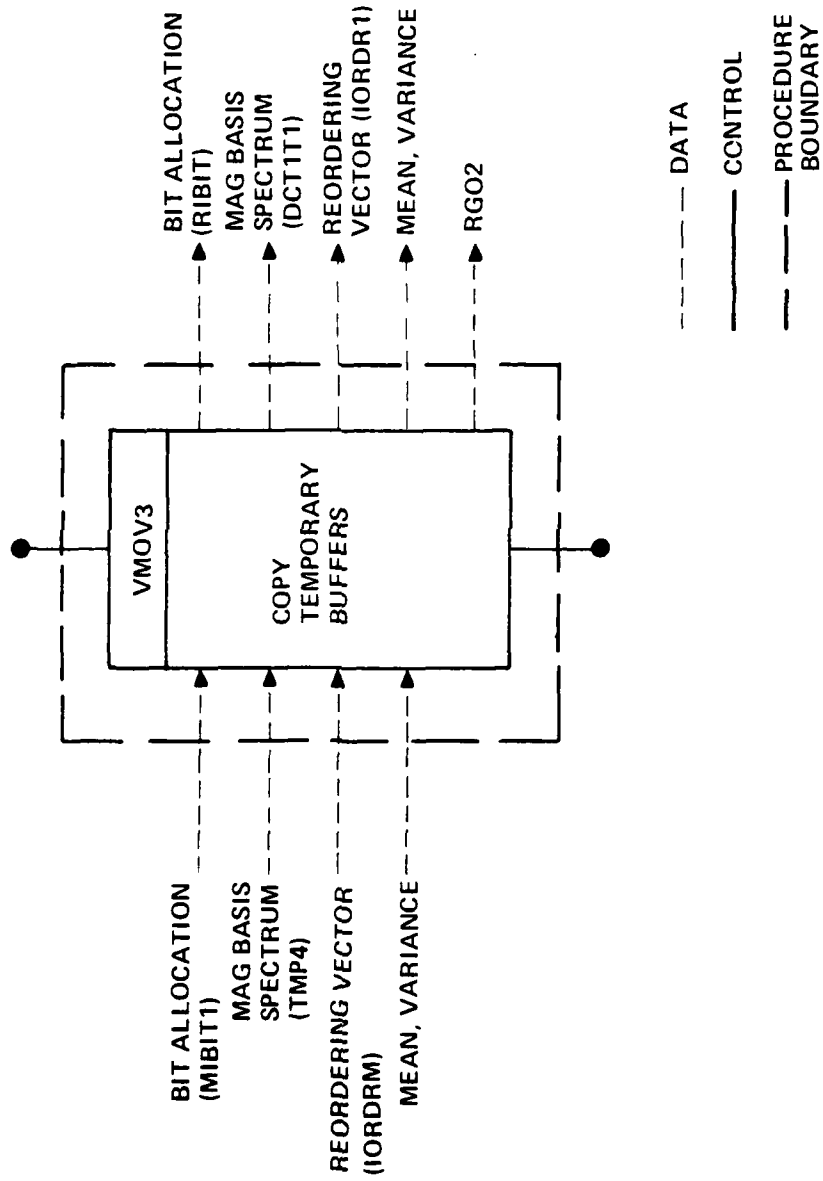
#### 3.2.6.5 Copy Temporary Buffers Procedure (FCB 240, PBFCB 174)

This procedure, shown in Figure 3-23, consists of a single array function, VMOV3. Its purpose is to copy data from temporary buffers, used to speed up computation, to permanent buffers, which will remain unmodified until the next instance of the Synthesize process. It also makes the DCT bit allocation information available to the Receive process and enables the Receive process to proceed by setting the flag RGO2 (integer scalar 116).

VMOV3 copies buffer IORDRM, the reordering indices, into buffer IORDR1, which is the first stage of the Synthesize ordering delay line described in subsection 3.2.6.1. It also copies TMP4, the reordered magnitude basis spectrum, into DCT1T1, the first stage of the Synthesize basis spectrum delay line. Buffer MIBIT1, the first stage of the Synthesize bit allocation delay line, already contains the bit allocation information, as a result of MPCDBA. VMOV3 copies this information into buffer RIBIT, which will be used by the Receive process to deserialize the received DCT coefficients, and sets flag RGO2 to enable the Receive process to proceed with this deserialization. VMOV3 also copies the dequantized mean and variance from scalars 88 and 89 to scalars 102 and 103.

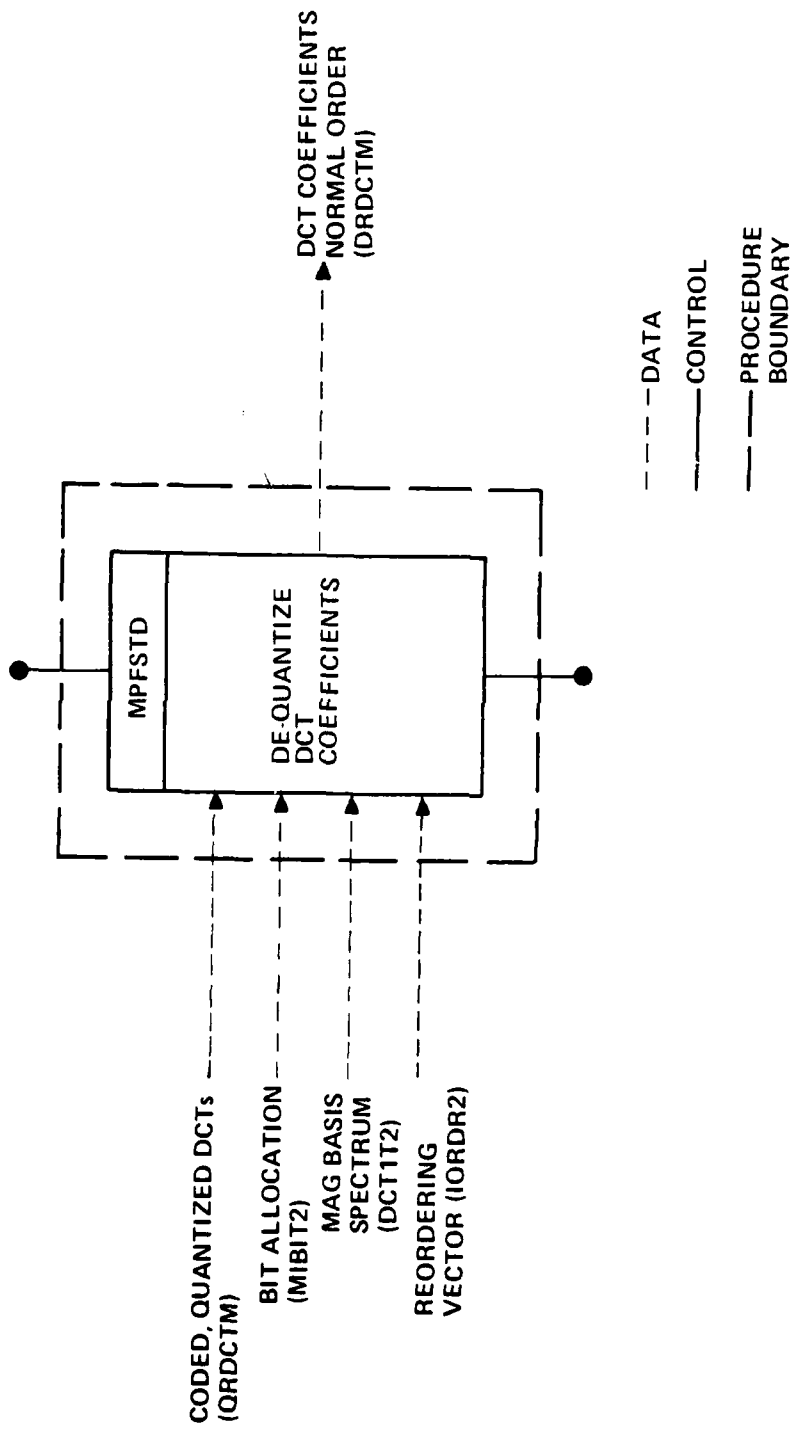
#### 3.2.6.6 DCT Coefficient Dequantization Procedure (FCB 252, PBFCB 164)

This procedure, shown in Figure 3-24, consists of a single array function, MPFSTD. MPFSTD uses each coded DCT coefficient (sorted order)



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FIGURE 3-23: COPY TEMPORARY BUFFERS PROCEDURE



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FIGURE 3-24: DCT DEQUANTIZATION PROCEDURE

from buffer QRDCTM as an index into a decoding table. The particular table used is determined from the bit allocation vector, MIBIT2. The value found from the decoding table is multiplied by the corresponding magnitude basis spectrum coefficient (DCT1T2). The resulting product is the dequantized DCT coefficient and is stored in buffer DRDCTM, using the reordering vector (ICRDR2) as the index into this output buffer so that the results will be in normal order. That is,

$$\text{DRDCTM}(\text{ICRDR2}(I)) = \text{Decode}(\text{QRDCTM}(I)) \times \text{DCT1T2}(I)$$

The decoding tables are given in Table 3-8.

#### 3.2.6.7 Inverse DCT Procedure

This procedure, shown in Figure 3-25, consists of three array functions, MPIDCM, FFTIN, and MPVDNM. It transforms the dequantized DCT coefficients back to a time domain waveform, restores the variance and mean to this waveform, and overlaps it with the waveform obtained in the previous frame to generate the output speech waveform.

##### 3.2.6.7.1 MPIDCM (FCB 249, PBFCB 165)

This array function performs the inverse of the correction applied by the Analyze process array function MPDCTM (subsection 3.2.5.2.3). It forms a conjugate symmetric spectrum to which an inverse FFT will later be applied from the dequantized DCT coefficients in buffer DRDCTM. The real and complex parts of the output buffer X2 (XR(K) and XI(K)), respectively, are formed as follows:

Code	3 Bit Value	2 Bit Value	1 Bit Value
7	-3.0867	-	-
6	-1.6725	-	-
5	-0.8330	-	-
4	-0.2334	-	-
3	3.0867	-1.8340	-
2	1.6725	-0.4196	-
1	0.8330	1.8340	-0.7071
0	0.2334	0.4196	0.7071

TABLE 3-8: DCT COEFFICIENT DEQUANTIZATION

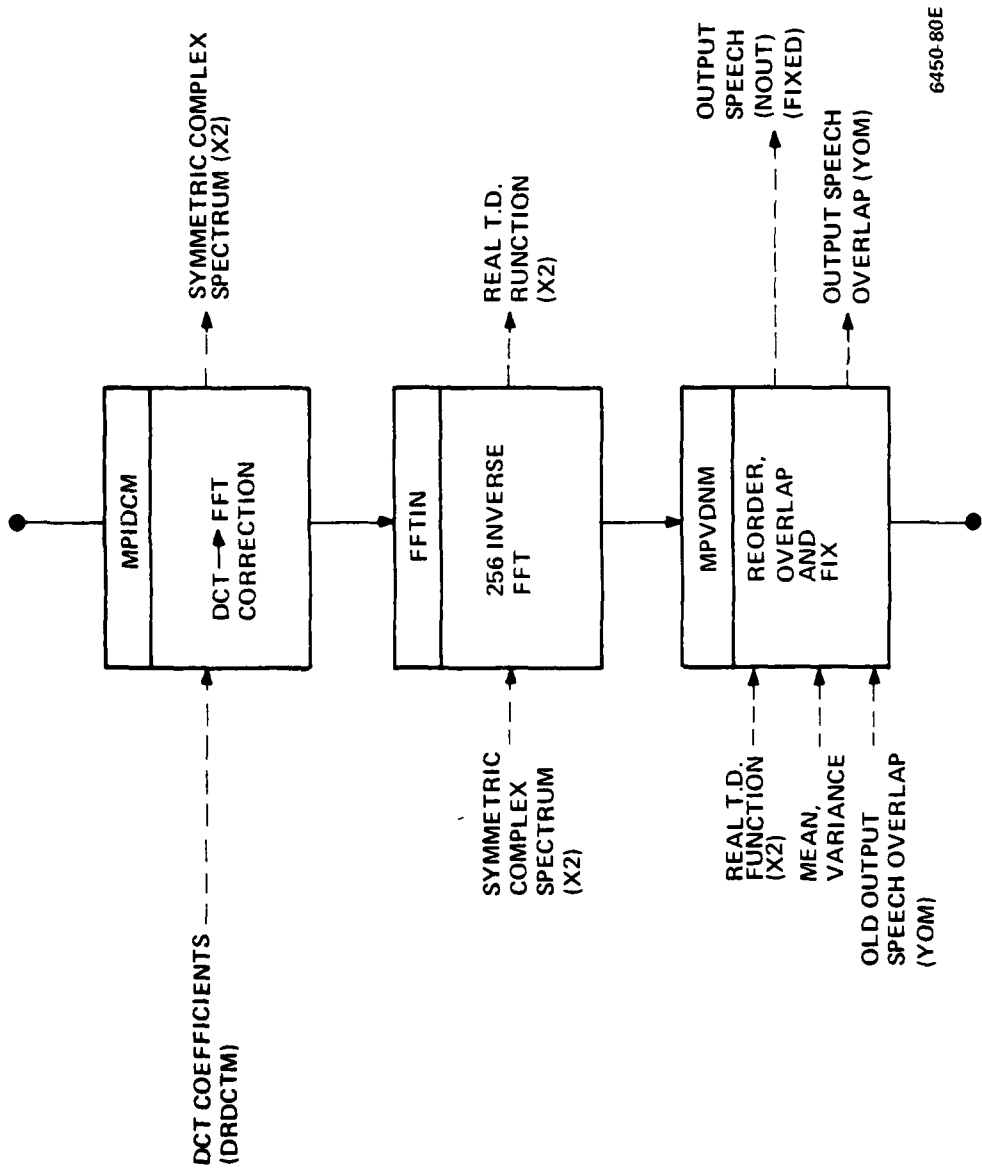


FIGURE 3-25: INVERSE DCT PROCEDURE



$$XR(K) = XR(N-K) = \cos(\pi K/2N) * DCT(K) + \sin(\pi K/2N) * DCT(N-K)$$

$$XI(K) = -XI(N-K) = \sin(\pi K/2N) * DCT(K) - \cos(\pi K/2N) * DCT(N-K)$$

$$XR(0) = DCT(0), XI(0) = 0$$

where N is 256 and K varies between 1 and 255.

This array function also computes a corrected mean and variance from the (delayed) dequantized mean and variance. The dequantized variance (scalar 101) is in dB and is changed to be linear. The dequantized mean (scalar 100) is multiplied by this linear variance to form an adjusted mean. Finally, the linear variance is divided by the frame length to form the per sample variance. The resulting mean and variance are stored in scalars 100 and 101, respectively.

#### 3.2.6.7.2 FFTIN (FCB 206, PBFCB 171)

This array function, supplied by CSPI, performs a 256 complex inverse FFT (not in place) on the corrected DCT coefficients in buffer X2. The resulting time domain function is also stored in X2.

#### 3.2.6.7.3 MPVDNM (FCB 253, PBFCB 166)

This array function restores natural order to the 256 point time domain function in X2, multiplies it by the variance (scalar 101), adds the mean (scalar 100), and overlaps the result with the last 10 samples of the output from the previous frame to form 246 output speech samples which are stored in buffer NOVt.

The overlapping consists of multiplying the first 10 samples by an increasing ramp and the last 10 samples of the previous frame by a descending ramp and adding the results as follows:

$$\text{OUTPUT}(I) = \text{NEW}(I) \cdot .1 \cdot I + \text{OLD}(236) * (1 - .1 \cdot I)$$

where I ranges from 0 through 9.

The reordering of the time domain function forms the even numbered DCT coefficients from the first half of the real parts of buffer X2. The odd numbered coefficients are obtained by accessing the second half of the buffer in reverse order.

### 3.2.7 Executive Process

The Executive process is used to schedule the tasks performed by the Arithmetic Processor and the CSPU. The Executive, as supplied by CSPI, runs in the CSPU and assigns the AP sequential tasks from function lists, loading the AP with and starting it on a new array function as soon as it has finished with the previous one. Responding to interrupts and this AP scheduling are the only functions required of the CSPU in the standard SNAP software system. The ATC system, however, also uses the CSPU to perform computations required by the algorithm and unsuited to the AP, such as serialization and error correction. The standard SNAP executive has been modified to include this additional use of the CSPU.

#### 3.2.7.1 Executive Modifications

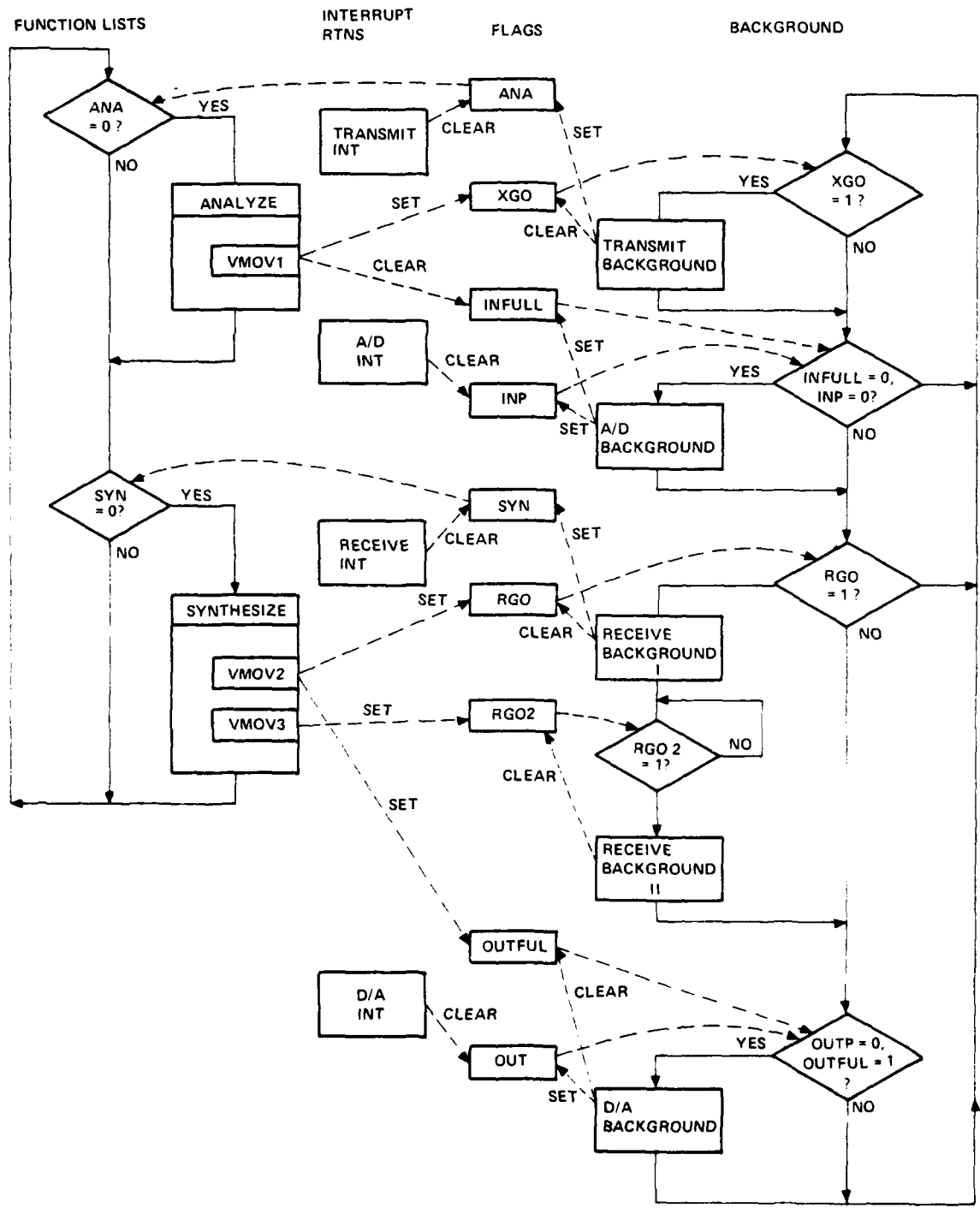
The new CSPU program, which will be referred to as the Background program, actually includes the four background processes and a dispatch routine to schedule them. It is interfaced to the existing executive as a co-routine. However, in order to minimize modifications to the executive, it is implemented as a single interrupt routine containing multiple exits.

The standard Executive loads and starts the AP, prepares the next function to be loaded into the AP, and then waits for the AP to finish. The ATC modification changes this wait to a programmed interrupt (Device 24, level 8) which causes the Background program to continue from its last exit. The Background program periodically checks flag APDNFL (integer scalar 126) to determine whether the AP has finished. If it has, the Background program saves its state and exits, returning to the point in the Executive program following the original wait. The Executive then continues normally, restarting the AP and preparing the next array function, until it again reaches the state where it would wait for the AP.

The APDONE interrupt service routine, also part of the original Executive, has been modified for this new program structure. It now sets APDNFL before returning.

#### 3.2.7.2 Process Scheduling

The process scheduling required in the ATC system includes the scheduling of the Analyze and Synthesize processes as well as the scheduling of the four I/O process background programs. The internal scheduling of array functions in the Analyze and Synthesize processes is accomplished by function lists, as described in subsection 3.3. The scheduling of the processes themselves is determined by flags ANA (integer scalar 100) and SYN (integer scalar 102) as shown in Figure 3-26. The Analyze process is enabled by flag ANA being clear. ANA is cleared by the Transmit interrupt routine to indicate that the last output has been used and the output buffer is available. ANA is set by the Transmit background program, which will fill the output buffer. The Synthesize process is enabled by flag SYN being clear. SYN is cleared by the Re-



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FIGURE 3-26: PROCESS SYNCHRONIZATION



ceive interrupt routine to indicate that the input buffer has been filled and is, therefore, available. SYN is set by the Receive background program which will read this buffer.

The Transmit background program is enabled by flag XG0 being set. It is set by VMOV1 in the Analyze process when the output data has been copied into buffers AQPB and ACTDCT and is thus available to the Transmit background process. The background process clears the flag.

The Receive background program is enabled by flag RG0 being set. It is set by VMOV2 in the Synthesize process when the data in ARQPB has been copied into a processing buffer, thus making ARQPB available for writing by the Receive background program, which clears the flag. When the Receive background program has deserialized the sideband parameters, it waits until the bit allocation determined by the Synthesize process is made available to it. VMOV3 in the Synthesize process sets flag RG02 when it has written new data into buffer RIBIT. RG02 being set enables the Receive background program to proceed to deserialize the DCT coefficients and to clear RG02.

The A/D background program is enabled by a combination of two flags. It runs when new input is available to it and its previous output has been used. Flag INP is cleared by the A/D interrupt routine to indicate that new input data is available. Flag INFULL is cleared by VMOV1 in the Analyze process to indicate that it has read the previous output of the background program (INPB) and that buffer is available for refilling. The A/D background program sets both of these flags.

The D/A background program is also enabled by a combination of two flags. It runs when its output buffer is empty and its input buffer is

full. Flag OUTP is cleared by the D/A interrupt routine to indicate that a buffer has been emptied and is available for more output. Flag OUTFUL is set by VMOV2 in the Synthesize process when new output speech has been copied to buffer OUTB and is available to the background program. The background program clears OUTFUL and sets OUTP.

### 3.3 System Software

The ATC system software consists of MAP-300 software and host PDP-11 software.

#### 3.3.1 MAP-300 Software

The MAP-300 software includes the array functions described individually in section 3.2 and the Executive program. It also includes the IOS-2SM program, LINE. Areas of MAP memory used by the MAP-300 software are shown in Table 3-9.

##### 3.3.1.1 Array Functions

The array functions contained in the ATC system are shown in Table 3-10. The starred functions have been written by GTE. All other array functions normally included in the CSPI SNAP II system have been deleted. This means that before a program using any of these deleted functions can be run after running the ATC system, the standard SNAP II system must be reloaded into the MAP.

##### 3.3.1.2 Executive Program

The Executive program that is included in the ATC system has been modified from the standard SNAP II release. The modifications consist of a revised APDONE routine which has had buffer checking eliminated for reasons of efficiency and which has added flag control and additional software to provide computational support for the AP. The additions to the Executive are described in subsection 3.2.7.

Bus 1

0-21FE	EXEC
2260-38A4	Prebinding Buffers
4000-4678	SNAP Array Functions
4690-49C4	FF2R
49D0-515A	IOS
5714-5BC4	Scroll Program Buffers
639C-6EC4	GTE Array Functions
7150-89F8	GTE Array Functions
8A00-8AA5	Data Buffers
8AA6-AC60	CSPUIS (background programs)
AC62-ACA8	Scroll Interrupt Additions
BFFF	Top of Memory

Bus 2

400-17C0	Data Buffers
1D30-230D	Table Storage
231E-2F43	Data Buffers
3FFF	Top of Memory

Bus 3

0-1FFF	Data Buffers
1FFF	Top of Memory

TABLE 3-9: MEMORY MAP



Function	FCB Number	GTE-Supplied
VFLT (Y, A, U, B)	136	
VMOV (Y, U)	143	
VSMA1 (Y, A, U, B)	144	
VCOS (Y, A, U, B, V, C)	186	
FFTIN (Y, U, V, W)	204	
FFTINB	205	
FFTIN (Y, U, V, W)	206	
FFTINB	207	
FF2R (Y, U, V, W)	214	
FF2RB	215	
VMOV1 (Y)	237	*
VMOV2 (Y)	238	*
VMOV3 (Y)	239	*
MPFDVM (Y, U, V)	240	*
MPDCTM (Y, U, V, W)	241	*
MPQDPP (Y, U, V)	242	*
MPDQPP (Y, U, V)	243	*
MPMWGX (Y, U, V, W)	244	*
MPFSTV (U)	245	*
MPSSRT (Y)	247	*
MPIDCM (Y, U, V)	248	*
MPASRT (Y)	249	*
MPFSTQ (Y, U, V, W)	250	*
MPFSTD (Y, U, V, W)	251	*
MPVDNM (Y, U, V)	252	*
MPBASP (Y, U, V, W)	253	*
MPSCAN (Y)	254	*
MPCDBA (Y)	255	*

TABLE 3-10: ARRAY FUNCTIONS

### 3.3.1.3 LINE Program

This program runs in the IOS-2SM scroll. It is loaded, initialized, and started by the host Fortran program. The function and operation of this program is described in subsection 3.2.3.1.

### 3.3.2 PDP-11 Software

The PDP-11 software consists of a Fortran control program, used to configure and initialize MAP buffers and to define function lists; software which provides the interface between this control program and the MAP driver; and utility software which provides an improved user interface to the MAP assembler.

#### 3.3.2.1 Fortran Control Program

The Fortran control program consists of five major segments. These are: Configure and Initialize Buffers and Scalars (MAPON), Prebind Functions (CREATE), Define Function Lists (DEFINE), Configure Scroll Programs (SETUP), and Execute Function List (SYSTEM).

These five segments are each subroutines, contained in separate overlays. The main program, MASTER, calls these subroutines in sequence. Data is passed between the main program and the subroutines through named common blocks. Other subroutines, mainly debugging aids, are included in the Fortran program which was designed to operate in any of three modes - Real-Time, Timing, and File-to-File. Only the Real-Time mode is operative in the delivered system.

##### 3.3.2.1.1 Configure and Initialize Buffers

The buffer locations, sizes, types, and identifiers are shown in Table 3-11. The buffer initialization defines three cosine vectors.

M A P - 3 0 0  
B U F F E R   A R E A S

BID	BID #	BUS	BA	BA+AS-1	AS	ST	SI	WS
AOPB	43	1	35328.	35341.	14	I	E	L
AQDCT	44	1	35342.	35597.	256	I	E	L
AIBIT	45	1	35598.	35853.	256	I	E	L
SEN	49	1	35854.	36223.	370	I	E	L
INPB	50	1	36224.	36479.	256	I	E	L
BF1	63	1	36480.	36848.	369	I	E	L
BF2		1	36849.	37217.	369	I	E	L
BF3		1	37218.	37586.	369	I	E	L
ZRO	1	1	37587.	37955.	369	I	E	L
OUTB	59	1	37956.	38211.	256	I	E	L
RECV	56	1	38212.	38581.	370	I	E	L
ARQPB	57	1	38582.	38595.	14	I	E	L
ARQDCT	58	1	38596.	38851.	256	I	E	L
SYNB1	62	1	38852.	39220.	369	I	E	L
SYNB2		1	39221.	39589.	369	I	E	L
RIBIT	2	1	39590.	36245.	256	I	E	L
BIDBF1	63	1	10000.	14499.	4500	I	E	L
BIDBF2		1	8800.	9999.	1200	I	E	L
COSZ	24	2	1024.0	1535.0	256	R	E	L
YOM	26	2	2048.0	2067.0	10	R	E	L
XOM	27	2	2068.0	2087.0	10	R	E	L
MAPX#	28	2	2068.0	2579.0	256	R	E	L
MIBIT	29	2	2088.0	2343.0	256	I	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION

DCTM	30	2	2344.0	2855.0	256	R	E	L
		2	2856.0	2867.0	12	I	E	L
QTDCTM	32	2	2864.0	3123.0	256	I	E	L
NINM	33	2	3124.0	3369.0	246	J	E	L
NIN1	34	2	3370.0	3615.0	246	I	E	L
NIN2	35	2	3616.0	3861.0	246	I	E	L
NOUTM	36	2	3862.0	4107.0	246	I	E	L
NOUT1	37	2	4108.0	4353.0	246	I	E	L
NOUT2	38	2	4354.0	4599.0	246	I	E	L
RECV1	39	2	4600.0	4964.0	369	I	E	L
RECV2	40	2	4969.0	5337.0	369	I	E	L
SEN1	41	2	5340.0	5709.0	370	I	E	L
SEN2	42	2	5710.0	6079.0	370	I	E	L
DCT1T1	51	2	9000.0	9511.0	256	R	E	L
DCT1T2	52	2	9512.0	10023.0	256	R	E	L
IURDR1	53	2	10024.0	10279.0	256	I	E	L
IURDR2	54	2	10280.0	10535.0	256	I	E	L
MIBIT1	55	2	10536.0	10791.0	256	I	E	L
MIBIT2	60	2	10792.0	11047.0	256	I	E	L
QTDCT1	7	2	11048.0	11303.0	256	I	E	L
MIBIT3	5	2	11304.0	11559.0	256	I	E	L
MIBIT4	6	2	11560.0	11815.0	256	I	E	L
QPRM	31	2	11816.0	11829.0	14	I	E	L
QRPRM	8	2	11830.0	11843.0	14	I	E	L
QRDCIM	9	2	11844.0	12099.0	256	I	E	L
NDPK	10	3	0.0	2047.0	512	C	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION (CONT'D)

X1	11	3	2048.0	4095.0	512	C	E	L
X1R*		3			512	R	E	L
X1I*		3			512	R	E	L
X2	48	3	2048.0	3071.0	256	C	E	L
IORDRM#	12	3	0.0	255.0	256	I	E	L
PWEITM#	13	3	0.0	255.0	256	I	E	L
DCTM1#	14	3	512.0	1023.0	256	R	E	L
DCTM2#	15	3	1024.0	1535.0	256	R	E	L
R1#	16	3	0.0	17.0	9	R	E	L
A1#	17	3	18.0	33.0	8	R	E	L
KF#	18	3	35.0	66.0	16	R	E	L
PARC*	3	3			8	R	E	L
IMP1#	19	3	2048.0	2559.0	256	R	E	L
IMP2#	20	3	2560.0	3071.0	256	R	E	L
IMP3#	46	3	3584.0	4095.0	256	R	E	L
IMP4#	47	3	3072.0	3583.0	256	R	E	L
DRDCTM#	21	3	1024.0	1535.0	256	R	E	L
YNM#		3	512.0	1023.0	256	R	E	L
VLONG	23	2	4096.0	5119.0	512	R	E	L
VSHRT	25	2	5120.0	5631.0	256	R	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION (CONT'D)

Two of these, VSHRT and VLONG, are used for FFT's and contain a full cycle of the cosine each, consisting of 256 and 512 points, respectively. The third, COSZ, is used in the DCT correction and contains one quarter cycle of the cosine in 256 points. The initialization also clears the overlap buffers XOM and YOM, the I/O buffers NIN1, NIN2, NOUT1, NOUT2, RECV1, RECV2, SEN1, and SEN2. It also clears the data delay line buffers AQP, AQDCT, DCT1T1, DCT1T2, QDCT1, QPRM, and QRPRM, and the sync buffers BF1, BF2, SYN1, and SYN2. It stores a buffer of zeros, ZRO, for subsequent use by the ATC system. It initializes the reordering vectors IOR1 and IOR2 to be the integers from 0 to 255, and sets the bit allocation buffers, MIB1, MIB2, MIB3, and MIB4 to contain 134 values of two and the remainder of the values in these buffers to be zero.

The scalars used and their initial values are shown in Table 3-12.

#### 3.3.2.1.2 Prebound FCB's

To reduce AP loading overhead, all array functions used in the real-time loop of the ATC system are prebound. That is, a separate copy of the APS portion of each array function is maintained for each set of calling parameters given the function. The values of these parameters are stored in the copy at pre-binding and the time copy is block-loaded into the APS at execution time. Table 3-13 lists the prebound functions and the associated calling parameters.

#### 3.3.2.1.3 Function Lists

The prebound array functions are collected into function lists. The use of function lists allows the MAP to run independently of the host which, by eliminating host overhead, increases the speed of the system.

SID	NAME	DESCRIPTION	INITIAL VALUE	CONSTANT
50		constant	1/512.	*
51		constant	1/256.	*
52	unused			
53	unused			
54	LTHINV	LTH Inverse	1/256.	*
55	unused			
56	DARG	constant	1/1024.	*
57	MDARG	constant	1/1024.	*
58	unused			
.	.			
.	.			
73	unused			
74	LPCNPI	LPC order +1	9	*
75	LPCN	LPC order	8	*
76		constant	4.0	*
77		constant	10E-10	*
78	unused			
79	BTLTH	Bits avail. for DCTs	268	*
80	CTEMP	Bit all offset	-	
81	unused			
82	BITSMN	Temp for CTEMP calc.	-	
83	ITOT	# of DCTs to be coded	-	
84	BITMAX	Max bits/DCT	3.0	*
85		CTEMP +.5	-	
86		LPC Threshold	.995	*
87	LTH	Frame Length	256	*
88	DQDC	Dequantized Mean	-	
89	DQVAR	Dequantized Variance	-	
90	DQPG	Dequantized Pitch Gain	-	
91	DQM	Dequantized Pitch	-	
92	LTH2	2*LTH	512	*
93		Inverse Variance	-	
94		constant	20.0	*
95		Log 16 (10)		*
96	LTH4	4*LTH	1024.	*
97	LTHM1	LTH-1	255	*
98		constant	.05	*
99	unused			
100	DC <sup>-2</sup>	Mean from 2nd prev. frame	-	
101	VAR <sup>-2</sup>	Variance from 2nd prev. frame	-	
102	DC <sup>-1</sup>	Mean from prev. frame	-	
103	VAR <sup>-1</sup>	Variance from prev. frame	-	

TABLE 3-12: SCALARS

```

STATUS=MPCHF(BIDBF1,153)
INSTR=153
STATUS=MPEDVM(MAPX,NIMM,X2)
INSTR=154
STATUS=MPDCFM(DCTM,X1,X2,COSZ)
INSTR=155
STATUS=MPWGX(P1,NIMM,KF,A1)
INSTR=156
STATUS=MPQDPP(OPRM,PARC,A1)
INSTR=157
STATUS=MPFSTV(A1)
INSTR=158
STATUS=MPBASP(DCTM1,PWEITM,TMP2,X1)
INSTR=159
STATUS=MPASKT(IORDRM)
INSTR=160
STATUS=MPSCAN(DCTM2)
INSTR=161
STATUS=MPCDHA(MIBIT3)
INSTR=162
STATUS=MPFSTQ(QTDCFM,MIBIT3,TMP1,TMP2)
INSTR=163
STATUS=MPHQPP(PARC,QRPRM,A1)
INSTR=164
STATUS=MPFSTD(DRDCTM,QRDCTM,TMP1,IORDR2)
INSTR=165
STATUS=MPIDCM(X2,DRDCTM,COSZ)
INSTR=166
STATUS=MPVDM(NQIM,YOM,X2)
INSTR=167
STATUS=MPSSRF(IORDRM)
STATUS=MPTRF(0)
INSTR=3
STATUS=MPCHF(BIDBF2,168)
INSTR=168
STATUS=FFIN(X2,1,X2,VSHPI,WORK)
INSTR=169
STATUS=FFIN(X1,1,X1,VLONG,WORK)
INSTR=170
STATUS=FF2R(X1,4,X1,VLONG,WORK)
INSTR=171
STATUS=FFIN(X2,1,X2,VSHRT,WORK)
INSTR=4
STATUS=MPTRF(0)
STATUS=MPCHF(BIDBF3,172)
INSTR=172
STATUS=VMOV1(AQPB)
INSTR=173
STATUS=VMOV2(QRPRM)
INSTR=174
STATUS=VMOV3(RIBIT)
INSTR=175
STATUS=MPCDHA(MIBIT1)
STATUS=MPTRF(0)

```

TABLE 3-13: PREBOUND FCB'S



The top level function list (WAIT) is very simple. It consists of three subordinate function lists, STRTUP, ATCSYN, and ATCANA, which are conditionally executed or not based on the state of three associated scalars, INIT, SYN, and ANA. STRTUP is executed only once. Its execution causes INIT to be set to unity, which denies the execution condition for all succeeding passes through the main function list. STRTUP initializes the process synchronization flags and double buffer pointers as shown in Table 3-14. It also loads and starts the three I/O scrolls.

Both ATCSYN and ATCANA are unconditional, sequential lists of pre-bound array functions. They are shown in Table 3-15. ATCANA is the control structure of the Analyze process, as discussed in section 3.2.5, and ATCSYN is the control structure of the Synthesize process, as discussed in section 3.2.6.

#### 3.3.2.2 Host Support Software

The host support software consists of a function definition module for each array function, an additional function, FCBGN, called by each of these definitions, error reporting routines, and the MAP driver interface routine. This software is contained in the SNAPHS library. This library has been modified for the ATC system, as detailed in section 3.5. The changes are to delete unused array function definitions, to make separate libraries for each of the three MAP drivers in the host operating system, and to change FCBGN to support newly released array functions. This last modification increases the size of table ISARG from 18 to 22. ISARG contains parameter usage configurations. The ATC system requires these additional configurations.

Flag	Integer Scalar	Value	Meaning	Description
OUT	104	1	false	A/D buffer full
INP	106	1	false	D/A buffer empty
AFLG	101	0	buffer 1	current send buffer
SFLG	103	0	buffer 1	current receive buffer
IFLG	107	0	buffer 1	current A/D buffer
OFLG	105	0	buffer 1	current D/A buffer
APDNFL	126	0	false	APDONE has occurred
RG0	114	0	false	enable Receive background
RG02	116	0	false	enable Receive background part 2
XGO	115	0	false	enable Transmit background
INFULL	110	0	false	Analyze input full
OUTFUL	111	0	false	Synthesize output full
SYNC	117	0	-	sync position
RSYN	118	0	sync lost	sync state
NEWSYN	119	0	-	frames since sync acquired
OLSYN	120	0	-	previous frame sync bit
LSTER	121	0	false	error previous frame
ERSYN	122	0	-	number of sync bit errors

TABLE 3-14: SYSTEM FLAG INITIALIZATION

```

C      LIST "WAIT": THE WAIT LOOP
      STATUS=MPEFL(WAIT)
      STATUS=MPIIF(INIT,EQ,0,STRTUP,FLO)
      STATUS=MPIIF(SYN,EQ,0,ATCSYN,FLO)
      STATUS=MPIIF(ANA,EQ,0,ATCANA,FLO)
      STATUS=MPEFL(FL3)

```

```

C      LIST "STRTUP": INITIALIZATION LOOP
      STATUS=MPEFL(STRTUP)
      STATUS=MPIST(INIT,1)
      STATUS=MPIST(OUT,1)
      STATUS=MPIST(INP,1)
      STATUS=MPIST(AFLG,0)
      STATUS=MPIST(SFLG,0)
      STATUS=MPIST(IFLG,0)
      STATUS=MPIST(OFLG,0)
      STATUS=MPIST(INFULL,-1)
      STATUS=MPIST(OUTFUL,-1)
      STATUS=MPIST(APDNFL,0)
      STATUS=MPIST(RG0,0)
      STATUS=MPIST(XG0,0)
      STATUS=MPIST(RG02,0)
      STATUS=MPIST(SYNC,0)
      STATUS=MPIST(RSYN,0)
      STATUS=MPIST(MEWSYN,0)
      STATUS=MPIST(OLSYN,0)
      STATUS=MPIST(LSTER,0)
      STATUS=MPIST(FPSYN,0)
      STATUS=MPLDS(AOM,IOS,AOMP*)
      STATUS=MPRNS(AOM,IOS,AUMSA)
      STATUS=MPLDS(ADAM,IOS,ADAMP*)
      STATUS=MPRNS(ADAM,IOS,ADAVSA)
      STATUS=MPLDS(IOSID,IOS,IOSPM*)
      STATUS=MPRNS(IOSID,IOS,IOSSA)
      STATUS=MPEFL(FL4)

```

TABLE 3-15: FUNCTION LISTS

```

C      LIST "ATCANA": ATC ANALYZER *ERROR CODING AND SERIALIZATION
        INSTR=1043
        STATUS=*PREF(ATCANA)
C      HF(172)=*MDDV1(AJPH)
        STATUS=*PXHF(172)
C      HF(153)=*MFDVM(MAPX,HINM,X2)
        STATUS=*PXHF(153)
C      HF(168)=*FFTQ(X2,1,X2,VSHBT,WORK)
        STATUS=*PXHF(168)
C      HF(154)=*MPDCTM(DCTA,X1,X2,COSZ)
        STATUS=*PXHF(154)
C      HF(169)=*FFTIN(X1,1,X1,VLONG,WORK)
        STATUS=*PXHF(169)
C      HF(155)=*MPWGX(R1,HINM,KE,A1)
        STATUS=*PXHF(155)
C      HF(156)=*MPQDPP(OPRA,PARC,A1)
        STATUS=*PXHF(156)
C      HF(157)=*MPSIV(A1)
        STATUS=*PXHF(157)
C      HF(170)=*FF2P(X1,4,X1,VLONG,WORK)
        STATUS=*PXHF(170)
C      HF(158)=*MPRASP(DCTM1,PWEITM,IMP2,X1)
        STATUS=*PXHF(158)
C      HF(159)=*MPASRT(IORDRM)
        STATUS=*PXHF(159)
C      HF(160)=*MPCAN(DCTM2)
        STATUS=*PXHF(160)
C      HF(161)=*MPCDPA(UNIT3)
        STATUS=*PXHF(161)
C      HF(162)=*MPESTQ(QDCTA,UNIT,IMP1,IMP2)
        STATUS=*PXHF(162)
C      IF(TIMING.EQ.YES)STATUS=*PXFEL(ATCSYN)
        INSTR=1050
        STATUS=*PXFEL(FL1)
C

```

TABLE 3-15: FUNCTION LISTS (CONT'D)

```

C      LIST "ATCSYN": ATC SYNTHESIZER W/ERROR DECODER & DESERIALIZATION
      INSTR=1055
      STATUS=MPEFL(ATCSYN)
C      BF(173)=V*OV2(QRPRK)
      STATUS=MPXBF(173)
C      BF(163)=*PIQEP(PAPC,QRPRK,A1)
      STATUS=MPXBF(163)
C      BF(157)=*PESTV(A1)
      STATUS=MPXBF(157)
C      BF(170)=*PE2R(X1,4,X1,VLONG,*DRF)
      STATUS=MPXBF(170)
C      BF(158)=*PBASP(DCT*1,P*EITM,IMP2,X1)
      STATUS=MPXBF(158)
C      BF(167)=*PSSRI(10PDR*)
      STATUS=MPXBF(167)
C      BF(160)=*PSCAN(DCT*2)
      STATUS=MPXBF(160)
C      BF(175)=*PCDBA(M*BIT1)
      STATUS=MPXBF(175)
C      BF(174)=V*OV3(R*BIT)
      STATUS=MPXBF(174)
C      BF(164)=*PESTB(DR*DCM,DR*DCM,IMP1,10PDR2)
      STATUS=MPXBF(164)
C      BF(165)=*PIDCM(X2,DR*DCM,COSZ)
      STATUS=MPXBF(165)
C      BF(171)=*PE11N(X2,1,X2,V*HRT,*DRK)
      STATUS=MPXBF(171)
C      BF(166)=*PVDRM(VOUT*4,Y(04,X2)
      STATUS=MPXBF(166)
      STATUS=MPEFL(FI,2)

```

TABLE 3-15: FUNCTION LISTS (CONT'D)

AD-A091 663

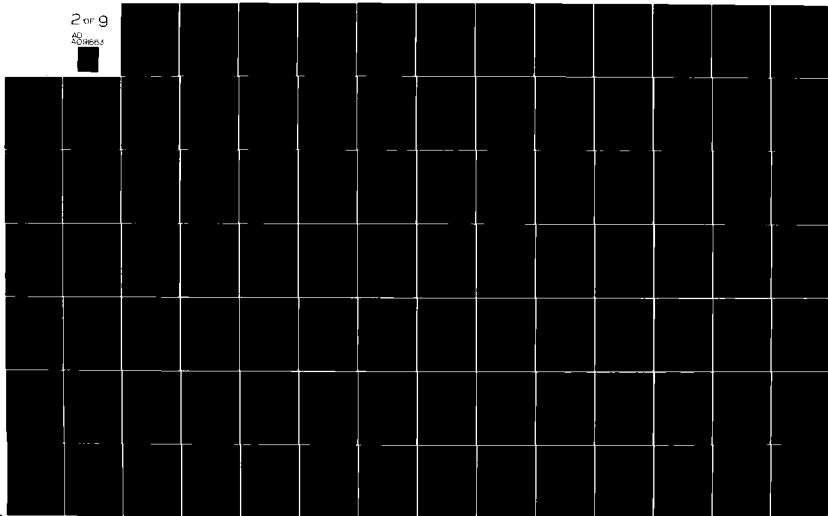
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND, VOLUME 2, REAL-TIME 50--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

2 of 9

AD  
DOWNS



### 3.3.2.3 Utility Software

A utility program, PREMAP, is included in the delivered ATC system. This program reformats text file to be acceptable to the MAP assembler, MAPASM. In particular, it changes tabs in the input text to sequences of spaces in the output text and supplies continuation lines for comments.

### 3.4 System Hardware

The ATC system includes equipment manufactured by CSP Inc., Billerica, Mass., and equipment designed and built by GTE Sylvania.

#### 3.4.1 CSPI-Supplied Hardware

The CSPI-supplied hardware consists of a MAP-300 Array Processor, Model 1030, with attached options. These options are:

- 8K x 32 MOS Master Memory, 500 nsec, Bus 1, model 2030
- 16K x 32 MOS Slave Memory, 500 nsec, Bus 1, model 2050
- 8K x 32 MOS Master Memory, 300 nsec, Bus 2, model 2203
- 4K x 32 MOS Master Memory, 170 nsec, Bus 3, model 2410
- PDP-11 Interface, model 3110
- I/O Scroll type 2SM, model 4020
- Bus Switch (2), model 4040
- Analog Data Acquisition Module (ADAM), model 5120
- Analog Output Module (AOM), model 5130
- Expansion Chassis, model 6100
- Auxiliary Power Supply, model 6200

The MAP includes preprogrammed micro-code in read-only memory. The ATC system requires Revision 18 of this micro-code.

#### 3.4.2 GTE-Supplied Hardware

The GTE-supplied hardware consists of the Speech Processing Interface (SPI), which provides an analog interface to the MAP, and the Full Duplex Interface (FDI), which provides a digital interface to the MAP. These interfaces were supplied to two other contractors as well as being used in the GTE ATC system.

As the ATC project progressed, it became apparent that modifications to the GTE-supplied hardware were required. Subsection 3.4.2.3 describes these modifications.



#### 3.4.2.1 GTE Speech Processing Interface (SPI)

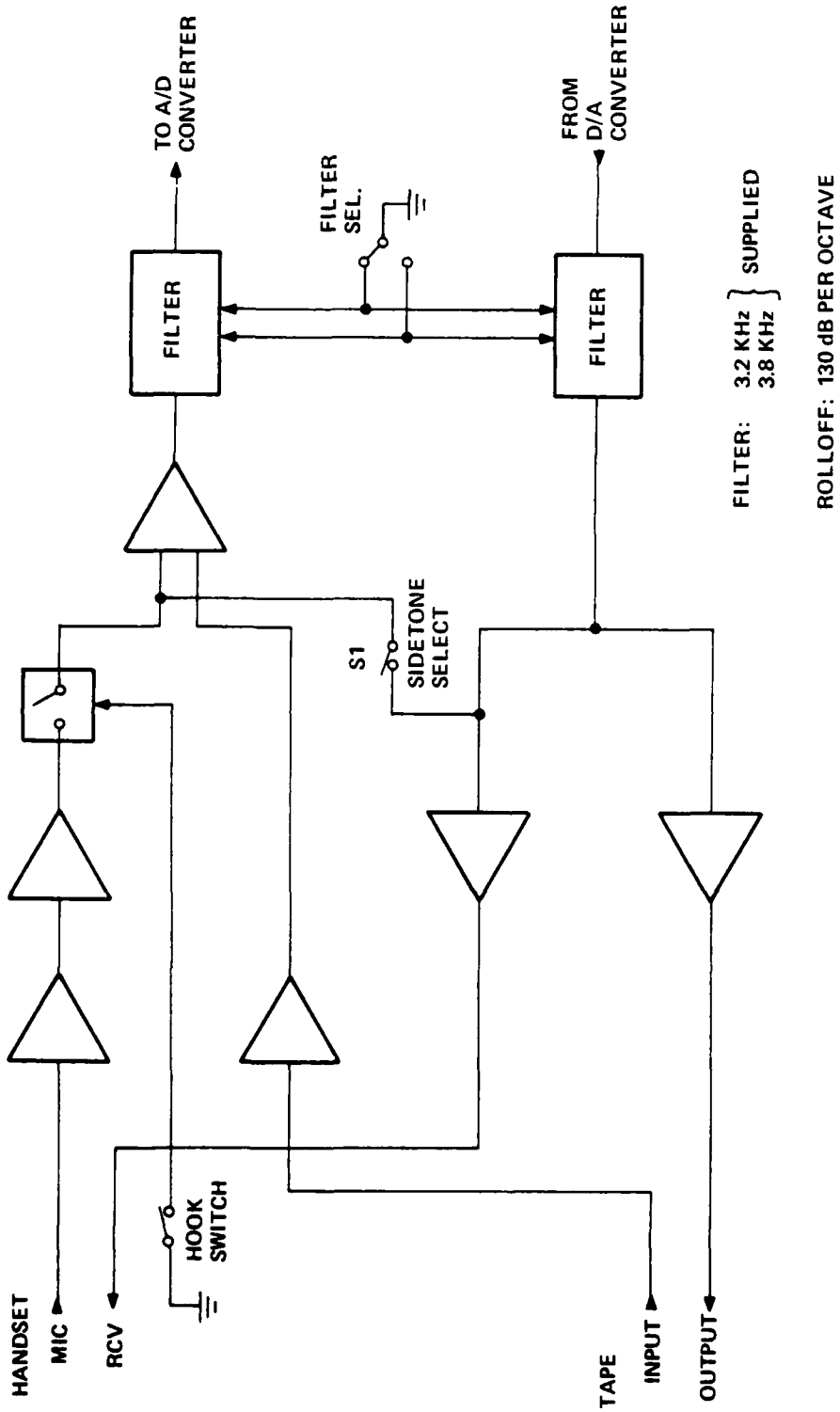
The SPI assembly provides the amplification, equalization, and filtering necessary to interface an audio handset to the MAP-300 A/D and D/A converters. A block diagram of the SPI is shown in Figure 3-27. The SPI also serves as the common junction point for all digital signals between the MAP-300 and an external modem. The interconnections between the MAP, the SPI, and the external devices are shown in Figure 3-28.

A switch on the SPI front panel, which is shown in Figure 3-29, permits the selection of either of two sets of filters, thereby permitting a choice of cutoff frequency. Filters having cutoff frequencies of 3200 Hz and 3800 Hz are provided. The filters are of the plug-in type, thereby enabling the user to install other filters with different cutoff frequencies of his choice if so desired.

The handset provided with the equipment uses a dynamic microphone which has been designed to GTE Sylvania specifications and has been optimized for use in speech processing applications. The handset connects directly to the front panel of the audio interface assembly and may be stored on the hookswitch, which is also located on the front panel. When "on hook," the audio circuits (both receiving and transmitting) for the handset are disabled. A 25-foot extension cable for use with the handset is also provided.

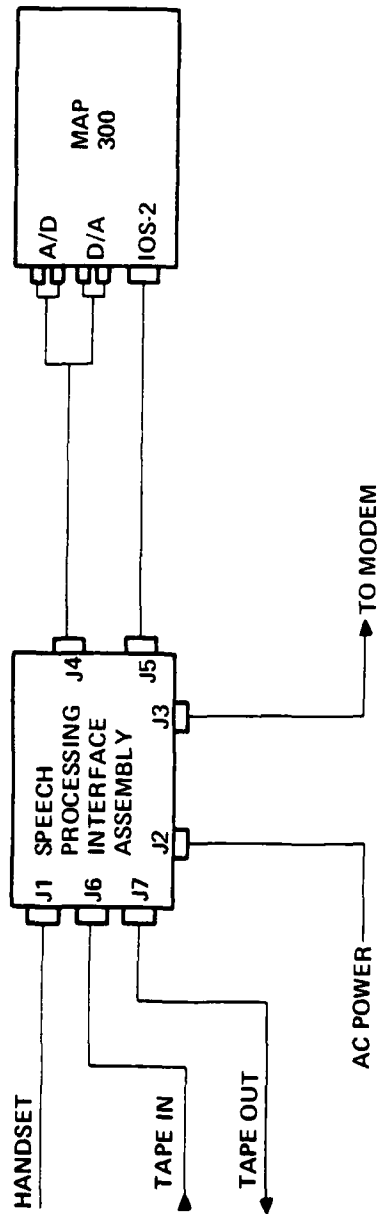
A pair of telephone jacks located on the rear panel of the audio interface unit (shown in Figure 3-30) may be used to connect a tape recorder or test equipment for test and measurement purposes. The audio circuits for the tape recorder are always active and are unaffected by the operation of the hookswitch. A single connector, also located on the rear panel, is used to connect the assembly to the A/D-D/A converters of the MAP-300. Table 3-16 gives the specifications of the SPI.





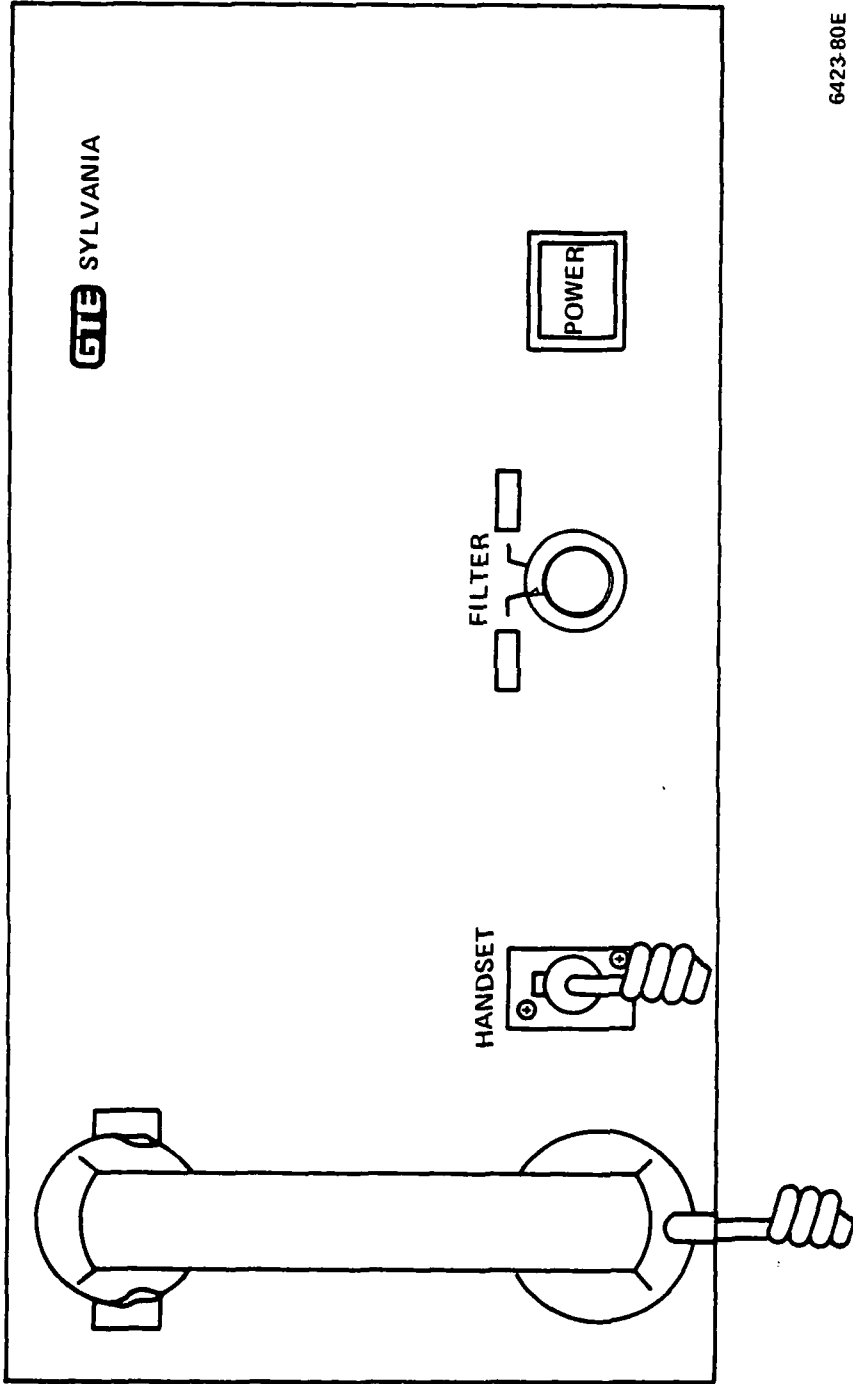
6421-80E

FIGURE 3-27: SPI BLOCK DIAGRAM



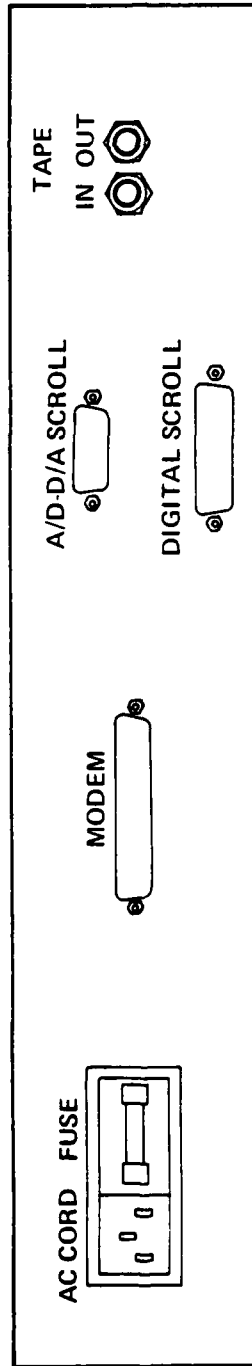
6435-80E

FIGURE 3-28: INTERCONNECTION DIAGRAM



6423-80E

FIGURE 3-29: FRONT PANEL.



6422-80E

FIGURE 3-30: REAR PANEL

#### 3.4.2.2 Full Duplex Interface (FDI)

The CSPI IOS-2SM scroll has been modified by GTE Sylvania to provide a means for interfacing to any modem employing an EIA RS-449/RS-432 interface. The interface connections are given in Table 3-17. The interface design is such that an EIA RS-232-C interface may also be used, and the line drivers and receivers have been selected such that the protective networks for RS-449/RS-232 interoperation (refer to EIA Industrial Bulletin No. 12) are not required. Table 3-18 is a comparison between RS-449 and RS-232-C conventions. In addition to the modem interface, the modified I/O scroll includes a programmable real-time clock which generates the timing signals for speech sampling and the modem data. The I/O scroll is connected to the audio interface assembly by means of a single cable.

The data and speech sampling rates are set by issuing a single 16-bit control word from the IOS-2SM. The format of this control word and the FDI data formats are shown in Figure 3-31. The most significant byte of the control word determines the data rate, whereas the least significant byte determines the speech sampling rate. These control bytes and the associated data and sampling rates are given in Table 3-19. Each control byte controls a separate frequency divider. The basic clock frequency, 1.536 MHz, is first divided by four and then further divided by the selected divide ratio. The modem clock is then divided by an additional factor of two to produce a square wave with a 50% duty cycle. Once the real-time clock has been so programmed, it is not necessary to issue any other control words unless it is desired to change either of the clock rates, or unless power is removed from the MAP-300. It should be noted that the entire control word must be issued whenever changing rates, even if only one rate is to be changed.

<u>PIN</u>	<u>SIGNAL</u>
2	SI - Signal Rate Indicator
4	SD - Send Data
6	RD - Receive Data
7	RS - Request to Send
8	RT - Receive Timing
9	CS - Clear to Send
11	DM - Data Mode
12	TR - Terminal Ready
13	RR - Receiver Ready
15	IC - Incoming Call
16	SR - Signal Rate Selector
17	TT - Terminal Timing
19	SG - Signal Ground
20	RC - Receive Common
33	SQ - Signal Quality
37	SC - Send Common

TABLE 3-17: MODEM INTERFACE CONNECTIONS (RS-449)



RS-449		RS-232C	
SG SC RC	SIGNAL GROUND SEND COMMON RECEIVE COMMON	AD	SIGNAL GROUND
IS IC TR DM	TERMINAL IN SERVICE INCOMING CALL TERMINAL READY DATA MODE	CE CD CC	RINGS INDICATOR DATA TERMINAL READY DATA SET READY
SD RD	SEND DATA RECEIVE DATA	DA DB	TRANSMITTED DATA RECEIVED DATA
TT ST RT	TERMINAL TIMING SEND TIMING RECEIVE TIMING	DA DB DD	TRANSMITTER SIGNAL ELEMENT TIMING (DTE SOURCE) TRANSMITTER SIGNAL ELEMENT TIMING (DCE SOURCE) RECEIVER SIGNAL ELEMENT TIMING
RS CS RR SQ NS SF SR SI	REQUEST TO SEND CLEAR TO SEND RECEIVER READY SIGNAL QUALITY NEW SIGNAL SELECT FREQUENCY SIGNALING RATE SELECTOR SIGNALING RATE INDICATOR	CA CB CF CG CH CI	REQUEST TO SEND CLEAR TO SEND RECEIVED LINE SIGNAL DETECTOR SIGNAL QUALITY DETECTOR DATA SIGNAL RATE SELECTOR (DTE SOURCE) DATA SIGNAL RATE SELECTOR (DCE SOURCE)
SSD SRD	SECONDARY SEND DATA SECONDARY RECEIVE DATA	SRA SBB	SECONDARY TRANSMITTED DATA SECONDARY RECEIVED DATA
SRS SCS SRR	SECONDARY REQUEST TO SEND SECONDARY CLEAR TO SEND SECONDARY RECEIVER READY	SCA SCB SCF	SECONDARY REQUEST TO SEND SECONDARY CLEAR TO SEND SECONDARY RECEIVED LINE SIGNAL DETECTOR
LL RL TM	LOCAL LOOPBACK REMOTE LOOPBACK TEST MODE		
SS SB	SELECT STANDBY STANDBY INDICATOR		

TABLE 3-18: RS-449/RS-232C COMPARISON

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHz

PAGE 1

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
0	00	00000000	256	0.750	1.500
1	01	00000001	255	0.753	1.506
2	02	00000010	254	0.756	1.512
3	03	00000011	253	0.759	1.518
4	04	00000100	252	0.762	1.524
5	05	00000101	251	0.765	1.530
6	06	00000110	250	0.768	1.536
7	07	00000111	249	0.771	1.542
8	08	00001000	248	0.774	1.548
9	09	00001001	247	0.777	1.555
10	0A	00001010	246	0.780	1.561
11	0B	00001011	245	0.783	1.567
12	0C	00001100	244	0.787	1.574
13	0D	00001101	243	0.790	1.580
14	0E	00001110	242	0.793	1.587
15	0F	00001111	241	0.797	1.593
16	10	00010000	240	0.800	1.600
17	11	00010001	239	0.803	1.607
18	12	00010010	238	0.807	1.613
19	13	00010011	237	0.810	1.620
20	14	00010100	236	0.814	1.627
21	15	00010101	235	0.817	1.634
22	16	00010110	234	0.821	1.641
23	17	00010111	233	0.824	1.648
24	18	00011000	232	0.828	1.655
25	19	00011001	231	0.831	1.662
26	1A	00011010	230	0.835	1.670
27	1B	00011011	229	0.838	1.677
28	1C	00011100	228	0.842	1.684
29	1D	00011101	227	0.846	1.692
30	1E	00011110	226	0.850	1.699
31	1F	00011111	225	0.853	1.707
32	20	00100000	224	0.857	1.714
33	21	00100001	223	0.861	1.722
34	22	00100010	222	0.865	1.730
35	23	00100011	221	0.869	1.738
36	24	00100100	220	0.873	1.745
37	25	00100101	219	0.877	1.753
38	26	00100110	218	0.881	1.761
39	27	00100111	217	0.885	1.770
40	28	00101000	216	0.889	1.778
41	29	00101001	215	0.893	1.786

TABLE 3-19: REAL TIME CLOCK CONTROL

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHz

PAGE 2

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
42	2A	00101010	214	0.997	1.794
43	2B	00101011	213	0.991	1.803
44	2C	00101100	212	0.985	1.811
45	2D	00101101	211	0.979	1.820
46	2E	00101110	210	0.974	1.829
47	2F	00101111	209	0.969	1.837
48	30	00110000	208	0.963	1.846
49	31	00110001	207	0.958	1.855
50	32	00110010	206	0.952	1.864
51	33	00110011	205	0.947	1.873
52	34	00110100	204	0.941	1.882
53	35	00110101	203	0.936	1.892
54	36	00110110	202	0.930	1.901
55	37	00110111	201	0.925	1.910
56	38	00111000	200	0.920	1.920
57	39	00111001	199	0.915	1.930
58	3A	00111010	198	0.910	1.939
59	3B	00111011	197	0.905	1.949
60	3C	00111100	196	0.900	1.959
61	3D	00111101	195	0.895	1.969
62	3E	00111110	194	0.890	1.979
63	3F	00111111	193	0.885	1.990
64	40	01000000	192	1.000	2.000
65	41	01000001	191	1.005	2.010
66	42	01000010	190	1.011	2.021
67	43	01000011	189	1.016	2.032
68	44	01000100	188	1.021	2.043
69	45	01000101	187	1.027	2.053
70	46	01000110	186	1.032	2.065
71	47	01000111	185	1.038	2.076
72	48	01001000	184	1.043	2.087
73	49	01001001	183	1.049	2.099
74	4A	01001010	182	1.055	2.110
75	4B	01001011	181	1.061	2.122
76	4C	01001100	180	1.067	2.133
77	4D	01001101	179	1.073	2.145
78	4E	01001110	178	1.079	2.157
79	4F	01001111	177	1.085	2.169
80	50	01010000	176	1.091	2.182
81	51	01010001	175	1.097	2.194
82	52	01010010	174	1.103	2.207
83	53	01010011	173	1.110	2.220

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)  
3-100

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.535 MHZ

PAGE 3

DECIMAL	COUNTER SETTING		DIVIDE RATIO	OUTPUT RATE - KHZ	
	HEXADECIMAL	BINARY		LINE	SPEECH
84	54	01010100	172	1.116	2.233
85	55	01010101	171	1.123	2.246
86	56	01010110	170	1.129	2.259
87	57	01010111	169	1.136	2.272
88	58	01011000	168	1.143	2.286
89	59	01011001	167	1.150	2.299
90	5A	01011010	166	1.157	2.313
91	5B	01011011	165	1.164	2.327
92	5C	01011100	164	1.171	2.341
93	5D	01011101	163	1.178	2.356
94	5E	01011110	162	1.185	2.370
95	5F	01011111	161	1.193	2.385
96	60	01100000	160	1.200	2.400
97	61	01100001	159	1.208	2.415
98	62	01100010	158	1.215	2.430
99	63	01100011	157	1.223	2.446
100	64	01100100	156	1.231	2.462
101	65	01100101	155	1.239	2.477
102	66	01100110	154	1.247	2.494
103	67	01100111	153	1.255	2.510
104	68	01101000	152	1.263	2.526
105	69	01101001	151	1.272	2.543
106	6A	01101010	150	1.280	2.560
107	6B	01101011	149	1.289	2.577
108	6C	01101100	148	1.297	2.595
109	6D	01101101	147	1.306	2.612
110	6E	01101110	146	1.315	2.630
111	6F	01101111	145	1.324	2.648
112	70	01110000	144	1.333	2.667
113	71	01110001	143	1.343	2.685
114	72	01110010	142	1.353	2.704
115	73	01110011	141	1.362	2.723
116	74	01110100	140	1.371	2.743
117	75	01110101	139	1.381	2.763
118	76	01110110	138	1.391	2.783
119	77	01110111	137	1.401	2.803
120	78	01111000	136	1.412	2.824
121	79	01111001	135	1.422	2.844
122	7A	01111010	134	1.433	2.866
123	7B	01111011	133	1.444	2.887
124	7C	01111100	132	1.455	2.909
125	7D	01111101	131	1.466	2.931

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)  
3-101

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 4

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
126	7E	01111110	130	1.477	2.954
127	7F	01111111	129	1.483	2.977
128	80	10000000	128	1.500	3.000
129	81	10000001	127	1.512	3.024
130	82	10000010	126	1.524	3.048
131	83	10000011	125	1.536	3.072
132	84	10000100	124	1.543	3.087
133	85	10000101	123	1.561	3.122
134	86	10000110	122	1.574	3.148
135	87	10000111	121	1.587	3.174
136	88	10001000	120	1.600	3.200
137	89	10001001	119	1.613	3.227
138	8A	10001010	118	1.627	3.254
139	8B	10001011	117	1.641	3.282
140	8C	10001100	116	1.655	3.310
141	8D	10001101	115	1.670	3.339
142	8E	10001110	114	1.684	3.368
143	8F	10001111	113	1.699	3.397
144	90	10010000	112	1.714	3.429
145	91	10010001	111	1.730	3.459
146	92	10010010	110	1.745	3.491
147	93	10010011	109	1.761	3.523
148	94	10010100	108	1.773	3.556
149	95	10010101	107	1.794	3.589
150	96	10010110	106	1.811	3.623
151	97	10010111	105	1.829	3.657
152	98	10011000	104	1.846	3.692
153	99	10011001	103	1.864	3.729
154	9A	10011010	102	1.882	3.765
155	9B	10011011	101	1.901	3.802
156	9C	10011100	100	1.920	3.840
157	9D	10011101	99	1.939	3.879
158	9E	10011110	98	1.959	3.918
159	9F	10011111	97	1.977	3.959
160	A0	10100000	96	2.000	4.000
161	A1	10100001	95	2.021	4.042
162	A2	10100010	94	2.043	4.085
163	A3	10100011	93	2.065	4.129
164	A4	10100100	92	2.087	4.174
165	A5	10100101	91	2.110	4.220
166	A6	10100110	90	2.133	4.267
167	A7	10100111	89	2.157	4.315

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)  
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PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 5

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
168	A8	10101000	88	2.162	4.364
169	A9	10101001	87	2.207	4.414
170	AA	10101010	86	2.233	4.465
171	AB	10101011	85	2.259	4.518
172	AC	10101100	84	2.286	4.571
173	AD	10101101	83	2.313	4.627
174	AE	10101110	82	2.341	4.683
175	AF	10101111	81	2.370	4.741
176	B0	10110000	80	2.400	4.800
177	B1	10110001	79	2.430	4.861
178	B2	10110010	78	2.462	4.923
179	B3	10110011	77	2.494	4.987
180	B4	10110100	76	2.526	5.053
181	B5	10110101	75	2.560	5.120
182	B6	10110110	74	2.595	5.189
183	B7	10110111	73	2.630	5.260
184	B8	10111000	72	2.667	5.333
185	B9	10111001	71	2.704	5.408
186	BA	10111010	70	2.743	5.486
187	BB	10111011	69	2.783	5.565
188	BC	10111100	68	2.824	5.647
189	BD	10111101	67	2.866	5.731
190	BE	10111110	66	2.909	5.818
191	BF	10111111	65	2.954	5.908
192	C0	11000000	64	3.000	6.000
193	C1	11000001	63	3.043	6.085
194	C2	11000010	62	3.087	6.174
195	C3	11000011	61	3.148	6.295
196	C4	11000100	60	3.200	6.400
197	C5	11000101	59	3.254	6.508
198	C6	11000110	58	3.310	6.621
199	C7	11000111	57	3.363	6.737
200	C8	11001000	56	3.429	6.857
201	C9	11001001	55	3.491	6.982
202	CA	11001010	54	3.556	7.111
203	CB	11001011	53	3.623	7.245
204	CC	11001100	52	3.692	7.385
205	CD	11001101	51	3.765	7.529
206	CE	11001110	50	3.840	7.680
207	CF	11001111	49	3.918	7.837
208	D0	11010000	48	4.000	8.000
209	D1	11010001	47	4.085	8.170

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHz

PAGE 6

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
210	02	11010010	45	4.174	8.348
211	03	11010011	45	4.267	8.533
212	04	11010100	44	4.364	8.727
213	05	11010101	43	4.465	8.930
214	06	11010110	42	4.571	9.143
215	07	11010111	41	4.683	9.366
216	08	11011000	40	4.800	9.600
217	09	11011001	39	4.923	9.846
218	0A	11011010	38	5.053	10.105
219	0B	11011011	37	5.189	10.377
220	0C	11011100	36	5.333	10.667
221	0D	11011101	35	5.483	10.971
222	0E	11011110	34	5.647	11.294
223	0F	11011111	33	5.818	11.636
224	10	11100000	32	6.000	12.000
225	11	11100001	31	6.194	12.387
226	12	11100010	30	6.400	12.800
227	13	11100011	29	6.621	13.241
228	14	11100100	28	6.857	13.714
229	15	11100101	27	7.111	14.222
230	16	11100110	26	7.385	14.769
231	17	11100111	25	7.680	15.360
232	18	11101000	24	8.000	16.000
233	19	11101001	23	8.348	16.696
234	1A	11101010	22	8.727	17.455
235	1B	11101011	21	9.143	18.286
236	1C	11101100	20	9.600	19.200
237	1D	11101101	19	10.105	20.211
238	1E	11101110	18	10.667	21.333
239	1F	11101111	17	11.294	22.588
240	20	11110000	16	12.000	24.000
241	21	11110001	15	12.800	25.600
242	22	11110010	14	13.714	27.429
243	23	11110011	13	14.769	29.538
244	24	11110100	12	16.000	32.000
245	25	11110101	11	17.455	34.909
246	26	11110110	10	19.200	38.400
247	27	11110111	9	21.333	42.667
248	28	11111000	8	24.000	48.000
249	29	11111001	7	27.429	54.857
250	2A	11111010	6	32.000	64.000
251	2B	11111011	5	38.400	76.800

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)

PROGRAMMABLE OSCILLATOR OUTPUT RATES

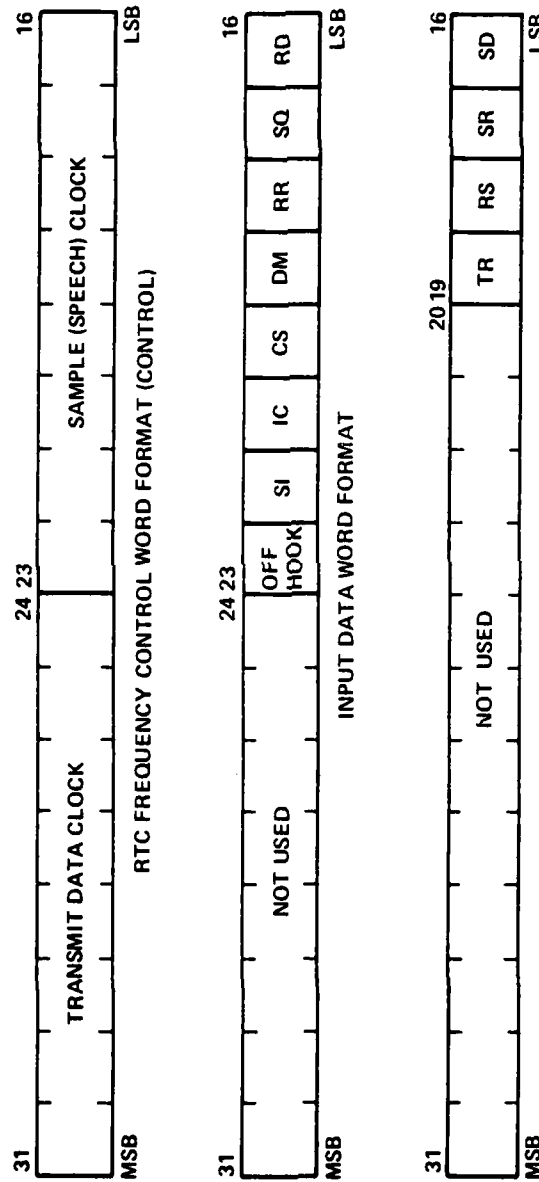
OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 7

DECIMAL	COUNTER SETTING		DIVIDE RATIO	OUTPUT RATE - KHZ	
	HEXADECIMAL	BINARY		LINE	SPEECH
252	FC	11111100	4	48.000	96.000
253	FD	11111101	3	64.000	128.000
254	FE	11111110	2	96.000	192.000
255	FF	11111111	1	192.000	384.000

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)





6434-80E (A)

FIGURE 3-31: FDI FORMATS

Data transfers between the MAP-300 and the modem take place via the IOS-2SM data bus. The data transfer process is interrupt driven, with timing based on the transmit data clock, for output transfers, and the modem receiver clock, for input transfers. The interrupts will, therefore, occur at the data rate, thereby allowing sufficient time for the IOS-2SM to acknowledge each interrupt and perform the appropriate data transfer. The interrupts are controlled by a two-phase clock such that simultaneous interrupts can never occur. Furthermore, the interrupts are mutually exclusive so that the IOS-2SM can receive only one interrupt at a time. Interrupt number 1 is used for receive data, and interrupt number 2 is used for transmit data.

Upon receiving interrupt number 1, the processor should perform an input data transfer, acknowledging the interrupt after the transfer is complete. The input data word will contain the current modem data sample (least significant bit), RS-449 interface status data (bits 17-22), and a hookswitch status bit (bit 23). The receive interrupts are timed by the modem receive data clock; hence, the maximum response time to input the data and acknowledge the interrupt is approximately one-half of a period at the modem data rate. (The one-half period results from the fact that the processor must also supply transmit data.)

Upon receiving interrupt number 2, the processor should perform an output data transfer, acknowledging the interrupt after the transfer is complete. The output data word must contain the next data bit in the least significant bit position, and the appropriate RS-449 interface control bits in bit positions 17-19. The transmit interrupts are timed by the transmit data clock; hence, the maximum response time to output the data and acknowledge the interrupt is approximately one-half of a

period at the transmit data rate. In order to insure the correct timing of the transmit data at the modem interface, the output data is double buffered. Hence, while the MAP-300 is transferring transmit data bit n, data bit n-1 appears at the modem interface.

The audio interface has been tested according to the attached table. These tests insure that all amplifiers and filters are performing to the designed specifications. The loop tests performed for both the handset and tape audio paths insure that the entire audio interface functions properly with the MAP-300, and that the overall loop gains and frequency response requirements are met.

The real time clock portion of the modified IOS-2SM scroll has been tested by programming several different line and speech sampling rates. A sufficient number of possible rates have been programmed to insure proper functioning of all RTC control bits. The modem interface has been tested by first outputting various control and data bit patterns from the MPA-300, and insuring that the correct bit patterns appear at the modem interface connector. Once the output had been determined to function properly, the modem input was tested by inputting various control and data bit patterns, looping through the MAP-300, and observing the bit patterns at the interface connector. A digital loop test was performed in which the MAP-300 output a digital data stream which was looped back at the modem interface connector and fed back into the processor where the resulting input was compared to the original output.

#### 3.4.2.3 Modifications on Map Analog I/O Hardware

Due to the fact that the Map Analog circuitry is located in a very noisy environment (with the switching power supplies and the digital

circuitry nearby), noise reduction techniques are very important in order to reduce the background noise in the presence of a signal. The modifications listed below were found necessary to reduce the background noise to  $\pm 1$  LSB of a 12 bit A/D converter operating at 5V full scale. The noise reduction techniques consisted of the reducing ground loops, separating ground and signal ground, and the reconfiguring the sample and hold amplifier with a differential input to reject common mode noise. It was observed that a good air circulation was required to reduce the terminal drift of the components in the analog circuitry.

#### List of Modifications on Map Analog I/O Hardware

##### 1. ADAM Board

- a. Added decoupling capacitors between  $\pm 15V$  and ground at the A/D, S/H, and MUX
- b. Cut ground etch to eliminate ground loops on analog circuit (around S/A and Hold capacitor)
- c. Added ground from A/D to S/H directly.
- d. Converted S/H to differential input. As a result, signal inputs are on Pin 28 (signal +) and Pin 29 (signal -) instead of Pin 34 and Pin 36. (Note: Only S/H designated as #4 on schematic has been modified.)

##### 2. AOM Board

- a. Cut ground etch from I/O pin 15 and 16 and added ground wire directly from D/A (pin 21) to I/O pin 15 and 16.
- b. D/A cable modified. Wires to connector P4 pin 7 (purple) and pin 8 (black) have been removed to separate I/O drawer ground from AOM board ground.

### 3.5 System Usage

This section describes the operating instructions for executing the GTE system and for generating it. The examples in this section will use MAP A. Changes required for MAPS B and C are noted when not apparent.

Three steps are necessary for running the ATC system once it has been generated. First, the MAP must be allocated and loaded with the required programs as follows:

```
>ALL MAPS
>
>RUN MAPA
PROJECT INPUT?
BINARY INPUT?GTE.BD
-- STOP
>
```

Second, the task must be installed as follows:

```
>INS ATCA
>
```

Third, the ATC task must be run, as follows:

```
>RUN ATCA
>
```

```
>>>> GTE 9600 RPS ATC SYSTEM <<<<<
```

```
START-UP(Y/N)? Y
(1) MAP OPENED AND BUFFERS DECLARED!
(2) PRE-BOUND FUNCTIONS CREATED!
(3) FUNCTION LISTS DEFINED!
(4) IOS-2 SETUP COMPLETE!
(5) ADM SETUP COMPLETE!
(6) ADAM SETUP COMPLETE!
```

```
PDP <=> MAP LINKAGE HAS BEEN SUSPENDED
      (DATE AND TIME)
TYPE "RPS ATCA" TO TERMINATE
```

The file LINE.PBJ must be available for proper execution. The task starts MAP A and then suspends itself. To halt the ATC system, the task must be resumed as follows:

```
RES ATCA
>
ATCA -- STOP
>
```

When resumed, the task closes the MAP and exits.

If a complete, two MAP system is desired, the process above must be repeated for the second MAP.

If the ATC system is to be started again, without additional loading of GTE.B0, the start-up procedure can be avoided by answering "N" to the "Start-up" question, as follows:

```
>RUN ATCA
>
```

```
>>>> GTE 9600 RPS ATC SYSTEM <<<<
```

```
START-UP(Y/N)? N
```

- (1) MAP OPENED AND BUFFERS DECLARED!
- (2) PRE-ROUND FUNCTIONS CREATED!
- (3) FUNCTION LISTS DEFINED!
- (4) IOS-2 SETUP COMPLETE!
- (5) AUM SETUP COMPLETE!
- (6) ADAM SETUP COMPLETE!

```
PDP <=> MAP LINKAGE HAS BEEN SUSPENDED
      (DATE AND TIME)
TYPE "RES ATCA" TO TERMINATE
```

### 3.5.1 System Generation

The ATC system generation consists of generating the MAP load module and of generating the ATCA, ATCB, and ATCC tasks.

#### 3.5.1.1 MAP Load Module Generation

The process of generating a MAP load module consists of four steps. For purposes of illustration, the array function VMOV1 will be used in the example to follow.

First, the source file, VMOV1.TXT must be reformatted using PREMAP, as follows:

```
>RUN PREMAP
INPUT FILENAME= VMOV1.TXT
OUTPUT FILENAME= VMOV1.MAP
NORMAL EXIT AFTER MANY LINES!

-- STOP
>
```

VMOV1.MAP is the reformatted source file, acceptable to the MAP assembler. Instructions for building the PREMAP tasks are included in Section 3.5.2.2.

Second, the reformatted source file must be assembled as follows:

```
>RUN MAPASM
SOURCE FILENAME?
VMOV1.MAP
OBJECT FILENAME?
VMOV1.PRJ
LISTING OUTPUT?
VMOV1.LST
0:
$1,9999

LINES WITH ERRORS:    0 (MAP VERSION 800101.10)  E=    0
>
```

Third, all of the PBJ modules must be combined into a single PBJ file. This can be accomplished by using the command file GTESYS.COM as follows:

```

@GTESYS
PIP *AF.PBJ:*/*DE
PIP GTE.PBJ:*/*DE
PIP GTE.HO:*/*DE
PIP *AP.PBJ=S300EX.PBJ,APDONE.PBJ,GTE300.PBJ,FF2K.PBJ,IUS.PBJ
PIP GTE.PBJ=*AP.PBJ,*MPDVM*.PBJ,*MPDCTM.PBJ,*MPDOPP.PBJ,*MPBOPP.PBJ
PIP GTE.PBJ=*GTE.PBJ,*MPMSGX.PBJ,*MPESTV.PBJ,*MPASKI.PBJ,*MPESSKI.PBJ
PIP GTE.PBJ=*GTE.PBJ,*MPESTG.PBJ,*MPESTD.PBJ,*MPHASP.PBJ,*MPSCAN.PBJ
PIP GTE.PBJ=*GTE.PBJ,*MPCDRA.PBJ,*MPIDCR.PBJ,*MPVDM*.PBJ,*GPATCH.PBJ
PIP GTE.PBJ=*GTE.PBJ,*VMOV1.PBJ,*VMOV2.PBJ,*VMOV3.PBJ,*CSPUIS.PBJ
PIP GTE.PBJ/PU
;
;      USE MACRO DUCH!
;      1<N(CR)FFFF(CR)S=1LSKSTS>SS
;
;      USE MACRO 23 TIMES!
;      23<N(FFFF)SOLS2KIS>SS
;
PFC GTE.PBJ
*1KN
FFFF
S=1LSKSTS>SS
10*  APDONE      AP PROCESS INTERRUPT HANDLER      MAY 28, 1980
23<N(FFFF)SOLS2KIS>SS
10*  SNAP-II  MAP-300 ARITH. MODULES - PROG #830101.03 MAY 7, 1980
10:FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS      18 AUG
10*  SNAP-II  IUS PACKAGE ---- JUNE 25, 1979 ---- PROGRAM # 840001.00
10'?FCH 241... "MPDCTM(Y,U,V,W)"?FORN TRUE DCT AND ITS REFLECTED MAG SQ
10'?FCH 242... "MPDOPP(Y,U,V)"?QUANTIZE/DEQUANTIZE SIDEBAND
10'?FCH 243... "MPDOPP(Y,U,V)"?DEQUANTIZE SIDEBAND
10'?FCH 244... "MPMSGX(Y,U,V,W)"?EXTRACT M,PG,MOVE X
10'?FCH 245... "MPESTV(U)"?CREATE PITCH AND VOCAL TRACT RESPONSE
10'?FCH 249... "MPASKI(U)"?SORT DCT1(.) COEFFICIENTS
10'?FCH 247... "MPESSKI(Y)"?SORT DCT1(.) COEFFICIENTS
10'?FCH 250... "MPESTG(Y,U,V,W)"?QUANTIZE DCT PARAMETERS
10'?FCH 251... "MPESTD(Y,U,V,W)"?DEQUANTIZE DCT PARAMETERS
10'?FCH 253... "MPHASP(Y,U,V,W)"?BASIS SPECTRUMCALCULATION
10'?FCH 254... "MPSCAN(Y,U,V)"?SCAN DCT BASIS SPECTRUM
10'?FCH 255... "MPCDRA(Y,U,V)"?COMPLETE DCT BIT ASSIGNMENT
10'?FCH 248... "MPIDCR(Y,U,V)"?MAKHOUZ IDCT CORRECTION
10'?FCH 252... "MPVDM(Y,U,V)"?VAR.,DC,INTERPOLATE,FIX
10      #L=S7AID
10'?FCH 237... "VMOV1(Y)"?MOVE BUNCH OF BUFFERS TO OTHER BUFFERS
10'?FCH 238... "VMOV2(Y)"?MOVE BUNCH OF BUFFERS TO OTHER BUFFERS
10'?FCH 239."VMOV3(Y)"?MOVE BUNCH OF BUFFERS TO OTHER
10'?PROG. CSPUSP.TXT
*
*EXSS
PIP GTE.PBJ/PU
*PI.
*CR -- TASK NOT IN SYSTEM
>

```



Note that once the command file has invoked TECO, the user must give the specified TECO commands. The PREMAP and assembly procedures must be repeated for each of the files used in GTESYS.CMD. If the file 5300EX.PBJ is unavailable, it must be obtained from the delivered tape, as assembly is not possible. In addition, the procedure must be performed on LINE.TXT if LINE.PBJ does not exist.

Fourth, the file GTE.PBJ must be transformed by the MAP Loader into a binary object file, as follows:

```
>RUN MAPL
OBJECT INPUT?GTE.PBJ
BINARY OUTPUT?GTE.BO
LOAD MAP? (Y OR N) N
    -- STOP 2
>
```

The file GTE.BO is then ready for use.

### 3.5.1.2 Task Generation

The command files ATCA.CMD and TBATCA.CMD are used to build the ATC task. When taskbuilding the ATC tasks, it is necessary, for reasons of space, to use the GTE-supplied RSX system library, F4PLIB.OLB. The existing system library, SYSLIB.OLB, should be renamed to something else, e.g., OSYSLIB.OLB, and F4PLIB.OLB should be renamed to SYSLIB.OLB. The file ATCA.ODL is used to specify the overlay structure. The contents of ATCA.CMD are:

```
PIV
PSE TBATCA
PIV
```

The contents of TBATCA are:

```
ATCA=ATCA/*P
TVLIS=10
ASG=TI:7
ASG=NI:5
ASG=NY:4
//
```

The contents of ATCA.ODL are:

```
.ROOT A=*(B,C,D,K,H,I,V,F,F,G,J,L,M,O,P,R,S,T,FO,H,UO,Y,Z,ZO,Z2,Z3)
A: .FCTR MASTER=MSMPA
A: .FCTR INIT
C: .FCTR USER
D: .FCTR MAPOR
K: .FCTR CREATE
V: .FCTR DEFDEF=10/18
I: .FCTR SETUP=(LOAD,IUSA=(0/18,ADA=10/18,ADAM=10/18)
M: .FCTR SYSTEM=10/18
E: .FCTR INPUT-TAPE2=(OPT1,OPT2,OPT3,OPT4,OPT5,OPT6,OPT7,OPT8A)
F: .FCTR DCVAR
G: .FCTR DCTF
J: .FCTR DDPARM
M: .FCTR PIFIPC
U: .FCTR VPS
O: .FCTR BASIS
P: .FCTR ASRT
R: .FCTR SPC1
S: .FCTR B11A
I: .FCTR DDCT
FO: .FCTR CHANL
U: .FCTR DPARM
UO: .FCTR SSET
Y: .FCTR DDCT
Z: .FCTR DCTF
ZO: .FCTR VARDC
Z2: .FCTR OUTPUT-TAPE2=(OPT1,OPT2,OPT3,OPT4,OPT5,OPT6,OPT7,OPT8A)
Z3: .FCTR SNR
.END
```

ATCA.ODL requires a particular host support library, NSNPA, which has the unused array functions deleted. The command file to create this library is AGTESN.COMD. Its contents are

```

TIM
;
;   CREATE SEPARATE VERSION OF "SNPLIB.OLB"
;
PIP NSNPA.OLB=SNALB.OLB
;
;   ADD ATC HSP ROUTINES
;
LBR NSNPA/IN=FF2RHSP
LBR NSNPA/IN=MPFDVM,MPDCTM,MPMAGX,MPQDPP,MPFSTV,MPRASP,MPASRT
LBR NSNPA/IN=MPSCAB,MPCDBA,MPFSTG,MPDQPP,MPSSRT,MPFSTG,MPDCH,MPVDVM
;
;   DELETE UNUSED "COMPLEX FCF" HSP ROUTINES
;
LBR SY:NSNPA/DE:CCVMB:CMINV:CPAL:CRECT:CSOCT:CSMA1:CSMA2:CVABD
LBR SY:NSNPA/DE:CVCAJ:CVMBL:CVRCP:CVSHR:CXMLR:CXMBL
;
;   DELETE UNUSED "ARITHMETIC FCF" HSP ROUTINES
;
LBR SY:NSNPA/DE:SDUT:SMAX:SMIN:SMVAH:SMXAB:SMYAB:SMYSJ:SAADD:SSUM
LBR NSNPA/IN=VMOV1,VMOV2,VMOV3
LBR SY:NSNPA/DE:DCVY:DDIFF:DFL22:DFINQ:DFRF
LBR NSNPA/DE:MYLD:SDIV:SMUL:SSUM:VSA1:VTRF
LBR SY:NSNPA/DE:FFLR3:FFTR:FFTL:FFTR:FFTR:FFTR:FFTR:FFTR:FFTR:FFTR
LBR SY:NSNPA/DE:VALGH:VALN:VHIST:VEN:VRAAP:VRAN1:VSA2:VSA3
LBR SY:NSNPA/DE:VMAX:VMIN:VMINS:VA:VAH:VAHSQ:VAD
LBR SY:NSNPA/DE:VALOG:VAL2:VCLIP:VCOMP:VDIV:VDV:VFIX8:VFETH
LBR SY:NSNPA/DE:VERCT:VFTI:VINPT:VITE:VLG16:VLIM:VLOG:VLOGH
LBR SY:NSNPA/DE:VL2:VMAG:VMG:VMGSQ:VMI:VMYAB:VMSQ:VMTHR:VMUL
LBR SY:NSNPA/DE:VMXAB:VNEG:VPOLY:VPOW:VRC:VRCP:VS:VSAD:VSB
LBR SY:NSNPA/DE:VSINT:VSMUL:VSO:VSQRT:VSSQ:VTAG:VXP
;
;   DELETE UNUSED "MANAGEMENT FCF" HSP ROUTINES
;
LBR SY:NSNPA/DE:MPCLM:MPCOL:MPNXC:MPNXR:MPROW:MPRSA:MPRSI:MPART
LBR NSNPA/DE:MPCLA:MPCRH:MPFTM:MPFOR:MPTLB
LBR NSNPA/DE:MPCSO:MPAPA:MPCEH:MPFSS:MPWIC:MPRRC:MPRLM:MPMUL
LBR NSNPA/DE:MPIAD:MPICS:MPIDV:MPIF:MPICL:MPIFR:MPIFX
LBR NSNPA/DE:MPIAD:MPIML:MPIRS:MPISS:MPITM:MPITE:MPITS:MPIWL:MPIWS
LBR NSNPA/DE:MPHFJ:MPHFD:MPDHR:MPFRA:MPRBS:MPSCR:MPSRB
LBR NSNPA/DE:MPISH:MPISB:MPABA:MPFVN:MPDOD:MPWTS
;
;   EXTRACT AS AN OBJECT FOR ROOT SEGMENT
;
LBR SY:NSNPA=NSNPA/FX
PIP NSNPA.OBJ/LI
PIP NSNPA.*/*
TIM

```

The library NSNPA is based on the library SNALB, which contains the GTE-delivered version of FCBGN, as well as SNAPHX, EAFHSP, MRTPCK, and MAADV.R.

If it is necessary to compile the Fortran files, three command files are provided for this purpose. GTEFTN.COMD compiles the host support modules for the GTE-written array functions. Its contents are:

```
F4P VMOV1,LP/LI:1=VMOV1/DE/NOTR
F4P VMOV2,LP/LI:1=VMOV2/DE/NOTR
F4P VMOV3,LP/LI:1=VMOV3/DE/NOTR
F4P MPFDVM,LP/LI:1=MPFDVM/DE/NOTR
F4P MPDCTN,LP/LI:1=MPDCTN/DE/NOTR
F4P MPGDPP,LP/LI:1=MPGDPP/DE/NOTR
F4P MPDQPP,LP/LI:1=MPDQPP/DE/NOTR
F4P MPVAGX,LP/LI:1=MPVAGX/DE/NOTR
F4P MPFSTV,LP/LI:1=MPFSTV/DE/NOTR
F4P MPSSRT,LP/LI:1=MPSSRT/DE/NOTR
F4P MPIDCM,LP/LI:1=MPIDCM/DE/NOTR
F4P MPASR1,LP/LI:1=MPASR1/DE/NOTR
F4P MPFSTU,LP/LI:1=MPFSTU/DE/NOTR
F4P MPFSTD,LP/LI:1=MPFSTD/DE/NOTR
F4P MPVDNM,LP/LI:1=MPVDNM/DE/NOTR
F4P MPBASP,LP/LI:1=MPBASP/DE/NOTR
F4P MPSCAN,LP/LI:1=MPSCAN/DE/NOTR
F4P MPCDBA,LP/LI:1=PCDBA/DE/NOTR
```

CFTN.COMD compiles most of the modules in the Fortran control program.

The delivered version is incorrect and must be modified to correspond to the following:

```
F4P MASTER=MASTER.ATC/NOTR
F4P INIT=INIT/DE/NOTR
F4P USER=USER/DE/NOTR
F4P MAPON=MAPON/DE/NOTR
F4P CREATE=CREATE/DE/NOTR
F4P DEFINE=DEFINE/DE/NOTR
F4P SETUP=SETUP/DE/NOTR
F4P LOAD=LOAD/NOTR
F4P IUS*=IUS*/NOTR
F4P ADM=ADM/NOTR
F4P ADA*=ADA*/NOTR
F4P SYSTEMB=SYSTEMB/DE/NOTR
F4P SYSTEMC=SYSTEMC/DE/NOTR
F4P SYSTEMA=SYSTEMA/DE/NOTR
F4P INPUT=INPUT/DE/NOTR
F4P DCVAR=DCVAR/DE/NOTR
F4P DCTF=DCTF/DE/NOTR
F4P PITLEPC=PITLEPC/DE/NOTR
F4P QDPARM=QDPARM/DE/NOTR
F4P VPBS=VPBS/DE/NOTR
F4P BASIS=BASIS/DE/NOTR
F4P ASPT=ASPT/DE/NOTR
F4P SUCT=SDCT/DE/NOTR
F4P BITA=BITA/DE/NOTR
F4P QDCT=QDCT/DE/NOTR
F4P CHANI=CHANI/DE/NOTR
F4P DPARM=DPARM/DE/NOTR
F4P SSRT=SSRT/DE/NOTR
F4P DDCT=DDCT/DE/NOTR
F4P DCTR=DCTR/DE/NOTR
F4P VANDC=VANDC/DE/NOTR
F4P OUTPUT=OUTPUT/DE/NOTR
F4P SWR=SWR/DE/NOTR
```

The command file TAPFTN.COMD compiles the remaining files needed by the Fortran control program. Its contents must be modified to conform to the following:

```
F4P TAPE2=TAPE2/NOTR
F4P OPT1=OPT1/NOTR
F4P OPT2=OPT2/NOTR
F4P OPT3=OPT3/NOTR
F4P OPT4=OPT4/NOTR
F4P OPT5=OPT5/NOTR
F4P OPT6=OPT6/NOTR
F4P OPT7=OPT7/NOTR
F4P OPT8=OPT8/NOTR
F4P OPT8A=OPT8A/NOTR
F4P OPT8B=OPT8B/NOTR
F4P OPT8C=OPT8C/NOTR
```

A command file for generating PREMAP is also provided. The contents of PREMAP.COMD are:

```
FOR SY:PREMAP,LP/LI:1=SY:PREMAP
TRK SY:PREMAP,LP=SY:PREMAP,FILE%
PIP PREMAP.*/*
```

### 3.6 System Listings

The ATC system listings which follow are divided into three sections: MAP Functions, Executive Programs, and Fortran Host Support, Control, and Support Programs.

#### 3.6.1 MAP Functions

The modules contained in this section are:

VMOV1  
VMOV2  
VMOV3  
MPFDVM  
MPDCTM  
MPQDPP  
MPDQPP  
MPMWGX  
MPFSTV  
MPSSRT  
MPIDCM  
MPASRT  
MPFSTQ  
MPFSTD  
MPVDNM  
GPATCH  
MPBASP  
MPSCAN  
MPCDBA  
LINE

FCH 237...VM0V1(Y)\* MOVE HUNCH OF BUFFERS TO OTHER BUFFERS  
ORIGINATED: 12-MAY-80  
UPDATED: 20-JUN-80

FCH 237...VM0V1(Y)\*

(00001) \*

(00002) \*

(00003) \*

(00004) \*

(00005) \*

(00006) \*

(00007) \*

(00008) \*

(00009) \*

(00010) \*

(00011) \*

(00012) \*

(00013) \*

(00014) \*

(00015) \*

(00016) \*

(00017) \*

(00018) \*

(00019) \*

(00020) \*

(00021) \*

(00022) \*

(00023) \*

(00024) \*

(00025) \*

(00026) \*

(00027) \*

(00028) \*

(00029) \*

(00030) \*

(00031) \*

(00032) \*

(00033) \*

(00034) \*

(00035) \*

(00036) \*

(00037) \*

(00038) \*

(00039) \*

(00040) \*

(00041) \*

TO

ADPH

AUTDCT

UTDCTI

AIRIT4

MIRIT3

NINM

DEFINE GLOBAL SYMBOLS

AFDTSUPG=SEPH

CSPUSNMS=S21FC

INMS=S794

MS=3

MSS=0

TSVTS=S0502

TSAS001=ISVTS+D'11'

TSAS106=ISVTS+D'106'

TSAS115=ISVTS+D'115'

OPRMS=11R16

OTDCTIS=1104H

OTDCTMS=2R6R

MIRIT4S=11560

MIRIT3S=11304

INPHS=36224

ADPHS=3532R

AUTDCTS=35342

AIRIT4S=3559R

NINMS=3124

START=25500

EXPAND ARRAY FUNCTION DISPATCH TABLE

#=AFDTSORC+1\*2\*(237-12R)

ADDR VHV1S(R7,1)

ADDR VMV1S(R7,1)

ADDR CSPUSNMS(,1,0)

EJECT







DUMMY FOR EXFC  
DUMMY

AOPH OUT

AOTDCT OUT

AIRIT4 OUT

12192 HALFWORDS

NINM OUT

OTDCT1 OUT

MIRIT4 OUT

SET XGD

```

A1F 063FF 3C500000 (00104) LOAD(RW1,101)
A1F 063F0 3F500000 (00105) LOAD(RW1,MSS)
A20 063F2 40500000 (00106) LOAD(RW1,MSS)
A21 063F4 42C24000 (00107) LOAD(RW0,AUPHS(1),TF)
A22 063F6 44500005 (00108) LOAD(RW1,5)
A23 063FH 46A00002 (00109) #1 ADD(RW0,2,TF)
A24 063FA 48112381 (00110) SUBL(RW1,1),JUMPP(#1)
A25 063FC 4AC28A0F (00111) LOAD(RW0,AOTDCTS(1),TF)
A26 063FE 4C50007F (00112) LOAD(RW1,126)
A27 06400 4F8A0002 (00113) #2 ADD(RW0,2,TF)
A28 06402 50112781 (00114) SUBL(RW1,1),JUMPP(#2)
A29 06404 52C2480F (00115) LOAD(RW0,AIRIT4S(1),TF)
A2A 06406 5450007F (00116) LOAD(RW1,126)
A2B 06408 56A00002 (00117) #4 ADD(RW0,2,TF)
A2C 0640A 58112781 (00118) SUBL(RW1,1),JUMPP(#4)
A2D 0640C 5AC40C3A (00119) LOAD(RW0,NINMS(2),TF)
A2E 0640E 5C500079 (00120) LOAD(RW1,121)
A2F 06410 5F8A0002 (00121) #6 ADD(RW0,2,TF)
A30 06412 60112F81 (00122) SUBL(RW1,1),JUMPP(#6)
A31 06414 62C32828 (00123) LOAD(RW0,OTDCTS(2),TF)
A32 06416 6450007F (00124) LOAD(RW1,126)
A33 06418 66A00002 (00125) #3 ADD(RW0,2,TF)
A34 0641A 68113381 (00126) SUBL(RW1,1),JUMPP(#3)
A35 0641C 6AC42D28 (00127) LOAD(RW0,MIRIT4S(2),TF)
A36 0641E 6C50007F (00128) LOAD(RW1,126)
A37 06420 6F8A0002 (00129) #5 ADD(RW0,2,TF)
A38 06422 70113781 (00130) SUBL(RW1,1),JUMPP(#5)
      (00131) *
      (00132) *
A39 06424 73420575 (00133) IMPSO LOAD(RW0,ISAS115(1),TF)
      (00134) *
A3A 06426 74200030 (00135) DNFSO CLEAR(M0)
A3B 06428 76000020 (00136) MUP(0)
      000043FF (00137) VMVISA=BC
      0642A (00138) FND BA=1
      (00139) *STOPPAGE HEDCK FOR CONSTRUCTED INSTRUCTION
      0642A 00000000 (00140) VMV1$ DATA 1F'0,0'
      0000007A (00141) VMVISZ=BI-VMV11$
      0642C (00142) FND
  
```

PAGE 5: ECH 237...VMVIVIVY" MOVE BUNCH OF BUFFERS TO OTHER BUFFERS

AFTSDPG: 00000 (00014) (00017)  
AINT4S: 00000 (00031) (00115)  
ADPRS: 00000 (00029) (00107)  
ADTCTS: 00000 (00040) (00111)  
CSPUSNDS: 02100 (00015) (00040)  
DMS: 00794 (00016)  
DNF31: 00010 (00100)  
DNF30: 00030 (00135)  
DNF31: 00010 (00098)  
DNF30: 00030 (00133)  
DNF31: 00010 (00028)  
DNF30: 00030 (00020) (00098)  
ISAS001: 00503 (00020) (00098)  
ISAS106: 00506 (00021)  
ISAS114: 00575 (00022) (00133)  
ISV78: 00502 (00019) (00020) (00021) (00022)  
MSS: 00000 (00018) (00105) (00106)  
MINT3S: 02028 (00027) (00094)  
MINT4S: 02028 (00026) (00082) (00127)  
MINTS: 00030 (00032) (00119)  
OPRMS: 02128 (00023) (00073)  
OTDCTS: 02028 (00024) (00077) (00173)  
OTDCTS: 00030 (00025) (00090)  
SCIPS: 00001 (00064) (00072)  
START: 06300 (00034) (00042)  
VMV1S: 06300 (00038) (00068)  
VMV1SA: 06300 (00067) (00137)  
VMV1S1: 06470 (00063) (00140)  
VMV1SSA: 00000 (00046) (00050) (00057)  
VMV1SSZ: 00000 (00047) (00057) (00058)  
VMV1S2: 00070 (00066) (00141)  
VMV1S: 06302 (00039) (00064) (00070) (00141)  
VMV1S: 00010 (00071) (00103)

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) E= 0

FCH 238... "VMOV2(Y)" MOVE HUNCH OF BUFFERS TO OTHER BUFFERS  
 ORIGINATED: 13-MAY-80  
 UPDATED: 20-JUL-80

```

(00001) * FCH 238... "VMOV2(Y)"
(00002) *
(00003) *
(00004) *
(00005) * MOVE FROM TO
(00006) * ARQPR ORPRM
(00007) * ARQCTM ORQCTM
(00008) * MIRIT1 MIRIT2
(00009) * TORPR1 TORPR2
(00010) * DCTIT1 DCTIT2
(00011) * VOUT OUTR
(00012) ** AND SCALAR PAIRS: UVAR,UDC
(00013) * VAR,DC UVAR,UDC
(00014) *
(00015) * DEFINE GLOBAL SYMBOLS
(00016) * AFDSUPG=SMR
(00017) * CSPUSNOS=S21FC
(00018) * DMS=S794
(00019) * TSVS=S502
(00020) * ISAS001=ISVTS+D*11'
(00021) * ISAS104=ISVTS+D*104'
(00022) * ISAS111=ISVTS+D*111'
(00023) * ISAS114=ISVTS+D*114'
(00024) * ISAS116=ISVTS+D*116'
(00025) * RM=3
(00026) * MSS=0
(00027) * SVTS=S03R2
(00028) * SAS100=SVTS+2*D*100'
(00029) * SAS101=SVTS+2*D*101'
(00030) * SAS102=SVTS+2*D*102'
(00031) * SAS103=SVTS+2*D*103'
(00032) * ARQPRS=3R5R2
(00033) * ARQCTS=3R5R6
(00034) * MIRIT1S=1053R
(00035) * TORPR1S=10024
(00036) * DCTIT1S=9000
(00037) * NOUTS=3R62
(00038) * QPRRMS=11R30
(00039) * UPDCTMS=11R44
(00040) * MIRIT2S=107R2
(00041) * TORPR2S=107R0
(00042) * DCTIT2S=9R12
(00043) * OUTRS=31R5R6
(00044) *
    
```

PAGE 2: FCR 23R...VMV2(Y)\* MOVE MUNCH OF BUFFERS TO OTHER BUFFERS

```
00006450 (00045)
(00046) *
000007C (00047) * EXPAND ARRAY FUNCTION DISPATCH TABLE
001F6452 (00048) #1=AFDISUPG+32*(23R-12R)
001F6464 (00049) ADDR VMV2S(R7,1)
001021FC (00050) ADDR VMV2IS(R7,1)
000511 (00051) ADDR CSPUSNDS(1,0)
00052 (00052) FUNCT
```

FCR 23R









PAGE 03 PCH 238...VM012(Y)\* MOVE RUNCH UP BUFFERS TO OTHER BUFFERS

064EC (00158) END

APTSDM: 00016 (00016) (00048)  
 ARDCTS: 096C4 (00033) (00087)  
 ANOPMS: 09616 (00032) (00083)  
 CSPHMS: 021FC (00017) (00051)  
 DCTTIS: 0232H (00036) (00099)  
 DCTTIS: 0252R (00042) (00138)  
 DMVS: 00794 (00018)  
 DMVSA: 00003 (00065)  
 DMVSI: 0001F (00117)  
 DMVSI: 00041 (00151)  
 INDRPIS: 02724 (00035) (00095)  
 INDRPIS: 0282H (00041) (00134)  
 ISAS001: 00503 (00020) (00109)  
 ISAS104: 0056A (00021)  
 ISAS111: 00571 (00022) (00149)  
 ISAS114: 00574 (00023) (00148)  
 ISAS116: 00576 (00024) (00149)  
 ISVTS: 00502 (00019) (00020)  
 MGS: 00000 (00026) (00117)  
 MIHTIS: 0242R (00034) (00091)  
 MIHTIS: 02A2R (00040) (00130)  
 MIVTIS: 00F16 (00037) (00103)  
 NOUTS: 09414 (00043) (00142)  
 OPICTS: 02F44 (00039) (00124)  
 OPPRMS: 02F36 (00038) (00120)  
 SAS100: 0044A (00028) (00146)  
 SAS101: 0044C (00029) (00147)  
 SAS102: 0041F (00030) (00107)  
 SAS103: 00450 (00031) (00108)  
 SCIRS: 00001 (00074) (00082)  
 START: 06450 (00045) (00053)  
 SVTS: 00382 (00027) (00028)  
 VMV2S: 06452 (00049) (00059)  
 VMV2SA: 0644A (00077) (00153)  
 VMV2SI: 064FA (00073) (00156)  
 VMV2SSA: 00000 (00057) (00067)  
 VMV2SSZ: 00005 (00058) (00067)  
 VMV2S7: 0008H (00076) (00157)  
 VMV2IS: 06464 (00050) (00074) (00080) (00157)  
 VMV2US: 00020 (00081) (00115)







```

A20 06556 40C4272H (00104) LOAD(HW0, IOMDRIS(2), TF)
A21 06558 4250007F (00105) LOAD(HW1, I26)
A22 0655A 44RA0002 (00106) #3 ADD(HW0, 2, TF)
A23 0655C 461122H1 (00107) SUB(HW1, 1), JUMP(#3)
A24 0655E 48C2044F (00108) LOAD(HW0, SAS102(1), TF)
A25 06560 4AC2045D (00109) LOAD(HW0, SAS101(1), TF)
A26 06562 4D420576 (00110) LOAD(HW0, ISAS11A(1), TF)
A27 06564 4F200030 (00112) CLEAR(R0)
A28 06566 50000020 (00113) NOP(0)
      00006540 (00114) VMV3SA=RC
      0656H (00115) FND #A-1
      0656H 00000000 (00116) *STORAGE HIJACK FOR CONSTRUCTED INSTRUCTION
      0656H 00000054 (00117) VMV3ST DATA 1F'0,0'
      0656A (00118) VMV3SZ=#1-VMV3IS
      (00119) FND

```

TIMDR1 OUT

SFT RG02 TO LFT CSPH CON  
TIME

AFDTSRG: 00854 (00014) (00038)  
 CSPUSHMS: 0215C (00015) (00041)  
 DCT118: 0232R (00031) (00100)  
 DMVS: 00704 (00016)  
 IURDRIS: 0272M (00032) (00104)  
 IURDRMS: 00000 (00029) (00082)  
 ISAS1: 00503 (00025) (00088)  
 ISAS11A: 00576 (00026) (00110)  
 ISVTS: 00502 (00024) (00025) (00026)  
 MSS: 00000 (00014) (00094) (00095)  
 MIRTHS: 0082R (00027)  
 MIRTHS: 0242R (00033) (00073)  
 MIRTHS: 04AA6 (00030) (00096)  
 SAS102: 0044F (00022) (00108)  
 SAS103: 00450 (00023) (00109)  
 SASAR: 00432 (00020) (00086)  
 SASRQ: 00434 (00021) (00087)  
 SCIFS: 00001 (00064) (00072)  
 START: 06500 (00035) (00043)  
 SVTS: 00382 (00019) (00020) (00021) (00022) (00023)  
 TPF4S: 00000 (00028) (00078)  
 VMV3S: 06502 (00039) (00049)  
 VMV3SA: 06540 (00067) (00114)  
 VMV3SI: 06568 (00063) (00117)  
 VMV3SSA: 00000 (00047) (00051) (00057)  
 VMV3SSZ: 00006 (00048) (00057) (00058)  
 VMV3SZ: 00054 (00066) (00118)  
 VMV3IS: 06516 (00040) (00064) (00070) (00118)  
 VMV3IS: 00014 (00071) (00092)



FLOJAT,DCRIAS,VAR,REFLECT INPUT VIA MAKHHOU, ALG.  
ORIGINATED:06-OCT-79  
UPDATED:10-JUL-80

```

(0000) * FCH 240... "MPEFVM(Y,U,V)"
(0002) *
(0003) *
(0004) * DEFINE GLOBAL SYMBOLS
(0005) *
(0006) *
(0007) *
(0008) *
(0009) *
(0010) *
(0011) *
(0012) *
(0013) *
(0014) *
(0015) *
(0016) *
(0017) *
(0018) *
(0019) *
(0020) *
(0021) *
(0022) *
(0023) *
(0024) *
(0025) *
(0026) *
(0027) *
(0028) *
(0029) *
(0030) *
(0031) *
(0032) *
(0033) *
(0034) *
(0035) *

```

INFULL

FCH 240

```

* EXPAND ARRAY FUNCTION DISPATCH TABLE
*
*1=AFDTSUMG+1*2*(240-128)
ADDR FDS(CH,1)
ADDR G200S(CH,1)
ADDR CSPUMSUS(+1,0)
EJECT

```







```

A45 OR6A4 02C00000 (00163)
A46 OR6A4 04400000 (00164)
A47 OR6A4 41140000 (00165)
A48 OR6A4 1F100000 (00166)
A49 OR6A4 04400000 (00167)
A4A OR6A4 04400000 (00168)
A4B OR6A4 2F000000 (00169) #2
A4C OR6A4 04400000 (00170)
A4D OR6A4 45000000 (00171)
A4E OR6A4 04400000 (00172)
A4F OR6A4 44400000 (00173)
A50 OR6A4 04400000 (00174)
A51 OR6A4 25000000 (00175)
A52 OR6A4 02000000 (00176)
A53 OR6A4 04400000 (00177)
A54 OR6A4 04400000 (00178)
A55 OR6A4 45000000 (00179)
A56 OR6A4 04400000 (00180)
A57 OR6A4 44400000 (00181)
A58 OR6A4 04400000 (00182)
A59 OR6A4 25000000 (00183)
A5A OR6A4 04400000 (00184)
A5B OR6A4 44000000 (00185)
A5C OR6A4 04400000 (00186)
A5D OR6A4 42000000 (00187)
A5E OR6A4 04400000 (00188)
A5F OR6A4 45600000 (00189)
A60 OR6A4 24400000 (00190)
A61 OR6A4 04400000 (00191)
A62 OR6A4 04400000 (00192)
A63 OR6A4 04400000 (00193)
A64 OR6A4 90140044 (00194)
A65 OR6A4 02000000 (00195)
A66 OR6A4 04400000 (00196)
A67 OR6A4 20372037 (00197)
(00198) #
(00199) # VAR=1.07VAR
A68 OR6A4 04400000 (00200)
A69 OR6A4 04400000 (00201)
A6A OR6A4 16401640 (00202)
A6B OR6A4 04400000 (00203)
A6C OR6A4 2F000000 (00204)
A6D OR6A4 04400000 (00205)
A6E OR6A4 44400000 (00206)

X(5)
A0=X(4,K)
A4=X(5),X(6,K)
A0=X(6,K),X(7,K)
M2=X(7,K)
A0=X(7,K)
X(8,K)
M4=X(8,K)
X(9,K)
A2=X(9,K)
X(10,K)
M2=X(10,K)
X(11,K)
GET X(7,K)
M4=X(11,K)
M2=X(7,K)
X(12,K)
A2=X(12,K)
X(13,K)
M2=X(13,K)
X(14,K)
M4=X(14,K)
X(15,K)
A2=X(15,K)
X(16,K)
M2=X(16,K)
X(1,K)
X(5)
A0=X(7,K)
M2=X(7,K)
A4=X(5)
DONE YET?
YES!
VAR=SQRT(SD)
RELEASE:APS INPUT

M4=SC=VAR
A0=SC=VAR
R=2.0;R=2.0
A6=2
R1=1/C+DEL(=FO)
M0=F0
P1=F0+C

```

```

A6F 086FE 08R10000 (00207)
A70 08700 49C00000 (00208)
A71 08702 08R00000 (00209)
A72 08704 84000000 (00210)
A73 08706 08A00000 (00211)
A74 08708 84300000 (00212)
A75 0870A 08A10000 (00213)
A76 0870C 49C00000 (00214)
A77 0870E 08R00000 (00215)
A78 08710 84000000 (00216)
A79 08712 08R00000 (00217)
A7A 08714 08R00000 (00218)
      (00219) *
      (00220) * MAPX(I)=MAPX(I)*VAR' I=0,1,2...LTH-1
      (00221) * ALSO DO MAKHOUU, REFLECTION V(K)=X(2K), V(N-K-1)=X(2K+1)
A7H 08716 08A004F (00222)
A7C 08718 08R00000 (00223)
A7D 0871A 000008F (00224)
A7E 0871C 84608460 (00225)
A7F 0871E 08R081C (00226)
A80 08720 081C08HC (00227)
A81 08722 9036007C (00228)
A82 08724 20372037 (00229)
A83 08726 00000000 (00230)
      (00231) *
      (00232) * VAR'=A1.0G10(VAR)
A84 08728 08F00000 (00233)
A85 0872A 08F00000 (00234)
A86 0872C 08F70000 (00235)
A87 0872E 08F00000 (00236)
A88 08730 08F20000 (00237)
A89 08732 08F00000 (00238)
A8A 08734 26400000 (00239)
A8B 08736 08F10000 (00240)
A8C 08738 26290000 (00241)
A8D 0873A 84830000 (00242)
A8E 0873C 08F20000 (00243)
A8F 0873E 45710000 (00244)
A90 08740 124C0000 (00245)
A91 08742 08R50000 (00246)
A92 08744 85R00000 (00247)
A93 08746 2A200000 (00248)
A94 08748 08F20000 (00249)
A95 0874A 0F310000 (00250)

MOV(P,A1)\NOP
SUR(A6,A1)\NOP
MOV(K,M4)\NOP
MUL(M0,M3)\NOP
MOV(P,M0)\NOP
MUL(M0,M6)\NOP
MOV(P,A1)\NOP
SUR(A6,A1)\NOP
MOV(K,M4)\NOP
MUL(M0,M4)\NOP
MOV(P,M0)\NOP
MAPX(I)=MAPX(I)*VAR' I=0,1,2...LTH-1
ALSO DO MAKHOUU, REFLECTION V(K)=X(2K), V(N-K-1)=X(2K+1)
VARI.S
MOV(P,M7)\MOV(P,X),M7)
NOP\MOV(T0A,M0)
MUL(M0,M7)
MOV(P,00)\MOV(ZERO,00)
MOV(ZERO,00)\MOV(P,00)
JUMP(C01,FWT)
CLEAR(M1)
NOP

M6=SA=A1.0G16(10,0)
M2=12H
A7=64
M7=CR
A2=X(0,K)
M5=X(0,K)
X(1,K)
A1=X(3,K)
M1=X(1,K),X(4,K)
A3=X(18,K-1),X(2,K)
A2=X(5,K)
A1=X(4,K),X(19,K-1)
M4=X(19,K-1),X(8,K)
A5=X(2,K)
M0=X(2,K),X(20,K-1)
M5=X(8,K),X(6,K)
A2=EC3
A1=X(6,K),X(9,K)

MOV(P,A1)\NOP
SUR(A6,A1)\NOP
MOV(K,M4)\NOP
MUL(M0,M3)\NOP
MOV(P,M0)\NOP
MUL(M0,M6)\NOP
MOV(P,A1)\NOP
SUR(A6,A1)\NOP
MOV(K,M4)\NOP
MUL(M0,M4)\NOP
MOV(P,M0)\NOP
MAPX(I)=MAPX(I)*VAR' I=0,1,2...LTH-1
ALSO DO MAKHOUU, REFLECTION V(K)=X(2K), V(N-K-1)=X(2K+1)
VARI.S
MOV(P,M7)\MOV(P,X),M7)
NOP\MOV(T0A,M0)
MUL(M0,M7)
MOV(P,00)\MOV(ZERO,00)
MOV(ZERO,00)\MOV(P,00)
JUMP(C01,FWT)
CLEAR(M1)
NOP

M6=SA=A1.0G16(10,0)
M2=12H
A7=64
M7=CR
A2=X(0,K)
M5=X(0,K)
X(1,K)
A1=X(3,K)
M1=X(1,K),X(4,K)
A3=X(18,K-1),X(2,K)
A2=X(5,K)
A1=X(4,K),X(19,K-1)
M4=X(19,K-1),X(8,K)
A5=X(2,K)
M0=X(2,K),X(20,K-1)
M5=X(8,K),X(6,K)
A2=EC3
A1=X(6,K),X(9,K)

```

```

A96 0R74C 44740000 (00251)
A97 0R74F 43510000 (00252)
A98 0R750 09F40000 (00253)
A99 0R75J 09F40000 (00254)
A9A 0R754 442C0000 (00255)
A9B 0R756 85920000 (00256)
A9C 0R759 4C310000 (00258)
A9D 0R75A 85330000 (00259)
A9E 0R75C 43110000 (00260)
A9F 0R75E 09F40000 (00261)
AA0 0R760 09F40000 (00262)
AA1 0R762 85900000 (00263)
AA2 0R764 44300000 (00264)
AA3 0R766 4F010000 (00265)
AA4 0R768 09F20000 (00266)
AA5 0R76A 84330000 (00267)
AA6 0R76C 43500000 (00268)
AA7 0R76F 09F40000 (00269)
AA8 0R770 09F40000 (00270)
AA9 0R772 85920000 (00271)
AAA 0R774 44300000 (00272)
AAB 0R776 02A10000 (00273)
AAC 0R77H 09F20000 (00274)
AAD 0R77A 84330000 (00275)
AAE 0R77C 43480000 (00276)
AAF 0R77E 09F40000 (00277)
AAG 0R780 46650000 (00278)
AAH 0R782 49090000 (00279)
AAI 0R784 84030000 (00280)
AAJ 0R786 09F50000 (00281)
AAK 0R788 44760000 (00282)
AAL 0R78A 09D20000 (00283)
AAM 0R78C 09F00000 (00284)
AAN 0R78E 264C0000 (00285)
AAO 0R790 841C0000 (00286)
AAP 0R792 90160088 (00287)
AAR 0R794 20372037 (00288)

```

```

MOV(A3),MUL(M0,M7)NOP
MOV(A1),ADD(A2,A3)NOP
MOV(10A,A0)NOP
MOV(10A,A4)NOP
MOV(M4),ADD(A1,A4)NOP
MOV(A2),MUL(M3,M4)NOP
MOV(A1),SUB(A1,A4)NOP
MOV(A3),MUL(M2,M5)NOP
MOV(A1),ADD(A0,A3)NOP
MOV(10A,A4)NOP
MOV(M4)NOP
MOV(A0),MUL(M3,M4)NOP
ADD(A2,A4)NOP
MOV(M5),SUB(A0,A7)NOP
MOV(10A,A2)NOP
MOV(A3),MUL(M0,M5)NOP
MOV(A0),ADD(A2,A3)NOP
MOV(10A,A4)NOP
MOV(M4)NOP
MOV(A2),MUL(M3,M4)NOP
ADD(A2,A4)NOP
MOV(M5),R(A5)NOP
MOV(10A,A2)NOP
MOV(A3),MUL(M0,M5)NOP
MOV(M3),ADD(A2,A3)NOP
MOV(10A,A4)NOP
MOV(A5),ADD(A5,A6)NOP
MOV(M1),SUB(A0,A1)NOP
MOV(A3),MUL(M1,M6)NOP
MOV(10A,A5)NOP
MOV(A6),ADD(A3,A4)NOP
MOV(10,A2)NOP
MOV(10A,M5)NOP
MOV(M4),RCH(A2)NOP
MOV(00),MUL(M0,M4)NOP
JUMP(R7,FWI)
CLEAR(M1)

```

```

A1=X(20,K-1),X(7,K)
A1=X(9,K),X(21,K-1)
A0=C7
A4=16**4
M4=X(21,K-1),CORRECT X(9,
K)
A2=X(7,K),X(22,K-1)
CORRECT X(9,K)
A1=X(22,K-1),X(10,K)
A1=X(9,K),X(23,K-1)
A4=C7
M4=X(23,K-1)
A0=X(10,K),X(24,K-1)
X(11,K)
M5=X(11,K),X(12,K)
A2=C1
A1=X(24,K-1),X(13,K)
A0=X(12,K),X(25,K-1)
A4=C6
M4=X(25,K-1)
A2=X(13,K),X(26,K-1)
X(15,K)
M5=X(15,K)
A2=C0
A1=X(26,K-1),X(16,K)
M1=X(7,K),X(27,K-1)
A4=C5
A5=X(27,K-1),X(28,K-1)
M1=X(28,K-1),X(14,K)
A3=X(16,K),X(29,K-1)
A5=C4
A6=X(14,K),X(17,K)
A2=X(0,K+1)
M5=X(0,K+1)
M4=X(17,K),X(1,K+1)
M0=X(29,K-1),X(18,K)
FOR I=1,2
RELEASEF APS INPUT

```

```

SA=DCHIAS
SH=1.0/VAR
SA'=20.0
SH'=VAR**

```

```

DCHIAS=VAR**
VAR'=20.0*VAR**
SNHMS MOV(10A,M0)NOP
MOV(10A,M7)NOP
NOP\MOV(10A,M0)
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PAGE 8: FCH 240... "MPEDM(Y,U,V)" FLDAT,DCRIAS,VAR,REFLECT INPUT VIA MAXHOUT. ALG.

```
APF 0RT9F 84K0R460 (00295)
AC0 0RTA0 08K0RRC (00296)
AC1 0RTA2 20172037 (00297) ?
AC2 0RTA4 081C0000 (00298) *
AC3 0RTA6 20120000 (00300) DNESA
AC4 0RTA8 00000000 (00301)
AC5 0RTAA 10000000 (00302)
AC6 0RTAC 20462046 (00303)
      00000007 (00304) ?
      00000007 (00306)
      00000007 (00307)
      00000007 (00308)

MOV(MU,M/)
MOV(P,00)
CLEAR(WI)
MOV(ZERO,00)\NOP
CLEAR(RA)\NOP
NOP
JUMP(0)
SPT(G2)

FDVSSZ=EA-FDVSSA
END FDVSSZ
EJECT

SA*SR:SA!*SR!
SC=SA*SK=DC/VAR;SC'=SA!*
SD'=20.0*ALOG10(VAR)
RELEASE APS INPUT

CLEAR INFULL.
APS DONE!

>>>> FOR TIMING PURPOSE
S ONLY!
```





```

(00353) *
(00354) *
A1D 0R7P0 3AC2861R (00355) ISORS1
A1E 0R7E2 3CMA0002 (00356)
A1F 0R7F4 3E8A0002 (00357)
A20 0R7F6 40C203F0 (00358)
A21 0R7E8 428A03F0 (00359)
A22 0R7FA 458A0001 (00360)
A23 0R7FC 46300037 (00361) *
(00362) *
A24 0R7FE 48090015 (00363)
A25 0R800 4A190017 (00364)
A26 0R802 4C8A0002 (00365) TVDVST
A27 0R804 4E192681 (00366)
A28 0R806 50300037 (00367)
A29 0R808 52000020 (00368) *
(00369) *
A2A 0R80A 54C20440 (00370) TLGGSS
A2B 0R80C 56C28600 (00371)
A2C 0R80E 588A0002 (00372)
A2D 0R810 5A8A0002 (00373)
A2E 0R812 5C600001 (00374)
(00375) ?
A2F 0R814 5E0203F0 (00376) #2
A30 0R816 615A03F0 (00377)
A31 0R818 625A03F0 (00378)
A32 0R81A 658A0001 (00379)
A33 0R81C 66C28606 (00380)
A34 0R81E 68700007 (00381)
A35 0R820 6A8A0002 (00382) #1
(00383) ?
A36 0R822 6C393581 (00384)
A37 0R824 6E292F81 (00385)
A38 0R826 70C20794 (00386)
A39 0R828 72300037 (00387)
A3A 0R82A 74000020 (00388) *
(00389) *
A3B 0R82C 76C203FA (00390) TNRMS5
A3C 0R82E 78C2043C (00391)
A3D 0R830 7AC2043E (00392)
A3E 0R832 7CC203F0 (00393)
A3F 0R834 7E300037 (00394)
(00395) *
(00396) *
LOAD(HR0,ISORS1(1),TF)
ADD(HR0,MS,TF)
ADD(HR0,MS,TF)
LOAD(HR0,SAS55(1),TF)
LOAD(HR0,SAS55(1),S)
ADD(HR0,MS,TH)
SFT(W1)
MOVH(HR0,HR2)
MIVH(HR1,HR3)
ADD(HR0,MS,TF)
SUMI(HR1,1),JUMPP(TVDVST)
SFT(W1)
NOP
LOAD(HR0,SAS95(1),TF)
LOAD(HR0,LOGS1(1),S,TF)
ADD(HR0,MS,TF)
ADD(HR0,MS,TF)
LOAD(HR2,HS)
LOAD(HR1,SAS55(1),TF)
LOAD(HR1,SAS55(1),S,TF)
LOAD(HR1,SAS55(1),S)
ADD(HR1,MS,TH)
LOAD(HR0,LOGS2(1),TF)
LOAD(HR3,7)
ADD(HR0,MS,TF)
SUMI(HR3,HS),JUMPP(#1)
SUMI(HR2,HS),JUMPP(#2)
LOAD(HR0,DMYS(1),TF)
SFT(W1)
NOP
LOAD(HR0,SAS52(1),TF)
LOAD(HR0,SAS93(1),TF)
LOAD(HR0,SAS94(1),TF)
LOAD(HR0,SAS55(1),TF)
SFT(W1)
IO=CONST=0.5
IO=CONST=16**7
IO=CONST=2.0
IO=VAR**2
IO=EXPONENT
STALL APS INPUT
MAPX(.) HA-2
MAPX(.) SIZE=-1
IO=MAPX(1)
FOR I=0,1,2,...,LTH-1
STALL APS INPUT
IO=ALOG16(10.0)
CONST=128.
CONST=64.
CONSTANT=CR
? INPUT LOOPS FOR API(CR
P!!)
VAR WHOLE WORD
VAR HR
VAR ER
CONSTANT=C3
R REMAINING CONSTANTS
C2,16**4,C7,C1,C6,C0,C5,
C4
FOR CONSTNT=1,2,...,8
FOR LOOP=1,2
LAST INPUT THROWN AWAY
STALL APS INPUT
IO=DC
IO=1.0/VAR
IO=20.0
IO=ALOG10(VAR)
STALL APS INPUT

```

A40	08836	80200031	(00397)	DNF\$1	CLPAR(R1)	APS INPUT DONE!
A41	08836	82000020	(00398)	NDP		
			(00399)	*		
A42	08834	84300012	(00400)	G200S0	SETR(A)	ENARL APU
A43	0883C	8742057F	(00401)		FLOAD(HW0,ISAS175(1),TF)	00=CALLIN CARD=0
A44	0883F	88701080	(00402)	0FLTS	LOAD(RW3,11)	DUMMY OP FOR EXEC
A45	08840	8A500000	(00403)		LOAD(RW1,MSS)	NIN(.) SIZE=1
A46	08842	8C320000	(00404)		SUR(HW3,MSS)	DUMMY OP FOR EXEC
A47	08844	8E400006	(00405)		LOAD(HW0,10)	MAPX(.) RA
A48	08846	90700000	(00406)		LOAD(RW3,MSS)	MAPX(.) SIZE=1
A49	08848	92020000	(00407)		SUR(HW0,MSS)	MAPX(.) RA=2
A4A	0884A	94210010	(00408)		MOVH(RW2,HW0)	SAVE MAPX(.) RA=2
A4B	0884C	960A0014	(00409)		ADD(RW0,NP2S)	MAPX(.) RA=2+2*NP
A4C	0884F	988A0002	(00410)	0FLTSJ	ADD(HW0,WS,TF)	00=MAPX(J)
A4D	08850	9AA00002	(00411)		ADD(HW0,WS,TF)	00=MAPX(J+1)
A4E	08852	9C114CH2	(00412)		SURL(HW1,2),JUMPP(0FLTSJ)	FOR J=NP,NP+1,....LTH=1
			(00413)	*		
A4F	08854	9FC203FA	(00414)	0SUMSS	LOAD(HW0,SAS52(1),TF)	00=DCRIAS
			(00415)	*		
A50	08856	A0010014	(00416)	0SURSD	MOVH(RW0,HW2)	SAVE MAPX(.) RA=2
A51	08858	A2110016	(00417)		MOVH(RW1,HW1)	SAVE MAPX(.) SIZE=1
A52	0885A	A4AA0002	(00418)	0SURSJ	ADD(HW0,WS,TF)	00=MAPX(J)
A53	0885C	A6AA0002	(00419)		ADD(HW0,WS,TF)	00=MAPX(J+1)
A54	0885F	A8115282	(00420)		SURL(RW1,2),JUMPP(0SURSJ)	FOR J=0,1,2,....LTH=1
			(00421)	*		
A55	08860	AAC203F0	(00422)	0VS0SS	LOAD(HW0,SAS55(1),TF)	00=VAR**2
			(00423)	*		
A56	08862	ACC203F0	(00424)	0SURSS	LOAD(HW0,SAS55(1),TF)	00=SQRT(SD)=VAR
			(00425)	*		
A57	08864	AFC2043C	(00426)	01NVSS	LOAD(HW0,SAS53(1),TF)	00=1.0/VAR
			(00427)	*		
A58	08866	B0402022	(00428)	0DIVSD	LOAD(HW0,121)	REFLECTION BASE
A59	08868	B2500000	(00429)		LOAD(RW1,MSS)	REFLN SIZE=1
A5A	0886A	B4600000	(00430)		LOAD(RW2,MSS)	DUMMY
A5B	0886C	B6710010	(00431)		MOVH(RW2,HW0)	(N-1)*2
A5C	0886F	B811002A	(00432)		ADDR(RW1,HW1)	(N-1)*4 + REFLN BASE =XR
A5D	08870	BA21002A	(00433)		ADDR(RW2,HW1)	(N-1)
A5E	08872	BC21002A	(00434)		ADDR(RW2,HW1)	XI(-1)
			(00435)	*		XI(N)
A5F	08874	BE010032	(00436)		SURL(HW0,2)	XR(K)
A60	08876	C021003C	(00437)		ADDR(HW2,4)	XI(K)
A61	08878	C28A0002	(00438)	0PFLSJ	ADD(HW0,2,TF)	XI(N-K-1)
A62	0887A	C48A0002	(00439)		ADD(HW0,2,TF)	
A63	0887C	C6A20002	(00440)		SUR(HW2,2,TF)	

PAGE 12: PCH 240... "MPF00VM(V,U,V)" FLOAT,DCHEAS,VAR,REFLECT INPUT VIA MAKH000L AGG.

```
A64 00M7E CMA20007 (00441) SUBR(MW2,2,TF)
A65 00M80 CALLA1R4 (00442) SUBR,(M1,4),,MMP(ORF1,SJ)
A66 00M82 CCC20794 (00443) * LOAD(MW0,DWYS(1),TF)
A67 00M84 CFC203F0 (00445) * LOAD(MW0,SAS55(1),TF)
A68 00M86 D0C203FA (00447) DMRWSS LOAD(MW0,SAS52(1),TF)
A69 00M88 D2C203F0 (00448) * LOAD(MW0,SAS55(1),TF)
A6A 00M8A D5420570 (00449) * LOAD(MW0,TSAS110(1),TF)
A6B 00M8C D6200030 (00451) * CLEAR(M0)
A6C 00M8E D8000020 (00453) * MDP
      00008466 (00455) * G200SA=BC
      00008467 (00456) *
      00008468 (00457) * END BA-1
      00008469 (00458) * STORAGE BLKCK FOR CONSTRUCTED INSTRUCTIONS
      00008470 (00459) * G200ST DATA 6F'0.0'
      ...
      00008476 (00460) * G200SZ=BT-G200S
      00008477 (00461) * END
```

XR(N-K-1)  
FOR K=0,1,...,N/2-1.

00=7  
00=ALOG10(VAR)

00=DC/VAR  
00=20.0\*ALOG10(VAR)  
CLEAR INFULL.

APS OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN  
CHOR

APTSDMG: 0000H (00007) (00031)  
 CSPIUSMUS: 0214C (00008) (00034)  
 DMS: 00744 (00000) (00386) (00443)  
 DMPSA: 00007 (00300)  
 DMPSI: 00040 (00347)  
 DMFSU: 0006H (00452)  
 FIVS: 0K620 (00032) (00081)  
 FIVSSA: 00000 (00079) (00083) (00306)  
 FIVSSZ: 00007 (00080) (00306) (00307)  
 FLSM1: 00006 (00091) (00094)  
 FLSM2: 0000F (00098) (00101)  
 FLSM: 00000 (00099) (00107)  
 G200S: 0K7H4 (00033) (00315) (00371) (00460)  
 G200SA: 0K466 (00318) (00455)  
 G200S1: 0K490 (00313) (00459)  
 G200S0: 00042 (00322) (00400)  
 G200S7: 000F6 (00317) (00460) (00360) (00374) (00379) (00384) (00385)  
 H8: 00001 (00011) (00339)  
 IFL1S: 00002 (00315) (00374)  
 IFL1J: 00010 (00339) (00340)  
 ILUGS1: 0K600 (00043) (00371)  
 ILUGS2: 0K606 (00049) (00380)  
 ILUGSS: 0002A (00370)  
 INHMS5: 0003R (00390)  
 ISAS110: 00570 (00016) (00449)  
 ISAS125: 0057F (00015) (00401)  
 ISORS1: 0K61H (00068) (00355)  
 ISUMS1: 00010 (00355)  
 ISUMS1: 00019 (00349) (00350)  
 ISUMSS: 00014 (00344)  
 ISVTS: 00502 (00014) (00015) (00016)  
 IUPS: 0000C (00315) (00336)  
 IVDV51: 00026 (00365) (00366)  
 MSS: 00000 (00012) (00327) (00328) (00330) (00331) (00345) (00346) (00403) (00404) (00406)  
 MP2S: 00014 (00013) (00409)  
 NDIVS0: 0005H (00428)  
 OFL1S: 00044 (00407)  
 OFL1S1: 0004C (00410) (00417)  
 OFNVSS: 00057 (00426)  
 OLOGSD: 00066 (00444)  
 ONRMS5: 0006R (00447)  
 OPL1S1: 00061 (00438) (00447)  
 OSORS5: 00056 (00424)

OSUMSD:	00050	(00416)			
OSUMSJ:	00052	(0041R)	(00420)		
OSUMSS:	0004F	(00414)			
OVSUSS:	00054	(00422)			
SAS30:	00300	(0001H)	(00324)		
SAS52:	003FA	(00019)	(00390)	(00414)	(00447)
SAS54:	003F4	(00020)	(00325)		
SAS55:	003F0	(00021)	(00354)	(00377)	(0037R)
		(0044H)		(00393)	(00427)
				(00424)	(00445)
SAS93:	0043C	(00022)	(00391)	(00426)	
SAS94:	0043F	(00023)	(00392)		
SAS95:	00440	(00024)	(00370)		
SINVS:	0006R	(00200)			
SINDCS:	00064	(00233)			
SNPMS:	0006R	(00291)			
SOI:	00024	(00126)	(00135)		
SOZ:	00025	(00125)	(00127)		
SSORTS:	00019	(00151)			
START:	00000	(00025)	(00030)		
SVTS:	003M2	(00017)	(0001R)	(00019)	(00020)
VMIJLS:	0007H	(00222)		(00021)	(00023)
VSUS:	00071	(00124)			
WS:	00002	(00026)	(00340)	(00356)	(00357)
		(0041H)	(00419)	(00372)	(00373)
				(00382)	(00410)
					(00411)
WWS:	00004	(00027)			
ZS:	00003	(0002R)			







```

A19 08934 08F005F8 (00073) ?
A1A 08936 08F008F9 (00074)
A1B 08938 08920M92 (00075)
A1C 0893A 43533353 (00076)
A1D 0893C 08F008F8 (00077)
A1E 08940 08F008F8 (00079)
A20 08942 08F008F8 (00080)
A21 08944 85408540 (00081)
A22 08946 089C089C (00082)
A23 08948 089C089C (00083)
A24 0894A 0000089C (00084)
A25 0894C 089C0000 (00085)
A26 0894E 089C089C (00086)
A27 08950 0000089C (00087)
A28 08952 089C0000 (00088)
A29 08954 901D0015 (00089)
A2A 08956 20127037 (00090)
A2B 08958 00000000 (00091)
A2C 0895A 10000000 (00092)
      (00093) *
      00000020 (00094)
      0895C (00095)
      (00096)

      MOV(10A,M0)
      MOV(10A,M1)
      MOV(R,A2)
      MOV(A3),AND(A2,A3)
      MOV(10A,M4)
      MOV(R,M2)
      MOV(R,M6)
      MUL(M2,M6)
      MOV(R,M0)
      NOP^MOV(P,00)
      MOV(ZERO,00)^NOP
      MOV(P,00)^MOV(ZERO,00)
      NOP^MOV(P,00)
      MOV(ZERO,00)^NOP
      JMP(C,DCTIP,F1)
      CLFAR(RA)
      NOP
      JMP(0)

      DCTMSSZ=8A-DCTMSSA
      END DCTMSS7
      EJECT
  
```

\*VI  
 SIN(K+1)  
 GET FACTOR (OF 2 IN THERE)  
 VI(K+1)  
 PREPARE TO SQUARE  
 SQUARE REAL IMAG PARTS  
 D(K)\D(N-K)  
 D(K)^2=MR(K)\MI(K)  
 MR(N-K)  
 MI(N-K)  
 MR(2N-K)\MI(2N-K)  
 MI(N+K)



(00141) \* MI(N-K),MH(2N-K),MI(2N-K),MH(N+K),MI(N+K),FD,  
 (00142) \* K=(N/2-1,N/2-2,...,1) N=256  
 (00143) \*

A19 0R996 3200032	DCTMSD	SFI(PI)	D(0)
A1A 0R998 340002A		LOAD(HW0,101)	N-1
A1B 0R99A 3600000		LOAD(HW1,MSS)	DUMMY
A1C 0R99C 3800000		LOAD(HW2,MSS)	MRC(0)
A1D 0R99E 3A001006		LOAD(HW2,111)	DUMMY
A1E 0R99D 3C700000		LOAD(HW3,MSS)	D(0)
A1F 0R9A2 3E700000		LOAD(HW3,MSS)	2*N/2
A20 0R9A4 40810010		MOVH(HW0,HW0,TF)	D(N/2)
A21 0R9A6 42110039		ADDL(HW1,1)	2*N/2+MS (=2*K)
A22 0R9A8 4411002A		ADPH(HW0,HW1,TF)	2N (=4*N/2)
A23 0R9AA 4611002A		ADPH(HW1,HW1)	4*2*N/2 (2*K=2*N/2)
A24 0R9AC 48110012		MOVH(HW3,HW3)	D(-N/2)
A25 0R9AF 4A11002E		ADPH(HW3,HW3)	2*(N/2-1)*MS (=2*K(NEW))
A26 0R9H0 4C010022		SURH(HW0,2)	MRC(0)
A27 0R9H2 4E01003A		ADDL(HW1,4)	MICO
A28 0R9H4 50110034		SURL(HW1,4)	MRC(N/2)
A29 0R9H6 52A10018		MOVH(HW2,HW2,TF)	MI(N/2)
A2A 0R9H8 54AA0002		ADD(HW2,2,TF)	MRC(N)
A2B 0R9HA 56AA01FE		ADPH(HW2,2,TF)	MI(N)
A2C 0R9HC 58AA0002		ADD(HW2,2,TF)	MRC(3N/2)
A2D 0R9HE 5AAA01FE		ADPH(HW2,2,TF)	MI(3N/2)
A2E 0R9HO 5CAA0002		ADD(HW2,2,TF)	4*2*(N/2-1)-1 (-1 FOR L0
A2F 0R9H2 5EAA01FE		ADPH(HW2,2,TF)	OP TEST UPDATE)
A30 0R9C4 60AA0002		ADD(HW2,2,TF)	RESTORE FROM LOOP TEST
A31 0R9C6 62320009		SUR(HW3,9)	D(K)
A32 0R9C8 64310030		ADDL(HW3,1)	D(N+K)
A33 0R9CA 66H1002A		ADPH(HW0,HW1,TF)	D(N-K)
A34 0R9CC 680A0200		ADD(HW0,2*256)	D(-K+1)
A35 0R9CF 6A410022		SURH(HW0,HW1,TF)	MRC(K)
A36 0R9D0 6C0201FE		SUR(HW0,2*256+2)	MRC(N+K)
A37 0R9D2 6E420406		SUR(HW2,4*256+6,TF)	MRC(N-K)
A38 0R9D4 70AA0002		ADD(HW2,2,TF)	MI(2N-K)
A39 0R9D6 722A03FE		ADPH(HW2,4*256+2)	MRC(2N-K)
A3A 0R9D8 74A10026		SURH(HW2,HW3,TF)	MI(2N-K)
A3B 0R9DA 76AA0002		ADD(HW2,2,TF)	MRC(N-K)
A3C 0R9DC 78AA03FE		ADPH(HW2,4*256+2,TF)	MRC(N+K)
A3D 0R9DE 7AAA0002		SUR(HW2,4*256+2)	MI(N+K)
A3E 0R9E0 7C720402		ADPH(HW2,HW3,TF)	
A3F 0R9E2 7EA1002E		ADD(HW2,2,TF)	
A40 0R9E4 80AA0002			

PAGE 6: PCH 241... "MPDCTREY,U,V,W" FIRM TRUE DCT AND ITS REFLECTED MAG SQUARED, VIA

A41 0R9F6 R2110034 (00185) SUR1(RW1,4) 2K GFIS 2(K-W8)  
A42 0R9F8 R4320004 (00186) SUR(RW3,R) 2K GFIS 2(K-CS) (COMPLEX  
A43 0R9FA R+313201 (00187) SUR1(RW3,1),JUMPP(81) 2K GFIS 2K-1 SO TEST PAI  
(00188) ? I.S FOR 0

A44 0R9FC R+200030 (00189) CLEAR(RO) 2K GFIS 2(K-W8)  
A45 0R9FF R+000070 (00190) MOP(0) 2K GFIS 2(K-CS) (COMPLEX

0000R99F (00191) \* 2K GFIS 2K-1 SO TEST PAI  
0R9FO 0000R99F (00192) DCTMSA=RC 2K GFIS 2(K-W8)

0R9FO 00000000 (00193) FND #A-1 2K GFIS 2(K-CS) (COMPLEX  
0R9FO 00000000 (00194) DCTMSI DATA 4F'0.0' 2K GFIS 2K-1 SO TEST PAI

... 00000094 (00195) DCTMSZ=RT-DCTMSS 2K GFIS 2(K-W8)  
0R9FR 00000094 (00196) FND 2K GFIS 2(K-CS) (COMPLEX

PAGE 7: FCH 241... "MPCDTM(Y,U,V,W)" FROM TRUF DCT AND ITS REFLECTED MAG SQUARED, VIA

AFDTSING:	0000R (0000A) (00020)
CSPUSNIS:	021FC (00007) (00023)
DCTLP:	00015 (00064) (00009)
DCTMS:	00002 (00021) (00043)
DCTMSA:	0000F (00107) (00142)
DCTMSI:	00000 (00103) (00194)
DCTMSO:	00019 (00111) (00144)
DCTMSS:	00064 (00022) (00104)
DCTMSSA:	00000 (00041) (00046) (00094)
DCTMSSZ:	00020 (00042) (00094) (00095)
DCTMSZ:	00004 (00106) (00195)
DMS:	00794 (00008) (00134)
M6:	00001 (00010)
M55:	00000 (00011) (00114) (00115) (00117) (00118) (00146) (00147) (00149) (00150)
MP2S:	00014 (00012)
START:	00000 (00014) (00025)
SVTS:	00002 (00013)
W5:	00002 (00015)
WMS:	00004 (00016)
Z5:	00003 (00017)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) E- 0

QUANTIZE/DEQUANTIZE SIDEHAND  
W/ K->A TRANSFORM  
ORIGINATED:04-AUG-79  
UPDATED:12-MAY-80

FCH 242... "MPDPPP(V,U,V)"

DEFINE GLOBAL SYMBOLS

```

(00001) * FCH 242... "MPDPPP(V,U,V)"
(00002) *
(00003) *
(00004) *
(00005) *
0000017 (00006)
0000018 (00007)
0001F00 (00008)
0000210 (00009)
0000009 (00010)
0000003 (00011)
0000001 (00012)
0000000 (00013)
0000000 (00014)
0000000 (00015)
0000000 (00016)
0000213 (00017)
0000214 (00018)
0000214 (00019)
0000214 (00020)
0000214 (00021)
0000220 (00022)
0000221 (00023)
0000221 (00024)
0000224 (00025)
0000224 (00026)
0000224 (00027)
0000032 (00028)
0000023 (00029)
0000007 (00030)
0000228 (00031)
0000228 (00032)
0000038 (00033)
0000038 (00034)
0000030 (00035)
0000030 (00036)
0000030 (00037)
0000030 (00038)
0000030 (00039)
0000030 (00040)
0000032 (00041)
0000032 (00042)
0000032 (00043)
0000032 (00044)

```

```

A1S=D'1H'
AFDTSORC=SRFH
AFSSMF=SFCCO
CSPUSMS=S21FC
PCPUS=D'1Q'
#M=3
HS=1
MS=0
#SS=0
MPOS=D'10'
PCPUS=D'17'
PDFOS1=S2134
PDFOS2=S2174
PDFOS3=S21H4
PDFOS4=S21D4
PDFOS5=S21F4
PDFOS6=S2204
PDFOS7=S2214
PDFOSR=S221C
DCSDFQ=S2224
VAPSDUQ=S2244
PCSDUQ=S22H4
MSDFQ=S228C
SVTS=S0382
PAPCS=D'35'
PSZFS=D'8'-D'1'
OPRMS=D'11R16'
SAS11=SVTS+2*D'11'
SAS38=SVTS+2*D'38'
SAS39=SVTS+2*D'39'
SAS52=SVTS+2*D'52'
SAS55=SVTS+2*D'55'
SAS60=SVTS+2*D'60'
SAS61=SVTS+2*D'61'
SAS62=SVTS+2*D'62'
SASRR=SVTS+2*D'RR'
SAS90=SVTS+2*D'H9'
SAS91=SVTS+2*D'91'

```

PAGE 2: FCH 242... "MPDPP(Y,U,V)" QUANTIZE/DEQUANTIZE SIDERAND

```
0000043A (00045) SAS92=SVTS+2*0'92'  
00000452 (00046) SAS104=SVTS+2*0'104'  
0000045F0 (00047) START=S65F0  
0000090A (00048) VPDS=0'11'  
0000000R (00049) VAPPOS=0'R'  
00000002 (00050) NS=2  
00000003 (00051) ZS=3  
00000052 (00052) *  
00000094 (00053) * EXPAND ARRAY FUNCTION DISPATCH TABLE  
001F65F2 (00054) #I=AFDTSORG+1*2*(242-128)  
001F65F2 (00055) ADDR QUANS(R7,1)  
001F65F2 (00056) ADDR G105S(R7,1)  
001021FC (00057) ADDR CSPHSNOS(1,0)  
0005R (00058) EJECT
```

FCR 242





01FR2 2R7ARD40					
01FR4 352C7KCD					
01FR6 41254640					
01FR8 48885KCU (00079)	DATA	0.59019E+00,	0.65272E+00,	0.70886E+00,	0.76395E+00
01FMA 538C5440					
01FMC 5A88FCC0					
01FME 61C41040					
01FQ0 68C0CR40 (00080)	DATA	0.81878E+00,	0.87625E+00,	0.94152E+00,	0.10123E+01
01FQ2 7028F5C0					
01FQ4 78M3H480					
01FQ6 881430C1					
01FQ8 98AD7741 (00081)	DATA	0.10847E+01,	0.11620E+01,	0.12381E+01,	0.13059E+01
01FQ0 044RC6C1					
01FQ2 04F7A0C1					
01FQ4 0A1278C1					
01FA0 0AF4A41 (00082)	DATA	0.13727E+01,	0.14357E+01,	0.14920E+01,	0.15468E+01
01FA2 0B7C5031					
01FA4 0E4490C1					
01FA6 0C5F09C1					
01FAR 0C004841 (00083)	DATA	0.16017E+01,	0.16527E+01,	0.16951E+01,	0.17323E+01
01FAA 0D38A4C1					
01FAC 0D9F00C1					
01FAE 0D08C041					
01FR0 0E257A41 (00084)	DATA	0.17683E+01,	0.18035E+01,	0.18365E+01,	0.18668E+01
01FR2 0E6D9141					
01FM4 0E4126C1					
01FM6 0E4F34C1					
01FMR 0E243641 (00085)	DATA	0.18961E+01,	0.19267E+01,	0.19616E+01,	0.10000E+21
01FMA 0E64F1C1					
01FMC 0E815441					
01FRE 245F34D1					
(00086) *					
(00087) * PARCOR(3) W/ 4 HITS					
(00088) PVFCS1 DATA		0.31528E+00,	0.45224E+00,	0.56230E+00,	0.65753E+00
01FC0 245H1840					
01FC2 39F30040					
01FC4 47F97240					
01FC6 5429F140					
01FCH 5F5A31C0 (00089)	DATA	0.74494E+00,	0.82867E+00,	0.91130E+00,	0.99458E+00
01FCA 6A11D4C0					
01FCF 7F4F65C0					
01FD0 88A2D0C1 (00090)	DATA	0.10745E+01,	0.11678E+01,	0.12608E+01,	0.13581E+01
01FD2 0957A7C1					
01FD4 0A161F41					

01FD6 0ADDKJCI				
01FD8 0PBIEMCI	(00091) *	DATA 0.14619E+01,	0.15854E+01,	0.17491E+01, 0.10000E+21
01FDA 0CAFE641				
01FDC 0DEF2841				
01FE6 2H5F3AD1	(00092) *			
	(00093) *	PARCOR(4) W/ 4 BITS		
01FE0 3462F5C0	(00094) PVFCS4	DATA 0.40927E+00,	0.57217E+00,	0.69580E+00, 0.80120E+00
01FE2 42AC0DC0				
01FE4 50FF940				
01FE6 668D0RC0				
01FE8 72C04AC0	(00095) *	DATA 0.89689E+00,	0.98763E+00,	0.10764E+01, 0.11650E+01
01FEA 7F6AARC0				
01FEC 089C77C1				
01FEF 0951FRC1				
01FF0 0A9A041	(00096) *	DATA 0.12547E+01,	0.13464E+01,	0.14408E+01, 0.15388E+01
01FF2 0AC56D41				
01FF4 088AC241				
01FF6 0C4F7641				
01FF8 0D210C1	(00097) *	DATA 0.16420E+01,	0.17540E+01,	0.18815E+01, 0.10000E+21
01FFA 0E083141				
01FFC 0F004FC1				
01FFE 2H5F3AD1	(00098) *			
	(00099) *	PARCOR(5) W/ 3 BITS		
02000 4747DR40	(00100) PVFCS5	DATA 0.55688E+00,	0.74670E+00,	0.89149E+00, 0.10220E+01
02002 5F93DCC0				
02004 721C5R40				
02006 082D0F41				
02008 093RR641	(00101) *	DATA 0.11526E+01,	0.12967E+01,	0.14823E+01, 0.10000E+21
0200A 0A5A4441				
0200C 0ADDRC041				
0200E 2H5F3AD1	(00102) *			
	(00103) *	PARCOR(6) W/ 3 BITS		
02010 49623RC0	(00104) PVFCS6	DATA 0.57331E+00,	0.76317E+00,	0.90902E+00, 0.10407E+01
02012 61AFRC0				
02014 745FC440				
02016 08535AC1				
02018 096147C1	(00105) *	DATA 0.11725E+01,	0.13190E+01,	0.15093E+01, 0.10000E+21
0201A 0AMD4FC1				
0201C 0C130RC1				
0201E 2H5F3AD1	(00106) *			

(00107) \* PARCOR(7) W/ 2 BITS  
 (00108) PVFCS7 DATA 0.56750E+00, 0.87517E+00, 0.11666E+01, 0.10000E+21  
 02020 4E43D740  
 02022 100591C0  
 02024 09551241  
 02026 2K5E3AD1  
  
 (00109) \*  
 (00110) \* PARCOR(8) W/ 2 BITS  
 (00111) PVFCS8 DATA 0.72194E+00, 0.98497E+00, 0.12430E+01, 0.10000E+21  
 02028 5CA82H40  
 0202A 7E137F40  
 0202C 04F1A9C1  
 0202E 2H5E3AD1  
  
 (00112) \*  
 (00113) \* DC RIAS W/ 4 BITS  
 (00114) DC8 DATA 0.12475E+00, 0.25019E+00, 0.37706E+00, 0.50625E+00  
 02030 0E77CFC0  
 02032 200639C0  
 02034 304180C0  
 02036 40CC0CC0  
 02038 51C486C0  
 0203A 63520040  
 0203C 75A2C640  
 0203E 888F27C1  
 02040 0908F241  
 02042 0E3F08C1  
 02044 0CC816C1  
 02046 0E8624C1  
 02048 109308C1  
 0204A 132680C1  
 0204C 14D53241  
 0204E 2K5E3AD1  
  
 (00115) DATA 0.63881E+00, 0.77594E+00, 0.91903E+00, 0.10699E+01  
 (00116) DATA 0.12309E+01, 0.14053E+01, 0.15977E+01, 0.18155E+01  
 (00117) DATA 0.20718E+01, 0.23938E+01, 0.28541E+01, 0.10000E+21  
  
 (00118) \*  
 (00119) \* VARIANCF W/ 5 BITS  
 (00120) VARS DATA 0.56312E+01, 0.77983E+01, 0.98311E+01, 0.12057E+02  
 02050 200CH2C1  
 02052 3E62E441  
 02054 4FA617C1  
 02056 60748C41  
 02058 746240C1  
 0205A 087F3H42  
 0205C 09A353C2  
 0205E 0AH68742  
 02060 0HH30742  
 02062 0CH51FC2  
 02064 0DHCFHC2  
 02066 0FR45A42  
 02068 0F96E442  
 (00121) DATA 0.14548E+02, 0.16994E+02, 0.19276E+02, 0.21426E+02  
 (00122) DATA 0.23405E+02, 0.25415E+02, 0.27476E+02, 0.29409E+02  
 (00123) DATA 0.31174E+02, 0.32888E+02, 0.34633E+02, 0.36477E+02

0206A 1071A9C2  
0206C 115106A2  
0206F 12300F42  
02070 13316842 (00124) DATA 0.3R3RHAF+02, 0.40204E+02, 0.41R644F+02, 0.43443F+02  
02072 141A1CC2  
02074 144497C2  
02076 154RH442  
02078 1670F242 (00125) DATA 0.449R7F+02, 0.464R3E+02, 0.47960E+02, 0.49431F+02  
0207A 1730D2C2  
0207C 174AF142  
0207F 1RH72H42  
02080 1973342 (00126) DATA 0.50900E+02, 0.523334E+02, 0.53709E+02, 0.55072E+02  
02082 1A2AC0C2  
02084 1ADAC0C2  
02086 1R843742  
02088 1C3FD942 (00127) DATA 0.56491F+02, 0.58016E+02, 0.59760E+02, 0.10000F+21  
0208A 1D20C42  
0208C 1E447C2  
0208E 2H5E3AD1 (00128) \*  
(00129) \* PITCH GAIN W/ 2 HITS  
(00130) PGS DATA 0.48797F+00, 0.66711E+00, 0.82399E+00, 0.10000E+21  
(00131) \*  
(00132) \* PITCH W/ 6 HITS  
(00133) PITCHS DATA 16D'0'  
(00134) DATA 1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19

0209R 0000  
...  
020AR 0001  
020AQ 0002  
020AA 0003  
020AH 0004  
020AC 0005  
020AD 0006  
020AF 0007  
020AF 0008  
020AO 0009  
020AI 000A  
020AJ 000B  
020AK 000C  
020AL 000D  
020AM 000E  
020AN 000F

020M7 0010  
020M8 0011  
020M9 0012  
020MA 0013  
020MR 0014 DATA 20,21,22,23,24,25,26,27,28,29,30,31,32,33,34,35  
020MC 0015 (00135)  
020MD 0016  
020ME 0017  
020MF 0018  
020MG 0019  
020MH 001A  
020MI 001B  
020MJ 001C  
020MK 001D  
020ML 001E  
020MM 001F  
020MN 0020  
020MO 0021  
020MP 0022  
020MQ 0023  
020MR 0024 DATA 36,37,38,39,40,41,42,43,44,45,46,47  
020MS 0025 (00136)  
020MT 0026  
020MU 0027  
020MV 0028  
020MW 0029  
020MX 002A  
020MY 002B  
020MZ 002C  
020NA 002D  
020NB 002E  
020NC 002F  
020ND 0030 DATA 48,49,49,49,50,50,51,51,52,52,53,53,54,54,55,55  
020NE 0031 (00137)  
020NF 0032  
020NG 0033  
020NH 0034  
020NI 0035  
020NJ 0036





```

A24 0662A 00000460 (00190)  NOP\MUL(M0,M7)
A25 0662C 000008A5 (00191)  NOP\MOV(P,A5)
A26 0662E 00003A00 (00192)  NOP\ALJCN(A5)
A27 06630 0000089C (00193)  NOP\MOV(K,M0)
A28 06632 901C0024 (00194)  JMPCC(B1,F0) #1
A29 06634 20370000 (00195)  CLEAR(WI)\NOP
A2A 06636 20500000 (00196)  SET(P0)\NOP
A2B 06638 08FC0000 (00197)  MOV(TWA,00)\NOP
A2C 0663A 901C002C (00198)  JMPCC(B2,F0) #2
A2D 0663C 20370000 (00199)  CLEAR(WI)\NOP
A2E 0663E 20500000 (00200)  SET(P0)\NOP
A2F 06640 08DC0000 (00201)  MOV(I0,M0)\NOP
A30 06642 000008FC (00202)  NOP\MOV(IA,M0)
A31 06644 901C0031 (00203)  JMPCC(B3,F0) #3
A32 06646 20370000 (00204)  CLEAR(WI)\NOP
A33 06648 20500000 (00205)  SET(P0)\NOP
A34 0664A 08FC0000 (00206)  MOV(IA,M0)\NOP
A35 0664C 901C0035 (00207)  JMPCC(B4,F0) #4
A36 0664E 20370000 (00208)  CLEAR(WI)\NOP
A37 06650 20500000 (00209)  SET(P0)\NOP
A38 06652 08FC0000 (00210)  MOV(IA,M0)\NOP
A39 06654 08F10000 (00211) *
A3A 06656 0A200000 (00212) * K->A TRANSFORM W/ FNG=SUM(1-K(I))**2
A3B 06658 04880000 (00213) KTOAS
A3C 0665A 048F0000 (00214)  NEG(A1)\NOP
A3D 0665C 04840000 (00215)  MOV(R,M0)\NOP
A3E 0665E 04830000 (00216)  MOV(R,M7)\NOP
A3F 06660 04810000 (00217)  MOV(R,A1)\NOP
A40 06662 84600000 (00218)  MUL(M0,M7)\NOP
A41 06664 16801680 (00219)  K(+1)
A42 06666 08D00800 (00220)  MOV(I0,A0)
A43 06668 08D50000 (00221)  MOV(R,A5)\NOP
A44 0666A 08D60806 (00222)  MOV(P,A6)
A45 0666C 3FA04FAD (00223)  SDR(A5,A6)
A46 0666E 02000200 (00224)  MOV(M5),P(A0)
A47 06670 088F088F (00225)  MOV(R,M7)
A48 06672 088F088F (00226)  MOV(R,M3)
A49 06674 3000006F (00227)  CALL(X23RD)
A50 06676 3000006F (00228)  CALL(X23RD)
A51 06678 3000006A (00229)  CALL(X4TH)
A52 0667A 00000260 (00230)  CALL(X5TH)
A53 0667C 30000062 (00231)  NOP(R,A3)
A54 0667E 30000064 (00232)  CALL(X6TH)
A55 06680 30000064 (00233)  CALL(X7TH)

SA*PITCH
NDW "FIX" PITCH
FORCE FXP->0
M->INSTR$0+1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
OVERWRITE IN$0
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
00=0(M)=0TM
OTM->INSTR$1+1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
OVERWRITE IN$1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
00=0(M)=DTM

P(1)
MAKE IT K(1)
K(1)**2 FOR ENERGY
SAVE K(1)
NO SQUARE IT
NEED THIS AROUND, TON
P(2)
1.0
K(1)**2
1.0-K(1)**2
PROD(1),GET K(2)
K(2)
K(2)
DO 2ND ITERATION (M=2)
DO 3RD ITER (M=3)

GET A(5)(3) READY
M=6
M=7

```





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(00278) ?
A70 066C2 42114219 (00279) ?
(00280) ?
(00281) ?
A71 066C4 08000000 (00282) ?
A72 066C6 04A00040 (00283) ?
A73 066C8 08000000 (00284) ?
A74 066CA 41174113 (00285) ?
(00286) ?
(00287) ?
(00288) ?
A75 066CC 85F08500 (00289) ?
A76 066CE 08000000 (00290) ?
A77 066D0 08000000 (00291) ?
A78 066D2 08010000 (00292) ?
A79 066D4 08F08F07 (00293) ?
A7A 066D6 08F08F00 (00294) ?
A7B 066D8 08060806 (00295) ?
A7C 066DA 4F000000 (00296) ?
A7D 066DC 08070807 (00297) ?
A7E 066DE 02F02F08 (00298) ?
A7F 066E0 85A08A00 (00299) ?
A80 066E2 08A008A0 (00300) ?
A81 066E4 08000000 (00301) ?
A82 066E6 08000800 (00302) ?
A83 066E8 A000A000 (00303) ?
(00304) *
066FA 00000004 (00305) ?
(00306) ?
(00307) ?
1)*K(M)
STORE A(M)(3),SUM FOR A(
M)(7)\A(M)(M-3),SUM A(M)
(M-2)
A70RE A(M)(M-2)\A(M)(2)
STORE A(M)(2),SUM FOR A(
M)(1)\A(M)(M-2),SUM A(M)
(M-1)
K(M)-2
A(M)(M-1)\A(M)(1)
A(M)(1)\A(M)(M-1)
-A(M)(M) (=P(M))
MAKE IT REFLN CONF
K(M)-2
K(M)=A(M)(M),1-K(M)-2
P(M+1)
1-K(M)-2,GFT K(M+1)
(1-K(M)-2)*PROD
NEW PROD
K(M+1)
K(M+1)
MOV(A3),ADD(A0,A2)\MOV(A4),ADD(A0,A2)
MOV(R,FX0)\MOV(R,FX0)
MOV(FXI,M2)\MOV(FXI,M1)
MOV(P,A0)\MOV(P,A0)
MOV(A2),ADD(A0,A1)\MOV(A3),ADD(A0,A1)
MOV(M3,M7)
MOV(R,FX0)\MOV(R,FX0)
MOV(FXI,M1)\MOV(FXI,M0)
MOV(R,A1)\MOV(R,A2)
MOV(10A,A7)
MFC(A7)
MOV(P,A6)
MOV(M0),SUB(A5,A6)\MOV(R,A1)
MOV(10,A7)
MOV(M3),R(A7)
MUL(M3,M5)
MOV(P,M5)
MOV(R,M3)
MOV(R,M7)
RETURN
QUANSS2=BA-DUANSSA
END QUANSSZ
EJECT

```

```

(00304) *
(00309) *
(00310) *
(00311)
(00312)
(00313) ;
(00314)
(00315)
(00316)
(00317)
(00318)
(00319) *
(00320) G105S
(00321)
(00322)
(00323)
(00324)
(00325)
(00326)
(00327)
(00328)
(00329)
(00330)
(00331)
(00332)
(00333)
(00334)
(00335) *
(00336)
(00337)
(00338)
(00339)
(00340)
(00341) *
(00342)
(00343)
(00344)
(00345)
(00346)
(00347) *
(00348)
(00349)
(00350)
(00351)

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```

066FA 000067F2 EVFN
066FC 000066F6 ADDR G105S+2*SCILRS
066FF 0000 DATA 0
066FF 0107 DATA G105SZ
066FD 000067H2 ADDR G105SA
EVFN
066F2 00105340 REGIN APS(G105S)
066F4 02300030 JSN(G105S1,P1)
A02 066F6 04C203CF SET(RO)
A03 066F8 06C80023 LOAD(HR0,SAS3R(1),TF)
A04 066FA 08D41F40 LOAD(HR0,PARCS(1),TF)
A05 066FC 0A9A0002 LOAD(HR1,PVFC(1),TF)
A06 066FE 0CF42134 ADD(HR1,WS,TF)
A07 06700 0E8F0002 LOAD(HR3,PVFC(2),TF)
ADD(HR3,WS,TF,C)
A08 06702 108A0002 ADD(HR0,WS,TF)
A09 06704 12D41F80 LOAD(HR1,PVFC(2),TF)
A0A 06706 149A0002 ADD(HR1,WS,TF)
A0B 06708 16F42174 LOAD(HR3,PVFC(2),TF)
A0C 0670A 188F0002 ADD(HR3,WS,TF,C)
A0D 0670C 1A8A0002 ADD(HR0,WS,TF)
A0E 0670E 1C041F00 LOAD(HR1,PVFC(1),TF)
A0F 06710 1E9A0002 ADD(HR1,WS,TF)
A10 06712 20F42184 LOAD(HR3,PVFC(2),TF)
A11 06714 228F0002 ADD(HR3,WS,TF,C)
A12 06716 248A0002 ADD(HR0,WS,TF)
A13 06718 26D41F80 LOAD(HR1,PVFC(4),TF)
A14 0671A 289A0002 ADD(HR1,WS,TF)
A15 0671C 2AF421D4 LOAD(HR3,PVFC(4),TF)
A16 0671E 2C8F0002 ADD(HR3,WS,TF,C)
A17 06720 2E8A0002 ADD(HR0,WS,TF)
A18 06722 30D42000 LOAD(HR1,PVFC(5),TF)
A19 06724 329A0002 ADD(HR1,WS,TF)
A1A 06726 34F421F4 LOAD(HR3,PVFC(5),TF)

```

```

POINTER TO CONSTRUCTED I
NSTRUCTION BLOCK
POINTER TO SCALAR BLOCK
NUMBER OF SCALARS
MODULE SIZE
POINTER TO CHAIN ANCHOR
END ON WORD BOUNDARY

SET INPUT PC(PCI)
ENABLE APS OUTPUT
IO=2*(-15)
IO=P(1)
IO=PTHR(0)
IO=PTHR(1)
IO=PDEC(0)
IO=PDEC(1),THEN PCI

IO=P(2)
IO=PTHR(0)
IO=PTHR(1)
IO=PDEC(0)
IO=PDEC(1),THEN PCI

IO=P(3)
IO=PTHR(0)
IO=PTHR(1)
IO=PDEC(0)
IO=PDEC(1),THEN PCI

IO=P(4)
IO=PTHR(0)
IO=PTHR(1)
IO=PDEC(0)
IO=PDEC(1),THEN PCI

IO=P(5)
IO=PTHR(0)
IO=PTHR(1)
IO=PDEC(0)

```

A1W 0672H 360E0002 (00352)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A1C 0672A 388A0002 (00353)	ADD(HR0,MS,TF)	IOEP(6)
A1D 0672C 3AD42010 (00354)	LOAD(HR1,PVFC56(2),TF)	IOEPTHR(0)
A1E 0672E 3C9A0002 (00355)	ADD(HR1,MS,TF)	IOEPTHR(1)
A1F 06730 3FE42204 (00356)	LOAD(HR3,PDF056(2),TF)	IO=PDFQ(0)
A20 06732 408E0002 (00358)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A21 06734 428A0002 (00359)	ADD(HR0,MS,TF)	IOEP(7)
A22 06736 44D42020 (00361)	LOAD(HR1,PVFC57(2),TF)	IOEPTHR(0)
A23 06738 469A0002 (00362)	ADD(HR1,MS,TF)	IOEPTHR(1)
A24 0673A 48F42214 (00363)	LOAD(HR3,PDF057(2),TF)	IO=PDFQ(0)
A25 0673C 4A8E0002 (00364)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A26 0673E 4C8A0002 (00365)	ADD(HR0,MS,TF)	IOEP(R)
A27 06740 4E442028 (00367)	LOAD(HR1,PVFC54(2),TF)	IOEPTHR(0)
A28 06742 509A0002 (00368)	ADD(HR1,MS,TF)	IOEPTHR(1)
A29 06744 52E4221C (00369)	LOAD(HR3,PDF058(2),TF)	IO=PDFQ(0)
A2A 06746 54300024 (00370)	SET(TAF1)	SET "END OF PARCOR" RIAS
A2R 06748 568E0002 (00371)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A2C 0674A 58C203FA (00372)	LOAD(HR0,SAS52(1),TF)	IO=DC RIAS
A2D 0674C 5AD42030 (00373)	LOAD(HR1,DCS(2),TF)	IOEPTHR(0)
A2E 0674E 5C8A0002 (00374)	ADD(HR1,MS,TF)	IOEPTHR(1)
A2F 06750 5E442224 (00375)	LOAD(HR3,DCS05(2),TF)	IO=PDFQ(0)
A30 06752 608E0002 (00377)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A31 06754 62C203FO (00378)	LOAD(HR0,SAS55(1),TF)	IO=VAR
A32 06756 64D42050 (00380)	LOAD(HR1,VAR5(2),TF)	IOEPTHR(0)
A33 06758 669A0002 (00381)	ADD(HR1,MS,TF)	IOEPTHR(1)
A34 0675A 68F42244 (00382)	LOAD(HR3,VAR50(2),TF)	IO=PDFQ(0)
A35 0675C 6A8E0002 (00383)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A36 0675E 6CC203FC (00385)	LOAD(HR0,SAS61(1),TF)	IO=PG
A37 06760 6E442090 (00386)	LOAD(HR1,PG5(2),TF)	IOEPTHR(0)
A38 06762 709A0002 (00387)	ADD(HR1,MS,TF)	IOEPTHR(1)
A39 06764 72F42284 (00388)	LOAD(HR3,PG50(2),TF)	IO=PDFQ(0)
A3A 06766 74300028 (00389)	SET(AP3)	GET READY TO TERMINATE
A3B 0676E 768E0002 (00391)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A3C 0676A 78C203FF (00392)	LOAD(HR0,SAS62(1),TF)	IO=M
A3D 0676C 7A300037 (00393)	SET(W1)	STALL APS INPUT
A3E 0676E 7C000020 (00394)	NOP	RELEASED BY APU
A3F 06770 7E4420E4 (00395)	LOAD(HR0,INSTR50(2),TF)	IO=INSTR50

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A40 06772 H0300037 (00396) SFT(MI)
A41 06774 W264709R (00497) LOAD(HR2,PITCHS(2))
A42 06776 N4500000 (00394) LOAD(HR1,MSS)
A43 06778 H729002H (00399) ADD(HR2,HR1,TF)
A44 0677A HR300037 (00400) SFT(MI)
A45 0677C H8000020 (00401) NOP
A46 0677E HC420FA (00402) LOAD(HR0,INSTRS1(2),TF)
A47 06780 H4300037 (00403) SFT(MI)
A48 06782 906422HC (00404) LOAD(HR2,MSDFU(2))
A49 06784 92500000 (00405) LOAD(HR1,MSS)
A4A 06786 9429002H (00406) ADD(HR2,HR1)
A4B 0678H 96A9002H (00407) ADD(HR2,HR1,TF)
A4C 0678A 98460021 (00409) *
A4D 0678C 9A500007 (00410) LOAD(HR0,PARCS-2(3))
A4E 0678E 9C8A0002 (00411) LOAD(HR1,PSIZES)
A4F 06790 9E1940H1 (00412) ADD(HR0,MS,TF)
A50 06792 A0C7043A (00413) * SURF(HR1,1),JIMPP(PINCS1)
A51 06794 A2200031 (00415) DNEFI LOAD(HR0,SAS92(1),TF)
A52 06796 A4000020 (00416) * CLEAR(HI)
A53 0679H A6205F40 (00418) * 0/00 APS SUBROUTINE
A54 0679A A8005960 (00419) JSN(G105SD,P2)
A55 0679C AA4A0002 (00420) T0PSP1 JUMP(FNTRYS)
A56 0679E AC9A0002 (00421) ADDST ADD(HR1,MS,TF)
A57 067A0 AF8A0002 (00422) ADD(HR1,MS,TF)
A58 067A2 B08A0002 (00423) ADD(HR3,MS,TF)
A59 067A4 B2300037 (00424) SFT(MI)
A5A 067A6 B4000020 (00425) * ENTPYS
A5B 067A8 B60055AA (00426) * JUMP(ADDS1,AF2)
A5C 067AA B82E0000 (00427) ADD(HR2,MSS,NA,C)
A5D 067AC BA20546A (00429) * JUMP(T0PSP1,AF2),CLEAR
A5E 067AE BC307C40 (00431) * JSN(G105S3,P3)
A5F 067B0 BE500007 (00432) * LOAD(HW1,PSIZES)
A60 067B2 C0400000 (00433) * LOAD(HW0,I0)
A61 067B4 C2700000 (00434) * LOAD(HW3,MSS)
A62 067B6 C4070000 (00435) * SUR(HW0,MSS)
A63 067B8 C6660021 (00436) * LOAD(HW2,PARCS-2(3))
A64 067BA C90A0001 (00437) * * ADD(HW0,HS,TF)
A65 067BC CAA80002 (00438) * ADD(HR2,MS,TF)
A66 067BE CC11A4B1 (00439) * SURF(HW1,1),JIMPP(#3)

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STALL APS INPUT
PITCH(.) RA
APU SFTS MSS->M
IO=OTM=PITCHCM
STALL APS INPUT
REFLASED BY APU
IO=INSTRS1
STALL APS INPUT
DEO(.) RA
APU SFTS MSS->DTM
DEO(DTM)
IO=DRM=DEO(2*DTM)

PARC(.) RA-2
PARC(.) SIZE-1
IO=PARC(I)
FOR I=0,1...7
IO=I*TH2

INPUT DONE!

SET OUTPUT PC(P2)
ENTER # STALL!
IO=D(K)
IO=D(K+1)
IO=E(K)
IO=E(K+1)
WAIT FOR THRESHOLD ...
DECISION
AF?0 THEN K+2,K+3
AF?1,THEN PC0->PC
RESFT LOOP PC & AF2

SET OUTPUT PC(PC3)
P(.) SIZE-1
OPRM(.) HA
DUMMY OP FOR EXEC
OPRM(.) RA-1
P(.) RA-2
OQ=OPRPM(I)
OO=DEO(OPRPM(I))
FOR I=1,2...8

```





PGS:	02000	(00130)	(00386)
PGSDFO:	02284	(00027)	(00388)
PGPUS:	0000C	(00016)	(00448)
PINGS1:	00044	(00411)	(00417)
PINC6J:	00078	(00458)	(00459)
PITCMS:	02008	(00133)	(00397)
PS1Z8:	00007	(00011)	(00410)
PVFC51:	01F40	(00068)	(00325)
PVFC52:	01F80	(00078)	(00331)
PVFC53:	01FC0	(00088)	(00337)
PVFC54:	01FF0	(00094)	(00343)
PVFC55:	02000	(00100)	(00349)
PVFC56:	02010	(00104)	(00355)
PVFC57:	02020	(00108)	(00361)
PVFC58:	02028	(00113)	(00367)
UPR8:	02F28	(00032)	(00440)
QUANS:	065F2	(00055)	(00152)
QUANSSA:	00000	(00150)	(00154)
QUANSSZ:	00084	(00151)	(00305)
SAS104:	00452	(00046)	(00306)
SAS11:	00398	(00033)	(00442)
SAS18:	0030F	(00034)	(00450)
SAS19:	00300	(00035)	(00456)
SAS2:	003FA	(00036)	(00373)
SAS5:	003F0	(00037)	(00379)
SAS6:	003FA	(00038)	(00457)
SAS1:	003FC	(00039)	(00385)
SAS2:	003FF	(00040)	(00392)
SAS8:	00432	(00041)	(00441)
SAS9:	00434	(00042)	(00443)
SAS0:	00436	(00043)	(00445)
SAS91:	00434	(00044)	(00453)
SAS2:	0043A	(00045)	(00413)
SCLMS:	00002	(00314)	(00323)
START:	065F0	(00047)	(00144)
SVTS:	003H2	(00029)	(00033)
		(00042)	(00043)
		(00044)	(00045)
		(00034)	(00035)
		(00036)	(00037)
		(00038)	(00039)
		(00040)	(00041)
		(00042)	(00043)
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DEQUANTIZE SIDFRAND  
W/ K->A TRANSFORM  
ORIGINATED:13-AUG-79  
UPDATED:16-MAY-80

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EXPAND ARRAY FUNCTION DISPATCH TABLE

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0217A 3E1465C0						
0217C 47167A40	(00056)	DATA 0.55635E+00,	0.62403E+00,	0.68141E+00,	0.73631E+00	
0217E 4F103740						
02180 57347140						
02182 5E3E67C0	(00057)	DATA 0.79160E+00,	0.84597E+00,	0.90653E+00,	0.97650E+00	
02184 653264C0						
02186 6C348FC0						
02188 74092CC0						
0218A 7C3DE3C0						
0218C 08624DC1	(00058)	DATA 0.10480E+01,	0.11214E+01,	0.12077E+01,	0.12736E+01	
0218E 0FE8A0C1						
02190 094E7141						
02192 0A305541	(00059)	DATA 0.13383E+01,	0.14072E+01,	0.14643E+01,	0.15197E+01	
02194 0A8AD6C1						
02196 0A41F241						
02198 086E2C1						
0219A 0C285841						
0219C 0C978D41	(00060)	DATA 0.15740E+01,	0.16295E+01,	0.16759E+01,	0.17142E+01	
0219E 0D093741						
021A0 0D683E41						
021A2 0D66A841						
021A4 0E0001C1	(00061)	DATA 0.17504E+01,	0.17863E+01,	0.18208E+01,	0.18521E+01	
021A6 0E4A57C1						
021A8 0E90F8C1						
021AA 0E1119C1						
021AC 0F01841	(00062)	DATA 0.18813E+01,	0.19108E+01,	0.19426E+01,	0.19807E+01	
021AE 0E4951C1						
021B0 0F8A71C1						
021B2 0E104941	(00063) *					
	(00064) *	PARCUR(3) w/ 4 BITS				
021B4 1E740840	(00065) PD+CS3	DATA 0.23813E+00,	0.39243E+00,	0.51204E+00,	0.61256E+00	
021B6 32342540						
021B8 418A86C0						
021BA 4E685DC0						
021BC 54F488C0	(00066)	DATA 0.70251E+00,	0.78737E+00,	0.86998E+00,	0.95262E+00	
021BE 64C88A40						
021C0 6E588140						
021C2 74FF73C0						
021C4 0E34C0C1	(00067)	DATA 0.10365E+01,	0.11226E+01,	0.12129E+01,	0.13086E+01	
021C6 08E415C1						
021C8 048404C1						
021CA 0A740341						

02100 0M2R41	(00068)	DATA	0.14075E+01,	0.15162E+01,	0.16545E+01,	0.18418E+01
02101 0C21041						
02102 0D30A41						
02103 0E01A41						
(00069) *						
(00070) * PARCOR(4) W/ 4 BITS						
02104 2E34C1C0	(00071)	DATA	0.31411E+00,	0.50442E+00,	0.61992E+00,	0.75167E+00
02105 409015C0						
02106 51E4E640						
02107 6016R6C0						
02108 6C450C40	(00072)	DATA	0.485074E+00,	0.94305E+00,	0.10122E+01,	0.11205E+01
02109 7850F6C0						
02110 0A41E241						
02111 0B46C8C1						
02112 09ACD9C1	(00073)	DATA	0.12094E+01,	0.13000E+01,	0.13929E+01,	0.14888E+01
02113 0A666641						
02114 0E24ARC1						
02115 04E90FC1						
02116 0E45DCC1	(00074)	DATA	0.15888E+01,	0.16951E+01,	0.18129E+01,	0.19500E+01
02117 0D4E90C1						
02118 0E80D1C1						
02119 0E9999C1						
(00075) *						
(00076) * PARCOR(5) W/ 3 BITS						
02114 38E40940	(00077)	DATA	0.44510E+00,	0.66865E+00,	0.82876E+00,	0.95821E+00
02115 559652C0						
02116 69018C40						
02117 7A8A040						
02118 04AFC41	(00078)	DATA	0.10859E+01,	0.12194E+01,	0.13739E+01,	0.15907E+01
02119 04E154C1						
02200 0AFD8E41						
02201 0E49C0C1						
(00079) *						
(00080) * PARCOR(6) W/ 3 BITS						
02204 3E210C0	(00081)	DATA	0.46200E+00,	0.68463E+00,	0.84172E+00,	0.97611E+00
02205 57A1E4C0						
02206 6HRD7R40						
02207 7C47H9C0						
02208 08D70A41	(00082)	DATA	0.11050E+01,	0.12400E+01,	0.13980E+01,	0.16206E+01
02209 09EHR541						
02210 04E41AC1						
02211 0CF6FD41						
(00083) *						
(00084) * PARCOR(7) W/ 2 BITS						

02213 12UDACC0	(00005) PDPCS7 DATA	0.19730E+00,	0.13761E+00,	0.10127E+01,	0.13204E+01
02214 5FNA0140					
02215 3H1A0241					
0221A 0A902BC1					
	(00006) *				
	(00007) * PARCORCH W/ 2 HITS				
0221C 4A0E4140	(00008) PDPCS8 DATA	0.57886E+00,	0.86541E+00,	0.11045E+01,	0.13815E+01
0221E 6FC5C140					
02220 04060331					
02222 06014FC1					
	(00009) *				
	(00000) * DC HIAS W/ 4 HITS				
	(00091) PCS DATA	0.67293E-01,	0.18721E+00,	0.31317E+00,	0.44096E+00
02224 7F937HRF					
02226 17E67E40					
02228 2815E340					
0222A 3H7160C0					
0222C 492H3AC0	(00092) DATA	0.57154E+00,	0.70609E+00,	0.84579E+00,	0.99277E+00
0222E 5A612H40					
02230 6C4208C0					
02232 7F07H3C0					
02234 092F1441	(00093) DATA	0.11475E+01,	0.13143E+01,	0.14964E+01,	0.16991E+01
02236 0AR4BFC1					
02238 0H5H80C1					
0223A 0D97C1C1					
0223C 0E74B7C1	(00094) DATA	0.10319E+01,	0.22117E+01,	0.25759E+01,	0.31324E+01
0223E 11B1BFC1					
02240 149B7141					
02242 140E77C1					
	(00095) *				
	(00096) * VARIANCE +/- 5 HITS				
	(00097) VARS DATA	0.43600E+01,	0.68023E+01,	0.87943E+01,	0.10868E+02
02244 23AE1341					
02246 366R1C41					
02248 465ARQC1					
0224A 56F1A9C1					
0224C 69F9D941	(00098) DATA	0.13247E+02,	0.15849E+02,	0.18140E+02,	0.20412E+02
0224E 7FCAC041					
02250 0911EPC2					
02252 0A340C42					
02254 0B3H1142	(00099) DATA	0.22430E+02,	0.24372E+02,	0.26458E+02,	0.28495E+02
02256 0C2E9DC2					
02258 0D3A9FC2					
0225A 0E4E5C32					
0225E 0F294742	(00100) DATA	0.30327E+02,	0.32035E+02,	0.33741E+02,	0.35525E+02
0225F 100474C2					

AD-A091 663

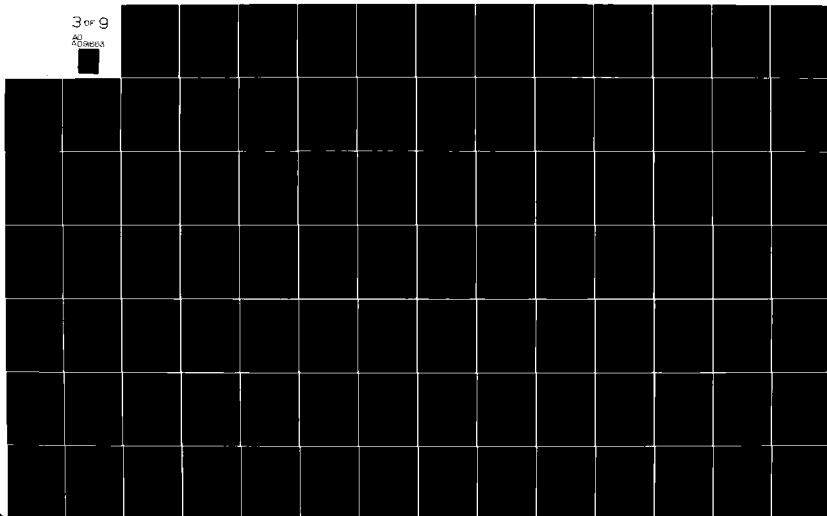
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

3 of 9

AD  
DCA100-78-C-0064



02260 10DFD942			
02262 11C33342			
02264 12H70A42	(00101)	DATA 0.37430E+02, 0.39343E+02, 0.41065E+02, 0.42664E+02	
02266 13AF742			
02268 14RHS1C2			
0226A 154FDC2	(00102)	DATA 0.44222E+02, 0.45741E+02, 0.47274E+02, 0.48695E+02	
0226C 161CA42			
0226E 16DFD942			
02270 179CAC42			
02272 185RF5C2	(00103)	DATA 0.50167E+02, 0.51633E+02, 0.53034E+02, 0.54385E+02	
02274 19156042			
02276 19D10642			
02278 1A845A42			
0227A 1P3147C2			
0227C 1HF10642	(00104)	DATA 0.55755E+02, 0.57223E+02, 0.58810E+02, 0.60709E+02	
0227E 1C9CRH42			
02280 1D67AF42			
02282 1E5AC0C2	(00105) *		
	(00106) *	PITCH GAIN W/ 2 BITS	
02284 32219640	(00107) P	AINS DATA 0.39165E+00, 0.48430E+00, 0.74993E+00, 0.89805E+00	
02286 4ACA57C0			
02288 5FDDH4C0			
0228A 72F34D40	(00108) *		
	(00109) *	PITCH W/ 6 BITS	
0228C 78000041	(00110) #	SDFC DATA 15.	
0228E 08000042	(00111)	DATA 16..17..18..19..20..21..22..23..24..25..26..27..28..29..30..31.	
02290 08H00042			
02292 09600042			
02294 09H00042			
02296 0A000042			
02298 0ARR0042			
0229A 0R000042			
0229C 0RH00042			
0229E 0C000042			
022A0 0CR00042			
022A2 0D000042			
022A4 0DR00042			
022A6 0F000042			
022A8 0FH00042			
022AA 0H000042			
022AC 0FR00042	(00112)	DATA 32..33..34..35..36..37..38..39..40..41..42..43..44..45..46..47.	
022AE 10000042			



022HD 10R00042  
022H2 11R00042  
022H4 11R00042  
022H6 12R00042  
022HR 12R00042  
022HA 13R00042  
022RC 13R00042  
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022C6 16R00042  
022CR 16R00042  
022CA 17R00042  
022CC 17R00042  
022CF 18R00042  
022D0 18R00042  
022D2 19R00042  
022D4 19R00042  
022D6 1A00042  
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022DC 1R00042  
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022E0 1CR00042  
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022E4 1DR00042  
022E6 1F00042  
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022EC 1FR00042  
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022F2 22R00042  
022F4 23R00042  
022F6 24R00042  
022F8 25R00042  
022FA 26R00042  
022FC 27R00042  
022FE 28R00042  
02300 29R00042  
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02304 2HR00042  
02306 2CR00042

(00113) DATA 4R..49..50..51..52..53..54..55..56..57..58..59..60..61..62.

(00114) DATA 63..65..67..69..71..73..75..77..79..81..83..85..87..89..91..93.

PAGE 01: FCB 243... "APDOPPV(U,V)" DEQUANTIZE SIDERAND

02308 20000042

0230A 20000042

(00115)

EVPN

0230C 5450

(00116)

INSTRSO DATA INSO\*0'S12'+X'50',X'0000'

0230D 0000

(00117)

RHS 1

000006800

(00118)

PL-START

(00119)

FJPCY





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A45 06RHC 00000000 (00208)
A46 06RHF 00400040 (00209)
A47 06R90 00000000 (00210)
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A48 06M92 04000400 (00212) X23RD
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A49 06M94 47134714 (00214)
      (00215) ;
      (00216) ;
A4A 06R96 00000000 (00217)
A4B 06R98 00400040 (00218)
A4C 06M9A 00000000 (00219)
A4D 06R9C 41124113 (00220)
      (00221) ;
      (00222) ;
      (00223) *
A4F 06R9E 85008500 (00224)
A4G 06RAD 00000000 (00225)
A50 06RA2 00400040 (00226)
A51 06RA4 00010000 (00227)
A52 06RA6 00070000 (00228)
A53 06BA8 00000000 (00229)
A54 06HAA 00060006 (00230)
A55 06RAC 40000000 (00231)
A56 06RAF 00070007 (00232)
A57 06H40 00000000 (00233)
A58 06RH2 00000000 (00234)
A59 06RH4 00000000 (00235)
A5A 06RH6 00000000 (00236)
A5B 06BHR 00000000 (00237)
A5C 06RHA 00000000 (00238)
      (00000050) (00239)
      (00240)
      (00241)

```

```

MOV(R,FX0)\MOV(R,FX0)
MOV(FX1,M4)\MOV(FX1,M2)
MOV(P,A0)
MUL(M0,M7)\MUL(M0,M7)
MOV(A1,ADD(A0,A2)\MOV(A4),ADD(A0,A2)
MOV(R,FX0)\MOV(R,FX0)
MOV(FX1,M2)\MOV(FX1,M1)
MOV(P,A0)\MOV(P,A0)
MOV(A2),ADD(A0,A1)\MOV(A3),ADD(A0,A1)
MUL(M3,M7)
MOV(P,FX0)\MOV(R,FX0)
MOV(FX1,M1)\MOV(FX1,M0)
MOV(K,A1)\MOV(R,A2)
MOV(T0A,A7)
NEG(A7)
MOV(P,A6)
MOV(M0),SUP(A5,A6)\MOV(R,A1)
MOV(I0,A7)
MOV(M3),R(A7)
MUL(M3,M5)
MOV(P,M5)
MOV(R,M3)
MOV(R,M7)
PRTURN
END DEQUSSZ
FJECT

```

```

STORE A[M](M-3)\A[M](3)
A[M-1](M-1)*K[M]\A[M-1](
1)*K[M]
STORE A[M](3),SUM FOR A[
M](2)\A[M](M-3),SUM A[M]
(M-2)
STORE A[M](M-2)\A[M](2)
STORE A[M](2),SUM FOR A[
M](1)\A[M](M-2),SUM A[M]
(M-1)
K[M]^2
A[M](M-1)\A[M](1)
A[M](1)\A[M](M-1)
-A[M](M) (=P[M])
MAKE IT REFLN COEF
K[M]^2
K[M]=A[M](M),1-K[M]^2
P[M+1]
1-K[M]^2,GET K[M+1]
(1-K[M]^2)*PROD
NEW PROD
K[M+1]
K[M+1]

```



A1F 06900 3C500007 (0028A)	LOAD(RW1,PSIZES)	PARC(.) SIZE=1
A1F 06902 4F400007 (00287)	ADD(RW0,MS,TF)	IO=PARC(I)
A20 06904 40191F41 (00284)	SUBL(RW1,1),JUMPP(PINCS1)	FOR I=0,1...7
A21 06906 47C2043A (00289)	LOAD(RW0,SAS92(1),TF)	IO=I,TH?
A22 06908 44200031 (00291)	DNFST	INPUT DONE!
A23 0690A 46000020 (00292)	NOP	
A24 0690C 48202F40 (00293)	NO	
A25 0690E 4A300037 (00294)	G106S1	SET OUTPUT PC(PC2)
A26 06910 4C000020 (00296)	TOPSP1	STALL APS INPUT
A27 06912 4FF4230C (00297)		RELEASED BY APJ
A28 06914 50300037 (00298)		IO=INSTR0
A29 06916 52000020 (00299)	INS0	STALL APS INPUT
A2A 06918 54500000 (00300)		APJ SFTS MSS->LEVEL
A2B 0691A 56290028 (00301)		D(OPRM(I))
A2C 0691C 58A90028 (00302)		IO=D(OPRM(21))
A2D 0691E 5A3F0000 (00303)		FOR PC1->PC0
A2E 06920 5C002560 (00304)	JUMP(TOPSP1)	RESET TOP-OF-I,NOJ
A2F 06922 5F304640 (00305)		
A30 06924 60400000 (00306)	G106S0	SET OUTPUT PC(PC3)
A31 06926 62500000 (00308)		PARC(.) HA
A32 06928 64020000 (00309)		PARC(.) SIZE=1
A33 0692A 663F0000 (00310)	PARCS0	PARC(.) HA=1
A34 0692C 688A0002 (00311)		PC3->PC
A35 0692E 6A113341 (00312)		NO=PARC(I)
A36 06930 6C300029 (00313)		ETC FOR I=1,2...8
A37 06932 6E3E0000 (00314)	VAR50	SET "END-OF-PARCON"
A38 06934 70C20434 (00315)		PC3->PC
A39 06936 723E0000 (00316)	DCS0	NO=D(OTVAR)=DTVAR
A3A 06938 73C20432 (00317)	MS0	PC3->PC
A3B 0693A 763E0000 (00318)		NO=D(OTDC)=DTDC
A3C 0693C 78C20438 (00319)	PGS0	PC3->PC
A3D 0693E 7A3E0000 (00320)		NO=D(OTM)=DTM
A3E 06940 7CC20436 (00321)		PC3->PC
A3F 06942 7F460010 (00322)		NO=D(OTG)=DTG
A40 06944 80500007 (00323)		
A41 06946 82F203FA (00324)		A1(.) BA=2
A42 06948 848A0007 (00325)		A1(.) SIZE=1
A43 0694A 86114241 (00326)		NO=PR0D(R)=ENG
A44 0694C 88200030 (00327)		NO=A(J)
		FOR J=0,1...7
		OUTPUT DONE!

```

A45 0694F 8A000020 (00330) NOP
      (00331) *
A46 06950 8C300032 (00332) SFT(RA)
A47 06952 8F64230D (00333) G106S1 LOAD(H#2,INSTRS0+1(2),TF)
A48 06954 90200030 (00334) C1FAR(R0)
      (00335) NOP
A49 06956 97000020 (00336) (J1AD)(H#2,AFSSP#4(1),TF)
A4A 06958 9443F5C0 (00337) C1FAR(R0)
A4B 0695A 96200030 (00338) ADD(H#3,MSS,NA,C)
A4C 0695C 983E0000 (00339) JUMP(TOPSP3)
A4D 0695E 9A004760 (00340) *
      (00341) G106SA= 8C
      (00342) ?
      (00343) FND RA-1
06960 (00344) ? STORAGE M10CK FOR CONSTRUCTED INSTRUCTIONS
06960 00000000 (00345) G106SI DATA IF'0.0'
      (00346) G106S7=81-G106S
06962 (00347) FND

```

```

ENABLE AP#
OVERWRITE INSTRS0+1
STALL APS OUTPUT
RELEASED BY AP#
OVERWRITE INSO
STALL APS OUTPUT
PC2->PC
RESET TOP-OF-LOOP

```

```

ASSIGN VALUE TO CHAIN AN
CHOR

```



AIS:	00017 (00006)	(00323)
AFDSORG:	00087 (00007)	(00032)
APSSBEM:	1F00 (00000)	(00336)
RTASS:	00000 (00139)	(00141)
CSPHSMNS:	0211C (00009)	(00035)
DCS:	02224 (00091)	(00276)
DCSO:	00039 (00316)	
DFUUS:	00002 (00033)	(00126)
DFUJSSA:	00000 (00124)	(00128) (00239)
DFUJSS7:	00050 (00125)	(00230) (00240)
DNFSA:	00034 (00184)	
DNFS1:	00022 (00291)	
DNFSU:	00044 (00329)	
G100S:	00004 (00034)	(00248) (00254) (00346)
G100S1:	00074 (00255)	(00294)
G100S3:	00044 (00306)	(00332)
G100SA:	00024 (00251)	(00341)
G100S1:	06060 (00246)	(00345)
G100S0:	0002F (00294)	(00306)
G100SZ:	0009F (00250)	(00346)
HS:	00001 (00011)	(00259) (00261) (00265) (00267) (00269) (00271) (00273) (00275)
		(00280) (00282)
INSD:	0002A (00116)	(00300)
INCSJ:	00003 (00131)	(00144)
INSTRSD:	0230C (00116)	(00297) (00333)
KAS:	00010 (00285)	
KTDAS:	00012 (00148)	
MS:	00000 (00012)	
MS5:	00000 (00013)	(00300) (00303) (00308) (00309) (00310) (00314) (00316) (00318) (00320)
		(00338)
MSDFC:	0228C (00110)	(00278)
MSU:	0003A (00318)	
PARCS:	00023 (00014)	(00285)
PARCSU:	00033 (00310)	(00312)
PDFCS1:	02134 (00045)	(00258)
PDFCS2:	02174 (00055)	(00260)
PDFCS3:	02144 (00065)	(00262)
PDFCS4:	02104 (00071)	(00264)
PDECS5:	02114 (00077)	(00266)
PDECS6:	02204 (00081)	(00268)
PDFCS7:	02214 (00085)	(00270)
PDFCSH:	0221C (00088)	(00272)
PGSU:	00030 (00320)	
PGAINS:	02284 (00107)	(00281)

P1NC\$1: 0001F (00287) (00288)  
 P1MC\$J: 00042 (00326) (00327)  
 P1Z\$8: 00007 (00015) (00286) (00324)  
 QPRM\$8: 02F 36 (00017) (00257)  
 SA\$11: 00348 (00019)  
 SA\$30: 00300 (00026)  
 SA\$60: 0038A (00021) (00325)  
 SA\$88: 00432 (00022) (00317)  
 SA\$89: 00434 (00023) (00315)  
 SA\$90: 00436 (00024) (00321)  
 SA\$91: 00438 (00025) (00319)  
 SA\$92: 0043A (00026) (00289)  
 SCLM\$: 00000 (00248) (00255)  
 START: 06800 (00027) (00118)  
 SVTS: 00342 (00018) (00019) (00020) (00021) (00022) (00023) (00024) (00025) (00026)  
 TOP\$P1: 00025 (00295) (00304)  
 TOP\$P3: 00047 (00333) (00339)  
 VAR\$8: 02244 (00097) (00274)  
 VAR\$10: 00037 (00314)  
 WS: 00002 (00028) (00287) (00311) (00326)  
 X23MD: 00048 (00162) (00163) (00212)  
 X4TH: 00042 (00164) (00203)  
 X5TH: 00043 (00165) (00202) (00204)  
 X6TH: 00038 (00167) (00194)  
 X7TH: 00030 (00168) (00193) (00196)  
 X8TH: 00036 (00170) (00188)  
 Z\$: 00003 (00029)

(00001) \* FCR 244... "MPMCKX(Y,U,V,W)" EXTRACT M,PG,MOVE X  
 (00002) \* ORIGINATED:04-OCT-79  
 (00003) \* UPDATED:29-MAY-80  
 (00004) \* Y RIB IS AUTOCORRELATIONS, USED AS BOTH OUTPUT AND INPUT.  
 (00005) \* THESE ARE MOVED FROM BUFFER X1R INTO Y.  
 (00006) \* U RIB IS INPUT SAMPLES FROM THE A/D. THE LAST  
 (00007) \* FEW POINTS OF THIS BUFFER ARE MOVED TO THE BEGINNING OF BUFFER  
 (00008) \* X0M, TO PROVIDE FRAME OVERLAP.  
 (00009) \* V RIB IS THE ARRAY OF PARCOR COEFFICIENTS (OUTPUT)  
 (00010) \* W RIB IS ARRAY OF PREDICT COEFFICIENTS(OUTPUT BUT UNUSED  
 (00011) \* SUBSEQUENTLY).

(00012) \* THIS ROUTINE FINDS THE LARGEST AUTOCORRELATION IN THE RANGE 0  
 (00013) \* INTERSTAND STORES IT IN THE PITCH PERIOD. IT ALSO  
 (00014) \* CALCULATES THE PITCH GAIN (PITCH)/RCO). FINALLY, IT FINDS  
 (00015) \* PARCOR COEFFICIENTS USING THE MWLD FUNCTION.  
 (00016) \* DEFINE GLOBAL SYMBOLS

(00017) \* APTSORC=SRFR  
 (00018) \* CSPUSMUS=S21FC  
 (00019) \* DAYS=S794  
 (00020) \* DPF=S1H  
 (00021) \* MW=3  
 (00022) \* MS=1  
 (00023) \* LIMITS=D'94  
 (00024) \* MSS=0  
 (00025) \* NPS=D'10  
 (00026) \* OFFSETS=D'15  
 (00027) \* RANGE=LIMITS-OFFSETS  
 (00028) \* SVTS=S03H2  
 (00029) \* SAS39=SVTS+2\*D'39  
 (00030) \* SAS61=SVTS+2\*D'61  
 (00031) \* SAS62=SVTS+2\*D'62  
 (00032) \* SAS46=SVTS+2\*D'46  
 (00033) \* START=\$ACHD  
 (00034) \* MS=2  
 (00035) \* ZS=1  
 (00036) \* DEFINE HARD WOULD BUFFERS  
 (00037) \* X1PS=D'204R  
 (00038) \* X0MS=D'206R  
 (00039) \*  
 (00040) \*  
 (00041) \* EXPAND ARRAY FUNCTION DISPATCH TABLE  
 (00042) \* EI=AFDISPRG+1+2\*(244-12R)  
 (00043) \* ADDR MCKXS(R7,1)  
 (00044) \* ADDR G11OS(R7,1)

PAGE 28 : FCH 244... "MPMNGI(Y.U.V.W)" EXTRACT M,PG.MOVE X

00RA4 0010215C (00045)  
(00046)

ADDN CSPUSMIS(,1,0)  
FJFCT





```

A23 06CCA 00000000C (00008) MXS6  NOP\MOV(R,00)
A24 06CCC 10000003R (00049)  JUMP(INVSK)
A25 06CCE 020000000 (00102) MXS3  R(A4)\NOP
A26 06CUD 040000000 (00103)  MOV(R,A4)\NOP
A27 06CUE 000000000 (00104)  RETURN
A28 06CU4 000000200 (00105) MXS4  NOP\R(A4)
A29 06CUG 000000000 (00106)  NOP\MOV(R,A6)
A2A 06CUM 000000000 (00107)  RETURN
A2B 06CUN 000000000 (00108)  *
A2C 06CUP 000000000 (00109)  * INVERSE SUHR
A2D 06CUC 100000000 (00110)  *R(FIP(X) IN M1, X IN M5. RETURNS 1/X IN P.
A2E 06CUE 000000000 (00111)  INV: MUL(M1,M5)
A2F 06CU4 000000000 (00112)  MOV(P,A0)
A2G 06CU4 000000000 (00113)  MOV(A1),SUH(A1,A0)
A2H 06CU4 000000000 (00114)  MOV(R,M6)
A2I 06CU4 000000000 (00115)  MUL(M1,M6)
A2J 06CU4 000000000 (00116)  MOV(P,M1)
A2K 06CU4 000000000 (00117)  MOV(A4),MUL(M1,M5)
A2L 06CU4 000000000 (00118)  MOV(P,A0)
A2M 06CU4 000000000 (00119)  SUH(A1,A0)
A2N 06CU4 000000000 (00120)  MOV(R,M6)
A2O 06CU4 000000000 (00121)  MUL(M1,M6)
A2P 06CU4 000000000 (00122)  *
A2Q 06CF2 000000000 (00123)  ?
A2R 06CF4 000000000 (00124)  RETURN
A2S 06CF6 000000000 (00125)  CLEAR(W1)\NOP
A2T 06CF8 000000000 (00126)  MOV(T0,A0)\NOP
A2U 06CF4 000000000 (00127)  PCP(A0)\NOP
A2V 06CFA 000000000 (00128)  MOV(T0A,M5)\NOP
A2W 06CFC 000000000 (00129)  MOV(R,M1)
A2X 06CFE 000000000 (00130)  CALL(INV)
A2Y 06D00 000000000 (00131)  MOV(T0A,M5)\NOP
A2Z 06D02 000000000 (00132)  MOV(P,M0)\NOP
A30 06D04 000000000 (00133)  MUL(M0,M5)\NOP
A31 06D06 000000000 (00134)  MOV(P,00)\NOP
A32 06D08 000000000 (00135)  *
A33 06D0A 000000000 (00136)  * YOM(I)=FLOAT(IN(NUTIPS=NP+1) I=0,1,2,...,NP-1)
A34 06D0C 000000000 (00137)  MOV(T0A,M7)
A35 06D0E 000000000 (00138)  MOV(T0A,A2)\NOP
A36 06D10 000000000 (00139)  NOP\MOV(T0A,A2)
A37 06D12 000000000 (00140)  *
A38 06D14 000000000 (00141)  MOV(K,M0)

```

NO=SM=100(MAX)  
NOW FORM 1.0/R(0)

R=1/0C  
A6=1.0C

R=1/0C  
A6=1.0C

X/X'  
2-X/X'

(2-X/X')/X'  
(2-X/X')/X'  
(2-X/X')\*X/X'

2-((2-X/X')\*X/X')

((2-X/X')/X')\*(2-((2-X/X')  
)X/X')

RELEASE APS INPUT  
R(0)

1/X'  
GET REAL INVERSE OF R(0)  
M5=R(MAX)  
M0=1.0/R0  
R(MAX)/R(0)  
NO=R'(MAX)

M7=SA=2\*\*15  
A2=UF  
A2=U0  
NRH<UF,FD>  
M0=NRH<UE>;M0=NRH<U0>

PAGE 6: PCH 244... "MPWNGX(Y,U,V,W)" EXTRACT M,PG,MOVE X

```
A47 06012 R47C47C (00142)
      (00143) 2
A48 06014 901A0043 (00144)
      (00145)
A49 06016 0HAC088C (00146) 01
      (00147) *
A4R 0601A 20172037 (00148) *
      (00149) * GO ON TO NEXT PORTION OF FUNCTION.
      (00150)
      EJECT

00=SA*NNM<UF(I-1),00(I-1
  )>.SA*NNM<UF,UD>
LOOP FOR I=0,1...NP-1
WAIT FOR OUTPUT TO CLEAR
LET AFS CONTINUE
```



```

(00151) *
(00152) *
(00153) *
(00154) *
(00155) *
(00156) *
(00157) *
(00158) *
(00159) *
(00160) *
(00161) *
(00162) *
(00163) *
(00164) *
(00165) *
(00166) *
(00167) *
(00168) *
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(00182) *
(00183) *
(00184) *
(00185) *
(00186) *
(00187) *
(00188) *
(00189) *
(00190) *
(00191) *
(00192) *
(00193) *
(00194) *

```

MWLD-APU PROGRAM

WEINER LEVINSON DURRAN INVERSE

MATHEMATICS

SFF J. MAKHOU, LINEAR PREDICTION REVIEW  
 PROC. IEEE, VOL. 63, PPS66, APR 1975

RUFFER DEFINITIONS

V(K), CORRELATION COEFFICIENTS, V(K)=R(K)  
 V(K) MUST BE COMPACT 32 BIT FLOATING POINT  
 VSIZE NOT USED IN COMPUTATION, BUT  
 VSIZE > USIZE FOR VALID RESULTS.

U(K), ACK COEFFICIENTS  
 U(K) MUST BE COMPACT 32 BIT FLOATING POINT  
 U(K)=A(K-1), IF A(0)=1 NOT IN RUFFER

Y(K), PARTIAL CORRELATIONS AND ERROR  
 Y(K) MUST BE COMPACT 32 BIT FLOATING POINT  
 YSIZE=2\*USIZE

Y(K)=P(K-1), 0<K<USIZE  
 Y(K+USIZE)=F(K-1), USIZE<=K<VSIZE

A, STABILITY TEST PARAMETER  
 IF /P(P)/ > CONTENTS OF (A), THEN  
 FOR J>0  
 A(N+J)=P(N+J)=0, F(V+J)=F(N)

M=LDSSA MOV(108,A7)ANOP A7=STABILITY TEST VALUE  
 MOV(ZERO,M4)  
 MOV(10,M5)ANOP M5=A5=F(0)=R(0)  
 MOV(108,A5)ANOP

MWLD-APU CALCULATION OF S(N)  
 S(N)=P(N) + SUM(I,N-1) OF R(N-J)A(N-1,J)  
 REGISTER CONTENTS AT START  
 M4=P(N-1), M5=A5=F(N-1), A6=1, A7=TEST

```

A50 06D24 2FA02FA0 (00195) B1      MOV(A5)
A51 06D26 08E20000 (00196)      MOV(10A,A2)\NOP
A52 06D28 08080808 (00197)      MOV(ZFPO,M0)
A53 06D2A 84008400 (00198)      MUL(M0,M4)
A54 06D2C 02400240 (00199)      MOV(M1),K(A2)
A55 06D2E 90080808 (00200)      JUMPC(MULDM4,AF2)
A56 06D30 9048085C (00202)      JUMPC(MULDSF,AF3),SFT
A57 06D32 20282028 (00203)      CLEAR(AF3)
A58 06D34 08FA0000 (00206) B2      MOV(10A,M2)\NOP
A59 06D36 08FE0000 (00207)      MOV(10A,M6)\NOP
A5A 06D38 85508550 (00208)      MOV(A0),MUL(M2,M6)
A5B 06D3A 42124212 (00209)      MOV(A2),ADD(A0,A2)
A5C 06D3C 08FA0000 (00211)      MOV(10A,M3)\NOP
A5D 06D3E 08FE0000 (00212)      MOV(10A,M7)\NOP
A5E 06D40 85F185F1 (00213)      MOV(A1),MUL(M3,M7)
A5F 06D42 42324232 (00214)      MOV(A2),ADD(A1,A2)
A60 06D44 90180058 (00215)      JUMPC(#2,FW1)
A61 06D46 08H008H0 (00218)      MOV(P,A0)
A62 06D48 42124212 (00219)      MOV(A2),ADD(A0,A2)
A63 06D4A 20372037 (00221)      MVI,DRF CLEAR(W1)
A64 06D4C 08FA0000 (00223)      *
A65 06D4E 3000002H (00224)      * MVI,DRF CALCULATION OF P(N)
A66 06D50 08AFO8AF (00225)      * P(N)=S(N)/F(N-1)
A67 06D52 A540A540 (00226)      * REGISTER CONTENTS AT START
A68 06D54 16H016H0 (00227)      * M4=P(N-1), A5=M5=F(N-1), A6=1, A7=TEST
A69 06D56 08960896 (00228)      * M1=RCPI(F(N-1)), R=S(N)
A6A 06D58 08R0808R (00230)      *
A6B 06D5A 08R0808R (00231)      *
A6C 06D5C 08R0808R (00232)      *
A6D 06D5E 08R0808R (00233)      *
A6E 06D60 08R0808R (00234)      *
A6F 06D62 08R0808R (00235)      *
A70 06D64 08R0808R (00236)      *
A71 06D66 08R0808R (00237)      *
A72 06D68 08R0808R (00238)      *
A73 06D6A 08R0808R (00239)      *
A74 06D6C 08R0808R (00240)      *
A75 06D6E 08R0808R (00241)      *
A76 06D70 08R0808R (00242)      *
A77 06D72 08R0808R (00243)      *
A78 06D74 08R0808R (00244)      *
A79 06D76 08R0808R (00245)      *
A7A 06D78 08R0808R (00246)      *
A7B 06D7A 08R0808R (00247)      *
A7C 06D7C 08R0808R (00248)      *
A7D 06D7E 08R0808R (00249)      *
A7E 06D80 08R0808R (00250)      *
A7F 06D82 08R0808R (00251)      *
A80 06D84 08R0808R (00252)      *
A81 06D86 08R0808R (00253)      *
A82 06D88 08R0808R (00254)      *
A83 06D8A 08R0808R (00255)      *
A84 06D8C 08R0808R (00256)      *
A85 06D8E 08R0808R (00257)      *
A86 06D90 08R0808R (00258)      *
A87 06D92 08R0808R (00259)      *
A88 06D94 08R0808R (00260)      *
A89 06D96 08R0808R (00261)      *
A8A 06D98 08R0808R (00262)      *
A8B 06D9A 08R0808R (00263)      *
A8C 06D9C 08R0808R (00264)      *
A8D 06D9E 08R0808R (00265)      *
A8E 06DA0 08R0808R (00266)      *
A8F 06DA2 08R0808R (00267)      *
A90 06DA4 08R0808R (00268)      *
A91 06DA6 08R0808R (00269)      *
A92 06DA8 08R0808R (00270)      *
A93 06DAA 08R0808R (00271)      *
A94 06DAC 08R0808R (00272)      *
A95 06DAE 08R0808R (00273)      *
A96 06DB0 08R0808R (00274)      *
A97 06DB2 08R0808R (00275)      *
A98 06DB4 08R0808R (00276)      *
A99 06DB6 08R0808R (00277)      *
AA0 06DB8 08R0808R (00278)      *
AA1 06DBA 08R0808R (00279)      *
AA2 06DBC 08R0808R (00280)      *
AA3 06DBE 08R0808R (00281)      *
AA4 06DB8 08R0808R (00282)      *
AA5 06DBA 08R0808R (00283)      *
AA6 06DBC 08R0808R (00284)      *
AA7 06DBE 08R0808R (00285)      *
AA8 06DB8 08R0808R (00286)      *
AA9 06DBA 08R0808R (00287)      *
AAA 06DBE 08R0808R (00288)      *
AAB 06DB8 08R0808R (00289)      *
AAC 06DBA 08R0808R (00290)      *
AAD 06DBE 08R0808R (00291)      *
AAE 06DB8 08R0808R (00292)      *
AAF 06DBA 08R0808R (00293)      *
AAG 06DBE 08R0808R (00294)      *
AAH 06DB8 08R0808R (00295)      *
AAI 06DBA 08R0808R (00296)      *
AAJ 06DBE 08R0808R (00297)      *
AAK 06DB8 08R0808R (00298)      *
AAL 06DBA 08R0808R (00299)      *
AAO 06DBE 08R0808R (00300)      *

```

S(N)  
GET 1.0/F(N-1)  
1.0/F

FOLLOWING CODE NEEDS A6=  
1

M0=M4=A4=P(N)







TO=NM(I)  
 FOR I=NTUPS-NP+1,...,NTUPS  
 -1  
 STALL

GO TO MWD PORTION

ENABLE API  
 K(.) RA  
 R(.) SIZE-1  
 R(.) RA-2  
 NO=R(I)  
 FOR I=0,1,2,...,LPCN+1

NO=XIR(MAX)  
 NO=M

NO=XIR'(MAX)

NO=?

NO=?  
 XOM(.) RA  
 XOM(.) SIZE-1  
 XOM(.) BA-2  
 NO=XOM(J)  
 FOR J=0,1,...,NP-1

OUTPUT DONE!

A1F 06F2 3F0A0001 (00371) IFLTSI ADD(HW0,MS,JK)  
 A20 06F4 402A1F01 (00372) SUBL(HW2,I),JUMPP(IFLTSI)

A21 06F6 42300037 (00374) SFT(MI)  
 A22 06F8 44000020 (00375) NOP

A23 06FA 46003A60 (00377) JUMPP(MKXSS)

A26 06FC 48300032 (00379) SET(RA)

A25 06FE 4A400018 (00380) LOAD(HW0,(0))

A26 06F0 4C500000 (00381) LOAD(HW1,MSS)

A27 06F2 4E620000 (00382) SUB(HW0,MSS)

A28 06F4 50FAR006 (00383) ADD(HW0,(HI),TF)

A29 06F6 521128A1 (00384) SUBL(HW1,I),JUMPP(BI)

A2A 06F8 54C203FC (00385) \*

A2A 06F8 54C203FC (00386) SCLRS LOAD(HW0,SASH(1),TF)

A2B 06FA 56C203FE (00387) LOAD(HW0,SASH2(1),TF)

A2C 06FC 58C203FC (00388) \*

A2C 06FC 58C203FC (00389) TMSD LOAD(HW0,SASH(1),TF)

A2D 06FE 5AC20794 (00391) UFLTS LOAD(HW0,JMYS(1),TF)

A2E 06F0 5CC20794 (00392) LOAD(HW0,PMYS(1),TF)

A2F 06F2 5F440814 (00393) LOAD(HW0,XOMS(2))

A30 06F4 60500009 (00394) LOAD(HW1,NPS-1)

A31 06F6 62020002 (00395) SUB(HW0,2)

A32 06F8 648A0002 (00396) UFLTSJ ADD(HW0,MS,TF)

A33 06FA 661132A1 (00397) SUBL(HW1,I),JUMPP(DFLTSJ)

A34 06FC 68200030 (00399) DNFSJ CLEAR(RU)

A35 06FE 6A000020 (00401) \*

(00402) \*

(00403) \*

(00404) \*

(00405) \*

(00406) \*

(00407) \*

A36 06F20 6CF2042F (00408) MKGXS LOAD(HW2,SASR(1),I,TF)

A37 06F27 6FC0001F (00410) LOAD(HW0,(0),I,TF)

A38 06F24 70500000 (00411) LOAD(HW1,MSS)

A39 06F26 720A0000 (00412) ADD(HW0,MSS)

A3A 06F28 74500000 (00413) LOAD(HW1,0)

A3B 06F2A 76010013 (00414) MOVH(HW0,API)

INPUT STARTLITY TEST

INPT R(0)

NOT USED

HR0=R(1)

HW0=0=2(N-1)



```

(00459) *
A52 06F58 A4194FH4 (00460) MWLAF SUBR(RW1,4), JUMPP(B3)
(00461) *
A53 06F5A A670302F (00462) MWLT LOAD(RW1,13),L)
A54 06F5C A8500000 (00463) LOAD(RW1,MSS)
A55 06F5E AA320000 (00464) SUB(RW3,MSS)
A56 06F60 AC19002H (00465) ADDR(RW1,RW1)
A57 06F62 AF49497H (00466) ADDL(RW2,0,TF), JUMP(BA+1)
A58 06F64 A0300037 (00467) SFT(W1)
A59 06F66 R2000020 (00468) NOP(0)
A5A 06F68 H3194FH0 (00469) SUBR(RW1,RW0), JUMPP(B1)
A5B 06F6A H67059F1 (00470) JUMPS(BA-2,R1),CLEAR
(00471) *
(00472) *
(00473) * MWLD = APS OUTPUT
(00474) *
(00475) * REGISTER CONTENTS AT START
(00476) * RW0=2N, RW2=A(N)
(00477) *
(00478) *
(00479) * MWLDT
A5C 06F6C H9300030 (00480) SFT(R0)
A5D 06F6E HAH10014 (00481) MOVR(RW3,RW2,TF)
A5E 06F70 HC210020 (00482) SUBR(RW2,RW0)
A5F 06F72 HD020794 (00483) LOAD(RW1,DMYS(1),TF)
A60 06F74 C091003H (00484) ADDL(RW1,0,TF)
A61 06F76 C2116450 (00485) *
A62 06F78 C4A00007 (00486) #4 ADD(RW2,2,TF)
A63 06F7A C6R20002 (00487) * SUBR(RW3,2,TF)
(00488) *
A64 06F7C CH1162H4 (00489) MWLDF SUBR(RW1,4),JUMPP(B4)
A65 06F7E CA602024 (00490) LOAD(RW2,F2),L)
A66 06F80 CC500000 (00491) LOAD(RW1,MSS)
A67 06F82 CF220000 (00492) SUB(RW2,MSS)
A68 06F84 D01A0001 (00493) ADD(HW1,1)
A69 06F86 D2A10028 (00494) ADDR(RW2,RW0,TF)
A6A 06F88 D4A1002A (00495) ADDR(RW2,RW1,TF)
(00496) *
A6B 06F8A D6000020 (00497) #5 NOP(0)
A6C 06F8C D8006H8H (00498) * JUMPC(B5,AP0)
(00499) *
A6D 06F8E DA206F6H (00500) * JUMP(BA+1,AP0),CLEAR
A6E 06F90 DC007DA9 (00501) * JUMPC(HW1,0D,AF1)
(00502) *

```

RW1=A(1)=AH  
 ASIZE=N,DONE  
 RW1=A(0)=AH-2  
 RW1=2N-2, DONE

CLEANUP OF A(N) CALC

RW3=A(N), OUTPUT  
 RW2=A(0)  
 DTPT DUMP  
 DTPT DUMP  
 RW1=2N

DTPT A(N,J)  
 DTPT A(N,N-1)

RW2=P(1)=PR

RW2=P(0)=PR-2  
 RW1=PSIZE=2ASIZE  
 DTPT P(N)  
 DTPT E(N)

WAIT ON APU

JUMP TO DONE IF NO ABORT





AEDTSORG:	0001H (0001R) (0004Z)
CSPUSMUS:	021FC (0001Q) (0004S)
DAYS:	00704 (00020) (00391) (00392) (00487)
DMFSL:	00023 (00377)
DMFSN:	00034 (00399)
EXRCTS:	00003 (00066)
FITS:	00043 (00138) (00144)
GILOS:	060R4 (00044) (00330) (00336) (00528)
GILOS0:	00024 (00337) (00379)
HS:	00001 (00023) (00371)
IPITS:	00014 (00364)
IFLTS1:	0001F (00371) (00372)
INV:	0002R (00111) (00130) (00232)
INVS1:	00016 (00361)
INVS0:	0002C (00389)
INVS2:	0003H (00047) (00049) (00125)
INVS3:	0004F (00024) (00028)
LIMITS:	00000 (00025) (00343) (00346) (00366) (00381) (00382) (00411) (00412) (00416)
MS:	00000 (00417) (00463) (00464) (00491) (00492) (00510) (00511)
MGXS:	06CMA (00043) (00060)
MGXSA:	06F9C (00333) (00523)
MGXSI:	06F47 (00328) (00527)
MGXSS:	00036 (00330) (00377) (00408)
MGXSSZ:	00094 (00059) (00372)
MGXSZ:	00110 (00332) (00528)
MWAF:	00052 (00454) (00460)
MWAFSA:	0004C (00185) (00316)
MWAFS:	0007A (00276) (00320)
MWAFW:	00047 (00260) (00319)
MWAFD:	0004F (00301) (00315)
MWAFR:	00063 (00221) (00318)
MWAFW:	00040 (00200) (00317) (00317)
MWAFS:	0005C (00202) (00211)
MWAFD:	0007C (00512) (00518)
MWAFD:	0007D (00501) (00520)
MWAFD:	00064 (00484) (00489)
MWAFD:	0005C (00452) (00479)
MWAFS:	0004H (00428) (00436) (00438)
MWAFS:	00045 (00429) (00433)
MWAFS:	00053 (00462)
MWAFS:	00014 (00073) (00083)
MWAFS:	00019 (00078) (00088)
MWAFS:	00025 (00080) (00090) (00102)
MWAFS:	0002H (00076) (00085) (00105)

MXS:	00010	(00007)	(00092)
MXS:	00023	(00094)	(00098)
NPS:	0000A	(00026)	(00369)
OFFSFT:	04FK0	(00052)	(00370) (00394)
OFFSFTS:	0000F	(00027)	(00028) (00353)
OFFTS:	00020	(00391)	
OFFTSU:	00032	(00396)	(00397)
OP:	00010	(00021)	
PITCSSA:	00000	(0005H)	(00062) (00063) (00372)
RANGS:	0004F	(00028)	(00354)
SAS3:	00300	(00030)	(00364)
SAS61:	003FC	(00031)	(00362) (00386) (00389)
SAS62:	003FF	(00032)	(00387)
SASHA:	0042F	(00033)	(00408)
SCLPS:	0002A	(00386)	
START:	06C80	(00034)	(00050)
SVTS:	00342	(00029)	(00030) (00031) (00032) (00033)
WS:	00002	(00035)	(00396)
XIPS:	00800	(00038)	(00340) (00350)
XIMS:	00M13	(00039)	(00393)
ZS:	00003	(00036)	

(00001) \* FCH 245... "MPFSTV(0)" CREATE PITCH & VOCAL TRACT RESPONSE  
(00002) \*  
(00003) \*  
(00004) \* DEFINE GLOBAL SYMBOLS  
(00005) \*  
(00006) \*  
(00007) \*  
(00008) \*  
(00009) \*  
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(00035) \*  
(00036) \*

OPADD EXP, (1 .US, 14) + (17 .US, 8) + S1F  
OPADD VOF, (1 .US, 10) + (12 .US, 5) + S1H  
AFD1BURG=SREFH  
APSSMFM=SIFFCO  
AS1ZS=D'H'-D'1'  
CSPUSMUS=S21FC  
DMYS=\$794  
IM=3  
HS=1  
LTHMIS=D'256'-D'1'  
MS=0  
MSS=0  
SVTS=S03R2  
SAS1R=SVTS+2\*D'3R'  
SAS92=SVTS+2\*D'92'  
SAS90=SVTS+2\*D'90'  
SAS91=SVTS+2\*D'91'  
SAS97=SVTS+2\*D'97'  
SAS73=SVTS+2\*D'73'  
S1ZFS=D'512'-D'1'  
START=\$6H70  
MS=2  
MWS=4  
X1S=D'204R'  
X1PS=X1S

00000RER (00007)  
0001FFCO (00008)  
00000007 (00009)  
000021FC (00010)  
00000794 (00011)  
00000003 (00012)  
00000001 (00013)  
000000FF (00014)  
00000000 (00015)  
00000000 (00016)  
000003R2 (00017)  
000003CF (00018)  
0000043A (00019)  
00000436 (00020)  
0000043H (00021)  
00000444 (00022)  
00000414 (00023)  
000003FF (00024)  
0000R70 (00025)  
00000002 (00026)  
00000004 (00027)  
00000R00 (00028)  
00000000 (00029)  
00000000 (00030)  
000000A6 (00031)  
000001F6R72 (00032)  
000R00 001F6C02 (00033)  
000R00 001021FC (00034)  
000R00 001021FC (00035)  
000R00 001021FC (00036)

EXPAND ARRAY FUNCTION DISPATCH TABLE

#1=AFDTSURG+1\*2\*(245-12H)  
ADDR VOCOS(R7,1)  
ADDR G107S(R7,1)  
ADDR CSPUSMUS(.1,0)  
EJECT





A24 06HBA 081C081C (00087)	MOV(ZERO,00)	Z4 OUT
A25 06HBC 081C081C (00088)	MOV(ZERO,00)	Z5 OUT
A26 06HBE 081C081C (00089)	MOV(ZERO,00)	Z6 OUT
A27 06HCF 081C081C (00090)	MOV(ZERO,00)	Z7 OUT
A28 06HC2 40004900 (00091)	SUB(A0,A1)	M'-1 FOR LATER TESTAN'-1
A29 06HC4 081C081C (00092)	MOV(ZERO,00)	4
A2A 06HC6 081C081C (00093)	MOV(ZERO,00)	Z8 OUT
A2B 06HCH 081C081C (00094)	MOV(ZERO,00)	Z9 OUT
A2C 06HCK 081C081C (00095)	MOV(ZERO,00)	Z10 OUT
A2D 06HCC 081C081C (00096)	MOV(P,M4)\NOP	G**P
A2E 06HCE 081C081C (00097)	MOV(ZERO,00)	Z11 OUT
A2F 06HCF 081C081C (00098)	MOV(ZERO,00)	Z12 OUT
A30 06HD0 081C081C (00099)	MOV(ZERO,00)	Z13 OUT
A31 06HD4 081C081C (00100)	MOV(P,A6)\NOP	G**P, TO ADD TO SUMG
A32 06HD6 49104A10 (00101)	MOV(ZERO,00)	Z14+ OUT
A33 06HDA 901E0031 (00102)	MOV(A0,SUB(A0,A1)\MOV(A0),SUR(A0,A2)	M'-1-L;M'-1-L-1\N'-1-L-L;
A34 06HDB 02400490 (00103)	JUMPC(#2,T1)	N'-15-L
A35 06HDC 081C081C (00104)	MUST BE TIME FOR PULSE	LOOP TIL M'-1-L .LF. 0
A36 06HDE 46000000 (00105)	P(A2)\MOV(R,A0)	RESTORE M\COUNT PULSE A
A37 06HE0 901E001F (00106)	MOV(P,00)\MOV(ZERO,00)	S OUTPUT
A38 06HE2 04954060 (00107)	MOV(A0,ADD(A5,A6)\NOP	PUT OUT PULSE
A39 06HE4 081C081C (00108)	JUMPC(#1,T2)	M';ADD PULSE TO SUMG
A3A 06HE6 00004A10 (00109)	MOV(R,A5)\ADD(A3,A0)	LOOP IF WE NEED MORE PUL
A3B 06HE8 901E0039 (00110)	MOV(ZERO,00)	SFS
A3C 06HEA 049C0000 (00111)	NOP\MOV(A0),SUR(A0,A2)	SUMG\N'+M=1 IS .LT. M, #
A3D 06HEC 20320332 (00112)	JUMPC(#3,T2)	SAVE COUNT;DECREMENT
A3E 06HEE 00000000 (00113)	MOV(R,00)\NOP	LOOP UNTIL WE HAVE ENOUGH
A3F 06HEF 00000000 (00114)	CLFAR(CA)	H ZEROS
A40 06HF0 00000000 (00115)	NOP	SUMG OUT
A41 06HF2 00000000 (00116)	NOP	
A42 06HF4 00000000 (00117)	JUMP(MOGD)	
A43 06HF6 10000047 (00118)	NOP	
A44 06HF8 00000000 (00119)	VOCSSZ=#A-VIUCSSA	
A45 06HFA 00000044 (00120)	END VIUCSSZ	
A46 06HFB 00000000 (00121)	F.I.F.C.T	
A47 06HFD 00000000 (00122)		
A48 06HFE 00000000 (00123)		
A49 06HFF 00000000 (00124)		
A50 06H00 00000000 (00125)		
A51 06H02 00000000 (00126)		
A52 06H04 00000000 (00127)		

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0A6FA 00006C14 *
0A6FC 00006C04 *
0A6FE 0000 *
0A6FF 0014 *
0AC00 00006C0C *
0A6C02 00200040 *
0A6C04 02300030 *
0A6C06 04C2043A *
0A6C08 06C20438 *
0A6C0A 08C20436 *
0A6C0C 0AC01000 *
0A6C0E 0C500000 *
0A6C10 0E500000 *
0A6C12 10500006 *
0A6C14 128A0007 *
0A6C16 14900981 *
0A6C18 16200031 *
0A6C1A 18000020 *
0A6C1C 1A300032 *
0A6C1F 1CC60802 *
0A6C20 1F5001FF *
0A6C22 20820002 *
0A6C24 228A0006 *
0A6C26 24820007 *
0A6C28 26111181 *
0A6C2A 28C20794 *
0A6C2C 2AC20794 *
0A6C2E 2CC20414 *
0A6C30 2F200030 *
0A6C32 30000020 *

00006C0C *

0A6FA 00006C14 *
ADDR G1075T
ADDR G107S+2+SC1MS
DATA 0
DATA G107SZ
ADDR G107SA
EVEN

REGIN APS(G107)
JRN(G107SD,P2)
SPT(R0)
LOAD(RW0,SAS92(1),TF)
LOAD(RW0,SAS91(1),TF)
LOAD(RR0,SAS90(1),TF)
LOAD(RR0,(1),TF)
LOAD(RR1,MSS)
LOAD(RR1,MSS)
LOAD(RR1,6)

ADD(RR0,2,TF)
SURF(RR1,1),JUMPP(81)
CFAR(RI)
NOP

SPT(MA)
LOAD(RW0,X1S+2(1),TF)
LOAD(RW1,SIZES-1)
SURF(RW0,2,TF)
ADD(RW0,6,TF)
SURF(RW0,2,TF)
SURF(RW1,1),JUMPP(81)
FOP N=1,2,...,255
LOAD(RW0,DMYS(1),TF)
LOAD(RW0,DMYS(1),TF)
LOAD(RW0,SAS73(1),TF)
CFAR(R0)
NOP

G107SA= 4C
    
```

POINTER TO CONSTRUCTED I  
 NSTRUCTION BLOCK  
 POINTER TO SCALAR BLOCK  
 NUMBER OF SCALARS  
 MODULE SIZE  
 POINTER TO CHAIN ANCHOR  
 FWD (IN WORD BOUNDARY)

SET OUTPUT PC(P2)  
 FNARIE OUTPUT ADDRESSING  
 512  
 M  
 G  
 A1  
 DUMMY  
 DUMMY  
 COUNT A'S, NEED 8 ALL TO  
 (P  
 A(N)

ENARIE APU  
 XI(.) HA+2 (=XI(0))  
 XI(.) SIZE-2  
 XIR(0)  
 XIR(N)  
 XIR(N)

TWO DUMMIES OUT  
 GSUM = ENERGY OUT

ASSIGN VALUE TO CHAIN AN  
 CHDR



PAGE 6: FCR 245... "MPFSV(U)" CREATE PITCH & VOCAL TRACT RESPONSE

06C34 (00172) PND 9A-1  
(00173) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS  
06C34 00000000 (00174) G107SI DATA IP'0.0'  
00000034 (00175) G107S7=8U-G107S  
06C36 (00176) END

AFDYSORG:	00NFR (00007)	(00032)
APSSMEM:	1FFC0 (0000H)	
ASTZFS:	00007 (00009)	
CIHM:	0002A (000M1)	(00094)
CSVUSMUS:	021FC (00010)	(00035)
DMS:	00794 (00011)	(00164)
G1078:	06C02 (00034)	(00134) (00140) (00175)
G1078A:	06C0C (00137)	(00170)
G1078I:	06C14 (00132)	(00174)
G1078O:	00000 (00141)	(00156)
G1078Z:	00014 (00136)	(00175)
HS:	00001 (00013)	
LTHMIS:	000FF (00014)	
MS:	00000 (00015)	
MSS:	00000 (00016)	
NOGDI:	00042 (00070)	(00147) (00148) (00123) (00123)
SAS3R:	003CF (0001H)	
SAS73:	00414 (00023)	(00166)
SAS90:	00416 (00020)	(00145)
SAS91:	00419 (00021)	(00144)
SAS92:	0043A (00019)	(00143)
SAS97:	00444 (00022)	
SLIHS:	00001 (00134)	(00142)
STZFS:	001FF (00024)	(00159)
START:	00A70 (00025)	(00040)
SVTS:	00A92 (00017)	(00019) (00019) (00021) (00022) (00073)
VOCUS:	00R12 (00033)	(00049)
VOCUSSA:	00000 (00047)	(00051) (00125)
VOCUSSZ:	00044 (00048)	(00125) (00126)
WS:	00002 (00026)	
WWS:	00004 (00027)	
X1S:	00000 (00028)	(00029) (00157)
X1RS:	00000 (00029)	

FCM 247... "MPSRRT(Y)" SORT DCT1(.) COEFFICIENTS  
ORIGINATED:13-DEC-79  
UPDATED:30-MAY-80

DEFIN: GLOBAL SYMBOLS  
OPADD EXP, (1 .U.S. 14) + (17 .U.S. 8) + S1K  
OPADD INF, (3 .U.S. 10) + (12 .U.S. 5) + S1R  
OPADD WAFF, (1 .U.S. 10) + (17 .U.S. 5) + S1A  
AFDTSORG=SRER  
APSSMEM=SIFCO  
CSPUSNOS=S21FC  
DCTS=D'2344'  
DCT1S=D'517'  
DCT2S=D'1024'  
DMS=S794  
SM=3  
HS=1  
INCRFS=D'256'  
INCRS=S1F24  
INRPRS=D'10'  
ISVTS=S402  
TSAS00=ISVTS+D'10'  
TSAS01=ISVTS+D'11'  
MS=0  
OVRSLAP=D'10'  
SIZES=D'256'-D'11'  
SVTS=S0382  
START=\$H300  
TABLS0=D'3072'  
TABLS1=TARI.S0\*INCRFS  
TABLS2=TARI.S1\*INCRFS  
TABLS3=TARI.S2\*INCRFS  
TMP2S=D'2560'  
TMP4S=D'3072'  
WS=2  
ZS=3

(00001) \* FCM 247... "MPSRRT(Y)" SORT DCT1(.) COEFFICIENTS  
(00002) \*  
(00003) \*  
(00004) \*  
(00005) \*  
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(00040) \*  
(00041) \*  
(00042) \*

EXPAND ARRAY FUNCTION DISPATCH TABLE  
#I=AFDTSORG+1\*2\*(247-128)  
ADDR SORTS(P7,1)  
ADDR G10AS(P7,1)  
ADDR CSPUSNOS(,1,0)  
F.FACT





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A24 04350 90160000 (00110)      JUMPC(INC1,FW1)
A25 04352 1045001A (00111)      JUMPC(MTSOUT,G1),SET
A26 04354 20490000 (00112) LVL$1  SET(AF1)\NOP
A27 04356 20530000 (00113)      SET(IN)\NOP
A28 04358 00004720 (00114)      NOP\ADD(A1,A7)
A29 0435A 04F004F4 (00115)      MOV(10A,A0)\MOV(10A,A4)
A2A 0435C 04F004F1 (00116)      MOV(10A,00)\MOV(H,A1)
A2B 0435E 20330000 (00117)      CLEAR(IN)\NOP
A2C 04360 44004040 (00118)      SUB(A0,A1)\SUB(A4,A5)
A2D 04362 91000020 (00119) #1    JUMPS(B1,AF1)
A2E 04364 90160000 (00120)      JUMPC(INC1,FW1)
A2F 04366 1045003A (00121)      JUMPC(MTSOUT,G1),SET
A30 04368 20490000 (00122) LVL$0  SET(AF0)\NOP
A31 0436A 20530000 (00123)      SET(IN)\NOP
A32 0436C 00004700 (00124)      NOP\ADD(A0,A7)
A33 0436E 04F004F4 (00125)      MOV(10A,A0)\MOV(10A,A4)
A34 04370 04F004F0 (00126)      MOV(10A,00)\MOV(H,A0)
A35 04372 20330000 (00127)      CLEAR(IN)\NOP
A36 04374 49004040 (00128)      SUB(A0,A1)\SUB(A4,A5)
A37 04376 91000037 (00129) #1    JUMPS(B1,AF0)
A38 04378 90160000 (00130)      JUMPC(INC1,FW1)
A39 0437A 20450000 (00131)      SET(G1)\NOP
A3A 0437C 00000200 (00132)      NOP\RC(A0)
A3B 0437E 0000023C (00133)      NOP\MOV(00),R(A1)
A3C 04380 0000025C (00134)      NOP\MOV(00),R(A2)
A3D 04382 0000027C (00135)      NOP\MOV(00),R(A3)
A3E 04384 0000049C (00136)      NOP\MOV(R,00)
A3F 04386 04FC041C (00137)      MOV(Z*F0,00)
A40 04388 04FC041C (00138)      MOV(Z*F0,00)
A41 0438A 00000000 (00139) #1    NOP
A42 0438C 901C0041 (00140)      JUMPC(B1,F0)
A43 0438E 20370000 (00141)      CLEAR(C1)\NOP
A44 04390 20500000 (00142)      SET(R0)\NOP
A45 04392 04FC0000 (00143)      MOV(10A,00)\NOP
A46 04394 04FC0000 (00143)      MOV(10A,00)\NOP
A47 04396 04FC0000 (00145)      MOV(10A,00)\NOP
A48 04398 04FC0000 (00146)      MOV(10A,00)\NOP
A49 0439A 00000000 (00147) #7    NOP
A4A 0439C 901C0049 (00148)      JUMPC(B2,F0)
A4B 0439E 20370000 (00149)      CLEAR(C1)\NOP
A4C 043A0 20500000 (00150)      SET(R0)\NOP
A4D 043A2 04FC0000 (00151) (R)MPS1  MOV(10A,00)\NOP
A4E 043A4 04FC0000 (00152)      MOV(10A,00)\NOP
A4F 043A6 04FC0000 (00153)      MOV(10A,00)\NOP

FOR I=0,1,2...LTH-1
GET LEVEL COUNTS
INFORM APS OF LVL1
FIXED POINT ADD
LVL1 CNT+1
A0=DCT1(I+1);A4=D
00=LVL1=LVL1+1
RESUME FLOATING POINT!
D-THRESH1:D-THRESH2

FOR I=0,1,2...LTH-1
GET LEVEL COUNTS
INFORM APS OF LVL0
FIXED POINT ADD
LVL0 CNT+1
A0=DCT1(I+1);A4=D
00=LVL0=LVL0+1
RESUME FLOATING POINT!
D-THRESH1:D-THRESH2

FOR I=0,1,2...LTH-1
GET LEVEL COUNTS
R=LVL0 CNT
R=LVL1 CNT
R=LVL2 CNT
R=LVL3 CNT
00=LVL3 CNT(0)DPTD LTH)
00=0 FOR INSTR$4+1...
WAIT FOR...
*LEVELS* TO SETTLE
RELEASE APS INPUT
RELEASE APS OUTPUT
SCATTER-WRITE INTO INS0
SCATTER-WRITE INTO INS1
SCATTER-WRITE INTO INS2
SCATTER-WRITE INTO INS3
WAIT FOR...
*WRITES* TO SETTLE
RELEASE APS INPUT
RELEASE APS OUTPUT
00=TORDR(I)
00=TORDR(I+2)

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A76 0R3FR 0RFC0000 (0019H)
A77 0R3FA 0RFC0000 (0019H)
A78 0R3FC 00000RFC (00200)
A79 0R3FF 00000RFC (00201)
A7A 0R400 0R420R00 (00202)
A7B 0R402 00000R42 (00203)
A7C 0R404 425C425C (00204)
A7D 0R406 0RFC0000 (00205)
A7E 0R408 00000RFC (00206)
A7F 0R40A 0RFC0000 (00207)
A80 0R40C 0RFC0000 (00208)
A81 0R40E 0RFC0000 (00209)
A82 0R410 00000RFC (00210)
A83 0R412 1000006F (00211)
A84 0R414 20300000 (00212)
A85 0R416 20320000 (00213)
A86 0R418 00000000 (00214)
A87 0R41A 00000000 (00215)
A88 0R41C 000000R9 (00216)
A89 0R41E 000000R9 (00217)
A8A 0R420 000000R9 (00218)
A8B 0R422 000000R9 (00219)
A8C 0R424 000000R9 (00220)

MOV(10A,00)\NOP
MOV(10A,00)\NOP
NOP\MOV(10A,00)
NOP\MOV(10A,00)
MOV(10A,A7)\NOP
NOP\MOV(10A,A7)
MOV(00),ADD(A2,A7)

MOV(10A,00)\NOP
NOP\MOV(10A,00)
MOV(10A,00)\NOP
MOV(10A,00)\NOP
NOP\MOV(10A,00)
NOP\MOV(10A,00)
JUMP(SHUFFLS)

CLEAR(RO)\NOP
CLEAR(PA)\NOP
NOP
SORTSSZ=#A-SORTSSA
END SORTSSZ
EJECT

DCT2(I)=TMP2(J*)
TMP4(I)=DCT1(J*)
DCT2(I+1)=TMP2(K*)
TMP4(I+1)=DCT1(K*)
A2=J1
A2=K1
J1->S4+1,J11;?K1->S5+1
?K11
INSTRS6 INTO INDS6
INSTRS7 INTO INDS7
DCT2(I+2)=TMP2(J*)
TMP4(I+2)=DCT1(J*)
DCT2(I+3)=TMP2(K*)
TMP4(I+3)=DCT1(K*)
"JUMP(...)" SETTLES I/O

APS CANNOT TURN ITSELF 0
FF!
API DONE!

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A1F 0R460 3F1421F1 (00265) TSTSI0 SURL(RR1,1),JUMP(N(STALS1)
A20 0R462 41091F79 (00266) ADDL(RR0,HS,TF),JUMP(TSTS1,0)
A21 0R464 42300037 (00267) STALS1 SFL(WT)
A22 0R466 44500103 (00268) LOAD(HR1,SIZE,S+4)
A23 0R468 47400000 (00270) LOAD(HR0,[0],TF)
A24 0R46A 48600000 (00271) LOAD(HR2,MSS)
A25 0R46C 480A0000 (00272) ADD(HR0,MSS,TF)
A26 0R46E 410A0001 (00273) ADD(HR0,HS,TF)
A27 0R470 4F0A0001 (00274) ADD(HR0,HS,TF)
A28 0R472 50F41F1C (00275) LOAD(HR2,INSTRS4(2),TF)
A29 0R474 52F41F1F (00276) LOAD(HR2,INSTRS4(2),TF)
A2A 0R476 54600000 (00277) LOAD(HR2,MSS)
A2B 0R478 562F0000 (00278) ADD(HR2,MSS,NA,C)
A2C 0R47C 5A2F0000 (00280) ADD(HR2,MSS,NA,C)
A2D 0R47E 510A0001 (00281) ADD(HR0,HS,TF)
A2E 0R480 5F0A0001 (00282) ADD(HR0,HS,TF)
A2F 0R482 60F41F20 (00283) LOAD(HR2,INSTRS6(2),TF)
A30 0R484 62F41F22 (00284) LOAD(HR2,INSTRS7(2),TF)
A31 0R486 64800000 (00285) LOAD(HR2,MSS)
A32 0R488 662F0000 (00286) ADD(HR2,MSS,NA,C)
A33 0R48C 68600000 (00287) ADD(HR2,MSS)
A34 0R490 6A2F0000 (00288) ADD(HR2,MSS,NA,C)
A35 0R492 6C192684 (00289) SURL(HR1,4),JUMPP(INDSJ)
A36 0R494 6E2901 (00290) *
A37 0R496 6F200031 (00291) DNEF1
A38 0R498 70000020 (00292) NOP
A39 0R49A 72204040 (00293) JSN(G106S0,P2)
A3A 0R49C 74760A00 (00294) LOAD(HR1,TEMP2S(3))
A3B 0R49E 76A90020 (00295) ADD(HR3,HR2,TF)
A3C 0R4A0 78760200 (00296) LOAD(HR3,DCT1S(3))
A3D 0R4A2 7A900020 (00297) ADD(HR3,HR2,TF)
A3E 0R4A4 7C2F0000 (00298) ADD(HR2,MSS,NA,C)
A3F 0R4A6 7E003A00 (00299) JUMPP(TOPSP1)
A40 0R4A8 80306D40 (00301) G106S0
A41 0R4AA 824600FF (00302) LOAD(HR0,TARISO-1(3))
A42 0R4AC 845600FF (00303) LOAD(HR1,TARIS1-1(3))
A43 0R4AE 866600FF (00304) LOAD(HR2,TARIS2-1(3))
A44 0R4B0 887600FF (00305) LOAD(HR3,TARIS3-1(3))
A45 0R4B2 8A0053E5 (00306) FLAGSTST
A46 0R4B4 8C0040E4 (00307) JUMPS(INSPTS0,AF1)
A47 0R4B6 8E0040E9 (00308) JUMPS(INSPTS1,AF1)

PICKUP TILL NO MORE
IO=TARIFO(I)
STALL APS INPUT
TORDR(.) SIZE-1+4

TORDR(.) HA,IO=TORDR=J
DUMMY OP FOR EXEC
IO=TORDR(1)=K
IO=J=TORDR(1+2)
IO=K'=TORDR(1+3)
OVERWRITE INDS4
OVERWRITE INDS5
MSS=2J,PC->PC1
RESET HR2 TO 0!
MSS=2J,PC->PC1
IO=J''
IO=K''
OVERWRITE INDS6
OVERWRITE INDS7
RESET HR2 TO 0!
MSS=2J,PC->PC1
RESET HR2 TO 0!
MSS=2J,PC->PC1
FOR I=0,1,2,...LTH+1

INPUT DONE!

SFT OUTPUT PC
TMP2(.) RA
IO=TMP2(J* OR K*)
DCT1(.) HA
IO=DCT1(J* OR K*)
PC->PC0
RESET PC AND SETTIF

SET OUTPUT PC(PC3)
TARIFO(.) RA-1
TARIFEI(.) RA-1
TARIE2(.) HA-1
TARIE3(.) HA-1
G1=1,SET LEVEL,COUNTS
AF0=1 THEN LEVEL,0
AF1=1 THEN LEVEL,1

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A48	08482	90004FA	(00309)	JUMPS(INSP1S2,AF2)	AF2=1 THEN LEVEL 2
A49	08484	920051FH	(00310)	JUMPS(INSP1S3,AF3)	AF3=1 THEN LEVEL 3
A4A	08486	94004560	(00311)	JUMP(FLGSTST)	RE-TEST ALL FLAGS
A4B	08488	9620002H	(00312)	INSRTS0 CLEAR(CAF0)	RELEASE APU
A4C	0848A	98014579	(00313)	ADDI(RW0,HS,TF),JUMPF(IGSTST)	00=TABLE0(I1),I1=I1+1
A4D	0848C	9A200029	(00314)	CLEAR(CAF1)	RELEASE APU
A4E	0848E	9C114579	(00315)	ADDI(RW1,HS,TF),JUMP(FLGSTST)	00=TABLE1(JJ),JJ=JJ+1
A4F	084C0	9E20002A	(00316)	CLEAR(CAF2)	RELEASE APU
A50	084C2	A1214579	(00317)	ADDI(RW2,HS,TF),JUMP(FLGSTST)	00=TABLE2(KK),KK=KK+1
A51	084C4	A220002H	(00318)	INSRTS3 CLEAR(CAF3)	RELEASE APU
A52	084C6	A5314579	(00319)	ADDI(RW3,HS,TF),JUMPF(IGSTST)	00=TABLE3(LL),LL=LL+1
A53	084C8	A6500007	(00320)	LOAD(RW0,INSTRS0-1(2))	R "INSTR" WORDS...
A54	084CA	A4411F13	(00321)	LOAD(RW1,1),JUMPP(*1)	TO BE SET OR CLEARED
A55	084CC	A0A00002	(00322) #1	ADD(RW0,WS,TF)	00=INSTRS#1
A56	084CE	AC1155M1	(00323)	SUBL(RW1,1),JUMPP(*1)	FOR I=0,1,2...7
A57	084D0	A1200030	(00324)	CLEAR(CR0)	STALL AFS OUTPUT
A58	084D2	A0500003	(00325)	LOAD(RW1,3)	3+1=4 SCATTER WRITES
A59	084D4	A2C3EFC0	(00326) #2	LOAD(RW0,APSSMFM(1),TF)	INSTRS# OVERWRITE
A5A	084D6	A41159R1	(00327)	SUBL(RW1,1),JUMPP(*2)	FOR I=0,1...3
A5B	084D8	A6200030	(00328)	CLEAR(CR0)	STALL AFS OUTPUT
A5C	084DA	A8000020	(00329)	NOP	RELEASED BY APU
A5D	084DC	A0400000	(00330)	LOAD(RW0,TORDRS(3),TF)	TORDR(.) RA,00=IORDR(0)
A5E	084DE	A50010R	(00331)	LOAD(RW1,SIZE+OVRSLAP-1)	IORDR(.) SIZE-2+OVRSLAP
A5F	084E0	A60A0001	(00332)	ADD(RW0,HS,TF)	00=IORDR(1)
A60	084E2	C0115F41	(00333)	SUBL(RW1,1),JUMPP(RDSOUT)	FOR I=1,2...LTH-1;+10 0'
			(00334) :		S
			(00335) *		
A61	084F4	C24603FF	(00336)	LOAD(RW0,DCT2S-2(3))	DCT2(.) RA-2
A62	084F6	C47604FF	(00337)	LOAD(RW3,TMP4S-2(1))	TMP4(.) RA-2
A63	084F8	C6000020	(00338)	NOP	LET "JUMP(...)" SETTLE
A64	084FA	C841F21	(00339)	LOAD(RW2,INSIRS6+1(2),TF)	SET MSS IN INSTRS6
A65	084FC	CA61F23	(00340)	LOAD(RW2,INSTRS7+1(2),TF)	SET MSS IN INSTRS7
A66	084FE	CC3EFC0	(00341)	LOAD(RW2,APSSMFM(1),TF)	OVERWRITE INDS4
A67	084F0	CEAF0000	(00342)	ADD(RW2,MSS,TF,C)	OVERWRITE INDS5;PC3->PC
A68	084F2	D1641F1D	(00343)	LOAD(RW2,INSTRS4+1(2),TF)	SET MSS IN INSTRS4
A69	084F4	D3641F1F	(00344)	LOAD(RW2,INSTRS5+1(2),TF)	SET MSS IN INSTRS5
A6A	084F6	D4F3EFC0	(00345)	LOAD(RW2,APSSMFM(1),TF)	OVERWRITE INDS6
A6B	084F8	D6AF0000	(00346)	ADD(RW2,MSS,TF,C)	OVERWRITE INDS7;PC3->PC
A6C	084FA	D8206370	(00347)	JUMP(INDS1,M0),CLEAR	RESET & STALL
A6D	084FC	DA300037	(00348)	SET(RA)	ENABLE APU
A6E	084FE	DC000020	(00349)	NOP	LET "JUMP(...)" SETTLE!
A6F	08500	DE0076AR	(00350)	JUMPC(DMYSLIL,AF0)	WAIT FOR "SKIP" FLAG
A70	08502	E0A00002	(00351)	ADD(RW0,WS,TF)	00=DCT2(J*)
A71	08504	E2A00002	(00352)	ADD(RW3,WS,TF)	00=TMP4(J*)=DCT1(J*)

PAGE 10: FCN 247... "MPSSPT(V)" SORT DCT1( ) COEFFICIENTS

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A72 04506 F44A0002 (00353)      ADD(HW0,MS,TF)
A73 04508 F44A0002 (00354)      ADD(HW3,MS,TF)
A74 0450A F42F0000 (00355)      CHNGSPC ADD(HW2,MSS,C)
A75 0450C F4006F60 (00356)      JUMP(TOPSP3)
A76 0450F FCF20794 (00357)      DMYSFIL LOAD(HW2,DMYS(1),TF)
A77 04510 F4AA0000 (00358)      ADD(HW2,MSS,TF)
A78 04512 F0AA0000 (00359)      ADD(HW2,MSS,TF)
A79 04514 F2A17474 (00360)      ADDI.(HW2,MSS,TF),JUMP(CHNGSPC)
                                (00361) *
                                0000R44R (00362)
                                (00363)-?
                                04516      (00364)      FND #A-1
                                04516 00000000 (00365) ? STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
                                000000F6 (00366) G10AS1 DATA 1F'0.0'
                                04518      (00367)      G106S2=BI-G106S
                                (00368)      FND

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00=DCT2(K*)
00=TMP4(K*)=DCT1T(K*)
NO IO! CHANGE PC
RESFT PC AND SETTLE
00=?
00=?
00=? THEN CHANGE PC
ASSIGN VALUE TO CHAIN AN
CHOR

```

AEUTS0RC:	0006H (0000H) (0003H)	
APSSMFM:	1EFC0 (00009) (00326)	(00341) (00345)
CMNGSPC:	0007A (00355) (00360)	
CMTSOUT:	0007A (00101) (00111)	(00121) (00132)
CSPUSMDS:	021FC (00010) (00041)	
DCTS:	0092R (00011)	
DCT1S:	00200 (00012) (00296)	
DCT7S:	00400 (00013) (00336)	
DMS:	00794 (00014) (00357)	
DMSFLL:	00076 (00350) (00357)	
DMSA:	00086 (00188) (00214)	
DMSI:	00037 (00291)	
FLGSTST:	00045 (00306) (00311)	(00313) (00315) (00317) (00319)
G10AS:	08427 (00040) (00277)	(00233) (00367)
G10AS1:	00039 (00234) (00293)	
G10AS3:	0006D (00301) (00348)	
G10ASA:	0846H (00230) (00362)	
G10AS1:	08516 (00225) (00366)	
G10ASD:	00030 (00243) (00301)	
G10ASZ:	000F6 (00229) (00367)	
HS:	00001 (00016) (00243)	(00254) (00258) (00262) (00266) (00273) (00274) (00281) (00282)
IINCST:	0000R (00313) (00315)	(00317) (00319) (00332)
INS0:	00014 (00049) (00264)	
INS1:	0001A (00050) (00260)	
INS2:	00016 (00051) (00256)	
INS3:	00012 (00052) (00252)	
INCS1:	0000C (00086) (00100)	(00110) (00120) (00130)
INCRFS:	00100 (00017) (00029)	(00030) (00031)
INSA:	00033 (00051) (00286)	
INS5:	00035 (00054) (00288)	
INS6:	0002H (00055) (00278)	
INS7:	0002D (00056) (00280)	
INPS1:	00063 (00338) (00347)	
INPSJ:	00026 (00273) (00289)	
INSRIS0:	0004H (00307) (00312)	
INSRIS1:	0004D (00308) (00314)	
INSRIS2:	0004F (00309) (00316)	
INSRIS3:	00051 (00310) (00318)	
INSTRS0:	01F14 (00049) (00247)	(00321)
INSTRS1:	01F16 (00050)	
INSTRS2:	01F18 (00051)	
INSTRS3:	01F1A (00052)	
INSTRS4:	01F1C (00053) (00275)	(00343)

INSTR\$5:	01F14 (00054)	(00276)	(00344)
INSTR\$6:	01F20 (00055)	(00283)	(00339)
INSTR\$7:	01F22 (00056)	(00284)	(00340)
INTGMS:	01F24 (00018)	(00241)	
INMPPS:	00000 (00019)	(00330)	
ISA\$00:	00502 (00021)		
ISA\$01:	00503 (00022)	(00238)	
ISVTS:	00502 (00020)	(00021)	(00022)
I0\$SU:	00010 (00261)	(00263)	
I1\$SU:	00019 (00257)	(00259)	
I2\$SU:	00015 (00253)	(00255)	
I3\$SU:	00011 (00251)		
LVI\$0:	00030 (00087)	(00172)	
LVI\$1:	00026 (00088)	(00112)	
LVI\$2:	00010 (00091)	(00102)	
LVI\$3:	00012 (00092)		
MSS:	00000 (00023)	(00252)	(00256)
	(00280)	(00285)	(00287)
	(00359)	(00360)	
ORDR\$1:	00040 (00151)	(00155)	
OVERSLAP:	00004 (00024)	(00331)	
POSOUT:	00054 (00332)	(00333)	
SCIRS:	00001 (00227)	(00235)	
SFTSCNT:	00053 (00306)	(00320)	
SHUFLS:	0006F (00188)	(00212)	
	00058 (00163)		
SI2FS:	000FF (00025)	(00240)	(00268)
SORTS:	00308 (00039)	(00072)	
SORT\$SA:	00000 (00070)	(00074)	(00218)
SORT\$SZ:	00089 (00071)	(00218)	(00219)
STALS:	00070 (00189)	(00189)	
STALS1:	00021 (00265)	(00267)	
START:	00100 (00027)	(00058)	
SVTS:	00342 (00026)		
TIS:	00300 (00062)	(00235)	
TIS:	00302 (00063)	(00236)	
TIS:	00304 (00064)	(00237)	
TARL\$0:	00000 (00028)	(00029)	(00263)
TARL\$1:	00000 (00029)	(00030)	(00302)
TARL\$2:	00000 (00030)	(00031)	(00259)
TARL\$3:	00000 (00031)	(00031)	(00255)
TARL\$3:	00000 (00031)	(00251)	(00305)
TMP2S:	00000 (00032)	(00239)	(00294)
TMP4S:	00000 (00033)	(00337)	
TMP\$PI:	0003A (00294)	(00290)	
		(00271)	(00272)
		(00264)	(00277)
		(00288)	(00346)
		(00298)	(00342)
		(00260)	(00278)
		(00285)	(00355)
		(00286)	(00358)

PAGE 13: 6CH 247... "MPSRT(V)" SONT DCTI(.) COEFFICIENTS

TOPSP3: 0006F (00349) (00356)  
TSTSLD: 0001F (00265) (00266)  
TSTSL1: 0001M (00261) (00262)  
TSTSL2: 00017 (00257) (00258)  
TSTSL3: 00013 (00253) (00254)  
WS: 00002 (00030) (00242) (00248) (00322) (00351) (00352) (00353) (00354)  
ZS: 00003 (00035)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) E- 0

MAKHOHI, IDCT CORRECTION  
ORIGINATED:16-JAN-80  
UPDATED:17-JUN-80

FCH 24R... "MPIDCM(Y,U,V)"

DEFINE GLOBAL SYMBOLS

MPADD EXP, (1 .LS, 14) + (12 .LS, 4) + \$1E  
MPADD OOF, (3 .LS, 10) + (12 .LS, 5) + \$1H  
MPADD WAFNF, (1 .LS, 10) + (12 .LS, 5) + \$1A  
MPADD FO, (3 .LS, 10) + (12 .LS, 5) + \$1C

AFPTSUMG=\$8FH

COSZSD=1024

CSPUSMUS=\$21FC

WYS=\$794

IPDCTS=D\*1024

MS=3

HS=1

WSS=0

SIZESD=256'-D\*1

STN2SD=1536

SVTS=\$03R2

SAS27=SVTS+2\*D\*27

SAS50=SVTS+2\*D\*50

SAS98=SVTS+2\*D\*98

SAS100=SVTS+2\*D\*100

SAS101=SVTS+2\*D\*101

START=\$7150

WS=2

WWS=4

WWS=5

WWS=6

ZS=3

ZZMS=7

(00001) \*  
(00002) \*  
(00003) \*  
(00004) \*  
(00005) \*  
(00006) \*  
(00007) \*  
(00008) \*  
(00009) \*  
(00010) \*  
(00011) \*  
(00012) \*  
(00013) \*  
(00014) \*  
(00015) \*  
(00016) \*  
(00017) \*  
(00018) \*  
(00019) \*  
(00020) \*  
(00021) \*  
(00022) \*  
(00023) \*  
(00024) \*  
(00025) \*  
(00026) \*  
(00027) \*  
(00028) \*  
(00029) \*  
(00030) \*  
(00031) \*  
(00032) \*

EXPAND ARRAY FUNCTION DISPATCH TABLE

\$1=AFPTSURG+3\*9\*(24R-12R)

ADDR IDCMSCR(,1)

ADDR G201SC(7,1)

ADDR CSPUSMUS(,1,0)

FACT

(00033) \*  
(00034) \*  
(00035) \*  
(00036) \*  
(00037) \*  
(00038) \*



```

(00034) *
(00040) *
(00041) *
00007150 (00042) #I=START
00007150 (00043) EVFN
07150 20000034 (00044) TARI.FS DATA 2.38414594-07
07152 40000044 (00045) DATA 3.27680000E+04
07154 08200030 (00046) DATA 1.5497208E-04
07156 3086415C (00047) DATA 0.248572748E+34
07158 12640007 (00048) DATA 0.712208897E+27
0715A 72551101 (00049) DATA 0.263632333E+21
0715C 1FRD^ACC (00050) DATA 0.675972338E+14
0715E 88000038 (00051) TARI.FS2 DATA -5.9604646E-08
07160 58893046 (00052) DATA 0.116291788E+08
07162 78FFFF40 (00053) DATA 0.9999999250
(00054) EFFECT
2**(-22)
2**15
4*65*16**(-6)
C5
C4
C3
C2
C1
C0

```



```

A10 071A0 ORFCORFC (00099)
A11 071A2 ORFDORFD (00100)
A12 071A4 ORFRORFR (00101)
A20 071A6 ORFQORFQ (00102)
A21 071A8 ORRQORRQ (00103)
A22 071AA 90160714 (00104)
A23 071AC 20372037 (00105)
      (00106)
A24 071AF 00000000 (00107)
A25 071AO ORFRORFR (00108)
A26 071H2 ORFDORFD (00109)
A27 071H4 RA200000 (00110)
A28 071H6 ORRCORRC (00111)
A29 071H8 20372037 (00112)
      (00113)
A2A 071HA ORFRORFR (00114)
A2B 071HC ORFCORFC (00115)
A2C 071HF R5H0R5H0 (00116)
A2D 071CO ORFDORFD (00117)
A2E 071C2 ORFRORFR (00118)
A2F 071C4 ORFQORFQ (00119)
A30 071C6 ORFRORFR (00120)
A31 071C8 ORFRORFR (00121)
A32 071CA ORF5ORF5 (00122)
A33 071CC ORFCORRC (00123)
A34 071CF ORFAORFA (00124)
A35 071CO R51C0000 (00125)
A36 071O2 3A6R3A6R (00126)
A37 071O4 1F731F73 (00127)
A38 071O6 R5D1H5D1 (00128)
A39 071O8 3A2R3A2R (00129)
A3A 071OA 32523252 (00130)
A3B 071OC ORR3ORR3 (00131)
A3C 071OE 46704670 (00132)
A3D 071FO 072R022R (00133)
      (00134)
A3E 071E2 R5COR5CO (00135)
A3F 071E4 ORF2ORF2 (00136)
A40 071E6 5A005A00 (00137)
A41 071EH ORR3ORR3 (00138)
A42 071EA 45704570 (00139)
A43 071EC 4A0R4A0R (00140)
A44 071EE R5COR5CO (00141)
A45 071EO ORF2ORF2 (00142)

      MOV(10A,M4)
      MOV(10A,M5)
      MOV(10A,M0)
      MOV(10A,M1)
      NOP
      JHMP(C1DCLEP,FWI)
      CLEAR(M1)
      NOP
      MOV(10A,M0)\NOP
      MOV(10A,M5)\NOP
      MUL(M0,M5)\NOP
      MOV(P,00)\NOP
      CLEAR(M1)
      MOV(10A,M3)
      MOV(10A,M4)
      MUL(M3,M4)
      MOV(10A,M5)
      MOV(10A,A4)
      MOV(10A,M1)
      MOV(10A,A7)
      MOV(10A,A6)
      MOV(10A,A5)
      MOV(P,M4)
      MOV(10A,M2)\NOP
      MOV(00),MUL(M2,M4)\NOP
      MOV(M3),ALIGNCA31
      MOVCA3),EXP(A3)
      MOV(A1),MUL(M3,M6)
      MOV(M0),ALIGNCA1)
      MOV(A2),MORM(A2)
      MOVIP,A3)
      MOV(A0),ADD(A3,A6)
      MOV(M3),R(A1)
      MUL(M3,M6)
      MOV(10A,A2)
      ADDIT(A0,A2)
      MOV(P,A3)
      MOV(A0),ADD(A3,A5)
      MOV(M3),ADD(A0,A4)
      MUL(M3,M6)
      MOV(10A,A2)

NEXT C(K)
NEXT C(N-K)
NEXT COS(K)
NEXT SIN(K)

M0=VAR''
M4=1.0/20.0=0.05
0.05*VAR''
00=VAR''=0.05*VAR''
RELFAF APS INPUT

M3=SA TEMPORARILY
M4=2**(-22) TEMPORARILY

M5=2**15)
M4=4*65*16**(-6)
M1=C5
A7=C4
A6=C3
A5=C2
M4=SA*2**(-22)
M2=X(0,K)=VAR''
00=X(19,K-2)=VAR,X(1,K)
A3=X(9,K-1),X(7,K-1)
A2=X(7,K-1),X(10,K-1)
A1=X(1,K),X(11,K-1)
M0=X(10,K-1),X(2,K)
A2=X(2,K),X(3A,K)
A3=X(11,K-1)
A0=X(3A,K),X(12,K-1)
M3=X(12,K-1),SET T HIT R
Y X(3A,K)
X(13,K-1)
A2=-16**(-6)
X(3R,K)
A3=X(13,K-1)
A0=X(3R,K),X(14,K-1)
M3=X(A6,K-1),X(4,K)
X(15,K-1)
A2=C1

```

```

A46 071F2 0RHGURR (00143)
A47 071F4 M531R533 (00144)
A48 071F6 4340340 (00145)
A49 071F8 482R42R (00146)
A4A 071FA M50R513 (00147)
A4B 071FC 0RF20RF2 (00148)
A4C 071FE 0RH00RH0 (00149)
A4D 07200 0RR10RH1 (00150)
A4E 07202 0RHF0RHF (00151)
A4F 07204 R4C0R4C0 (00152)
A50 07206 42203220 (00153)
A51 07208 0RHF0RHF (00154)
A52 0720A R31R471 (00155)
A53 0720C 47204720 (00156)
A54 0720E 90160034 (00157)
A55 07210 20372037 (00158)
A56 07212 0RHF0000 (00159) *
A57 07214 0RHF0RFD (00161)
A58 07216 00000RFR (00162)
A59 0721H R420R420 (00163)
A5A 0721A 0RHC0RHC (00164)
A5B 0721C 00165 ? (00165) ?
A5C 0721E 20320000 (00167) DNESA
A5D 07220 10000000 (00169)
A5E 07222 00000R5F (00170)
A5F 07224 00171 (00171)
A5G 07226 00172 (00172)

MOV(R,M2)
MOV(A3),MUL(M2,M5)
ADD(A2,A3)
MOV(M3),SUB(A1,A0)
MOV(A3),MUL(M3,M6)
MOV(IOA,A2)
MOV(R,MUL)
MOV(P,A1)
MOV(R,M6)
MUL(M1,M6)
ADD(A1,A2)
MOV(R,M7)
MOV(A1),MUL(M0,M7)
ADD(A1,A1)
JUMPC(B1,FMT)
CLEAR(M1)

MOV(IOA,M0)\NOP
MOV(IOA,M5)
NOP\MOV(IOA,M0)
MUL(M0,M5)
MOV(P,00)

CLEAR(MA)\NOP
NOP
JUMPC(0)
IDCMSS7=BA-IDCMSSA
END IDCMSZ
EJECT

M2=X(4,K)
A3=X(15,K-1),X(5,K)
X(16,K-1)
M3=X(16,K-1),X(6,K)
A3=X(5,K),X(17,K-1)
A2=(0)

A1=X(17,K-1)
M6=X(6,K)
X(R,K)
X(1R,K-1)
M7=X(1R,K-1)
A1=X(8,K),X(19,K-1)
X(9,K)
WAIT FOR DELAY
RELEASE APS INPUT

M0=DC'
M5=VAR
M0=2**(-MLONG)
DC'VAR;2**(-MLONG)*VAR
00=DC'VAR;2**(-
MLONG)*VAR

APU DUNE!

```



```

(00217) ;
(00218) *
A1F 07266 4C2041C (00219) IMHLS
LOAD(HR0,SAS101(1),TF)
A1F 07268 4C20446 (00220)
LOAD(HR0,SAS9R(1),TF)
A20 0726A 40300037 (00221)
SET(M1)
A21 0726C 42000020 (00222)
NOP
(00223) *
A22 0726E 44C2038H (00224)
LOAD(HR0,SAS27(1),TF)
A23 07270 46C27150 (00225)
LOAD(HR0,TARLFS(1),TF)
A24 07272 48H00002 (00226)
ADD(HR0,WS,TF)
A25 07274 4A8A0002 (00227)
ADD(HR0,WS,TF)
A26 07276 4C8A0002 (00228)
ADD(HR0,WS,TF)
A27 07278 4E8A0002 (00229)
ADD(HR0,WS,TF)
A28 0727A 508A0002 (00230)
ADD(HR0,WS,TF)
A29 0727C 528A0002 (00231)
ADD(HR0,WS,TF)
A2A 0727E 54700002 (00232)
LOAD(HR3,WS)
A2H 07280 56F2044C (00233) #1
LOAD(HR2,SAS101(1),TF)
A2C 07282 58C2715F (00234)
LOAD(HR0,TARLFS2(1),TF)
A2D 07284 5A8A0002 (00235)
ADD(HR0,WS,TF)
A2E 07286 5C8A0002 (00236)
ADD(HR0,WS,TF)
A2F 07288 5E8A0002 (00237)
SUBL(HR3,1),JUMPP(*1)
A30 0728A 60300037 (00238)
SET(M1)
A31 0728C 62000020 (00239)
NOP
(00240) *
A32 0728F 64C2044A (00241)
LOAD(HR0,SAS100(1),TF)
A33 07290 66C2044C (00242)
LOAD(HR0,SAS101(1),TF)
A34 07292 68F203F6 (00243)
LOAD(HR2,SAS50(1),TF)
(00244) *
A35 07294 6A200031 (00245)
CLEAR(M1)
A36 07296 6C000020 (00246)
NOP
(00247) *
A37 0729H 6E400032 (00248)
SET(RA)
A3H 0729A 70C00066 (00249)
LOAD(HW0,I01,TF)
A39 0729C 72500000 (00250)
LOAD(RW1,MSS)
A3A 0729E 74600000 (00251)
LOAD(RW2,MSS)
A3B 072A0 768A0002 (00252)
ADD(HW0,2,TF)
A3C 072A2 7811002A (00253)
ADDR(RW1,RW1)
A3D 072A4 7A10002A (00254)
ADDR(HR0,RW1,TF)
A3E 072A6 7C8A0002 (00255)
ADD(HW0,2,TF)
A3F 072A8 7E210010 (00256)
MOVH(HW2,HW0)
A40 072AA 80110013 (00257)
SUBL(HW1,3)
A41 072AC 82820006 (00258) #2
SUB(HW0,6,TF)
A42 072AE 848A0002 (00259)
ADD(HW0,2,TF)
A43 072H0 86AA0002 (00260)
ADD(HW2,2,TF)
D
IO=VAR**
IO=1.0/20.=0.05
STALL APS INPHI

IO=LOG16(10.0)
IO=2**(-22)
IO=2**15
IO=4*65*16**(-6)
IO=C5
IO=C4
IO=C3
IO=C2
%000PS P 4 IO'S=12 IO'S
IO=VAR**
IO=-16**(-6)
IO=C1
IO=C0
FOR I=0,1...3

IO=DC'
IO=VAR
SA=2**(-M*LONG)
INPUT DONE:I

ENABLE APU
VR(0)
N-1
DU4MY
YI(0)
2N-2
YR(N/2)
YI(N/2)
YI(N/2)
4*(N/2-1)-1
YR(K)
YI(K)
YR(N-K)

```

A44	072H7	H4A0002	(00261)	ADD(RW2,2,TF)	VI(N-K)
A45	072H4	RA1141H4	(00262)	SUBI(RW1,4),JUMPP(#2)	
A46	072H6	HCC2044C	(00263) *	LOAD(HW0,SAS101(1),TF)	00=VAP!!!
A47	072H4	HFC20794	(00264) *	LOAD(HW0,DAYS(1),TF)	00=?
A48	072H4	90C20794	(00265)	LOAD(HW0,DAYS(1),TF)	00=VAP
A49	072H4	92C2044C	(00266)	LOAD(HW0,SAS101(1),TF)	00=DC
A4A	072H4	94C2044A	(00267) *	LOAD(HW0,SAS101(1),TF)	00=VAR
A4B	072C0	96C2044C	(00270) 00CS	CLFAR(P0)	OUTPUT DONE!
A4C	072C7	98200030	(00271) *	MDP	ASSIGN VALUE TO CHAIN AN
A4D	072C4	9A000020	(00272) *	G201SA= 8C	CHNR
			(00273) ?	FND #A-1	
			(00274) ?	STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS	
			(00275) *	DATA 3F+0.0'	
			(00276)	G201SZ=#L-G201S	
			(00277)	FND	
			(00278)		
			(00279) ?		
			(00280)		
			(00281)		
			(00282)		

AFDYSORG:	00400 (00004)	(00034)
COSZS:	021FC (00011)	(00037)
CSPHSMOS:	00056 (00160)	
PCS:	00794 (00012)	(00212) (00214) (00266) (00267)
DNFSA:	00035 (00245)	
DNFSI:	0004C (00273)	
DNFSO:	00400 (00013)	
DRDCTS:	0002A (00114)	
FXPS:	0727A (00016)	(00179) (00185) (00281)
G201S:	0729A (00182)	(00276)
G201SA:	072C6 (00177)	(00280)
G201SI:	00037 (00186)	(00248)
G201SO:	000A7 (00181)	(00281)
G201S7:	00001 (00015)	
HS:	00032 (00241)	
IDCS:	00014 (00087)	(00104)
IDCLP:	07166 (00035)	(00061)
IDCMS:	00000 (00059)	(00067) (00170)
IDCMSSA:	0005F (00060)	(00170) (00171)
IDCMSSZ:	00022 (00224)	
IFXPS:	0001F (00219)	
IWHLS:	00000 (00016)	(00189) (00190) (00197) (00193) (00250) (00251)
MSS:	0004A (00270)	
NDCS:	00047 (00266)	
DFXPS:	00046 (00264)	
DMHLS:	0044A (00023)	(00241) (00270)
SAS100:	0044C (00024)	(00219) (00233) (00242) (00264) (00271)
SAS101:	0018K (00020)	(00274)
SAS27:	003F6 (00021)	(00243)
SAS50:	00446 (00022)	(00270)
SAS9H:	00001 (00179)	(00187)
SCMPS:	00600 (00018)	
SIZE:	000FF (00017)	
SMHLS:	00025 (00108)	(00042)
START:	07150 (00025)	
SVTS:	003M2 (00019)	(00021) (00022) (00023) (00024)
TAMHLS:	07150 (00044)	(00275)
TAMHFS2:	0715F (00051)	(00234)
TMS:	00002 (00026)	(00276) (00277) (00278) (00279) (00280) (00281) (00282) (00283) (00284) (00285)
WWS:	00004 (00027)	(00276) (00277) (00278) (00279) (00280) (00281) (00282) (00283) (00284) (00285)
WWSH:	00005 (00028)	
WWSZ:	00006 (00029)	



PAGE 10: FCH 74R... "MPJICH(V,U,V)" HAKHUU, IDCT CORRECTION

ZSS: 00003 (00030)  
ZSMS: 00007 (00031)

LINES WITH ERRORS: 0 (MAP VERSION H00101,10) F= 0

FCH 249... "MPASRT(U)" SORT DCTI(.) COEFFICIENTS  
ORIGINATED:13-DEC-79  
UPDATED:27-MAY-80

DEFINE GLOBAL SYMBOLS  
OPADD EXP, (1 .I.S. 14) + (12 .I.S. 8) + SIF  
OPADD OOF, (3 .I.S. 10) + (12 .I.S. 5) + SIF  
OPADD WAFNE, (1 .I.S. 10) + (12 .I.S. 5) + SIA  
AFDTSUKG=SRFX

000000RFH (0000M)  
0001FFC0 (00004)  
000021FC (00010)  
0000002H (00011)  
000000200 (00012)  
000000400 (00013)  
00000092H (00014)  
00000794 (00015)  
000000003 (00016)  
000000001 (00017)  
00000100 (00018)  
000000000 (00019)  
000000502 (00020)  
000000507 (00021)  
000000503 (00022)  
000000000 (00023)  
00000000A (00024)  
0000000FF (00025)  
0000003R2 (00026)  
000007300 (00027)  
000000C00 (00028)  
000000000 (00029)  
000000F00 (00030)  
000000F00 (00031)  
000000A00 (00032)  
000000F00 (00033)  
000000C00 (00034)  
000000002 (00035)  
000000003 (00036)  
000000003 (00037) \*

\* EXPAND ARRAY FUNCTION DISPATCH TABLE  
#I=AFDTSUKG+3\*2\*(249-128)  
ADDR SORTS(P7,1)  
ADDR G10AS(P7,1)  
ADDR CSPUSN05(,1,0)  
F.I.F.C.T



01E3A 0016	
01E3B 0017	
01E3C 0018	
01E3D 0019	
01E3E 001A	
01E3F 001B	
01E40 001C	
01E41 001D	
01E42 001E	
01E43 001F	
01E44 0020	
01E45 0021	
01E46 0022	
01E47 0023	
01E48 0024	
01E49 0025	
01E4A 0026	
01E4B 0027	
01E4C 0028	
01E4D 0029	
01E4E 002A	
01E4F 002B	
01E50 002C	
01E51 002D	
01E52 002E	
01E53 002F	
01E54 0030	
01E55 0031	
01E56 0032	
01E57 0033	
01E58 0034	
01E59 0035	
01E5A 0036	
01E5B 0037	
01E5C 0038	
01E5D 0039	
01E5E 003A	
01E5F 003B	
01E60 003C	
01E61 003D	
01E62 003E	
01E63 003F	
01E64 0040	
01E65 0041	
(00061)	DATA 0'24',0'25',0'26',0'27',0'28',0'29',0'30',0'31'
(00062)	DATA 0'32',0'33',0'34',0'35',0'36',0'37',0'38',0'39'
(00063)	DATA 0'40',0'41',0'42',0'43',0'44',0'45',0'46',0'47'
(00064)	DATA 0'48',0'49',0'50',0'51',0'52',0'53',0'54',0'55'
(00065)	DATA 0'56',0'57',0'58',0'59',0'60',0'61',0'62',0'63'
(00066)	DATA 0'64',0'65',0'66',0'67',0'68',0'69',0'70',0'71'

01E66 0042  
 01E67 0043  
 01E68 0044  
 01E69 0045  
 01E6A 0046  
 01E6B 0047  
 01E6C 0048  
 01E6D 0049  
 01E6E 004A  
 01E6F 004B  
 01E70 004C  
 01E71 004D  
 01E72 004E  
 01E73 004F  
 01E74 0050  
 01E75 0051  
 01E76 0052  
 01E77 0053  
 01E78 0054  
 01E79 0055  
 01E7A 0056  
 01E7B 0057  
 01E7C 0058  
 01E7D 0059  
 01E7E 005A  
 01E7F 005B  
 01E80 005C  
 01E81 005D  
 01E82 005E  
 01E83 005F  
 01E84 0060  
 01E85 0061  
 01E86 0062  
 01E87 0063  
 01E88 0064  
 01E89 0065  
 01E8A 0066  
 01E8B 0067  
 01E8C 0068  
 01E8D 0069  
 01E8E 006A  
 01E8F 006B  
 01E90 006C  
 01E91 006D

(00067) DATA D'172',D'173',D'174',D'175',D'176',D'177',D'178',D'179',

(00068) DATA D'180',D'181',D'182',D'183',D'184',D'185',D'186',D'187',

(00069) DATA D'188',D'189',D'190',D'191',D'192',D'193',D'194',D'195',

(00070) DATA D'196',D'197',D'198',D'199',D'100',D'101',D'102',D'103',

(00071) DATA D'104',D'105',D'106',D'107',D'108',D'109',D'110',D'111',

01F92 006F	
01F94 006F	
01F96 0070	
01F98 0071	
01F9A 0072	
01F9C 0073	
01F9E 0074	
01FA0 0075	
01FA2 0076	
01FA4 0077	
01FA6 0078	
01FA8 0079	
01FAA 007A	
01FAC 007B	
01FAE 007C	
01FB0 007D	
01FB2 007E	
01FB4 007F	
01FB6 0080	
01FB8 0081	
01FBA 0082	
01FBC 0083	
01FBE 0084	
01FBF 0085	
01FC1 0086	
01FC3 0087	
01FC5 0088	
01FC7 0089	
01FC9 008A	
01FCB 008B	
01FCD 008C	
01FCE 008D	
01FCF 008E	
01FD1 008F	
01FD3 0090	
01FD5 0091	
01FD7 0092	
01FD9 0093	
01FDB 0094	
01FDD 0095	
01FDE 0096	
01FDH 0097	
01FDI 0098	
01FDD 0099	
(00072)	DATA D'112',D'113',D'114',D'115',D'116',D'117',D'118',D'119'
(00073)	DATA D'120',D'121',D'122',D'123',D'124',D'125',D'126',D'127'
(00074)	DATA D'128',D'129',D'130',D'131',D'132',D'133',D'134',D'135'
(00075)	DATA D'136',D'137',D'138',D'139',D'140',D'141',D'142',D'143'
(00076)	DATA D'144',D'145',D'146',D'147',D'148',D'149',D'150',D'151'
(00077)	DATA D'152',D'153',D'154',D'155',D'156',D'157',D'158',D'159'

01E9E 009A	
01E9F 009B	
01E9C 009C	
01E9D 009D	
01E9E 009E	
01E9F 009F	
(00078)	DATA 0'160',0'161',0'162',0'163',0'164',0'165',0'166',0'167'
01E9A 00A0	
01E9B 00A1	
01E9C 00A2	
01E9D 00A3	
01E9E 00A4	
01E9F 00A5	
01E9A 00A6	
01E9B 00A7	
01E9C 00A8	
01E9D 00A9	
(00079)	DATA 0'168',0'169',0'170',0'171',0'172',0'173',0'174',0'175'
01E9E 00AA	
01E9F 00AB	
01E9A 00AC	
01E9B 00AD	
01E9C 00AE	
01E9D 00AF	
(00080)	DATA 0'176',0'177',0'178',0'179',0'180',0'181',0'182',0'183'
01E9E 00B0	
01E9F 00B1	
01E9A 00B2	
01E9B 00B3	
01E9C 00B4	
01E9D 00B5	
01E9E 00B6	
01E9F 00B7	
(00081)	DATA 0'184',0'185',0'186',0'187',0'188',0'189',0'190',0'191'
01E9A 00B8	
01E9B 00B9	
01E9C 00BA	
01E9D 00BB	
01E9E 00BC	
01E9F 00BD	
01E9A 00BE	
01E9B 00BF	
01E9C 00C0	
01E9D 00C1	
(00082)	DATA 0'192',0'193',0'194',0'195',0'196',0'197',0'198',0'199'
01E9E 00C2	
01E9F 00C3	
01E9A 00C4	
01E9B 00C5	









A24 07350 90180000 (00184)	JUMPC(INCS1,F*1)	FOR I=0,1,2...LTH-1
A25 07352 1048003A (00184)	JUMPC(NTSOUT,G1),SET	GET LEVEL COUNTS
A26 07354 20490000 (00185) IVELS1	SET(A*1)\NOP	INFORM APS OF LVL1
A27 07356 20530000 (00186)	SET(UN)\NOP	FIXED POINT ADD
A28 07358 00004720 (00187)	NOPADD(A1,A7)	LVL1 CNT+1
A29 0735A 08E006F4 (00188)	MOV(10A,A0)\MOV(10A,A4)	A0=DCT1(I+1);A4=0
A2A 0735C 08E00691 (00189)	MOV(10A,00)\MOV(R,A1)	00=I:LVL1=LVL1+1
A2B 0735E 20380000 (00190)	CLEAR(UN)\NOP	RESUME FLOATING POINT!
A2C 07360 49004080 (00191)	SURCA0,A1)\SURCA4,A5)	D-THRSHP;D-THRS2
A2D 07362 91090020 (00192) #1	JUMPS(R1,A*1)	
A2E 07364 90180000 (00193)	JUMPC(INCS1,F*1)	FOR I=0,1,2...LTH-1
A2F 07366 1048003A (00194)	JUMPC(NTSOUT,G1),SET	GET LEVEL COUNTS
A30 07368 20380000 (00195)	SET(A*1)\NOP	INFORM APS OF LVL0
A31 0736A 20580000 (00196)	SET(UN)\NOP	FIXED POINT ADD
A32 0736C 00004700 (00197)	NOPADD(A0,A7)	LVL0 CNT+1
A33 0736E 08E006F4 (00198)	MOV(10A,A0)\MOV(10A,A4)	A0=DCT1(I+1);A4=D
A34 07370 08E00690 (00199)	MOV(10A,00)\MOV(R,A0)	00=I:LVL0=LVL0+1
A35 07372 20380000 (00200)	CLEAR(UN)\NOP	RESUME FLOATING POINT!
A36 07374 49004080 (00201)	SURCA0,A1)\SURCA4,A5)	D-THRSHP;D-THRS2
A37 07376 91080037 (00202) #1	JUMPS(R1,A*0)	
A38 07378 90180000 (00203)	JUMPC(INCS1,F*1)	FOR I=0,1,2...LTH-1
A39 0737A 20450000 (00204)	SET(G1)\NOP	GET LEVEL COUNTS
A3A 0737C 00000200 (00205)	NOPAR(A0)	R=LVL0 CNT
A3B 0737E 0000023C (00206)	NOPMOV(00),P(A1)	R=LVL1 CNT
A3C 07380 0000025C (00207)	NOPMOV(00),R(A2)	R=LVL2 CNT
A3D 07382 0000027C (00208)	NOPMOV(00),R(A3)	R=LVL3 CNT
A3E 07384 0000089C (00209)	NOPMOV(R,00)	00=LVL3 CNT(0 UPTO LTH)
A3F 07386 08E0061C (00210)	MOV(ZERO,00)	00=0 FOR INSTRS4+1,...
A40 07388 08E0061C (00211)	MOV(ZERO,00)	THRU INSTRS7+1
A41 0738A 00000000 (00212) #1	NOP	WAIT FOR...
A42 0738C 901C0041 (00213)	JUMPC(R1,F0)	"DEFVLS" TO SETTLE
A43 0738E 20370000 (00214)	CLEAR(W1)\NOP	RELEASE APS INPUT
A44 07390 20500000 (00215)	SET(R0)\NOP	RELEASE APS OUTPUT
A45 07392 08E00000 (00216)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS0
A46 07394 08E00000 (00217)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS1
A47 07396 08E00000 (00218)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS2
A48 07398 08E00000 (00219)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS3
A49 0739A 00000000 (00220) #2	NOP	WAIT FOR...
A4A 0739C 901C0049 (00221)	JUMPC(R2,F0)	"WRITES" TO SETTLE
A4B 0739E 20370000 (00222)	CLEAR(W1)\NOP	RELEASE APS INPUT
A4C 073A0 20500000 (00223)	SET(R0)\NOP	RELEASE APS OUTPUT
A4D 073A2 08E00000 (00224)	MOV(10A,00)\NOP	00=IORDR(1)
A4E 073A4 08E00000 (00225)	MOV(10A,00)\NOP	00=IORDR(I+1)
A4F 073A6 08E00000 (00226)	MOV(10A,00)\NOP	00=IORDR(I+2)

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A50 07348 08FC0000 (00187)      MOV(10A,00)\NOP
A51 0734A 90160040 (00188)      JUMPC(ORDRS1,F,WI)
A52 0734C 20240000 (00189)      CLEAR("SKIP" FLAG)
A53 0734E 081C001C (00190)      MOV(ZF#0,00)
A54 07350 081C001C (00191)      MOV(ZF#0,00)
A55 07352 081C001C (00192)      MOV(ZF#0,00)
A56 07354 081C001C (00193)      MOV(ZF#0,00)
A57 07356 081C001C (00194)      MOV(ZF#0,00)
A58 07358 20370000 (00195) *    SHIFTSV CLEAR(CW)\NOP
A59 0735A 20530000 (00197)      SET(UN)\NOP
A5A 0735C 08F40000 (00198)      MOV(10A,A0)\NOP
A5B 0735E 000004F0 (00199)      NOP\MOV(10A,A0)
A5C 07360 40000400 (00200)      ADD(A0,A0)
A5D 07362 08F40000 (00201)      MOV(10A,A1)\NOP
A5E 07364 000008F1 (00202)      NOP\MOV(10A,A1)
A5F 07366 413F413C (00203)      MOV(00)\ADD(A1,A1)
A60 07368 08FC0000 (00204) *    MOV(10A,00)\NOP
A61 0736A 000008FC (00206)      NOP\MOV(10A,00)
A62 0736C 08FC0000 (00207)      MOV(10A,00)\NOP
A63 0736E 08FC0000 (00208)      MOV(10A,00)\NOP
A64 07370 08FC0000 (00209)      MOV(10A,00)\NOP
A65 07372 08FC0000 (00210)      MOV(10A,00)\NOP
A66 07374 08FC0000 (00211)      MOV(10A,00)\NOP
A67 07376 08FC0000 (00212)      MOV(10A,00)\NOP
A68 07378 08F20000 (00213)      MOV(10A,A2)\NOP
A69 0737A 000008F2 (00214)      NOP\MOV(10A,A2)
A6A 0737C 425C425C (00215)      MOV(00)\ADD(A2,A2)
A6B 0737E 08FC0000 (00217)      MOV(10A,00)\NOP
A6C 07380 000008FC (00219)      NOP\MOV(10A,00)
A6D 07382 08FC0000 (00219)      MOV(10A,00)\NOP
A6E 07384 08FC0000 (00220)      MOV(10A,00)\NOP
A6F 07386 08FC0000 (00221)      MOV(10A,00)\NOP
A70 07388 08FC0000 (00222)      MOV(10A,00)\NOP
A71 0738A 08FC0000 (00223)      MOV(10A,00)\NOP
A72 0738C 08FC0000 (00224)      MOV(10A,00)\NOP
A73 0738E 911000F5 (00225)      SHIFTS JUMPC(DRESA,F1)
A74 07390 901C0074 (00226)      STALS SET(AF0)\NOP
A75 07392 20480000 (00227)      MOV(10A,A1)\NOP
A76 07394 20500000 (00228)      MOV(10A,A1)\NOP
A77 07396 08F40000 (00229)      NOP\MOV(10A,A1)
A78 07398 00G008F1 (00230)

```

```

00=TORDR(1+3)
FOR I=0,1,2...LTH-1
CLEAR "SKIP" FLAG
00=BUFFER ZONE=0
00=BUFFER ZONE=0
00=BUFFER ZONE=0
00=BUFFER ZONE=0
00=BUFFER ZONE=0

RELEASE APS INPUT
FIXED POINT ADD
A0=J
2J/2K
A1=J
2J->S6+1,2J/2K->S7+1,2K
,
INSTR$4 INTO INDS4
INSTR$5 INTO INDS5
NULL=TMP2(J**)
NULL=DCT1(J**)
NULL=TMP2(K**)
NULL=DCT1(K**)
A2=J
A2=K
2J'->S4+1,2J'/2K'->S5+1,2K'
INSTR$6 INTO INDS6
INSTR$7 INTO INDS7
NULL=TMP2(J**)
NULL=DCT1(J**)
NULL=TMP2(K**)
NULL=DCT1(K**)
NULL=DCT1(K**)
I/O HAS SETTLED! DONE?
WAIT FOR OUTPUT TO CLEAR
SET OUTPUT "SKIP" FLAG
RELEASE APS OUTPUT
A1=J
A1=K

```

```

A79 073FA 41C413C (00231)
      (00232) ;
A7A 073FC 0000000 (00233)
      MOV(10A,00)\NOP
A7B 073FE 0000000 (00234)
      NOP\MOV(10A,00)
A7C 07400 0000000 (00235)
      MOV(10A,00)\NOP
A7D 07402 0000000 (00236)
      MOV(10A,00)\NOP
A7E 07404 0000000 (00237)
      MOV(10A,00)\NOP
A7F 07406 0000000 (00238)
      NOP\MOV(10A,00)
A80 07408 0000000 (00239)
      NOP\MOV(10A,00)
A81 0740A 0000000 (00240)
      MOV(10A,A2)\NOP
A82 0740C 0000000 (00241)
      MOV(10A,A2)\NOP
A83 0740E 0000000 (00242)
      MOV(10A,A2)\NOP
A84 07410 42A425C (00243)
      MOV(10A,A2)\NOP
      (00244) ;
A85 07412 0000000 (00245)
      MOV(10A,00)\NOP
A86 07414 0000000 (00246)
      NOP\MOV(10A,00)
A87 07416 0000000 (00247)
      MOV(10A,00)\NOP
A88 07418 0000000 (00248)
      MOV(10A,00)\NOP
A89 0741A 0000000 (00249)
      MOV(10A,00)\NOP
A8A 0741C 0000000 (00250)
      NOP\MOV(10A,00)
A8B 0741E 0000000 (00251)
      NOP\MOV(10A,00)
A8C 07420 0000000 (00252)
      NOP\MOV(10A,00)
A8D 07422 10000073 (00253)
      JUMP(SHIFT5)
A8E 07424 20320000 (00254)
      CFAR(RA)\NOP
A8F 07426 00000000 (00255)
      NOP
      DNEFA
      SORTSSZ=BA-SORTSSA
      FND SORTSSZ
      FJCT
      (00256)
      (00257)
      (00258)
      (00259)
2J->S6+1,2J';2K->S7+1,2K
INSTRS4 INTO INDS4
INSTRS5 INTO INDS5
DCT2(I)=TMP2(J*)
TMP4(I)=DCT1(J*)
TMP3(I)=DCTM(J*)
DCT2(I+1)=TMP2(K*)
TMP4(I+1)=DCT1(K*)
TMP3(I+1)=DCTM(K*)
A2=J'
A2=K'
2J' -> S4+1, 2J'; 2K' -> S5+1
, 2K'
INSTRS6 INTO INDS6
INSTRS7 INTO INDS7
DCT2(I+2)=TMP2(J*)
TMP4(I+2)=DCT1(J*)
TMP3(I+2)=DCTM(J*)
DCT2(I+3)=TMP2(K*)
TMP4(I+3)=DCT1(K*)
TMP3(I+3)=DCTM(K*)
"JUMP(...)"* SETTIFFS 1/0
APII DONE!

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A1F 0746F 3E1921E1 (00304) TSTS10 SUHL(HR1,1),JUMPR(STALS1)
A20 07470 41091F79 (00305) ADDL(HR0,HS,TF),JUMP(TSTS10)
A21 07472 42300037 (00306) SFT(M1)
A22 07474 44500103 (00307) LOAD(HR1,SIZE+4)
      (00308) *
A23 07476 47400000 (00309) LOAD(HR0,I01,TF)
A24 07478 48500000 (00310) LOAD(HR2,MSS)
A25 0747A 49D00000 (00311) ADD(HR0,MSS,TF)
A26 0747C 4F0A0001 (00312) INDS1
A27 0747E 4F0A0001 (00313) AND(HR0,HS,TF)
A28 07480 50F4111C (00314) LOAD(HR2,INSTRS4(2),TF)
A29 07482 52F3111F (00315) LOAD(HR2,INSTRS5(2),TF)
A2A 07484 54600000 (00316) LOAD(HR2,MSS)
A2B 07486 562F0000 (00317) AND(HR2,MSS,NA,C)
A2C 07488 58500000 (00318) LOAD(HR2,MSS)
A2D 0748A 5A2F0000 (00319) AND(HR2,MSS,NA,C)
A2E 0748C 510A0001 (00320) AND(HR0,HS,TF)
A2F 0748E 5F0A0001 (00321) ADD(HR0,HS,TF)
A30 07490 60F41120 (00322) LOAD(HR2,INSTRS6(2),TF)
A31 07492 62F41122 (00323) LOAD(HR2,INSTRS7(2),TF)
A32 07494 64600000 (00324) LOAD(HR2,MSS)
A33 07496 662F0000 (00325) AND(HR2,MSS,NA,C)
A34 07498 68500000 (00326) LOAD(HR2,MSS)
A35 0749A 6A2F0000 (00327) AND(HR2,MSS,NA,C)
A36 0749C 6C192684 (00328) SUHL(HR1,4),JUMPR(INDSJ)
      (00329) *
A37 0749E 6F200031 (00330) DNEF1
A38 074A0 70000020 (00331) NOP
A39 074A2 72204240 (00332) G106S1
A3A 074A4 7470A000 (00333) TOPSP1
A3B 074A6 76H90020 (00334) ADDH(HR3,HR2,TF)
A3C 074A8 78740020 (00335) LOAD(HR3,DCTMS(2))
A3D 074AA 7AH40020 (00336) ANDH(HR3,HR2,TF)
A3E 074AC 7C760020 (00337) LOAD(HR3,DCT1(3))
A3F 074AE 7E740020 (00338) ANDH(HR3,HR2,TF)
A40 074B0 802F0000 (00339) AND(HR2,MSS,NA,C)
A41 074B2 82003A60 (00340) JUMP(TOPSP1)
      (00341) *
A42 074B4 84307040 (00342) G106S0
A43 074B6 864600FF (00343) LOAD(HR0,TARUS0-1(3))
A44 074B8 885600FF (00344) LOAD(HR1,TARUS1-1(3))
A45 074BA 8A6600FF (00345) LOAD(HR2,TARUS2-1(3))
A46 074BC 8C7600FF (00346) LOAD(HR3,TARUS3-1(3))
A47 074BE 8E0055F5 (00347) FIGSTST
      JUMPS(SETSCNT,G1)

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```

PICKUP TILL, NO MORE
IO=TARL0(I)
STALL,APS INPUT
INDR(.) SIZE-1+4

IORDR(.) A,I0=IORDR=J
DUMMY OP FOR EXEC
IO=IORDR(I)=K
IO=J'=IORDR(I+2)
IO=K'=IORDR(I+3)
OVERWRITE INDS4
OVERWRITE INDS5
RESET HR2 TO 0!
MSS=2J,PC->PC1
RFSFT HR2 TO 0!
MSS=2J,PC->PC1
IO=J'
IO=K'
OVERWRITE INDS6
OVERWRITE INDS7
RESET HR2 TO 0!
MSS=2J,PC->PC1
RESET HR2 TO 0!
MSS=2J,PC->PC1
FOR I=0,1,2,...,LTH+1

INPUT DONE!

SET OUTPUT PC
TMP2(.) RA
IO=TMP2(J* OR K*)
DCTM(.) RA
IO=DCTM(J* OR K*)
DCT1(.) RA
IO=DCT1(J* OR K*)
PC->PC0
RESET PC AND SETTLE

SET OUTPUT PC(PC3)
TARUF0(.) RA-1
TARUF1(.) RA-1
TARUF2(.) RA-1
TARUF3(.) RA-1
G1=1,SFT LFVFI, COUNTS

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A48 074C0	Q00040D1R	(00348)	JUMPS(INSRST0,AF0)
A49 074C1	Q2004FF9	(00349)	JUMPS(INSRST1,AF1)
A4A 074C4	Q40051FA	(00350)	JUMPS(INSRST2,AF2)
A4B 074C6	Q60053FA	(00351)	JUMPS(INSRST3,AF3)
A4C 074C8	Q80047AD	(00352)	JUMP(FIGSTST)
A4D 074CA	QAZ0002H	(00353)	INSRST0 CLEAR(AF0)
A4E 074CC	Q0014779	(00354)	ADDL(RW0,HS,TF),JUMP(FIGSTST)
A4F 074CF	Q4200029	(00355)	CLEAR(AF1)
A50 074D0	A1114779	(00356)	INSRST1
A51 074D2	A220002A	(00357)	ADDL(RW1,HS,TF),JUMP(FIGSTST)
A52 074D4	A5214779	(00358)	CLEAR(AF2)
A53 074D6	AK20002H	(00359)	AFDL(RW2,HS,TF),JUMP(FIGSTST)
A54 074D8	A9314779	(00360)	ADDL(RW3,HS,TF),JUMP(FIGSTST)
A55 074DA	AA500007	(00361)	SETSCNT LOAD(RW1,7)
A56 074DC	AC441F13	(00362)	LOAD(RW0,INSTRSD-1(7))
A57 074DE	AF0A0002	(00363)	ADD(RW0,WS,TF)
A58 074E0	K01157H1	(00364)	SURL(RW1,1),JUMPP(#1)
A59 074E2	H2200030	(00365)	CLEAR(R0)
A5A 074E4	H4500003	(00366)	LOAD(RW1,3)
A5B 074E6	H6C3FFC0	(00367)	LOAD(RW0,APSSMEM(1),TF)
A5C 074E8	H8115RH1	(00368)	SURL(RW1,1),JUMPP(#2)
A5D 074FA	HA200030	(00369)	CLEAR(R0)
A5E 074FC	HC000020	(00370)	NOP
A5F 074FE	HE460000	(00371)	LOAD(RW0,IORDRS(3),TF)
A60 074F0	CF050010R	(00372)	LOAD(RW1,SIZES+OVRSLAP-1)
A61 074F2	C30A0001	(00373)	ADD(RW0,HS,TF)
A62 074F4	C41161H1	(00374)	SURL(RW1,1),JUMPP(RDSOUT)
		(00375)	?
		(00376)	*
A63 074F6	C64603FF	(00377)	LOAD(RW0,INSTRS-2(3))
A64 074F8	C8560DFE	(00378)	LOAD(RW1,TMP3S-2(3))
A65 074FA	CA760RFE	(00379)	LOAD(RW3,TMP4S-2(3))
A66 074FC	CC000020	(00380)	NOP
A67 074FE	CF641F21	(00381)	LOAD(RW2,INSTRS6+1(2),TF)
A68 07500	D1641F23	(00382)	LOAD(RW2,INSTRS7+1(2),TF)
A69 07502	D2E3FFC0	(00383)	LOAD(RW2,APSSMEM(1),TF)
A6A 07504	D4AF0000	(00384)	ADD(RW2,MSS,TF,C)
A6B 07506	D7641F10	(00385)	LOAD(RW2,INSTRS4+1(2),TF)
A6C 07508	D9641F1F	(00386)	LOAD(RW2,INSTRS5+1(2),TF)
A6D 0750A	DAB3FFC0	(00387)	LOAD(RW2,APSSMEM(1),TF)
A6E 0750C	DCAF0000	(00388)	ADD(RW2,MSS,TF,C)
A6F 0750E	DE206670	(00389)	JUMP(INSTR,RO),CLEAR
A70 07510	F0300032	(00390)	G10AS3
A71 07512	F2007A8A	(00391)	T0PSP3
			JUMPP(DMYSF11,AF0)

AF0=1 THEN LEVEL 0  
 AF1=1 THEN LEVEL 1  
 AF2=1 THEN LEVEL 2  
 AF3=1 THEN LEVEL 3  
 RE=TFST ALL FLAGS  
 RELFASF APU  
 00=TABLE0(II),II=II+1  
 RELEASE APU  
 00=TABLE1(JJ),JJ=JJ+1  
 RELFASF APU  
 00=TABLE2(KK),KK=KK+1  
 RELEASE APU  
 00=TABLE3(LL),LL=LL+1  
 8 "INSTR" WORDS...  
 TO BE SET OR CLEARED  
 00=INSTRS#+1  
 FOR I=0,1,2...7  
 STALL APS OUTPUT  
 3+1=4 SCATTER WRITES  
 INSTRS# OVERWRITE  
 FOR I=0,1...3  
 STALL APS OUTPUT  
 RELEASED BY APU  
 IORDR(.) RA,00=IORDR(0)  
 IORDR(.) SIZE-2+OVLAP  
 00=IORDR(I)  
 FOR I=1,2...I,TH-1,+10 0\*

DCT2(.) RA-2  
 TMP3(.) RA-2  
 TMP4(.) RA-2  
 LET "JUMP(...)" SETTLE  
 SET MSS IN INSTRS6  
 SET MSS IN INSTRS7  
 OVERWRITE INDS4  
 OVERWRITE INDS5;PC3->PC  
 SET MSS IN INSTRS4  
 SET MSS IN INSTRS5  
 OVERWRITE INDS6  
 OVERWRITE INDS7;PC3->PC  
 RESFT & STALL  
 ENABLE APU  
 WAIT FOR "SKTP" FLAG

S



```

A72 07514 F4HA0002 (00397)      ADD(HW0,MS,TF)
A73 07516 F6HA0002 (00394)      ADD(HW1,MS,TF)
A74 07518 F8HA0002 (00394)      ADD(HW3,MS,TF)
A75 0751A F8HA0002 (00395)      ADD(HW0,MS,TF)
A76 0751C F0HA0002 (00396)      ADD(HW1,MS,TF)
A77 0751E F2HA0002 (00397)      ADD(HW3,MS,TF)
A78 07520 F02F0000 (00398)      CHNGSPC ADD(HW2,MSS,C)
A79 07522 F2007160 (00399)      JUMP(TOPSP3)
A7A 07524 F4F70794 (00400)      DMYSFI. LOAD(HW2,IMYS(1),TF)
A7B 07526 F6AA0000 (00401)      ADD(HW2,MSS,TF)
A7C 07528 F8AA0000 (00402)      ADD(HW2,MSS,TF)
A7D 0752A FAAA0000 (00403)      ADD(HW7,MSS,TF)
A7E 0752C FCBA0000 (00404)      ADD(HW2,MSS,TF)
A7F 0752E FE17878 (00405)      ADD(HW2,MSS,TF),JUMP(CHNGSPC)
                                (00406) *
                                00007476 (00407)
                                (00408) ;
07530                                FND #A-1
07530 00000000 (00410) F STORAGE REUCK FOR CONSTRUCTED INSTRUCTIONS
                                G10KSI DATA 1F'0.0'
07532 00000102 (00412) G10KSI G10KSIZE=-G10K6
                                FND

```

```

00=DCT2(J*)
00=TMP3(J*)=DCTT(J*)
00=TMP4(J*)=DCTIT(J*)
00=DCT2(K*)
00=TMP3(K*)=DCTT(K*)
00=TMP4(K*)=DCTIT(K*)
NO IO! CHANGE PC
RFSFT PC AND SETTLE
00=?
00=?
00=?
00=?
00=? THEN CHANGE PC
ASSIGN VALUE TO CHAIN AN
CHOR

```

AFDTSORG:	0000H	(00000)	(00039)
APSSMEM:	1FCU	(00009)	(00367)
CHMGSPC:	0007R	(00398)	(00405)
CWTSOIT:	0003A	(00134)	(00144)
CSPUSMUS:	021FC	(00010)	(00042)
ICTS:	0002R	(00011)	(00337)
DCT16:	00200	(00017)	(00377)
DCT26:	00400	(00013)	(00377)
DCTMS:	0002H	(00014)	(00335)
DMS1:	0079A	(00015)	(00400)
DMSF11:	0007A	(00391)	(00400)
DMSA:	000HF	(00225)	(00255)
DMS1:	00037	(00330)	(00354)
FLGSTST:	00047	(00347)	(00352)
G1065:	07430	(00041)	(00266)
G10651:	00039	(00273)	(00332)
G10652:	00070	(00342)	(00390)
G1065A:	07476	(00269)	(00407)
G10651:	07530	(00264)	(00411)
G10650:	00042	(00332)	(00342)
G10652:	00102	(00266)	(00412)
HS:	00001	(00017)	(00282)
		(00354)	(00356)
		(00293)	(00297)
		(00301)	(00305)
		(00312)	(00313)
		(00320)	(00321)
		(00358)	(00360)
		(00373)	(00373)
IIMCS1:	0000H	(00281)	(00283)
IMSD:	0001A	(00050)	(00303)
IMS1:	0001A	(00051)	(00299)
IMS2:	00016	(00052)	(00295)
IMS3:	00012	(00053)	(00291)
IMCS1:	0000C	(00119)	(00133)
IMCS1:	00100	(0001H)	(00029)
IMCS4:	00033	(00054)	(00325)
IMDS5:	00035	(00055)	(00327)
IMDS6:	0002H	(00056)	(00317)
IMDS7:	00020	(00057)	(00314)
IMDS1:	00066	(00380)	(00389)
IMDS1:	00026	(00312)	(0032R)
IMSRT50:	00040	(00348)	(00353)
IMSRT81:	0004F	(00349)	(00355)
IMSRT82:	00051	(00350)	(00357)
IMSRT83:	00053	(00351)	(00359)
IMSTR50:	01F14	(00050)	(00286)
IMSTR81:	01F16	(00051)	(00362)
IMSTR52:	01F1H	(00052)	
IMSTR83:	01F1A	(00053)	

INSTR4:	01F1C (00054)	(00314)	(00345)
INSTR5:	01F1F (00055)	(00315)	(00346)
INSTR6:	01F20 (00056)	(00322)	(00341)
INSTR7:	01F22 (00057)	(00323)	(00342)
INTGR:	01F24 (00058)	(00280)	
INTDR:	00000 (00019)	(00371)	
ISAS00:	00502 (00021)		
ISAS01:	00503 (00022)	(00277)	
ISVT:	00502 (00020)	(00021)	(00022)
L0SS0:	00010 (00300)	(00302)	
L1SS0:	00019 (00296)	(00298)	
L2SS0:	00015 (00292)	(00294)	
L3SS0:	00011 (00290)		
LVI\$0:	00030 (00120)	(00155)	
LVI\$1:	00026 (00121)	(00145)	
LVI\$2:	00010 (00124)	(00135)	
LVI\$3:	00012 (00125)		
MSS:	00000 (00023)	(00291)	(00295)
	(00319)	(00324)	(00326)
	(00402)	(00403)	(00404)
	(00184)	(00188)	
NRDR\$1:	0000A (00024)	(00372)	
NRDR\$AP:	00061 (00373)	(00374)	
RDSOUT:	00001 (00266)	(00274)	
SC1RS:	00055 (00347)	(00361)	
SFT\$CNT:	00073 (00225)	(00253)	
SHUF1\$V:	0005H (00196)		
S1ZFS:	000FF (00025)	(00279)	(00307)
SORTS:	0730R (00040)	(00105)	
SORT\$SA:	00000 (00103)	(00107)	(00257)
SORT\$S7:	00090 (00104)	(00257)	(00258)
STAL\$:	00074 (00226)	(00226)	
STAL\$1:	00021 (00304)	(00306)	
START:	07300 (00027)	(00091)	
SVTS:	003H7 (00026)		
T1\$:	07300 (00095)	(00274)	
T2\$:	07302 (00096)	(00275)	
T3\$:	07304 (00097)	(00276)	
TAMU\$0:	00C00 (00028)	(00029)	(00302)
TAMU\$1:	00D00 (00029)	(00030)	(00343)
TAMU\$7:	00F00 (00030)	(00031)	(00294)
TAMU\$3:	00F00 (00031)	(00290)	(00344)
TAMU\$3:	00A00 (00032)	(00278)	(00345)
TMP2\$:	00F00 (00033)	(00333)	
TMP3\$:	00F00 (00033)	(00378)	
		(00310)	(00311)
		(00303)	(00316)
		(00327)	(00344)
		(00339)	(00398)
		(00344)	(00398)
		(00405)	(00401)

PAGE 14: FCW 249... "MPASHT(U)" SORT DCT1(.) COEFFICIENTS

TMPS: 0000 (00034) (00379)  
TOPSP1: 003A (00333) (00340)  
TOPSP3: 0071 (00391) (00399)  
TSTSL0: 001F (00304) (00305)  
TSTSL1: 001A (00300) (00301)  
TSTSL2: 0017 (00296) (00297)  
TSTSL3: 0013 (00292) (00293)  
WS: 0002 (00015) (00281) (00287) (00363) (00391) (00394) (00395) (00396) (00397)  
ZS: 0003 (00036)

LINES WITH ERRORS: 0 (MAP VERSION 000101.10) 4- 0

QUANTIZE DCT PARAMETERS  
ORIGINATED:25-JUL-79  
UPDATED:29-MAY-80

FCR 250... "MPFSTO(Y,U,V,W)"

```

(00001) *
(00002) *
(00003) *
(00004) *
(00005) *
(00006) *
(00007) *
(00008) *
(00009) *
(00010) *
(00011) *
(00012) *
(00013) *
(00014) *
(00015) *
(00016) *
(00017) *
(00018) *
(00019) *
(00020) *
(00021) *
(00022) *
(00023) *
(00024) *
(00025) *
(00026) *
(00027) *
(00028) *
(00029) *
(00030) *
(00031) *
(00032) *
(00033) *
(00034) *

```

```

* DEFINE GLOBAL SYMBOLS
  OPAND EXP, (1 .U.S. 14) + (17 .U.S. R) + $IF
  OPAND OUF, (3 .U.S. 10) + (17 .U.S. 5) + $IH
  OPAND WAFMT, (1 .U.S. 10) + (12 .U.S. 5) + $IA
  OPAND FO, (3 .U.S. 10) + (12 .U.S. 5) + $IC
  AFDTSORG=$RPH
  APSSMEM=$1FFC0
  CSPUSNOS=$21FC
  BW=$
  HS=1
  WS=0
  WSS=0
  ISVTS=$502
  ISAS01=$ISVTS+D'1'
  SVTS=$01R2
  SAS11=$SVTS+2*D'11'
  SAS38=$SVTS+2*D'1R'
  SAS39=$SVTS+2*D'3R'
  START=$7600
  TMP3S=D'35R4'
  TMP4S=D'3072'
  WS=2
  ZS=3

```

EXPAND ARRAY FUNCTION DISPATCH TABLE

```

$1=$AFDTSORG+3*2*(250-12R)
ADDR QUANS(R7,1)
ADDR GI04S(R7,1)
ADDR CSPUSNOS(1,0)
EJECT

```

```

(00035) *
(00036) *
(00037) *
(00038)
(00039)
(00040)
(00041) INSTRSD DATA INSURP1512,X150',X10000'
(00042)
(00043) NLS0 DATA 1F'0.0'
(00044) NLS1 DATA 1F'1.0'
(00045) NLS2 DATA 1F'2.0'
(00046) NLS3 DATA 1F'4.0'
(00047) NLS4 DATA 1F'8.0'
(00048) NLS5 DATA 1F'16.0'
(00049) * 0.1 1FVFL
(00050) DTHRS01 DATA 16F'1.0E10'
...
(00051) * 2 1FVFL
(00052) DTHRS2 DATA 1F'1.1260',15F'1.0E10'
...
(00053) * 3 1FVFL
(00054) DTHRS3 DATA 1F'0.5332',1F'1.2527',1F'2.3796',13F'1.0E10'
...
(00055) * 4 1FVFL
(00056) DTHRS4 DATA 1F'0.2644',1F'0.5667',1F'0.9198',1F'1.3444'
(00057) DATA 1F'1.8776',1F'2.5971',1F'3.724',9F'1.0E10'
...
(00058) * 5 1FVFL
(00059) DTHRS5 DATA 1F'0.1322',1F'0.2732',1F'0.4243',1F'0.5870'
(01014 22F'17C0
(01016 364F'7640
(01018 4M'2200C0
(0101A 61A0H0C0 (00060) DATA 1F'0.7632',1F'0.9555',1F'1.1669',1F'1.4019'

```

PAGE 3: FCN 250... "MPESTO(V,U,V,\*)" QUANTIZE DCT PARAMETERS

01D1C 7A40D2C0	
01D1F 0A55CF0C1	
01D20 0R371741	
01D22 00549541 (00061)	DATA 1F'1.6663', 1F'1.9687', 1F'2.3217', 1F'2.7463'
01D24 0FAPF5C1	
01D26 1292D741	
01D28 15F86C41	
01D2A 1A3C6A41 (000A7)	DATA 1F'3.2795', 1F'3.9990', 1F'5.1259', 1.0F10
01D2C 1F0F3C1	
01D2E 2001D7C1	
01D30 12A05E49	
00007600	HIS 1
(00063)	SI=START
(00064)	EVEN
(00065)	F.PCT
(00066)	





A20	07642	02600260	(00111)	R(A3)	R=LE';R=LO'
A21	07644	1000023	(00112)	JUMP(CJMSFLC)	REFST "FLAG JAMS"
A22	07646	430030	(00113)	ADD(A6,A3)	LE'+NI;LO'+NL
A23	07648	5560560	(00114)	MUL(M2,M7)	P<0 FORCING T-RIT->0 FOR
A24	0764A	08F00F0	(00115)	MOV(10A,M0)	DCTI(J)
A25	0764C	08F0000	(00116)	MOV(10A,M4)XNOP	D(K)
A26	0764E	000008FC	(00117)	NOP\MOV(10A,M4)	A=Q(K-2),Q(K):A4=Q(K-1)
A27	07650	8414414	(00118)	MOV(A4),MUL(M0,M4)	Q(K+1)
A28	07652	4C134 13	(00120)	MOV(A3),SUR(A0,A4)	A=Q(K-2),T(K-2);LO'=LO
A29	07654	08F00000	(00121)	MOV(10A,M5)\NOP	++2,T(K-1)
A2A	07656	000008ED	(00122)	NOP\MOV(10A,M5)	D(K+2)
A2B	07658	08000800	(00124)	MOV(R,NUL1)	D(K+3)
A2C	0765A	911F0039	(00125)	JUMPS(T3S,T1)	FORCE COMPLETION
A2D	0765C	911F003F	(00126)	JUMPS(T4S,T2)	IS T1=1 ON T(K-4)?
A2E	0765E	43A043A0	(00127)	ADD(A2,A3)	IS T2=1 ON T(K-3)?
A2F	07660	20370000	(00128)	CLFAR(W1)\NOP	LE'+2;LO'+2
A30	07662	84358435	(00129)	MOV(A5),MUL(M0,M5)	RELEASE APS INPUT
A31	07664	40134013	(00131)	MOV(A3),SUR(A0,A5)	A5=Q(K),Q(K+2);A5=Q(K+1)
A32	07666	08F00000	(00132)	MOV(10A,M4)\NOP	Q(K+3)
A33	07668	080008FC	(00134)	NOP\MOV(10A,M4)	LE'=DE'+2,T(K);LO'=LO'
A34	0766A	08000800	(00135)	MOV(R,NUL1)	+2,T(K+1)
A35	0766C	911F0039	(00136)	JUMPS(T3S,T1)	D(K+4)
A36	0766E	911F003F	(00137)	JUMPS(T4S,T2)	D(K+5)
A37	07670	43A043A0	(00138)	ADD(A2,A3)	FORCE COMPLETION
A38	07672	10370027	(00139)	JUMP(L00PS,W1),CLFAR	IS T1=1 ON T(K-4)?
A39	07674	02600000	(00140)	P(A3)\NOP	IS T2=1 ON T(K-3)?
A3A	07676	20480000	(00141)	SFT(AF0)\NOP	LE'+2;LO'+2
A3B	07678	084C0000	(00142)	MOV(P,00)\NOP	K=K+4 FOR K<LTH
A3C	0767A	91000043	(00143)	JUMPS(FIXS0,AF3)	LE
A3D	0767C	10370005	(00145)	JUMP(INCSJ,W1),CLFAR	INFORM APS
A3E	0767E	00000260	(00146)	NOP\R(A3)	NO=LE
A3F	07680	20480000	(00147)	SFT(AF0)\NOP	"FIX" ALL V VALUES @ ONC
A40	07682	0000089C	(00148)	NOP\MOV(R,00)	E
A41	07684	91000043	(00149)	JUMPS(FIXS0,AF3)	FOR I=01,2...LTH-1
A42	07686	10370005	(00151)	JUMP(INCSJ,W1),CLFAR	LO
A43	07688	20370000	(00152)	CLFAR(W1)\NOP	INFORM APS
A44	0768A	08F008FC	(00154)	MOV(10A,M7)	NO=LO

RELEASE APS INPUT  
M7=SCALAR A





A1F 0766C 4E9A0002 (00216)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I)
A20 0766F 40003860 (00217)	JUMP(ADDSI)	LKT APU CATCH-UP
A21 07660 428A0002 (00218)	ADD(HR0,MS,TF)	IO=DCTT(J)
A22 07662 340410C (00219)	LOAD(HR1,MS,S(2),TF)	IO=NL=4
A23 07664 46AA0002 (00220)	ADD(HR2,MS,TF)	IO=DCTT(J)
A24 07666 400410C (00221)	LOAD(HR1,DTHS3(2),TF)	IO=DTHR(3 LEVEL)
A25 07668 480410C (00222)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I)
A26 0766A 3C003860 (00223)	JUMP(ADDSI)	LKT APU CATCH-UP
A27 0766C 4E8A0002 (00224)	ADD(HR0,MS,TF)	IO=DCTT(J)
A28 0766E 500410C (00225)	LOAD(HR1,MS,S(2),TF)	IO=NL=2
A29 07700 52AA0002 (00226)	ADD(HR2,MS,TF)	IO=DCTT(J)
A2A 07702 540410C (00227)	LOAD(HR1,DTHS2(2),TF)	IO=DTHR(2 LEVEL)
A2B 07704 569A0002 (00228)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I)
A2C 07706 58003860 (00229)	JUMP(ADDSI)	LKT APU CATCH-UP
A2D 07708 5A8A0002 (00230)	ADD(HR0,MS,TF)	IO=DCTT(J)
A2E 0770A 5C0410C (00231)	LOAD(HR1,MS,S(2),TF)	IO=NL=1
A2F 0770C 5EAA0002 (00232)	ADD(HR2,MS,TF)	IO=DCTT(J)
A30 0770E 600410C (00233)	LOAD(HR1,DTHS0(2),TF)	IO=DTHR(1 LEVEL)
A31 07710 629A0002 (00234)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I)
A32 07712 64003860 (00235)	JUMP(ADDSI)	LKT APU CATCH-UP
A33 07714 668A0002 (00236)	ADD(HR0,MS,TF)	IO=DCTT(J)
A34 07716 680410C (00237)	LOAD(HR1,MS,S(2),TF)	IO=NL=0
A35 07718 6AA00002 (00238)	ADD(HR2,MS,TF)	IO=DCTT(J)
A36 0771A 6C0410C (00239)	LOAD(HR1,DTHS0(2),TF)	IO=DTHR(0 LEVEL)
A37 0771C 6E9A0002 (00240)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I)
A38 0771E 709A0002 (00241)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I+1)
A39 07720 729A0002 (00242)	ADD(HR1,MS,TF)	IO=DTHR(OFFSET+I+2)
A3A 07722 74000437 (00243)	SET(W3)	WAIT FOR THRESHOLD DEC.
A3B 07724 76000020 (00244)	NOP	RELEASED BY APU
A3C 07726 78003860 (00245)	JUMP(ADDSI,AF0)	APU DECISION ON AF0
A3D 07728 7A000048 (00246)	JUMP(C(LINCS1,AF3)	FOR I=0,1,2...LTH-1
A3E 0772A 7C0203CF (00247) *		
A3F 0772C 7E020398 (00248)	LOAD(HR0,MS,S(1),TF)	IO=2**(-15)
A40 0772E 8002027A (00249)	LOAD(HR0,MS,S(1)(1),TF)	IO=2**(-16)
A41 07730 82500000 (00250)	LOAD(HR0,(21)	TMP1(.) RA
A42 07732 84020000 (00251)	SUB(HR0,MS)	TMP1(.) SIZE-1
A43 07734 86AA0002 (00252)	ADD(HR0,MS,TF)	TMP1(.) RA-2
A44 07736 88AA0002 (00253)	ADD(HR0,MS,TF)	IO=TMP1(I)
A45 07738 8A194382 (00254)	SUBL(HR1,2),JUMP(P(FIXSI)	IO=TMP1(I+1)
A46 0773A 8C200031 (00255) *		FOR I=0,1,2...LTH-1
A47 0773C 8E000020 (00256)	CFAR(R1)	INPUT DONE!
A48 0773E 90000020 (00257)	ADD(HR0,MS,TF)	
A49 07740 92000020 (00258)	ADD(HR0,MS,TF)	
A50 07742 94000020 (00259)	ADD(HR0,MS,TF)	

A4R 0773R	90300032	(00260)	G104S0	SFT(RA)	FNAME APU
A4Q 07740	92402012	(00261)		LOAD(RW0,I2)	TMPI(.) RA
A4R 07742	94500000	(00262)		LOAD(RW1,MS)	TMPI(.) SIZE-1
A4H 07744	96020000	(00263)		SUB(RW0,MS)	TMPI(.) RA-2
A4C 07746	98631045	(00264)	01NCS,I	LOAD(RW2,INSTRS0+1(2),TF)	MSS=INITS IN INSO
A4D 0774N	9A200030	(00265)		CLEAR(R0)	STALL APS OUTPUT
A4F 0774A	9C000020	(00266)		NOP	RELEASED BY APU
A4Y 0774C	9E0056E9	(00267)		JUMP(SEL,STMP,AP1)	IF AP1=1 THEN ZERO FILL
A50 0774E	9F3EFC0	(00268)		LOAD(RW2,APSSMEM(1),TF)	INSTRS0 INTO APS INPUT
A51 07750	A2200030	(00269)		CLEAR(R0)	STALL APS OUTPUT
A52 07752	A4000020	(00270)		NOP	
A53 07754	A68A0002	(00271)		ADD(RW0,MS,TF)	00=TMP3(J)
A54 07756	A81340C4	(00272)		SUB(RW1,I),JUMPF(01NCS,I)	FOR J=0,1...LTH-1
A55 0775N	AA30596B	(00273)	*	JUMP(OFTXS,AF3),SFI	SIGNAL FND TO APS INPUT
A56 0775A	ACHA0002	(00274)	CLHSTMP	ADD(RW0,MS,TF)	00=TMP3(J)
A57 0775C	AF1568B1	(00276)		SUB(RW1,I),JUMPF(CLNSTMP)	FOR J=2...LTH-1
A5N 0775F	BD200030	(00277)		CLEAR(R0)	STALL APS OUTPUT
A5Q 07760	B2400020	(00278)	*	LOAD(RW0,I)	0DCT(.) RA
A5A 07762	B4500000	(00280)		LOAD(RW1,MS)	0DCT(.) SIZE-1
A5H 07764	B6020000	(00281)		SUB(RW0,MS)	0DCT(.) RA-1
A5C 07766	B8700003	(00282)		LOAD(RW3,I)	3+1=4 DUMPIES
A5D 0776N	BAF20794	(00283)	B1	LOAD(RW2,DVYS(1),TF)	00=?
A5F 0776A	BC3150H1	(00284)		SUB(RW1,I),JUMPF(B1)	4 TIMES
A5F 0776C	BE0A0001	(00285)	0FTXS,I	ADD(RW0,MS,TF)	00=0DCT(I)
A60 0776E	C10A0001	(00286)		ADD(RW0,MS,TF)	00=0DCT(I+1)
A61 07770	C2115E82	(00287)	*	SUB(RW1,2),JUMPF(0FTXS,I)	FOR I=0,1,2...LTH-1
A62 07772	C4200030	(00289)		CLEAR(R0)	OUTPUT DONE!
A63 07774	C6000020	(00290)		NOP	
	00007760	(00291)	*	G104SA= #C	ASSIGN VALUE TO CHAIN AN
		(00293)	;		CHOR
07776		(00294)		FND #A-1	
07776	00000000	(00295)	?	STORAGEF BLOCK FOR CONSTRUCTED INSTRUCTIONS	
...		(00296)	G104SI	DATA 4E'0,0'	
0777E	00009000	(00297)		G104SZ=01=-G104S	
		(00298)		FND	

ADDS1:	0003H (00211)	(00217)	(00223)	(00229)	(00235)	(00241)	(00245)
AFDTRNG:	0006H (00009)	(00010)					
APSRMFM:	1FFC0 (00010)	(00268)					
RS0:	00033 (00201)	(00236)					
RS1:	00020 (00202)	(00230)					
RS2:	00027 (00203)	(00224)					
RS3:	00021 (00204)	(00218)					
MS4:	00014 (00205)	(00212)					
MS5:	00015 (00206)						
MITSOK:	00012 (00017)	(00096)	(00096)				
CLRS:	0000P (00001)	(00094)					
CLMSTIG:	00023 (00112)	(00114)					
CLMSTAP:	00056 (00267)	(00275)	(00276)				
CSUSMMS:	021FC (00011)	(00033)					
UCTSM:	00022 (00110)	(00113)					
DMYS:	00794 (00012)	(00283)					
DMFSA:	0004B (00165)						
DMFS1:	0003A (00257)						
DMFS0:	00062 (00289)						
DTMFS01:	01C92 (00050)	(00233)	(00239)				
DTMFS2:	01C92 (00052)	(00227)					
DTMFS3:	01C92 (00054)	(00221)					
DTMFS4:	01CF2 (00056)	(00215)					
DTMFS5:	01D12 (00059)	(00209)					
FIX0:	00043 (00095)	(00143)	(00149)	(00153)			
G1048:	076AF (00032)	(00178)	(00184)	(00297)			
G1048A:	07760 (00181)	(00292)					
G1048B:	07776 (00176)	(00246)					
G1048C:	0004H (00185)	(00260)					
G1048D:	00000 (00180)	(00297)					
G1048E:	00001 (00014)	(00193)	(00285)	(00286)			
FIX1:	00043 (00253)	(00255)					
FIX0:	0000H (00193)	(00246)					
INC1:	00005 (00080)	(00145)	(00151)				
INSTMS0:	01C84 (00041)	(00197)	(00264)				
ISAS001:	00503 (00018)	(00187)					
ISVTS:	00502 (00017)	(00018)					
LOOPS:	00027 (00118)	(00139)					
MS:	00000 (00015)						
MS5:	00000 (00016)	(00189)	(00180)	(00200)	(00251)	(00262)	(00281)
MS0:	01C86 (00043)	(00237)					
MS1:	01C8H (00044)	(00231)					
MS2:	01C8A (00045)	(00225)					

NLS1: 01CR0 (00046) (00210)  
 NLS4: 01CR0 (00047) (00213)  
 NLS5: 01CR0 (00048) (00207)  
 OF1R1: 00059 (00273) (00270)  
 OF1R1: 0005F (00285) (00287)  
 OF1R3: 0004C (00264) (00272)  
 QUANS: 07607 (00031) (00073)  
 QUANSSA: 00000 (00071) (00075) (00169)  
 QUANSSZ: 00052 (00072) (00169) (00170)  
 SAS11: 0039R (00020) (00240)  
 SAS1R: 003C+ (00021) (00248)  
 SAS1Q: 00400 (00022)  
 SAS1R: 0003F (00178) (00196) (00248)  
 START: 07600 (00023) (00064)  
 SVTS: 003R2 (00019) (00020) (00021) (00022)  
 T3S: 00034 (00125) (00136) (00140)  
 T4S: 0003E (00126) (00137) (00146)  
 TMP1S: 00C00 (00025) (00192)  
 TMP4S: 00002 (00026) (00206) (0020H) (00210) (00212) (00214) (00216) (00218) (00220) (00222)  
 WS: (00224) (00226) (00228) (00230) (00232) (00234) (00236) (00238) (00240) (00241)  
 ZS: (00242) (00253) (00254) (00271) (00275)  
 ZF0S: 00003 (00027)  
 ZF0R: 0000A (00088)

DEQUANTIZE DCT PARAMETERS  
 ORIGINATED:14-JAN-80  
 UPDATED:07-APR-80

FCH 251... "MPFST(Y,U,V,W)"

DEFINING GLOBAL SYMBOLS  
 OPADD EXP, (1 .LS. 14) + (12 .LS. 8) + SIF  
 OPADD ODP, (1 .LS. 10) + (12 .LS. 5) + SIF  
 OPADD WAFMF, (1 .LS. 10) + (12 .LS. 5) + SIA  
 OPADD FD, (1 .LS. 10) + (12 .LS. 5) + SIC

AFITSORG=SMFH  
 APSSM=SIFFCO  
 CSPUSMOS=S2IFC  
 DECTIS=0'1024'  
 MS=3  
 MS=1  
 TRITS=0'2088'  
 MINIT2=>INIT  
 TRITS=0'10792'  
 MS=0  
 MSS=0  
 SVTS=S0387  
 SAS38=SVTS\*2\*0'38'  
 SAS39=SVTS\*2\*0'38'  
 START=S7FD  
 TMP4=0'3072'  
 DCTIT2=>TMP4  
 TMP4=0'9512'  
 MS=2  
 MS=3

EXPAND ARRAY FUNCTION DISPATCH TABLE

SI=AFITSORG\*3\*2\*(251-128)  
 ADDR GUANS(7,1)  
 ADDR G104S(7,1)  
 ADDR CSPUSMOS(1,0)  
 FJECT

(00001) \*  
 (00002) \*  
 (00003) \*  
 (00004) \*  
 (00005) \*  
 (00006) \*  
 (00007) \*  
 (00008) \*  
 (00009) \*  
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 (00022) \*  
 (00023) \*  
 (00024) \*  
 (00025) \*  
 (00026) \*  
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 (00029) \*  
 (00030) \*  
 (00031) \*  
 (00032) \*  
 (00033) \*  
 (00034) \*  
 (00035) \*





01D8C 0040A0C0  
 01D8E 00F353C1  
 01D90 0C9F55C1 (000659) DATA 1F'-1.5770', 1F'-2.1773', 1F'-3.0169', 1F'-4.4311'  
 01D92 91601C41  
 01D94 98229C41  
 01D96 A3724C1  
 01D98 084126C0 (000660) DECS DATA 1F'-0.0640', 1F'-0.2004', 1F'-0.3461', 1F'-0.5025'  
 01D9A 19A65540  
 01D9C 2C400140  
 01D9E 405164C0  
 01DA0 55F36640 (000661) DATA 1F'-0.6715', 1F'-0.8550', 1F'-1.0559', 1F'-1.2779'  
 01DA2 8D70A3C0  
 01DA4 087274C1  
 01DA6 0A3923C1  
 01DA8 0C350441 (000662) DATA 1F'-1.5259', 1F'-1.8068', 1F'-2.1306', 1F'-2.5129'  
 01DAA 0F7453C1  
 01DAC 11087441  
 01DAE 141A6441  
 01D90 13D66CC1 (000663) DATA 1F'-2.9797', 1F'-3.5793', 1F'-4.4188', 1F'-5.8330'  
 01DB2 1CA26841  
 01DB4 235463C1  
 01DB6 2F4944C1  
 01DB8 043126C0 (000664) DATA 1F'-0.0640', 1F'-0.2004', 1F'-0.3461', 1F'-0.5025'  
 01DBA 99A65540  
 01DBC AC3D0140  
 01DBE C05164C0  
 01DC0 05F36640 (000665) DATA 1F'-0.0715', 1F'-0.8550', 1F'-1.0559', 1F'-1.2779'  
 01DC2 FD70A3C0  
 01DC4 887274C1  
 01DC6 0A3923C1  
 01DC8 0C350441 (000666) DATA 1F'-1.5259', 1F'-1.8068', 1F'-2.1306', 1F'-2.5129'  
 01DCA 0F7453C1  
 01DCE 941A6441  
 01DD0 97D66CC1 (000667) DATA 1F'-2.9797', 1F'-3.5793', 1F'-4.4188', 1F'-5.8330'  
 01DD2 9CA26841  
 01DD4 A35463C1  
 01DD6 AFA944C1  
 00007740  
 000069  
 000070  
 000071  
 RUS 1  
 BL=START  
 FVFN  
 FJECT

(00072) *	START ADDRESS	FIXED PT. OP.
(00073) *	MODULF SIZE	INIT(1)->INSTRS1+1
(00074) *		WAIT4 INSTRS1+1 REWRITE
(00075)		RELEASE APS INPUT
(00076)		RELEASE APS OUTPUT
(00077)		INSTRS1 INTO IN\$1
(00078)		M7=TMP4(1)=DCTI(J)
(00079)		A0=M=QDCT(1)
(00080)		2M
(00081)		WAIT4 IN\$1 REWRITE
(00082)		RELEASE APS OUTPUT
(00083)		RELEASE APS INPUT
(00084)		2M->INSTRS2+1
(00085)		WAIT4 INSTRS2+1 REWRITE
(00086)		WAIT4 APS TO STALL
(00087)		RELEASE APS INPUT
(00088)		RELEASE APS OUTPUT
(00089)		INSTRS2 INTO IN\$2
(00090)		FLOATING PT. OP.
(00091)		WAIT4 IN\$2 REWRITE
(00092)		RELEASE APS INPUT
(00093)		RELEASE APS OUTPUT
(00094)		M0=DECR(2M)
(00095)		DECSR(2M)+TMP4(1)
(00096)		INIT(I+1)->INSTRS1+1
(00097)		00=TMP1(1)=>DRDCT(J)
(00098)		FIXED PT. OP.
(00099)		WAIT4 FOR 2 00'S TO SETT
(00100)		LP
(00101)		RELEASE APS INPUT
(00102)		FOR I=0,1,2...LTH-1
(00103)		
(00104)		RELEASE APS OUTPUT
(00105)		INSTRS4+1=0; INSTRS5+1=0
(00106)		INSTRS6+1=0; INSTRS7+1=0
(00107)		RELEASE APS INPUT
(00108)		
(00109)		
(00110)		
(00111)		
(00112)		
(00113)		
(00114)		
(00115)		

A22 07M26 08FC0000 (00116)	MOV(10A,AD)\NOP	AD=J
A23 07M26 000000F0 (00117)	NOP\MOV(10A,AD)	AD=K
A24 07M26 40004000 (00118)	ADD(40,AD)	2J;2K
A25 07M2C 08F10000 (00119)	MOV(10A,A1)\NOP	A1=J'
A26 07M2F 000000F1 (00120)	NOP\MOV(10A,A1)	A1=K'
A27 07M30 413C413C (00121)	MOV(00),ADD(A1,A1)	2J->INSTRS6+1,2J';2K->INSTRS7+1,2K'
A28 07M32 08FC0000 (00122)	MOV(10A,00)\NOP	INSTRS4 INTO INDS4
A29 07M34 000008FC (00123)	NOP\MOV(10A,00)	INSTRS5 INTO INDS5
A2A 07M36 08F00000 (00124)	MOV(10A,NUL)\NOP	NUL:=TMP1(J#)
A2B 07M38 000000F0 (00125)	NOP\MOV(10A,NUL)	NUL:=TMP1(K#)
A2C 07M3A 08F20000 (00126)	MOV(10A,A2)\NOP	A2=J'
A2D 07M3C 000000F2 (00127)	NOP\MOV(10A,A2)	A2=K'
A2E 07M3E 425C425C (00128)	MOV(00),ADD(A2,A2)	2J'->INSTRS4+1,2J';2K'->INSTRS5+1,2K'
A2F 07M40 08FC0000 (00129)	MOV(10A,00)\NOP	INSTRS6 INTO INDS6
A30 07M42 000008FC (00130)	NOP\MOV(10A,00)	INSTRS7 INTO INDS7
A31 07M44 08F00000 (00131)	MOV(10A,NUL)\NOP	NUL:=TMP1(J#)
A32 07M46 000000F0 (00132)	NOP\MOV(10A,NUL)	NUL:=TMP1(K#)
A33 07M48 08F10000 (00133)	MOV(10A,A1)\NOP	A1=J'
A34 07M4A 000000F1 (00134)	NOP\MOV(10A,A1)	A1=K'
A35 07M4C 413C413C (00135)	MOV(00),ADD(A1,A1)	2J->INSTRS6+1,2J';2K->INSTRS7+1,2K'
A36 07M4E 08FC0000 (00136)	MOV(10A,00)\NOP	INSTRS4 INTO INDS4
A37 07M50 000008FC (00137)	NOP\MOV(10A,00)	INSTRS5 INTO INDS5
A38 07M52 08F00000 (00138)	MOV(10A,00)\NOP	DRDCT(I)=TMP1(J#)
A39 07M54 000000F0 (00139)	NOP\MOV(10A,00)	DRDCT(I+1)=TMP1(K#)
A3A 07M56 08F20000 (00140)	MOV(10A,A2)\NOP	A2=J'
A3B 07M58 000000F2 (00141)	NOP\MOV(10A,A2)	A2=K'
A3C 07M5A 425C425C (00142)	MOV(00),ADD(A2,A2)	2J'->INSTRS4+1,2J';2K'->INSTRS5+1,2K'
A3D 07M5C 08FC0000 (00143)	MOV(10A,00)\NOP	INSTRS6 INTO INDS6
A3E 07M5E 000008FC (00144)	NOP\MOV(10A,00)	INSTRS7 INTO INDS7
A3F 07M60 08F00000 (00145)	MOV(10A,00)\NOP	DRDCT(I+2)=TMP1(J#)
A40 07M62 000000F0 (00146)	NOP\MOV(10A,00)	DRDCT(I+3)=TMP1(K#)
A41 07M64 401D0033 (00147)	JMPC(SHUFFLS,F1)	FOR I=0,1,2...LTH-1
A42 07M66 20320000 (00148)	CFAR(RA)\NOP	API DONE!
A43 07M68 00000000 (00149)	NOP	
A44 07M6A 10000000 (00150)	JUMP(0)	
A45 07M6C 00000045 (00151)	QUANSSZ=RA-QUANSSA	
07M6E 00000045 (00152)	END QUANSSZ	
07M6F 00000045 (00153)	FJFCT	



A1F 07M02 3F541054 (00203) HSD	LOAD(HR1,DKCS0(2))	DFCS0(.) RA
A20 07M04 40300037 (00204)	SFT(M1)	STALL APS INPUT
A21 07M06 52000020 (00205) STALS2	NOP	RELEASED BY APU
A22 07M08 84C41044 (00206)	LOAD(HR0,INSTRS2(2),TF)	REWRITE INS2
A23 07M0A 46300037 (00207)	SFT(M1)	STALL APS INPUT
A24 07M0C 4M090016 (00208)	MOVK(HR0,MS)	RECALL TMP4(.) ADDRESS
A25 07M0E 4A9A0000 (00209) IMS2	ADD(HR1,MS,TF)	APU SPTS MSS,IO=2M
A26 07M0G 4E300037 (00210)	ADD(HR3,HS,TF)	IO=INIT(I+1)
A27 07M0I 4F300037 (00211)	SFT(M1)	STALL APS INPUT
A28 07M0K 50000020 (00212)	NOP	RELEASED BY APU
A29 07M0M 520000AM (00213)	JUMPC(LDUPS1,AF3)	FOR I=0,1,2...LTH-1
A2A 07M0N 54300037 (00214)	SFT(M1)	STALL APS INPUT
A2B 07M0P 56000020 (00215)	NOP	RELEASED BY APU
A2C 07M0Q 59303054 (00216)	LOAD(HR0,I3I,TF)	IORDR(.) RA,IO=IORDR=J
A2D 07M0R 5A500000 (00217) *	LOAD(HR1,MSS)	IORDR(.) SIZE-1
A2E 07M0S 5B0A0000 (00218)	ADD(HR0,MSS,TF)	IO=IORDR(I)=K
A2F 07M0T 5C002006 (00219)	LOAD(HR2,I2I)	TMP1(.) RA
A30 07M0U 60700000 (00220)	LOAD(HR3,MSS)	DUMMY UP FOR EXEC
A31 07M0V 62220000 (00221)	SUB(HR2,MSS)	TMP1(.) RA-2
A32 07M0W 63220000 (00222)	SUB(HR2,DIR)	TMP1(.) RA-10.
A33 07M0X 6619003C (00223)	ADDL(HR1,4)	I EXTRA LOOP IS NEEDED
A34 07M0Y 690A0001 (00224)	ADD(HR0,HS,TF)	IO=J'=IORDR(I+2)
A35 07M0Z 6A0A0001 (00225)	ADD(HR0,HS,TF)	IO=K'=IORDR(I+3)
A36 07M0A 6C431050 (00226)	ADD(HR0,HS,TF)	IO=K'=IORDR(I+3)
A37 07M0B 6E431052 (00227)	LOAD(HR3,INSTRS4(2),TF)	RFWRITE INDS4
A38 07M0C 70A00002 (00228)	LOAD(HR3,INSTRS5(2),TF)	RFWRITE INDS5
A39 07M0D 72A00002 (00229)	ADD(HR2,MS,TF)	IO=TMP1(I)
A3A 07M0E 750A0001 (00230)	ADD(HR0,HS,TF)	IO=TMP1(I+1)
A3B 07M0F 770A0001 (00231)	ADD(HR0,HS,TF)	IO=J'
A3C 07M0G 79F41054 (00232)	LOAD(HR3,INSTRS6(2),TF)	IO=K''
A3D 07M0H 7AF41056 (00233)	LOAD(HR3,INSTRS7(2),TF)	RFWRITE INDS6
A3E 07M0I 7CA00002 (00234)	ADD(HR2,MS,TF)	RFWRITE INDS7
A3F 07M0J 7FA00002 (00235)	ADD(HR2,MS,TF)	IO=TMP1(I+2)
A40 07M0K 80393484 (00236)	SUBL(HR1,4),JUMPP(INDSJ)	IO=TMP1(I+3)
A41 07M0L 82200031 (00237) *	CLEAR(RI)	FOR I=0,1,2...LTH+1
A42 07M0M 84000020 (00238)	NOP	INPUT DONE!
A43 07M0N 86300032 (00239) *	SFT(RA)	ENABLE APU
A44 07M0P 8830202A (00240)	LOAD(HR0,I2I)	TMP1(.) RA
A45 07M0Q 8A500000 (00241)	LOAD(HR1,MSS)	TMP1(.) SIZE-1
A46 07M0R 8C020000 (00242)	SUB(HR0,MSS)	TMP1(.) RA-2

A47 07902 46641030 (002417)	LOAD(HW2, INSTRS1+1(2),TF)	SFT MSS IN INSTRS1
A48 07903 90700030 (002418)	CLEAR(R0)	STALL APS OUTPUT
A49 07904 97000020 (002419)	NOOP	RELEASED BY APU
A4A 07904 94F3FFC0 (002420)	UINC(SJ)	RFWRITE INSI
A4B 0790A 96200030 (002421)	LOAD(HW2, APSSMFM(1),TF)	STALL APS OUTPUT
A4C 0790C 95000020 (002422)	NOOP	RELEASED BY APU
A4D 0790E 9064103F (002423)	LOAD(HW2, INSTRS2+1(2),TF)	SFT MSS=2M IN INSTRS2
A4E 07910 9C200030 (002424)	CLEAR(R0)	STALL APS OUTPUT
A4F 07912 94000020 (002425)	NOOP	RELEASED BY APU
A50 07914 A0F3FFC0 (002426)	LOAD(HW2, APSSMFM(1),TF)	RFWRITE INSI2
A51 07916 A2200030 (002427)	CLEAR(R0)	STALL APS OUTPUT
A52 07918 A3000020 (002428)	NOOP	RELEASED BY APU
A53 0791A A7641040 (002429)	LOAD(HW2, INSTRS1+1(2),TF)	SFT MSS IN INSTRS1
A54 0791C A6A00002 (002430)	ADD(HW0,MSS,TF)	00=TMP1(I)>DRDCT(I)
A55 0791E A3114A61 (002431)	SUBE(HW1,1),JUMPP(UMYCSJ)	FOR I=0,1...LTH-1
A56 07920 AC400024 (002432)	SET(AP3)	SIGNAL END TO APS INPUT
A57 07922 AF200030 (002433)	CLEAR(R0)	STALL APS OUTPUT
A58 07924 B3441051 (002434)	LOAD(HW0, INSTRS4+1(2),TF)	00=0!
A59 07926 B441055 (002435)	LOAD(HW0, INSTRS5+1(2),TF)	00=0!
A5A 07928 B541055 (002436)	LOAD(HW0, INSTRS6+1(2),TF)	00=0!
A5B 0792A B7431057 (002437)	LOAD(HW0, INSTRS7+1(2),TF)	00=0!
A5C 0792C B4300030 (002438)	LOAD(HW0, I0)	DUMMY OP FOR EXEC
A5D 0792E B4500000 (002439)	LOAD(HW1,MSS)	DRDCT(.) SIZE-1
A5E 07930 BC020000 (002440)	SUB(HW0,MSS)	DUMMY OP FOR EXEC
A5F 07932 B4700002 (002441)	LOAD(HW1,2)	4 INTERMEDIARY DUMMIES..
A60 07934 C011003C (002442)	ADDE(HW1,4)	6 1 EXTRA LOOP
A61 07936 C3641055 (002443)	LOAD(HW2, INSTRS6+1(2),TF)	SFT MSS IN INSTRS6
A62 07938 C5641057 (002444)	LOAD(HW2, INSTRS7+1(2),TF)	SFT MSS IN INSTRS7
A63 0793A C6F3FFC0 (002445)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS4
A64 0793C C9F3FFC0 (002446)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS5
A65 0793E CA16A01 (002447)	SUBE(HW1,1),JUMPP(DUMYS1)	1 ST 4 00'S ARE ?
A66 07940 C6660300 (002448)	LOAD(HW2, DRDCTS(1))	DRDCT(.) HA
A67 07942 C7AA0000 (002449)	ADD(HW2,MSS,TF)	00=DRDCT(J#)
A68 07944 D0660400 (002450)	LOAD(HW2, DRDCTS(1))	DRDCT(.) HA
A69 07946 D2AA0000 (002451)	ADD(HW2,MSS,TF)	00=DRDCT(K#)
A6A 07948 D5641051 (002452)	LOAD(HW2, INSTRS4+1(2),TF)	SFT MSS IN INSTRS4
A6B 0794A D7641053 (002453)	LOAD(HW2, INSTRS5+1(2),TF)	SFT MSS IN INSTRS5
A6C 0794C D9641055 (002454)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS6
A6D 0794E DB641057 (002455)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS7
A6E 07950 DC1174E1 (002456)	SUBE(HW1,1),JUMPP(DUMYS2)	1 ST 4 00'S ARE ?
A6F 07952 DE660400 (002457)	ADD(HW2, DRDCTS(1))	DRDCT(.) HA
A70 07954 E0AA0000 (002458)	LOAD(HW2, DRDCTS(1))	00=DRDCT(J#)
A71 07956 E2660400 (002459)	LOAD(HW2, DRDCTS(1))	DRDCT(.) HA

AD-A091 663

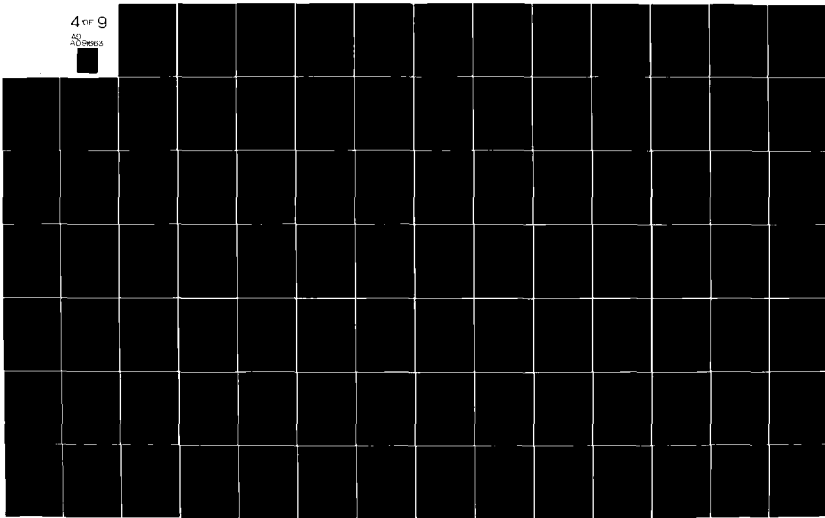
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME 50--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-76-C-0064

UNCLASSIFIED

NL

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AD  
Access







AFDYSGRG: 000FR (0000Y) (0003I)  
 APSSMEM: 1FFCO (00010) (00250) (00256) (00276) (00277) (00285) (00286)  
 RS0: 0001F (001FM) (00201)  
 RS1: 0001D (001M9) (00201)  
 RS2: 0001R (00190) (00190)  
 RS3: 00019 (00191) (00197)  
 RS4: 00017 (00192) (00195)  
 RS5: 00015 (00193)  
 CSPUSMOS: 021FC (00011) (00034)  
 DFCS0: 0105W (00048) (00203)  
 DFCS1: 0105C (00050) (00201)  
 DFCS2: 01060 (00052) (00199)  
 DFCS3: 01066 (00054) (00197)  
 DFCS4: 0107A (00056) (00195)  
 DFCS5: 0109R (00060) (00193)  
 DMYS1: 0006A (0027A) (002R1)  
 DMYS2: 00073 (002H7) (00292)  
 DMFSA: 00042 (00153)  
 DMFS1: 00041 (00240)  
 DMFSU: 00074 (00244)  
 DMFCTS: 00400 (00012) (00279) (00281) (00288) (00290)  
 G104S: 07R74 (00033) (00165) (00171) (00302)  
 G104SA: 0742C (0016H) (00297)  
 G104S1: 07460 (00163) (00301)  
 G104S0: 00043 (00172) (00243)  
 G104SZ: 000F6 (00167) (00302)  
 HS: 00001 (00014) (00179) (00184) (00210) (00226) (00227) (00232) (00233)  
 IATTS: 07A2H (00017) (00177)  
 IMS1: 0000F (00042) (00187)  
 IMS2: 00025 (00043) (00209)  
 IMS4: 00070 (00044) (00289)  
 INDS5: 00072 (00045) (00291)  
 INDSA: 00067 (00046) (00280)  
 INDS7: 00069 (00047) (00282)  
 INDS1: 00061 (00274) (00292)  
 INDSJ: 00034 (00226) (00238)  
 INSTRS1: 0104C (00042) (00182) (00247) (00259)  
 INSTRS2: 0104F (00043) (00206) (00253)  
 INSTRS4: 01050 (00044) (00228) (00265) (00283)  
 INSTRS5: 01052 (00045) (00229) (00266) (00284)  
 INSTRS6: 01054 (00046) (00234) (00267) (00274)  
 INSTRS7: 01056 (00047) (00235) (00268) (00275)  
 L00P9: 00004 (000H4) (00110)  
 L00P81: 0000A (00182) (00213)

MS:	00000 (00018)								
MS:	00000 (00019)	(00175)	(00187)	(00209)	(00219)	(00220)	(00222)	(00223)	(00245)
	(00246)	(00270)	(00271)	(00280)	(00282)	(00289)	(00291)		
MINCSJ:	0004A (00250)	(00261)							
MIANS:	077F2 (00032)	(0007H)							
MIAMSSA:	00000 (00076)	(00080)	(00156)						
MIAMSSZ:	00045 (00077)	(00156)	(00157)						
SAS38:	003CF (00021)								
SAS39:	00300 (00022)								
SCTRS:	00001 (00165)	(00173)							
SHUFLS:	00033 (00135)	(00151)							
SHUFLSV:	0001F (00112)								
STALSZ:	00021 (00194)	(00196)	(00198)	(00200)	(00202)	(00205)			
START:	077E0 (00023)	(00069)							
STARTS:	00001 (00081)								
SVTS:	00382 (00020)	(00021)	(00022)						
TMP4S:	02528 (00026)	(00178)							
MS:	00002 (00027)	(00183)	(00230)	(00231)	(00236)	(00237)	(00260)		
ZS:	00003 (00028)								

VAR, DC, INTERPOLATE, FIX  
ORIGINATED: 18-JAN-80  
UPDATED: 29-MAY-80

FCR 252... "MPVDNM(Y,U,V)"

DEFIN: GLOBAL SYMBOLS

OPADD EXP, (1 .U.S. 14) + (12 .U.S. 4) + \$1F  
OPADD ODP, (3 .U.S. 10) + (12 .U.S. 5) + \$1R  
OPADD WAFMF, (1 .U.S. 10) + (12 .U.S. 5) + \$1A  
OPADD FID, (3 .U.S. 10) + (12 .U.S. 5) + \$1C

0000008R (000004) APTSORG=\$RFH  
000021FC (000100) CPSUSMUS=\$21FC  
00000794 (000110) DMYS=\$0794

00000003 (000120) #M=3

00000001 (000130) #S=1

00000000 (000140) #S=0

00000000 (000150) #SS=0

00000016 (000160) #MUTS=0'3462'

000000F5 (000170) #SIZE=\$0'2461'-0'1'

00000037 (000180) #VTS=\$0347

00000039R (000190) #S311=\$VTS+240'11'

0000003CF (000200) #S31H=\$VTS+240'3R'

000000300 (000210) #S319=\$VTS+240'39'

00000044A (000220) #S100=\$VTS+240'100'

00000044C (000230) #S101=\$VTS+240'101'

000007980 (000240) #START=\$7980

00000002 (000250) #W=2

00000004 (000260) #W=4

00000000 (000270) #X2S=\$0'204R'

0000000FF (000280) #XSIZE=\$0'256'-0'1'

000000000 (000290) #YMS=\$0'204R'

000000009 (000300) #YSIZE=\$0'101'-0'1'

000000003 (000310) #ZS=3

(000320) \*

(000330) \*

EXPAND ARWAY FUNCTION DISPATCH TABLE

#1=\$AFDTSORC+342\*(252-12R)

ADDR VPINS(P7,1)

ADDR GI00S(P7,1)

ADDR CPSUSMUS(1,0)

OBJECT











```

A1D 07A4C 3A140034 (00142) SURL(HR1,4)
A1E 07A4E 3CA00004 (00143) #7 ADD(HR0,4,TF)
A1F 07A50 3FA90034 (00144) SURL(HR3,4,TF)
A20 07A52 40191427 (00145) SURL(HR1,2),JUMPP(#2)
A21 07A54 42020004 (00146) SUR(HR0,4)
A22 07A56 4434003C (00147) ADDL(HR3,4)
A23 07A58 46500002 (00148) LOAD(HR1,2)
A24 07A5A 488A0004 (00149) *JUMP FOR I=N2-4,N2-2,....,N2 OR 6 DUMMIES
A25 07A5C 4AB90034 (00191) #3 ADD(HR0,4,TF)
A26 07A5E 4C192481 (00192) SURL(HR1,4,TF)
A27 07A60 4E300037 (00193) SURL(HR1,1),JUMPP(#3)
A28 07A62 50640402 (00194) SET(W1)
A29 07A64 52500009 (00195) LOAD(HR2,YOMS+2(2))
A2A 07A66 548A0004 (00196) *JUMP FOR I=0,2,....,N2+0L0Z-2=N3-2
A2B 07A68 56A90034 (00197) #4 ADD(HR0,4,TF)
A2C 07A6A 58192482 (00198) SURL(HR3,4,TF)
A2D 07A6C 5A200031 (00200) * SURL(HR1,2),JUMPP(#4)
A2E 07A6E 5C000020 (00201) DNEST C1,FAR(R1)
A2F 07A70 5E300032 (00202) #1 NOP
A30 07A72 60500003 (00203) * SET(RA)
A31 07A74 63420794 (00204) G100SD LOAD(HR1,3)
A32 07A76 64113141 (00205) DMYSD LOAD(HW0,DMYS(1),TF)
A33 07A78 66440F15 (00206) SURL(HW1,1),JUMPP(DMYSD)
A34 07A7A 68500000 (00207) LOAD(RW0,NOUITS-1(2))
A35 07A7C 6A700009 (00208) * LOAD(RW1,MSS)
A36 07A7E 6D0A0001 (00209) * INTERPOLATED OUTPUTS
A37 07A80 6F313641 (00210) #1 ADD(HW0,HS,TF)
A38 07A82 71720794 (00211) SURL(HW3,1),JUMPP(#1)
A39 07A84 73720794 (00212) * LOAD(HW1,DMYS(1),TF)
A3A 07A86 75720794 (00213) * LOAD(HW3,DMYS(1),TF)
A3B 07A88 77720794 (00214) * LOAD(HW3,DMYS(1),TF)
A3C 07ARA 7850005H (00215) * NON-INTERPOLATED OUTPUTS
A3D 07ARC 7A0A0001 (00216) #2 LOAD(HW1,NSIZES-YIZES-1)
A3E 07ARE 7C113041 (00217) * ADD(HW0,HS,TF)
A3F 07A90 7E4407F4 (00218) SURL(HW1,1),JUMPP(#2)
A40 07A92 80500004 (00219) * NORMALIZED MEMORY
A41 07A94 828A0007 (00220) #3 LOAD(HW0,YOMS-2(2))
A42 07A96 848A0007 (00221) * LOAD(HW1,YIZES)
A43 07A98 868A0007 (00222) * ADD(HW0,WS,TF)
A44 07AA0 888A0007 (00223) *
A45 07AA2 8A8A0007 (00224) *
A46 07AA4 8C8A0007 (00225) *
A47 07AA6 8E8A0007 (00226) *
A48 07AA8 908A0007 (00227) *
A49 07AAA 928A0007 (00228) *
A50 07AAC 948A0007 (00229) *
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A53 07AB2 9A8A0007 (00232) *
A54 07AB4 9C8A0007 (00233) *
A55 07AB6 9E8A0007 (00234) *
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A266 07AAC F48A0007 (00445) *
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A280 07AA6 F08A0007 (00459) *
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A283 07AAB F68A0007 (00462) *
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A288 07AB4 F08A0007 (00467) *
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A290 07AB8 F48A0007 (00469) *
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A294 07AB8 FC8A0007 (00473) *
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A328 07ABC F08A0007 (00507) *
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A460 07AA6 F88A0007 (00639) *
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A466 07AB0 F48A0007 (00645) *
A467 07AB2 F68A0007 (00646) *
A468 07AB4 F88A0007 (00647) *
A469 07AB6 FA8A0007 (00648) *
A470 07AB8 FC8A0007 (00649) *
A471 07ABA FE8A0007 (00650) *
A472 07ABC F08A0
```

FOR I=0,1...YSIZE-1  
OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN  
CHDR

```

A42 07A96 R41141N1 (00226) * SUHL(RW1,1),JUMPP(B3)
      (00227) *
A43 07A98 R6200030 (00228) DMFSO CLFAR(RD)
A44 07A9A RR000020 (00229) MDP
A45 07A9C R8400000 (00230) LDADR(RW0,10)
A46 07A9E RC500000 (00231) LDADR(RW1,MSS)
A47 07AAD R4020000 (00232) * SUH(RW0,MSS)
      (00233) *
      00007A9C (00234) * G100SA= #C
      (00235) *
      07AA7 (00236) * FND #A-1
      07AA2 00000000 (00237) ? STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
      00000097 (00238) G100ST DATA 1F'0,0'
      07AA4 (00239) * G100SZ=#I-G100S
      (00240) * FND

```

AFTS(UMG):	00MFM (00004)	(00034)
CSPUSMUS:	021FC (00010)	(00037)
DMYS:	00794 (00011)	(00206)
DMYS0:	00031 (00206)	(00214)
DMYS0:	00036 (00131)	(00215)
DMYS1:	00020 (00201)	(00216)
DMYS0:	00043 (00224)	(00217)
FS0:	07480 (00044)	
FS1:	07482 (00045)	(00153)
FS2:	07484 (00046)	
FS3:	07984 (00047)	
FS4:	07984 (00048)	
FS5:	07484 (00049)	
FS6:	07984 (00050)	
FS7:	07484 (00051)	
FS8:	07480 (00052)	
FS9:	07492 (00053)	
FSA:	07494 (00054)	
G100S:	07A17 (00036)	(00144)
G100SA:	07A9C (00147)	(00230)
G100S1:	07AA2 (00132)	(00231)
G100S0:	0002F (00151)	(00232)
G100S2:	00097 (00146)	(00233)
HS:	00001 (00013)	(00212)
MS:	00000 (00014)	(00220)
MSS:	00000 (00015)	(00209)
MOUTS:	00F14 (00016)	(00209)
MS1ZFS:	000F5 (00017)	(00219)
SAS100:	0044A (00022)	(00155)
SAS101:	0044C (00023)	(00154)
SAS11:	00394 (00019)	(00157)
SAS3H:	0030C (00020)	(00156)
SAS39:	00300 (00021)	
SC1PS:	00001 (00144)	(00152)
STAPT:	07480 (00024)	(00042)
SVTS:	00342 (00018)	(00019)
VD1WS:	07994 (00035)	(00062)
VD1WSSA:	00000 (00060)	(00064)
VD1WSSZ:	00039 (00061)	(00135)
WS:	00002 (00025)	(00136)
WWS:	00004 (00026)	
X2S:	00000 (00027)	(00158)
XSI2FS:	000FF (00028)	(00159)
Y0MS:	00000 (00029)	(00160)
		(00194)
		(00223)

PAGE 4: PCH 257... "MPVDM(Y,U,V)" VAR.,DC,INTERPOLATE,FLX

VSIZES: 00009 (00010) (00161) (00195) (00210) (00219) (00224)  
ZS: 00003 (00011)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) E= 0

PAGE 1: RL=STAD

07A1D 7986 00007A1D (00001)  
07A1D 7986 (00002)  
07A1F 7986 00007A1F (00003)  
07A1F 7986 (00004)  
07986 2000 00007986 (00005)  
07986 2000 (00006)  
07987 003D  
07988 1000 (00007)  
07989 003D  
0799A (00008)

RL=STAD  
DATA S7986  
RL=STAD  
DATA S7986  
DATA S2000,S3D  
DATA S1000,S3D  
END

CHANGE FROM ISA TO LOCAL

2\*\*-14

2\*\*-15

PAGE 2: 0L=STAD

LENS WITH PRIMS: 0 (MAP VERSION M00101.10) K= 0

BASIS SPECTRUM CALCULATION  
ORIGINATED:31-OCT-79  
UPDATED:21-MAR-80

FCH 253... "MORASPV,U,V,W"

```

(00001) *
(00002) *
(00003) *
(00004) *
(00005) *
(00006) *
(00007) *
(00008) *
(00009) *
(00010) *
(00011) *
(00012) *
(00013) *
(00014) *
(00015) *
(00016) *
(00017) *
(00018) *
(00019) *
(00020) *
(00021) *
(00022) *
(00023) *
(00024) *
(00025) *
(00026) *
(00027) *
(00028) *
(00029) *
(00030) *
(00031) *
(00032) *
(00033) *
(00034) *
(00035) *
(00036) *

```

```

* DEFINE GLOBAL SYMBOLS
OPADD EXP, (1 .LS. 14) + (12 .LS. 4) + SIF
OPADD DDP, (3 .LS. 10) + (12 .LS. 5) + SIF
AFUTSORG=SHFH
CSPUSMOS=$21FC
PM=3
HS=1
MS=0
SVTS=$04H2
SAS00=SVTS+2*0'00'
SAS02=SVTS+2*0'02'
SAS06=SVTS+2*0'06'
SAS12=SVTS+2*0'12'
SAS14=SVTS+2*0'14'
SAS16=SVTS+2*0'16'
SAS17=SVTS+2*0'17'
SAS18=SVTS+2*0'18'
SAS19=SVTS+2*0'19'
SAS20=SVTS+2*0'20'
SAS21=SVTS+2*0'21'
SAS22=SVTS+2*0'22'
SAS23=SVTS+2*0'23'
SAS24=SVTS+2*0'24'
SAS25=SVTS+2*0'25'
SAS26=SVTS+2*0'26'
SAS27=SVTS+2*0'27'
SAS28=SVTS+2*0'28'
SAS29=SVTS+2*0'29'
SAS30=SVTS+2*0'30'
SAS31=SVTS+2*0'31'
SAS32=SVTS+2*0'32'
SAS33=SVTS+2*0'33'
SAS34=SVTS+2*0'34'
SAS35=SVTS+2*0'35'
SAS36=SVTS+2*0'36'
SAS37=SVTS+2*0'37'
SAS38=SVTS+2*0'38'
SAS39=SVTS+2*0'39'
SAS40=SVTS+2*0'40'
SAS41=SVTS+2*0'41'
SAS42=SVTS+2*0'42'
SAS43=SVTS+2*0'43'
SAS44=SVTS+2*0'44'
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SAS46=SVTS+2*0'46'
SAS47=SVTS+2*0'47'
SAS48=SVTS+2*0'48'
SAS49=SVTS+2*0'49'
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SAS66=SVTS+2*0'66'
SAS67=SVTS+2*0'67'
SAS68=SVTS+2*0'68'
SAS69=SVTS+2*0'69'
SAS70=SVTS+2*0'70'
SAS71=SVTS+2*0'71'
SAS72=SVTS+2*0'72'
SAS73=SVTS+2*0'73'
SAS74=SVTS+2*0'74'
SAS75=SVTS+2*0'75'
SAS76=SVTS+2*0'76'
SAS77=SVTS+2*0'77'
SAS78=SVTS+2*0'78'
SAS79=SVTS+2*0'79'
SAS80=SVTS+2*0'80'
SAS81=SVTS+2*0'81'
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SAS92=SVTS+2*0'92'
SAS93=SVTS+2*0'93'
SAS94=SVTS+2*0'94'
SAS95=SVTS+2*0'95'
SAS96=SVTS+2*0'96'
SAS97=SVTS+2*0'97'
SAS98=SVTS+2*0'98'
SAS99=SVTS+2*0'99'
SAS100=SVTS+2*0'00'

```

```

MS=$794
MS=3
HS=1
MS=0
SVTS=$04H2
SAS00=SVTS+2*0'00'
SAS02=SVTS+2*0'02'
SAS06=SVTS+2*0'06'
SAS12=SVTS+2*0'12'
SAS14=SVTS+2*0'14'
SAS16=SVTS+2*0'16'
SAS17=SVTS+2*0'17'
SAS18=SVTS+2*0'18'
SAS19=SVTS+2*0'19'
SAS20=SVTS+2*0'20'
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SAS26=SVTS+2*0'26'
SAS27=SVTS+2*0'27'
SAS28=SVTS+2*0'28'
SAS29=SVTS+2*0'29'
SAS30=SVTS+2*0'30'
SAS31=SVTS+2*0'31'
SAS32=SVTS+2*0'32'
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SAS64=SVTS+2*0'64'
SAS65=SVTS+2*0'65'
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SAS67=SVTS+2*0'67'
SAS68=SVTS+2*0'68'
SAS69=SVTS+2*0'69'
SAS70=SVTS+2*0'70'
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SAS89=SVTS+2*0'89'
SAS90=SVTS+2*0'90'
SAS91=SVTS+2*0'91'
SAS92=SVTS+2*0'92'
SAS93=SVTS+2*0'93'
SAS94=SVTS+2*0'94'
SAS95=SVTS+2*0'95'
SAS96=SVTS+2*0'96'
SAS97=SVTS+2*0'97'
SAS98=SVTS+2*0'98'
SAS99=SVTS+2*0'99'
SAS100=SVTS+2*0'00'

```

EXPAND ARRAY FUNCTION DISPATCH TABLE

```

*I=AFDTSIRC+1*2*(253-128)
ADDR HASS(P7,1)
ADDR G204S(R7,1)
ADDR CSPUSMOS(,1,0)
F.FECT

```

PAGE 2: FOR 253... "MPHASP(V,U,V,W)" BASIS SPECTRUM CALCULATION

```

(00037) *
(00038) *
(00039) *
00007000 (00040)
00007000 (00041)
07H90 20000030 (00042) * SPECIAL CONSTANTS FOR EXPONENTIALIZATION
07H92 40000044 (00043) TARIFFS DATA 2.3801459E-07
07H94 08200030 (00044) DATA 3.2768000E+04
07H96 3086415C (00045) DATA 1.5497208E-05
07H98 32690007 (00046) DATA 0.749572748E+34
07H9A 725511D1 (00047) DATA 0.712208897E+27
07H9C 1F8D5ACC (00048) DATA 0.263637333E+21
07H9E 88000038 (00049) DATA 0.675972338E+14
07RA0 5893046 (00050) TARIFFS2 DATA -5.9604644E-08
07RA2 7EFFFF40 (00051) DATA 0.116291788E+08
(00052)
(00053) EJECT
7**(-22)
2**15
4*65*16**(-6)
C5
C4
C3
C2
-1A**(-6)
C1
C0

```









```

A5H 07C5C 08F708F7 (00186)
A5C 07C5F 08F608F6 (00187)
A5D 07C60 08F508F5 (00188)
A5E 07C62 08AC08AC (00189)
A5F 07C64 08F00000 (00190) 81
A60 07C66 000008F4 (00191)
A61 07C68 851C851C (00192)
A62 07C6A 3A6H3A6H (00193)
A63 07C6C 1F731F73 (00194)
A64 07C6E 85D185D1 (00195)
A65 07C70 3A2H3A2H (00196)
A66 07C72 32523252 (00197)
A67 07C74 08H308H3 (00198)
A68 07C76 46704670 (00199)
A69 07C78 027H027H (00200)
A6A 07C7A 85C085C0 (00201) 7
A6B 07C7C 08F208F2 (00202)
A6C 07C7E 5A005A00 (00204)
A6D 07C80 08H308H3 (00205)
A6E 07C82 45704570 (00206)
A6F 07C84 440H440H (00207)
A70 07C86 85C085C0 (00208)
A71 07C88 08F208F2 (00209)
A72 07CHA 08H08H08 (00210)
A73 07CBC 853H8533 (00211)
A74 07CFE 434D434D (00212)
A75 07C90 8A2H8A2H (00213)
A76 07C92 85D385D3 (00214)
A77 07C94 08F208F2 (00215)
A78 07C96 08H08H08 (00216)
A79 07C98 08H108H1 (00217)
A7A 07C9A 08H08H08 (00218)
A7B 07C9C 84C084C0 (00219)
A7C 07C9E 47204720 (00220)
A7D 07CA0 08H08H08 (00221)
A7E 07CA2 84718471 (00222)
A7F 07CA4 47204720 (00223)
A80 07CA6 901C005F (00224)
A81 07CAM 20320000 (00225) 6
A82 07CAA 00000000 (00227)
A83 07CAC 10000000 (00228)
00000084 (00229)

M0V(T0A,A7)
M0V(T0A,A6)
M0V(T0A,A5)
M0V(P,M4)
M0V(T0A,M2)\M0P
M0P\M0V(T0A,M2)
M0V(M0),M0L(M2,M4)
M0V(M3),ALIG(A3)
M0V(A1),EXP(A3)
M0V(A1),M0L(M3,M6)
M0V(M0),ALIG(A1)
M0V(A2),N0M(A2)
M0V(P,A3)
M0V(A0),ADD(A3,A6)
M0V(M3),R(A1)
M0L(M3,M6)
M0V(T0A,A2)
ADD(T(A0,A2)
M0V(P,A3)
M0V(A0),ADD(A3,A5)
M0V(M3),ADD(A0,A4)
M0L(M3,M6)
M0V(T0A,A2)
M0V(R,M2)
M0V(A3),M0L(M2,M5)
ADD(A2,A3)
M0V(M3),SUR(A1,A0)
M0V(A3),M0L(M3,M6)
M0V(T0A,A2)
M0V(R,N0L(A1)
M0V(P,A1)
M0V(R,M6)
M0L(M1,M6)
ADD(A1,A2)
M0V(R,M7)
M0V(A1),M0L(M0,M7)
ADD(A1,A7)
JUMP(C(A1,F0))
CT,FAR(HA)\N0P
N0P
JUMP(W)
BAS52=BA-HASSA

```

```

A7=C4
A6=C3
A5=C2
M4=SA*28*(-22)
M2=X(0,F)
M0=X(19,K-2),X(1,K)
A3=X(9,K-1),X(7,K-1)
A3=X(7,K-1),X(10,K-1)
A1=X(1,K),X(11,K-1)
M0=X(10,K-1),X(2,K)
A2=X(2,K),X(3A,K)
A3=X(11,K-1)
A0=X(3A,K),X(17,K-1)
M3=X(12,K-1),SET T RIT H
Y X(3A,K)
X(13,K-1)
A2=-16*( -6)
X(3H,K)
A3=X(13,K-1)
A0=X(3H,K),X(14,K-1)
M3=X(A4,K-1),X(4,K)
X(15,K-1)
A2=C1
M2=X(4,K)
A3=X(15,K-1),X(5,K)
X(16,K-1)
M3=X(16,K-1),X(6,K)
A3=X(5,K),X(17,K-1)
A2=C0
A1=X(17,K-1)
M6=X(6,K)
X(R,K)
X(18,K-1)
M7=X(18,K-1)
A1=X(R,K),X(19,K-1)
X(9,K)
FOR I=0,1,2...LTH-1
APU NONE:

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PAGE 7: PCB 253... "MPRASP(Y,U,V,W)" BASIS SPECTRUM CALCULATION

07CAF

(00230)  
(00231)

FWD MASS52  
FJECT

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(00271) #2
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(00275)

07CAF 0000709A
07CR0 000070A6
07CW2 0000
07CW3 00F2
07CW4 00007096

07CR6 00204040
07CRH 02300030
07CRA 04C20414
07CRC 06C203FA
07CRD 04001000
07CCE 0A600000
07CC2 0C700000
07CC4 0F390011
07CCR 12340020
07CCA 14190017
07CCC 169A0002
07CCF 181A0002
07CD0 1AF20382
07CD2 1CF2039A
07CD4 1FF20382
07CD6 200A0002
07CD8 22290032

07CD9 249A0002
07CDC 269A0002
07CDF 289A0002
07CF0 2AA00002
07CF2 2C901242
07CF4 2F60000A
07CF6 30C20794
07CF8 32800011
07CFA 34291981
07CFC 36400011
07CFE 38400037
07CF0 3A000020

FVFN
ADDR G204SI
ADDR G204S
DATA 0
DATA G204SZ
ADDR G204SA
FVFN

BEGIN APS(G204)
JISN(G204SI,P2)
SFT(RO)
LOAD(HR0,SAS73(1),TF)
LOAD(HR0,SAS60(1),TF)
LOAD(HR0,I31)
LOAD(HR2,MSS)
LOAD(HR3,MSS)
MOVH(HR3,HR0)
ADDR(HR3,HR2)
MOVH(HR1,HR4)
ADD(HR1,2,TF)
ADD(HR1,2)
LOAD(HR3,SAS00(1),TF)
LOAD(HR3,SAS12(1),TF)
LOAD(HR3,SAS00(1),TF)
ADD(HR0,2)
SURJ(HR2,2)
FOR N = 1,2,3,...,V7/2-1
  ADD(HR1,2,TF)
  ADD(HR1,2,TF)
  ADD(HR0,2,TF)
  ADD(HR0,2,TF)
  SURJ(HR2,2),JUMPP(#1)
  LOAD(HR2,10)
  MOVH(HR0,HR0,TF)
  SURJ(HR2,1),JUMPP(#2)
  MOVH(HR0,HR0,TF)
  SFT(WI)
  NOP(0)

POINTER TO CONSTRUCTED I
INSTRUCTION BLOCK
POINTER TO SCALAR BLOCK
NUMBER OF SCALARS
MODULE SIZE
POINTER TO CHAIN ANCHOR
END ON WORD BOUNDARY

SET OUTPUT PC(P2)
ENABLE OUTPUT ADDRESSING
ENG
ENG
WRO = WRASE
VZ-1
DUMMY, SPACING MUST BE 4
NEED BASE FOR V START

VR(0)-2
VR(0)-2
VR(0)
SKIP VI(0)
VI0 = 0
WRO = 2*12
WI0 = 0
POINT TO WI0
VZ-3

VR(N)
VI(N)
WR(N)
WI(N)

NEED 13 DUMMIES TOTAL

ONE MORE MAKES 9

```

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(00276) *
(00277) *CHANGE FORMAT OF LOGS
A1F 07CF2 4C7C0386 (00278)
A1F 07CF4 34401036 (00279)
A20 07CF6 40500000 (00280)
A21 07CF8 42600000 (00281)
A22 07CFA 44641006 (00282)
A23 07CFB 46300000 (00283)
A24 07CFC 48100000 (00284)
A25 07CFD 49700000 (00285)
A26 07D00 4A040032 (00286)
A27 07D02 4C240031 (00287)
A27 07D04 4F140031 (00288)
A28 07D06 510A0002 (00289) * LOOP FOR N = 0,2,....,11Z-2
A29 07D08 530A0002 (00290) #3
A2A 07D0A 55AA0002 (00291) #3
A2A 07D0C 57AA0002 (00292) #3
A2C 07D0E 5A1428R2 (00293)
A2C 07D10 5A500004 (00294)
A2D 07D12 50040011 (00295) ?
A2F 07D14 5F040011 (00296) ?
A2F 07D16 60142FH3 (00297) #4
A31 07D18 63040011 (00298)
A32 07D1A 65040011 (00299)
A33 07D1C 66300037 (00300)
A34 07D1E 68000020 (00301)
A35 07D20 6AC20420 (00302)
A36 07D22 6CC203EF (00303)
A37 07D24 6E02038F (00304) *
A38 07D26 70027840 (00305) EXPST
A39 07D28 72600005 (00306) #1
A3A 07D2A 749A0002 (00307) #1
A3B 07D2C 76293AM1 (00308)
A3C 07D2E 78402034 (00309)
A3D 07D30 7A700000 (00310)
A3E 07D32 7C020000 (00311) #2
A3F 07D34 7E4A0002 (00312)
A40 07D36 808A0002 (00313)
A41 07D38 829A0002 (00314)
A42 07D3A 849A0002 (00315)
A43 07D3C 869A0002 (00316)

1/2 * LOG2(16) = 2
PWEIT Z=1
DUMMY
NEED TO ADDRESS IT AS SH
CRT, T00
DUMMY
DUMMY
UHASE -2
U=1
UZ-2
M(N)
M(N+1)
K(N)
F(N+1)
SIX PAIRS OF DUMMIES (10
+2)
"FM" AND "F"

WAIT FOR APU
ATLTH
1/1,TH
I0=0.25
I0=2**(-22)
I0=2**15,4*65*16**(-6)
C5,C4,C3,C2
6 CONSTANTS TOTAL:
TMP2(,) RA
TMP2(,) SIZE-1
TMP2(,) RA-2
I0=TMP2(1)
I0=TMP2(1+1)
I0=-16**(-6)
I0=C1
I0=C0
    
```

```

A44 0703F HRI90036 (00320)      SURL(RR1,6)
A45 07040 HA1933H2 (00321)      SURL(RR1,7),JUMPP(R2)
A46 07042 MCC20794 (00322)      LOAD(RR0,DMYS(1),TF)
A47 07044 RFC20794 (00323)      LOAD(RR0,DMYS(1),TF)
A48 07046 909A0002 (00324)      ADD(RR1,WS,TF)
A49 07048 929A0002 (00325)      ADD(RR1,WS,TF)
A4A 0704A 949A0002 (00326)      ADD(RR1,WS,TF)
A4B 0704C 96700031 (00328)      DNEFS1
A4C 0704F 98000020 (00329)      NOP
A4D 07050 9A100032 (00331)      G204SD
A4E 07052 9CF20414 (00332)      LOAD(RW3,SAS73(1),TF)
A4F 07054 9FC20794 (00333)      LOAD(RW0,DMYS(1),TF)
A50 07056 A0A10010 (00334)      MOVH(RW0,RW0,TF)
A51 07058 A2A10010 (00335)      MOVH(RW0,RW0,TF)
A52 0705A A4C0107C (00336)      LOAD(RW1,TF)
A53 0705C A6500000 (00337)      LOAD(RW1,MSS)
A54 0705F A8600000 (00338)      LOAD(RW2,MSS)
A55 07060 A8A00002 (00339)      ADD(RW0,2,TF)
A56 07062 AC110032 (00340)      SURL(RW1,2)
A57 07064 AF8A0002 (00342)      * LOOP FOR N = 2,4,...,UZ-2
A58 07066 A0A00002 (00343)      ADD(RW0,2,TF)
A59 07068 A21157A2 (00344)      SURL(RW1,2),JUMPP(R5)
A5A 0706A H4C20794 (00346)      * OUTPUT FLT PT LOGS
A5B 0706C H6A10010 (00347)      LOAD(RW0,DMYS(1),TF)
A5C 0706E H8C02014 (00349)      MOVH(RW0,RW0,TF)
A5D 07070 RA500000 (00350)      LOAD(RW1,MSS)
A5E 07072 HC600000 (00351)      LOAD(RW2,MSS)
A5F 07074 HF8A0002 (00352)      ADD(RW0,2,TF)
A60 07076 C0110032 (00353)      SURL(RW1,2)
A61 07078 C2A00002 (00355)      * LOOP FOR N=2,3,...,YZ-2
A62 0707A C4A00002 (00356)      ADD(RW0,2,TF)
A63 0707C C61161A2 (00357)      ADD(RW0,2,TF)
A64 0707E C8C20472 (00358)      SURL(RW1,2),JUMPP(R6)
A65 07080 CAC20794 (00360)      EXPND
A66 07082 CC20794 (00361)      LOAD(RW0,DMYS(1),T,1)
A67 07084 CE20794 (00362)      LOAD(RW0,DMYS(1),TF)
A68 07086 D0C0001A (00363)      LOAD(RW0,101,TF)

```

```

-16**(-6) RA
FOR I=0,1,2,...,LTH-1
I0=?
I0=?
I0=-16**(-6)
I0=C1
I0=C0
INPUT DONE!

ENRARE APU
EGR = 1/EGR
NEED 3 DUMMIES OUT

U(0)
UZ-1
DUMMY, SPACING MUST BE 2
U(1)

U(N)
U(N+1)

2 DUMMIES
TMP2(0)
YZ-1
DUMMY
Y(1)

Y(N)
Y(N+1)

NORMALIZED SUM OF LOGS

O0=?
O0=?
O0=?
DCTM1(, ) RA,00=DCTM1(0)

```



```

A6Q 0708H 12700000 (00364) LOAD(HW1,MSS)
A6A 0708A 14027000 (00365) SUB(HW0,MSS)
A6M 0708C 165A0002 (00366) *1 ADD(HW0,MS,TF)
A6C 0708E 16BA0002 (00367) ADD(HW0,MS,TF)
A6D 07090 16AA0002 (00368) ADD(HW0,MS,TF)
A6E 07092 16CA0002 (00369) ADD(HW0,MS,TF)
A6F 07094 163A0004 (00370) SUB(HW1,4),JUMPY(B1)
      (00371) *
A70 07096 16200030 (00372) DMPSU CLEAR(PU)
A71 07098 12000020 (00373) NOP
      (00374) *
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      (00999) *
  
```

```

DCTM1(.) SIZE=1
DCTM1(.) RA=2
DQ=DCTM1(I)
DQ=DCTM1(I+1)
DQ=DCTM1(I+2)
DQ=DCTM1(I+3)
FOR I=1,2,...,LTH=1
  OUTPUT DONE!
  ASSIGN VALUE TO CHAIN AN
  CHOR
  
```

```

ASSIGN VALUE TO CHAIN AN
CHOR
  
```

0709A STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS

```

*** 00000007 (00380) G704SZ=81-G704S
070AH 00000007 (00381) END
  
```

AFUTSORG:	00WFR (00007)	(00032)
MASS:	07RA6 (00033)	(00060)
PASSA:	00000 (00058)	(00075)
PASSZ:	00084 (00059)	(00229)
CSPUSNOS:	021FC (00000)	(00035)
DMYS:	00794 (00009)	(00270)
DNFSA:	00041 (00226)	(00372)
DNFS1:	00048 (00328)	(00360)
DNFS0:	00070 (00372)	(00361)
FXPS:	00055 (00180)	(00362)
FXPS1:	00037 (00307)	(00363)
FXPS0:	00065 (00360)	(00364)
G204S:	07C86 (00033)	(00238)
G204SA:	07086 (00241)	(00375)
G204S1:	0709A (00236)	(00370)
G204S0:	00040 (00245)	(00331)
G204SZ:	00042 (00240)	(00380)
HS:	00001 (00011)	(00165)
IP:	00034 (00150)	(00250)
MSS:	00000 (00012)	(00251)
		(00284)
		(00285)
		(00313)
		(00314)
		(00337)
		(00351)
		(00364)
		(00365)
MLP:	00019 (00102)	(00119)
SAS00:	00342 (00014)	(00258)
SAS01:	00386 (00015)	(00274)
SAS02:	0038E (00016)	(00307)
SAS12:	0039A (00017)	(00250)
SAS54:	003FF (00018)	(00305)
SAS60:	003FA (00019)	(00248)
SAS73:	00414 (00020)	(00247)
SAS76:	0041A (00021)	(00332)
SAS77:	0041C (00022)	(00304)
SAS79:	00420 (00023)	(00354)
SASR0:	00422 (00024)	(00040)
START:	07R00 (00025)	(00040)
SVTS:	003R2 (00013)	(00014)
		(00015)
		(00016)
		(00017)
		(00018)
		(00019)
		(00020)
		(00021)
		(00022)
TARIFS:	07H90 (00043)	(00304)
TARIFS2:	07R9F (00050)	(00310)
WS:	00002 (00026)	(00315)
		(00316)
		(00317)
		(00318)
		(00324)
		(00325)
		(00326)
WWS:	00004 (00027)	(00367)
ZS:	00003 (00024)	(00366)
ZZHS:	00007 (00029)	(00369)

SCAN DCT BASIS SPECTRUM  
ORIGINATED:02-NOV-79  
UPDATED:25-MAY-80

FCR 254... "MPCAN(Y,U,V)"

```

(00001) * FCR 254... "MPCAN(Y,U,V)"
(00002) *
(00003) *
(00004) *
(00005) *
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```

* DEFINE GLOBAL SYMBOLS
* OPADD EXP, (3 .US. 14) + (12 .US. 8) + $1F
* OPADD DDF, (3 .US. 10) + (12 .US. 5) + $1H
* APSSMFM=$1F$C0
* CSPUSNOS=$21FC
* M=3
* HS=1
* MSS=0
* SVTS=$03R2
* SAS17=$SVTS+2*D'12'
* SAS13=$SVTS+2*D'13'
* SAS79=$SVTS+2*D'79'
* SASR0=$SVTS+2*D'R0'
* SASR2=$SVTS+2*D'R2'
* SASR3=$SVTS+2*D'R3'
* SASR4=$SVTS+2*D'R4'
* START=$7F10
* STZFS=D'256'-D'1'
* TMP15=D'204H'
* TMP25=D'2560'
* WS=2
* WMS=4
* ZS=3
* ZZHS=7

```

EXPAND ARRAY FUNCTION DISPATCH TABLE

```

* #1=AFDTSORPC+3*2*(254-12R)
* ADDR SCAS(R7,1)
* ADDR G205S(R7,1)
* ADDR CSPUSNOS(,1,0)
* PJFCT

```

```

CORDC 0017F17
CORDF 0017F24
CORDEB 001721FC

```





```

A21 07F54 02A00000 (00087) ?
A22 07F54 02A00000 (00088) VALDSN R(A5)\NOP
A23 07F54 02A00000 (00089) MOV(R,00)\NOP
A24 07F54 02A00000 (00090) P(A3)\NOP
A25 07F54 02A00000 (00091) MOV(R,00)\NOP
A26 07F54 20372037 (00092) CLEAR(WI)
A27 07F54 02A00000 (00093) *
A28 07F54 02A00000 (00094) * ITOT'=1.0/ITOT
A29 07F54 02A00000 (00095) SINVS MOV(R,M6)\NOP
A30 07F54 02A00000 (00096) MOV(R,A0)\NOP
A31 07F54 02A00000 (00097) K(2)
A32 07F54 02A00000 (00098) MOV(R,A6)\NOP
A33 07F54 02A00000 (00099) P(P(A0))\NOP
A34 07F54 02A00000 (00100) MOV(H,M0)\NOP
A35 07F54 02A00000 (00101) MUL(M0,M6)\NOP
A36 07F54 02A00000 (00102) MOV(P,A1)\NOP
A37 07F54 02A00000 (00103) SUR(A6,A1)\NOP
A38 07F54 02A00000 (00104) MOV(R,M4)\NOP
A39 07F54 02A00000 (00105) MUL(M0,M4)\NOP
A40 07F54 02A00000 (00106) MOV(P,M0)\NOP
A41 07F54 02A00000 (00107) MUL(M0,M6)\NOP
A42 07F54 02A00000 (00108) MOV(P,A1)\NOP
A43 07F54 02A00000 (00109) SUR(A6,A1)\NOP
A44 07F54 02A00000 (00110) MOV(H,M4)\NOP
A45 07F54 02A00000 (00111) MUL(M0,M4)\NOP
A46 07F54 02A00000 (00112) MOV(P,M7)\NOP
A47 07F54 02A00000 (00113) *
A48 07F54 02A00000 (00114) * CTEMP=(RTLTH-SLOG)/ITOT
A49 07F54 02A00000 (00115) SSUMS MOV(10A,A6)\NOP
A50 07F54 02A00000 (00116) MOV(ZFR0,A0)
A51 07F54 02A00000 (00117) P(A0)
A52 07F54 02A00000 (00118) #1 MOV(10A,A0)\MOV(A7),ADD(A0,A7)
A53 07F54 02A00000 (00119) ?
A54 07F54 02A00000 (00120) NOP
A55 07F54 02A00000 (00121) JUMPS(SS2,FW1)
A56 07F54 02A00000 (00122) MOV(A7),ADD(A0,A7)\MOV(10A,A1)
A57 07F54 02A00000 (00123) NOP
A58 07F54 02A00000 (00124) JUMPS(SS3,FW1)
A59 07F54 02A00000 (00125) MOV(10A,A1)\MOV(A7),ADD(A1,A7)
A60 07F54 02A00000 (00126) ?
A61 07F54 02A00000 (00127) NOP
A62 07F54 02A00000 (00128) JUMPS(SS4,FW1)
A63 07F54 02A00000 (00129) MOV(A7),ADD(A1,A7)\MOV(10A,A0)
A64 07F54 02A00000 (00130) ?
S
A5=UF(?) *CTEMP
A0=ITOT
A3=ITOT
A6=2
R1=1/(C+DEL(=FO))
M0=FO
P1=FO *C
A1=FO *C
R2=2-FO *C
M4=R2
P2=FO *R2(=F1)
M0=FO
P3=FO *C
A1=FO *C
R3=2-FO *C
M4=R3
P4=FO *R3(=F2)
M7=1.0/ITOT
M6=SC=ITOT
A0=ITOT
P=2.0;R=2.0
A6=2
R1=1/(C+DEL(=FO))
M0=FO
P1=FO *C
A1=FO *C
R2=2-FO *C
M4=R2
P2=FO *R2(=F1)
M0=FO
P3=FO *C
A1=FO *C
R3=2-FO *C
M4=R3
P4=FO *R3(=F2)
M7=1.0/ITOT
A6=RTLTH
A0=0.0
R=0.0
A0=UF;A7=SUM0(-2),A7+U0(-1)
?
A7=SUME(-1),A7+UF;A1=U0
?
A1=UF(+1);A7=SUM0(-1),A7+SUM0
?
A7=SUME.A7+UF(+1);A0=U0(+1)

```

```

A45 07F9C 00000000 (00131)
A46 07F9F 00160030 (00132)
A47 07FA0 00104717 (00133)
A48 07FA2 47170011 (00134) SS2
A49 07FA4 00114737 (00135) SS3
A4A 07FA6 47370000 (00136) SS4
A4B 07FA8 00000000 (00137)
A4C 07FAA 00000000 (00138)
A4D 07FAC 43004190 (00139)
A4E 07FAE 40030000 (00140)
A4F 07FB0 00000000 (00141)
A50 07FB2 00000000 (00142)
A51 07FB4 00000000 (00143)
A52 07FB6 20372037 (00144)
A53 07FB8 16601660 (00145)
A54 07FBA 00000000 (00146)
A55 07FBC 00000000 (00147)
A56 07FBE 41110000 (00148)
A57 07FC0 47570000 (00149)
A58 07FC2 00140000 (00150)
A59 07FC4 00000000 (00151)
A5A 07FC6 40000000 (00152)
A5B 07FC8 00000000 (00153)
A5C 07FCA 00000000 (00154)
A5D 07FCC 40000000 (00155)
A5E 07FCE 00000000 (00156)
A5F 07FD0 47600200 (00157)
A60 07FD2 00100000 (00158)
A61 07FD4 00000000 (00159)
A62 07FD6 00000000 (00160)
A63 07FD8 00000000 (00161)
A64 07FDA 00000000 (00162)
A65 07FDC 00000000 (00163)
A66 07FDE 00000000 (00164)
A67 07FE0 00000000 (00165)
A68 07FE2 00000000 (00166)
A69 07FE4 00000000 (00167)
A6A 07FE6 00000000 (00168)
A6B 07FE8 00000000 (00169)
A6C 07FEA 00000000 (00170)
A6D 07FEC 00000000 (00171)
A6E 07FEE 00000000 (00172)
A6F 07FEF 00000000 (00173)
A70 07FF0 00000000 (00174)

```

```

NOP
JUMPC(R1,F*1)
MOV(ZFRO,A0)\MOV(A7),ADD(A0,A7)
MOV(A7),ADD(A0,A7)\MOV(ZFRO,A1)
MOV(ZFRO,A1)\MOV(A7),ADD(A1,A7)
MOV(A7),ADD(A1,A7)\NOP
MOV(R,A0)\MOV(R,F*0)
MOV(F*1,A1)\NOP
ADD(A0,A1)
MOV(A3),SUR(A6,A3)\NOP
MOV(R,M0)
MUL(M0,A7)\NOP
MOV(P,M0)\NOP
CIRAR(H1)
TAP2(I)=TMP2(I)+CTEMP+0.5 I=0,1,2,...,I.TH-1
K(0.5)
MOV(P,F*0)\NOP
MOV(P,A0)\MOV(F*1,A0)
MOV(A1),ADD(A0,A1)\NOP
MOV(A7),ADD(A2,A7)\NOP
MOV(ZFRO,A4)\NOP
MOV(IO,A3)\MOV(IO,A6)
SUR(A3,A7)\NOP
MOV(R,A2)\NOP
MOV(IO,A3)\NOP
SUR(A3,A2)\NOP
MOV(IO,F*0)\NOP
ADD(A3,A7)\R(A6)
JUMPC(M0K,T1)
MOV(A5),MAX(A5,A4)\NOP
NOP\MOV(F*1,F*0)
MOV(F*1,A2)\NOP
MOV(R,F*0)\NOP
JUMPS(ZR0,T1)
MOV(R,M0)\MOV(F*1,A6)
JUMPC(VADD52,F*1)
JUMP(VADD52F)
MOV(IO,A0)\NOP

```

```

7
A0=SUMF;EXO=SUMO
A1=SUMO
A3=(SUMF+SUMO),RTI,TH=A3
M0=RTI,TH-(SUMF+SUMO)
ITOT'*(RTI,TH-(SUMF+SUMO)
)
SA=CTEMP=(RTI,TH-SI,NG)/IT
RT
REF:ASF APS INPUT
H=.5
EXO=CTEMP
A0=CTEMP
A1=0.5*CTEMP+0.5
A7=CTEMP+0.5, ADD FIRST
AND CONST.
MIN
MAX\OLD SUM=MAX
SIH CONST, SINCE WF ADD
IT LATER
THIS IS NOW OLD VALUE.
A3=NEW
NEW=OLD
SAVE NEW
NEW SUM\OLD SUM
JUMPIF NEW>OLD
NEWSUM,ZERO
NEW INPUT
NEW INPUT
OLD SUM <= NEW SUM
JUMPIF 0.GF,NEWSUM
OUTPUT NEW SUM,OLD SUM <=
- NEWSUM
LOOP
DUMMY TO USE UP INPUT

```

```

A6A 07FF6 081C0000 (00175) Z80      MOV(ZERO,00)\NOP
A6B 07FF6 90160069 (00176)      JUMPC(ZR0LP,FWI)
      (00177) ?
A6C 07FF6 20372037 (00178)      CLEAR(CMT)
A6D 07FFC 10000071 (00179)      JUMPI(VINPTS)
A6E 07FF6 0000009C (00180) MDK      NOP\MOV(R,00)
A6F 07FF0 9016005C (00181)      JUMPC(VAIDS2,FWI)
A70 07FF2 20372037 (00182) VAIDS2F  CLEAR(MI)
      (00183) *
A71 07FF4 08FF00FF (00184) * TMP1(T)=INTEGER PART(TMP2(I)) I=0,1,2,...,LTH-1
A72 07FF6 08FF00FF (00185) VINPTS  MOV(10A,M6)
A73 07FF8 04170017 (00186)      MOV(ZERO,A7)
A74 07FFA 08E00000 (00188)      MOV(10A,M0)\NOP
A75 07FFC 08E00000 (00189)      NOP\MOV(10A,M0)
A76 07FF6 000000FF (00190)      NOP\MOV(10A,M6)
A77 07F00 000000FF (00191)      MOV(A1),MUL(M0,M6)
A78 07F02 84518451 (00192) 81      MOV(M1),R(A6)
A79 07F04 02C902C9 (00193) ?
      (00194) ?
A7A 07F06 5F205F20 (00195)      MOV(MUL),ADDI(A1,A7)
A7B 07F08 84F084F0 (00196)      MOV(A0),MUL(M1,M7)
A7C 07F0A 3A1C3A1C (00197)      MOV(00),ALIGN(A0)
A7D 07F0C 08E00000 (00198)      MOV(10A,M0)\NOP
A7E 07F0E 08E00000 (00199)      MOV(10A,M6)\NOP
A7F 07F10 000000FF (00200)      NOP\MOV(10A,M0)
A80 07F12 000000FF (00201)      NOP\MOV(10A,M6)
A81 07F14 37103710 (00202)      MOV(A0),NORM(A0)
A82 07F16 901C0078 (00203)      JUMPC(A1,F0)
      (00204) *
A83 07F18 20320000 (00205) DNFS4  CLEAR(MA)\NOP
A84 07F1A 00000000 (00206)      NOP
      SCASSZ=#A-SCASSA
      END SCASSZ
      07F1C      EJECT

```

```

KEEP GOING TIL INPUT DON
F
LET APS GO
GO ON TO NEXT RTN
OLD SUM OUT
LOOP
RELEASE APS INPUT
M7=16**6)
M6=16**(-6)
A7=SA=0.0
M0=X(0,1)
A6=X(0,1-4)
M0=X(0,1+1)
A6=X(0,1-3)
A1=X(4,1-2),P=X(1,1)
M1=X(3,1-1),TI=SIGN(X(0,
I-1))
R=X(5,1-2)
A0=X(1,1),P=X(4,1-1)
M0=X(5,1-2),R=X(2,1)
M0=X(0,1+2)
A6=X(0,1-2)
X=(0,1+3)
X=(0,1-1)
A0=X(2,1),R=X(3,1)
FOR I=0,1,2,...,LTH-1
APU DONE!

```



```

(00210) *
(00211) *
(00212) *
(00213)
(00214)
(00215) ;
(00216)
(00217)
(00218)
(00219)
(00220)
(00221) *
(00222) G2055
(00223)
(00224)
(00225) SC1RS
(00226)
(00227)
(00228)
(00229) *
(00230) TSUMSS
(00231)
(00232)
(00233)
(00234) *
(00235) IADDS
(00236)
(00237)
(00238)
(00239) *
(00240) INTPTS
(00241)
(00242)
(00243)
(00244)
(00245) #1
(00246)
(00247)
(00248)
(00249) INTPST
(00250)
(00251)
(00252)
(00253) #2

```

```

07F1C 00007FA2
07F1E 00007F28
07F20 0000
07F21 0080
07F22 00007F2A

```

```

FVFN
ADDR G2055I
ADDR G2055S+2*SCLRS
DATA 0
DATA G2055Z
ADDR G2055A
FVFN
REGM APS(G205)
JSM(G205S1,P1)
SFT(M0)
LOAD(HR2,SASR0(1),TF)
LOAD(HR0,I0,TF)
ADD(HR1,MSS)
ADD(HR0,MS,TF,C)
LOAD(HR2,SAS79(1),TF)
LOAD(HR0,TMP2S(3),TF)
LOAD(HR1,SIZES)
ADD(HR0,MS,TF,C)
LOAD(HR2,SASR4(1),TF)
LOAD(HR0,TMP2S(3),TF)
LOAD(HR1,SIZES)
ADD(HR0,MS,TF,C)
LOAD(HR2,SAS12(1),TF)
LOAD(HR2,SAS13(1),TF)
LOAD(HR0,TMP2S-2(3))
LOAD(HR2,TMP2S-2(4))
LOAD(HR3,3)
ADD(HR0,MS,TF)
ADD(HR2,MSS,TF)
SHR1(HR3,1),JUMPP(#1)
LOAD(HR1,SIZES-4)
ADD(HR2,MS,TF)
SHR1(HR1,1),JUMPP(INTPST)
LOAD(HR3,5)
ADD(HR0,MSS,TF)

```

```

POINTER TO CONSTRUCTED I
NSTRUCTION BLOCK
POINTER TO SCALAR BLOCK
NUMBER OF SCALARS
MODULE SIZE
POINTER TO CHAIN ANCHOR
END ON WORD BOUNDARY

```

```

SFT INPUT PC(P1)
ENABLE OUTPUT ADDRESSING
I0=CTEMP
I0=DCTM2(0)
DCTM2(.) SIZE=1
I0=DCTM2(1),CHANGE PC
I0=RTLTH
I0=TMP2(0)
TMP2(.) SIZE=1
I0=TMP2(1),CHANGE PC
I0=5.0
I0=TMP2(0)
TMP2(.) SIZE=1
I0=TMP2(1),CHANGE PC
I0=16**6
I0=16**(-6)
TMP2(.) RA=2
TMP2(.) RA=2
SFT UP LOOP W/ ...
I0=TMP2(1)
I0=TMP2(-1)
FOR I=0,1,2,3
TMP2(.) SIZE=1-4
I0=TMP2(I+4)
I0=TMP2(1)
FOR I=0,1,2,...LTH-6
SFT UP LOOP W/ ...
I0=?

```

A1C 07F5C 38A0002 (00254)	ADD(RW2,WS,TF)	IO=TMP2(LTH-4)
A1D 07F5F 3A39181 (00255)	SHR1(RW3,1),JUMPP(#2)	FOR I=0,1,...5
A1E 07F60 3C200031 (00256) *	CLFAR(R1)	INPUT DONE!
A1F 07F62 3E000020 (00258)	NOP	
A20 07F64 40202740 (00259)	JSN(G205S0,P2)	SET OUTPUT PC(PC2)
A21 07F66 42140032 (00260)	SHR1(RR1,2)	SIZE-1-2
A22 07F68 44A0002 (00261) #1	ADD(RW0,WS,TF)	IO=DCTM2(1) OR TMP2(1)
A23 07F6A 46192241 (00262)	SHR1(RR1,1),JUMPP(#1)	FOR J=2,3,...LTH-2
A24 07F6C 48300037 (00263) ?	SFT(M1)	STALL BEFORE JUMP! CRITI
A25 07F6E 4A2F0000 (00265)	ADD(RW2,MSS,NA,C)	PC->PC0
A26 07F70 4C002160 (00266) *	JUMP(TOPSP1)	LET *CHANGE* SETTLE
A27 07F72 4E303940 (00268)	JSN(G205S3,P3)	SET OUTPUT PC(PC3)
A28 07F74 50C60A00 (00269)	LOAD(RW0,TMP2S(3),TF)	IO=TMP2(0)
A29 07F76 525000FD (00270)	LOAD(RW1,SIZES-2)	TMP2(,) SIZE-3
A2A 07F78 548E0002 (00271)	ADD(RW0,WS,TF,C)	IO=TMP2(1),CHANGE PC
A2R 07F7A 56F20426 (00272)	LOAD(RW2,SASH2(1),TF)	IO=U(7)+CTEMP
A2C 07F7C 58F2042H (00273)	LOAD(RW2,SASH3(1),TF)	IO=ITOT
A2D 07F7E 5AF20422 (00275) OSUMS	LOAD(RW2,SASH0(1),TF)	IO=CTEMP
A2E 07F80 5C60A00 (00277) *	LOAD(RW0,TMP2S(3),TF)	IO=TMP2(0)
A2F 07F82 5F5000FD (00278)	LOAD(RW1,SIZES-2)	TMP2(,) SIZE-3
A30 07F84 608F0002 (00279) *	ADD(RW0,WS,TF,C)	IO=TMP2(1),CHANGE PC
A31 07F86 62700003 (00281)	LOAD(RW3,3)	3+1=4 DUMMIES
A32 07F88 64F20794 (00282) #2	LOAD(RW2,DMS(1),TF)	OO=?
A33 07F8A 66313241 (00283)	SHR1(RW3,1),JUMPP(#2)	FOR I=0,1,...3
A34 07F8C 68C60A00 (00284)	LOAD(RW0,TMPLS(3),TF)	IO=TMP1(0)
A35 07F8E 6A5000FD (00285)	LOAD(RW1,SIZES-2)	TMP1(,) SIZE-3
A36 07F90 6C8F0002 (00286) *	ADD(RW0,WS,TF,C)	IO=TMP1(1),CHANGE PC
A37 07F92 6E200030 (00287) *	CLFAR(R0)	OUTPUT DONE!
A38 07F94 70000020 (00289)	NOP	
A39 07F96 72300032 (00290)	SFT(PA)	ENABLE APU
A3A 07F98 74110031 (00291)	SHR1(RW1,1)	SIZE-1-1
A3R 07F9A 768A0002 (00292) #1	ADD(RW0,WS,TF)	IO=TMP2(1) OR TMP1(1)
A3C 07F9C 78113841 (00293)	SHR1(RW1,1),JUMPP(#1)	FOR I=2,3,...LTH-2
A3D 07F9E 7A8F0002 (00294)	ADD(RW0,WS,TF,C)	IO=... (LTH-1),CHANGE PC
A3F 07FA0 7C0048A0 (00295) *	JUMP(TOPSP3)	PC->PC2
00007F2A (00297)	G205SAE BC	ASSIGN VALUE TO CHAIN AN

CHDR

07FA2 (00298) ?  
 (00299) ? FND BA-1  
 (00300) ? STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS  
 07FA2 00000000 (00301) G2055Y DATA IP'0.0'  
 00000000 (00302) G2055Z=01-G2055  
 07FA4 (00303) FND



TAP1S: 00000 (00024) (00284)  
 TAP2S: 00000 (00025) (00231) (00236) (00242) (00243) (00269) (00277)  
 TAP3P1: 00021 (00260) (00266)  
 TAP3P3: 0001A (00291) (00295)  
 VADUS: 00006 (00054) (00071)  
 VADUS2: 0005C (00160) (00172) (00181)  
 VADUS2A: 00070 (00173) (00182)  
 VADUS2M: 00021 (00076) (00079) (00083) (00084)  
 VADUS2M: 0001C (00063) (00082) (00086)  
 VADUS2M: 00014 (00066) (00073)  
 VADUS2M: 00071 (00179) (00185)  
 VADUS2M: 00002 (00026) (00228) (00233) (00234) (00245) (00250) (00254) (00261) (00271)  
 VADUS2M: (00279) (00286) (00292) (00294)  
 VADUS2M: 00004 (00027)  
 VADUS2M: 00003 (00028)  
 VADUS2M: 0006A (00169) (00175)  
 VADUS2M: 00069 (00174) (00176)  
 VADUS2M: 00007 (00024)

COMPLETE DCT HIT ASSIGNMENT

ORIGINATED:25-JUN-79  
UPDATED:22-MAY-80

FCR 255... "MPCDHA(Y,U,V)"

```

(00001) * FCR 255... "MPCDHA(Y,U,V)"
(00002) *
(00003) *
(00004) * DEFINE GLOBAL SYMBOLS
(00005) *
(00006) *
(00007) *
(00008) *
(00009) *
(00010) *
(00011) *
(00012) *
(00013) *
(00014) *
(00015) *
(00016) *
(00017) *
(00018) *
(00019) *
(00020) *
(00021) *
(00022) *
(00023) *
(00024) *
(00025) *
(00026) *
(00027) *
(00028) *
(00029) *
(00030) *

```

```

OPADD EXP, (1 .LS. 14) + (12 .LS. 8) + $IF
OPADD UDF, (3 .LS. 10) + (12 .LS. 5) + $IH
AFDTSORG=$RFR
APSSMFM=$IFFCO
CSPUSNDS=$21FC
DMS=$794
TRTS=0'20RH'
#M=3
#S=1
MS$=0
SVTS=$03M2
SAS3H=$VTS+2#D'3H'
SAS7Q=$VTS+2#D'7Q'
SASH4=$VTS+2#D'4H'
STZFS=D'256'-D'11'
START=$H100
TMPIS=D'204H'
WS=2
WWS=4

```

EXPAND ARRAY FUNCTION DISPATCH TABLE

```

R1=$AFDTSORG+3#2*(255-12R)
ADDR CDRAS(R7,1)
ADDR G102S(R7,1)
ADDR CSPUSNDS(1,0)
EJECT

```



Address	Hex	Label	Code	Comment
08100	0000			
08101	0031			
08102	00F10000			
08104	00F000F7			
08106	4820089C			
08108	083102F0			
0810A	911F0009			
0810C	901F0001			
0810E	000C089C			
08110	901C0007			
08112	10370010			
08114	40200000			
08116	089C0000			
08118	10490000			
0811A	08F0081C			
0811C	901F000C			
0811E	901C000F			
08120	10370029			
08122	088000E0			
08124	0821680			
08126	00000092			
08128	02200280			
0812A	089108D3			
0812C	4140481C			
0812E	000000F4			
08130	911F001C			
08132	911E0022			
08134	911F0013			
08136	00004280			
08138	10000014			
0813A	911E0022			
0813C	901F001F			
0813E	00000000			
08140	000411			
08142	000427			
08144	000443			
08146	000459			
08148	000475			
0814A	000491			
0814C	000507			
0814E	000523			
08150	000539			
08152	000555			
08154	000571			
08156	000587			
08158	000603			
0815A	000619			
0815C	000635			
0815E	000651			
08160	000667			
08162	000683			
08164	000699			
08166	000715			
08168	000731			
0816A	000747			
0816C	000763			
0816E	000779			
08170	000795			
08172	000811			
08174	000827			
08176	000843			
08178	000859			
0817A	000875			
0817C	000891			
0817E	000907			
08180	000923			
08182	000939			
08184	000955			
08186	000971			
08188	000987			
0818A	001003			
0818C	001019			
0818E	001035			
08190	001051			
08192	001067			
08194	001083			
08196	001099			
08198	001115			
0819A	001131			
0819C	001147			
0819E	001163			
081A0	001179			
081A2	001195			
081A4	001211			
081A6	001227			
081A8	001243			
081AA	001259			
081AC	001275			
081AE	001291			
081B0	001307			
081B2	001323			
081B4	001339			
081B6	001355			
081B8	001371			
081BA	001387			
081BC	001403			
081BE	001419			
081C0	001435			
081C2	001451			
081C4	001467			
081C6	001483			
081C8	001499			
081CA	001515			
081CC	001531			
081CE	001547			
081D0	001563			
081D2	001579			
081D4	001595			
081D6	001611			
081D8	001627			
081DA	001643			
081DC	001659			
081DE	001675			
081E0	001691			
081E2	001707			
081E4	001723			
081E6	001739			
081E8	001755			
081EA	001771			
081EC	001787			
081EE	001803			
081F0	001819			
081F2	001835			
081F4	001851			
081F6	001867			
081F8	001883			
081FA	001899			
081FC	001915			
081FE	001931			
08200	001947			
08202	001963			
08204	001979			
08206	001995			
08208	002011			
0820A	002027			
0820C	002043			
0820E	002059			
08210	002075			
08212	002091			
08214	002107			
08216	002123			
08218	002139			
0821A	002155			
0821C	002171			
0821E	002187			
08220	002203			
08222	002219			
08224	002235			
08226	002251			
08228	002267			
0822A	002283			
0822C	002299			
0822E	002315			
08230	002331			
08232	002347			
08234	002363			
08236	002379			
08238	002395			
0823A	002411			
0823C	002427			
0823E	002443			
08240	002459			
08242	002475			
08244	002491			
08246	002507			
08248	002523			
0824A	002539			
0824C	002555			
0824E	002571			
08250	002587			
08252	002603			
08254	002619			
08256	002635			
08258	002651			
0825A	002667			
0825C	002683			
0825E	002699			
08260	002715			
08262	002731			
08264	002747			
08266	002763			
08268	002779			
0826A	002795			
0826C	002811			
0826E	002827			
08270	002843			
08272	002859			
08274	002875			
08276	002891			
08278	002907			
0827A	002923			
0827C	002939			
0827E	002955			
08280	002971			
08282	002987			
08284	003003			
08286	003019			
08288	003035			
0828A	003051			
0828C	003067			
0828E	003083			
08290	003099			
08292	003115			
08294	003131			
08296	003147			
08298	003163			
0829A	003179			
0829C	003195			
0829E	003211			
082A0	003227			
082A2	003243			
082A4	003259			
082A6	003275			
082A8	003291			
082AA	003307			
082AC	003323			
082AE	003339			
082B0	003355			
082B2	003371			
082B4	003387			
082B6	003403			
082B8	003419			
082BA	003435			
082BC	003451			
082BE	003467			
082C0	003483			
082C2	003499			
082C4	003515			
082C6	003531			
082C8	003547			
082CA	003563			
082CC	003579			
082CE	003595			
082D0	003611			
082D2	003627			
082D4	003643			
082D6	003659			
082D8	003675			
082DA	003691			
082DC	003707			
082DE	003723			
082E0	003739			
082E2	003755			
082E4	003771			
082E6	003787			
082E8	003803			
082EA	003819			
082EC	003835			
082EE	003851			
082F0	003867			
082F2	003883			
082F4	003899			
082F6	003915			
082F8	003931			
082FA	003947			
082FC	003963			
082FE	003979			
08300	003995			
08302	004011			
08304	004027			
08306	004043			
08308	004059			
0830A	004075			
0830C	004091			
0830E	004107			
08310	004123			
08312	004139			
08314	004155			
08316	004171			
08318	004187			
0831A	004203			
0831C	004219			
0831E	004235			
08320	004251			
08322	004267			
08324	004283			
08326	004299			
08328	004315			
0832A	004331			
0832C	004347			
0832E	004363			
08330	004379			
08332	004395			
08334	004411			
08336	004427			
08338	004443			
0833A	004459			
0833C	004475			
0833E	004491			
08340	004507			
08342	004523			
08344	004539			
08346	004555			
08348	004571			
0834A	004587			
0834C	004603			
0834E	004619			
08350	004635			
08352	004651			
08354	004667			
08356	004683			
08358	004699			
0835A	004715			
0835C	004731			
0835E	004747			
08360	004763			
08362	004779			
08364	004795			
08366	004811			
08368	004827			
0836A	004843			
0836C	004859			
0836E	004875			
08370	004891			
08372	004907			
08374	004923			
08376	004939			
08378	004955			
0837A	004971			
0837C	004987			
0837E	005003			
08380	0			



COMPLETE DCT HIT ASSIGNMENT

PAGE 4: FOR 255... "MPCDRA(Y,U,V)"

```

A1F 0R13E 00000R12 (000R1) NOP\MOV(ZERO,A2)
A1F 0R140 00004240 (000R2) NOP\ADD(A4,A2)
A20 0R142 00000R9C (000R3) NOP\MOV(R,00)
A21 0R144 10000027 (000R4) JUMP(MTSOUT)
A22 0R146 00000240 (000R5) NOP\RA(A4)
A23 0R148 00000R9C (000R6) NOP\MOV(R,00)
A24 0R14A 10400026 (000R7) JUMP(MTSTST,A1),SFT
A25 0R14C 0RFC0000 (000R8) MOV(10A,00)\NOP
A26 0R14E 90160025 (000R9) MTSTST JUMPC(MTSIN,F,W1)
A27 0R150 90100027 (000R1) MTSOUT JUMPC(MTSOUT,00R)
A28 0R152 20372037 (000R2) CLEAR(W1)
A29 0R154 0RFF0RFF (000R4) * MOV(10A,M7)
A2A 0R156 0RFR0000 (000R5) #1 MOV(10A,M0)\NOP
A2B 0R158 00000RFR (000R6) NOP\MOV(10A,M0)
A2C 0R15A R471R471 (000R7) MOV(A1),MUL(M0,M7)
A2D 0R15C 3A3C3A3C (000R8) MOV(00),ALIGN(A1)
A2E 0R15E 9010002A (00100) * JUMPC(#1,F1)
A2F 0R160 20370000 (00101) DNESA CLEAR(CR)\NOP
A30 0R162 00000000 (00102) NOP
0R164 00000031 (00104) * CDRASSZ=RA-CDHASSA
00105 (00105) END CDRASSZ
00106 (00106) FJFCT
    
```

```

INCREMENT=0
IRIT(LTH)+?
IRIT(LTH)=IRIT(LTH)+?
INPUT CLFAR,CHECK OUTPUT
R=IRIT(?)
IRIT(?)=IRIT(?)
ENTER "MT OUFUF" a TEST
IRIT(1)=IRIT(1)
WAIT FOR INPUT TO COMPLE
TE.
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
M7=SA
M0=0FF
M0=00
A0=PI,P=SA*U
00=R',R'=IFIX"(P)
FOR ALJ. I=0,1,2...LTH-1
API DONE
    
```

```

(00107) *
(00108) *
(00109) *
(00110) *
(00111) *
(00112) ?
(00113) ?
(00114) *
(00115) ?
(00116) *
(00117) *
(00118) *
(00119) *
(00120) *
(00121) *
(00122) ?
(00123) ?
(00124) *
(00125) *
(00126) *
(00127) *
(00128) *
(00129) *
(00130) *
(00131) *
(00132) *
(00133) *
(00134) *
(00135) ?
(00136) *
(00137) *
(00138) *
(00139) *
(00140) *
(00141) *
(00142) *
(00143) *
(00144) *
(00145) *
(00146) *
(00147) *
(00148) *
(00149) *
(00150) *

```

OR164 0000041F6  
 OR166 000004170  
 OR168 0000  
 OR169 007C  
 OR16A 0000041DA  
 OR16B 0000041F6  
 OR16C 00201F40  
 OR16D 02300030  
 OR170 04F20420  
 OR172 064607FF  
 OR174 045000FF  
 OR176 0A8A0002  
 OR178 0C4A0002  
 OR17A 0F1905H2  
 OR17C 10300037  
 OR17E 12000020  
 OR180 14300030  
 OR182 16001F40  
 OR184 18F2042A  
 OR186 1A7000FF  
 OR188 1C3607FF  
 OR18A 1F5000FF  
 OR18C 200014F9  
 OR18E 228A0002  
 OR190 241911E1  
 OR192 263900F3  
 OR194 28300037  
 OR196 2A000020  
 OR198 2C300030  
 OR19A 2FC203CE  
 OR19C 303607FF  
 OR19E 32500103  
 OR1A0 348A0002  
 OR1A2 368A0002

EVEN  
 ADDR G10251  
 ADDR G10252\*2\*INSS  
 DATA 0  
 DATA G1025Z  
 ADDR G1025A  
 EVEN  
 HFCIN APS(G102)  
 JSN(G10250,F2)  
 SFT(40)  
 LOAD(RR2,SAS7q(1),TF)  
 LOAD(RR0,TMPS-2(3))  
 LOAD(RR1,SIZES)  
 ADD(RR0,WS,TF)  
 SUBL(RR1,2),JUMPP(INST)  
 SFT(40)  
 NOP  
 SFT(40)  
 JUMPS(IFIXSS,AP1)  
 LOAD(RR2,SASR4(1),TF)  
 LOAD(RR3,SIZES)  
 LOAD(RR0,TMPS-2(3))  
 LOAD(RR1,SIZES)  
 JUMPS(FINSI,AF1)  
 ADD(RR0,WS,TF)  
 SUBL(RR1,1),JUMPP(INCSI)  
 SUBL(RR3,1),JUMPP(PASSSI)  
 SFT(40)  
 NOP  
 SFT(40)  
 LOAD(RR0,SAS3R(1),TF)  
 LOAD(RR0,TMPS-2(3))  
 LOAD(RR1,SIZES+4)  
 ADD(RR0,WS,TF)  
 ADD(RR0,WS,TF)

POINTER TO CONSTRUCTED I  
 NSTRUCTION BLOCK  
 POINTER TO SCALAR BLOCK  
 NUMBER OF SCALARS  
 MODULE SIZE  
 POINTER TO CHAIN ANCHOR  
 END ON WORD BOUNDARY

SET OUTPUT PC(P2)  
 ENABLE OUTPUT ADDRESSING  
 IO=RTLTH  
 TMP1(.) RA-2  
 TMP1(.) SIZE-1  
 IO=TMP1(I)  
 IO=TMP1(I+1)  
 FOR J=0,1,2...LTH-1  
 STALL APS INPUT  
 RELEASED BY API  
 RELEASE APS OUTPUT  
 AF1=1 THEN "IFIX" NOW

IO=THRESHOLD=5.0  
 TMP1(.) SIZE-1=# OF PASS  
 ES  
 TMP1(.) RA-2  
 TMP1(.) SIZE-1  
 API HAS FINISHED  
 IO=TMP1(I)  
 FOR I=0,1,2...LTH-1  
 PASS=1,2...LTH  
 STALL APS INPUT  
 RELEASED BY API  
 RELEASE APS OUTPUT

SCALAR SA=2\*(I-15)  
 TMP1(.) RA-2  
 TMP1(.) SIZE-1+4  
 IO=TMP1(I)  
 IO=TMP1(I+1)

```

A1C 081A4 38191AH2 (00151) SURL(RW1,2),JUMPP(PTXSI)
(00152) *
A1D 081A6 3A200031 (00153) DNEST CLEAR(RT)
A1E 081A8 3C000020 (00154) NOP
(00155) *
A1F 081AA 3E300032 (00156) G10280 SET(RA)
A20 081AC 40F20794 (00157) LOAD(RW2,DMSYS(1),TF)
A21 081AE 424607FF (00158) LOAD(RW0,TMPLS-2(3))
A22 081B0 445000FF (00159) LOAD(RW1,SIZES)
A23 081B2 468A0002 (00160) OUTST ADD(RW0,MS,TF)
A24 081B4 48RA0002 (00161) ADD(RW0,MS,TF)
A25 081B6 4A112382 (00162) SURL(RW1,2),JUMPP(OUTST)
A26 081B8 4C200030 (00163) CLEAR(RU)
A27 081BA 4E000020 (00164) NOP
A28 081BC 500032F9 (00165) JUMPS(OFTXS,AF1)
(00166) *
A29 081BF 52F20794 (00167) CLMPS0 LOAD(RW2,DMSYS(1),L,TF)
A2A 081C0 547000FF (00168) LOAD(RW3,SIZES)
(00169) ;
A2B 081C2 564607FF (00170) PASSSD LOAD(RW0,TMPLS-2(3))
A2C 081C4 585000FF (00171) LOAD(RW1,SIZES)
A2D 081C6 5A0031F9 (0 72) JUMPS(FINSU,AF1)
A2E 081C8 5C8A0002 (0 73) INCSJ ADD(RW0,MS,TF)
A2F 081CA 5E112F81 (00174) SURL(RW1,1),JUMPP(INCSJ)
A30 081CC 60312881 (00175) SURL(RW3,1),JUMPP(PASSSD)
A31 081CE 62700030 (00176) FINS0 CLEAR(RU)
(00177) *
A32 081D0 64700003 (00178) OFTXS LOAD(RW3,3)
A33 081D2 66F20794 (00179) #1 LOAD(RW2,DMSYS(1),TF)
A34 081D4 68313341 (00180) SURL(RW3,1),JUMPP(#1)
A35 081D6 6A400000 (00181) LOAD(RW0,[0])
A36 081D8 6C500000 (00182) LOAD(RW1,MSS)
A37 081DA 6E020000 (00183) SUR(RW0,MSS)
A38 081DC 710A0001 (00184) OFTXSI ADD(RW0,HS,TF)
A39 081DE 730A0001 (00185) APD(RW0,HS,TF)
A3A 081E0 74113842 (00186) SURL(RW1,2),JUMPP(OFTXSI)
(00187) *
A3B 081E2 76200030 (00188) DNESS0 CLEAR(RU)
A3C 081E4 78000020 (00189) NOP
(00190) *
081E6 000081D6 (00191) G1028A= #C
(00192) END #A-1
081E8 00000000 (00193) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
081EA 00000000 (00194) G1028I DATA 1F'D,0'

```

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FOR I=1,2,...,LTH+1
INPUT DONE!
ENABLE: APU
FOR OO=?
TMPL(. ) RA-2
TMPL(. ) SIZE-1
OO=TMPL(J)
FOR J=1,2,...,LTH-1
STALL APS OUTPUT
RELEASED BY APS INPUT
AFI=1 THEN "IFIX" NOW
FOR OO=?
TMPL(. ) SIZE-1=# OF PASS
ES
TMPL(. ) RA-2
TMPL(. ) SIZE-1
APU HAS FINISHED
OO=TMPL(J)
FOR J=1,2,...,LTH-1
PASS=1,2,...,LTH
STALL APS OUTPUT
3+1=4 DUMMIES
OO=?
4 TIMES
IBIT(. ) RA
IBIT(. ) SIZE-1
IBIT(. ) RA-1
OO=IBIT(I)
OO=IBIT(I+1)
FOR I=1,2,...,LTH-1
APU DONE.
FREEZE CHAIN ANCHOR

```

COMPLETE DCT HIT ASSIGNMENT

PAGE 7: FCR 255... "MPCDRA(Y,U,V)"

ORLEN 0000007C (00195) C102SZ=81-6102S  
(00196) END

COMPLETE DCT HIT ASSIGNMENT

PAGE 02 FCH 255... "MPCDRA(V,U,V)"

ADRS:	00009	(00051)	(00057)
AFDTSURC:	00MFR	(00007)	(00026)
APSSMFM:	1PFC0	(00008)	
CDHAS:	04107	(00027)	(00043)
CDHASSA:	00000	(00041)	(00045) (00104)
CDHASSZ:	00031	(00042)	(00104) (00105)
CLMPS:	00010	(00055)	(00066)
CLMPS1:	0000C	(00133)	
CLMPS0:	00029	(00167)	
CLPS:	0000C	(00060)	(00061)
CSPUSMOS:	021FC	(00009)	(00029)
DWYS:	00794	(00010)	(00157) (00167) (00170)
DWYSA:	0002F	(00101)	
DWY1:	0001D	(00153)	
DWES0:	0003M	(0014M)	
FINS1:	00014	(00138)	
FINS0:	00031	(00172)	(00176)
FIXS:	00029	(00063)	(00094)
FWISC:	0000B	(00059)	(00061)
G102S:	0816C	(00028)	(00113) (00119) (00195)
G102SA:	08106	(00116)	(00191)
G102S1:	081F6	(00111)	(00194)
G102S0:	0001F	(00120)	(00156)
G102S7:	0007C	(00115)	(00195)
HS:	00001	(00013)	(00184) (00185)
IRITS:	00H2M	(00011)	
IFX1S:	0001A	(00149)	(00151)
IFXSS:	00017	(00131)	(00146)
INSS:	00002	(00113)	(00122)
INST:	00005	(00125)	(00127)
INCS1:	00011	(00139)	(00140)
INCS0:	0002F	(00173)	(00174)
LIMITS:	0001K	(00040)	(00042)
LIMITS:	00013	(00064)	(00076)
LSTISA:	0001C	(00074)	(00079)
MSS:	00000	(00014)	(00142) (00183)
MTSA:	00027	(00075)	(00079) (00085)
MTSIN:	00025	(00048)	(00089)
MTSOUT:	00027	(00044)	(00091)
MTSTST:	00026	(00047)	(00089)
OFIXS:	00032	(00155)	(00178)
OFIX1:	00039	(00184)	(00186)
OUTST:	00023	(00160)	(00167)
PASS61:	0000F	(00136)	(00141)

PAGE 9: FOR 255... "MPCDHA(Y,U,V)" (COMPLETE DCT HIT ASSIGNMENT)

PASS80: 0002H (00170) (00175)  
SAS3H: 003CF (00016) (00146)  
SAS79: 00420 (00017) (00122)  
SAS84: 0047A (00018) (00133)  
S17FS: 0006F (00019) (00124) (00130) (00137) (00140) (00159) (00168) (00171)  
START: 00100 (00020) (00034)  
SVTS: 00302 (00015) (00036) (00017) (00018)  
TRPIS: 00000 (00021) (00123) (00136) (00147) (00158) (00170)  
TST85: 00001 (00046) (00052)  
WS: 00014 (00070) (00078)  
WWS: 00002 (00022) (00125) (00170) (00149) (00150) (00160) (00161) (00173)  
00004 (00023)

LINE5 WITH ERRORS: 0 (MAP VERSION R00101.10) F- 0

LINCOLN ORIGINAL:18-APR-79

T A B L E O F C O N T E N T S

MODIFIED:03-JUN-80

PAGE 13 LINF1.TXT ORIGINATED:18-APR-79

(00001) \* LINF1.TXT ORIGINATED:18-APR-79



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(018355) ;
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A06 0000C 07820794 (00046) LOAD(R2,PKYS(1),L)  
 A07 0000B 08150000 (00047) ADDR(R2,ZPOS,T4)  
 A08 00010 09820793 (00048) SETSF LOAD(P2,PKYS(1),L)  
 A09 00012 00041593 (00049) SFISO LOAD(R0,SENPTR(2),L)  
 A0A 00013 081111F (00050) SFTSI LOAD(R1,RCV1M1(2),L)  
 A0B 00014 0C100180 (00051) CF1  
 A0C 00014 01100780 (00052) CF2  
 A0D 0001A 04100000 (00053) CHKSD JFEC(CHKSI,F1)  
 A0E 0001F 1024163C (00054) JNF(CHKS01,R0,SENPTRSM1)  
 A11 00022 12302000 (00055) INT1  
 A12 00024 13041640 (00056) ?  
 A13 00026 1A100180 (00057) ?  
 A14 00024 10241708 (00058) CHKS01 JUMP(CHKSI),CF1  
 A15 0002A 00000000 (00059) ?  
 A17 00024 14302000 (00060) ?  
 A18 00024 10241708 (00061) ?  
 A19 00032 1A04130A (00062) ?  
 A1A 00034 1C101800 (00063) ?  
 A1B 00036 00000000 (00064) ?  
 A1C 0003A 15681468 (00065) ?  
 A1D 0003E 20100000 (00066) ?  
 A1E 00040 21441468 (00067) ?  
 A1F 00042 28100180 (00068) ?  
 A20 00044 24681402 (00069) ?  
 A21 00046 00000000 (00070) ?  
 A22 0004A 26100180 (00071) ?  
 A23 0004C 27441468 (00072) ?  
 A24 0004E 28100180 (00073) ?  
 A25 00050 2A101C00 (00074) ?  
 A26 00052 00000000 (00075) ?  
 A27 00054 2C100240 (00076) ?  
 A28 00056 20100180 (00077) ?  
 A29 00058 24100180 (00078) ?  
 A2A 0005A 26100180 (00079) ?  
 A2B 0005C 28100180 (00080) ?  
 A2C 0005E 2A100180 (00081) ?  
 A2D 00060 2C100180 (00082) ?  
 A2E 00062 24100180 (00083) ?  
 A2F 00064 26100180 (00084) ?

SFT INPUT REG. & DUMMY  
 GEN ADDR TO CLEAR P1  
 SFT P2 FOR FIRST INPUT  
 SET OUTPUT REG. @ BASE-1  
 SET INPUT REG. @ BASE-1  
 RESET "R0 RUMPED" FLAG  
 RESET "R1 RUMPED" FLAG  
 R0 RUMPED?  
 YES,CHECK INDEX @ MIDDLE  
 MIDDLE! RELEASE. MID0,ATT  
 ACH MID2  
 RESET BASE. JUST RFL0W 2  
 NO RUFFFR  
 RESET "R0 RUMPED" FLAG  
 YES,CHECK INDEX @ END AD  
 R0 RUMPED?  
 END! RELEASE. MID2,ATTACH  
 RESET "R0 RUMPED" FLAG  
 RESET OUTPUT REG. @ BASE  
 -1  
 R1 RUMPED?  
 YES,CHECK INDEX @ MIDDLE  
 MIDDLE! RELEASE. MID1,ATT  
 ACH MID3  
 RESET BASE. JUST RFL0W 2  
 NO RUFFFR  
 RESET "R1 RUMPED" FLAG  
 YES,CHECK INDEX @ END AD  
 R1 RUMPED?  
 END! RELEASE. MID3,ATTACH  
 RESET INPUT REG. @ BASE-  
 1  
 RESET "R1 RUMPED" FLAG  
 CHECK P2 FOR DATA REQUES  
 T.  
 MAP HEAD(M -> 1)  
 GEN ADDRESS & RESET P2  
 SFT "R0 RUMPED" FLAG  
 CHECK P1 FOR DATA REQUES

```
A31 00062 32300200 (00062) ?  
A32 00064 33300700 (00064) *  
A33 00066 34300799 (00066) *  
A34 00068 35300001 (00068) *  
A35 0006C 00300400 (0006C) *  
A36 0006E 36300000 (0006E) *  
A37 00070 00300400 (00070) *  
A38 00072 33300000 (00072) *  
A39 00074 33300000 (00074) *
```

```
T.  
SET "PI HUMPER" FLAG  
WASTE FIRST INPUT  
GEN ADDRESS & RESET PI  
TOP OF LOOP  
GEN ADDRESS & RESET PI  
TOP OF LOOP  
CSPU SHUTS IT OFF!
```

ALPH:	00006 (00021)		
CHRS0:	00001 (00053)	(00083)	(00002)
CHRS1:	00014 (00054)	(00060)	
CHRS2:	0001A (00053)	(00050)	(00066)
CHRS3:	00022 (00067)	(00073)	
CHRS4:	00028 (00066)	(00072)	(00073) (00079)
CHRS5:	00026 (00079)	(00084)	
CHRS6:	00003 (00012)	(00011)	
CHRS7:	00079 (00013)	(00014)	(00046)
CHRS8:	00094 (00014)	(00044)	(00084)
CHRS9:	00034 (00043)		
CHRS10:	00037 (00084)	(00091)	
CHRS11:	00008 (00023)	(00031)	
CHRS12:	00001 (00015)	(00082)	
CHRS13:	00171 (00020)	(00022)	(00089) (00091)
CHRS14:	00170 (00022)	(00023)	(00040) (00042) (00035) (00036)
CHRS15:	011F8 (00024)	(00033)	(00035)
CHRS16:	011F7 (00034)	(00050)	(00076)
CHRS17:	013F8 (00035)	(00067)	
CHRS18:	01369 (00025)	(00034)	(00036)
CHRS19:	01364 (00031)	(00070)	
CHRS20:	01409 (00036)	(00073)	
CHRS21:	00026 (00076)		
CHRS22:	0140C (00026)	(00028)	(00030) (00031)
CHRS23:	0140E (00028)	(00064)	
CHRS24:	01E4F (00030)	(00053)	
CHRS25:	01594 (00031)	(00049)	
CHRS26:	01E4F (00027)	(00029)	(00032)
CHRS27:	01E4D (00029)	(00057)	
CHRS28:	0170F (00042)	(00060)	
CHRS29:	00009 (00044)		
CHRS30:	0000A (00050)		
CHRS31:	00008 (00044)	(00048)	
CHRS32:	00000 (00041)		
CHRS33:	00000 (00016)	(00043)	(00047)

### 3.6.2 Executive Programs

The modules contained in this section are:

GTE 300

IOS

FF2R

APDONE

CSPUIS

T A B L E O F C O N T E N T S

OFFLINE SYMBOLS FOR ARRAY FUNCTION ASSEMBLIES	PAGE 1
ARITHMETIC FUNCTIONS DISPATCH TABLE	PAGE 4
DISPATCH TABLE ENTRIES FOR FFTN, FFTMR, FFTMI, FFTNI, FFTNH	PAGE 14
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APU3-VCOS VECTOR COSINE	PAGE 20
APU3-VFLT	PAGE 25
APS3-V1124	PAGE 26
APS3-V1124A	PAGE 29
APS3-V2134R	PAGE 32
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OUT-IN-PLACE FFT FOR ONE AND TWO DIMENSIONS	PAGE 36
FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS	PAGE 36
ENTRY TO SPECIAL HANDLING MODULE FOR VECTOR FFT SETUP	PAGE 38
SPECIAL HANDLING FOR PHASE, PHASE, AND UNPHASE	PAGE 48
SET PENDING SLOTS TO PROPER VALUES	PAGE 50
FFT - AP PROGRAMS	PAGE 53
APU PROGRAMS	PAGE 53
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SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD	PAGE 55
SUCCESSIVE RADIX-4 STAGES, FORWARD	PAGE 57
SCRAMBLE AND FIRST RADIX-2 STAGE, REVERSE	PAGE 61
SCRAMBLE AND FIRST RADIX-4 STAGE, REVERSE	PAGE 63
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APS PROGRAMS	PAGE 69
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CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)	PAGE 72
CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)	PAGE 74
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VM0V	PAGE 81
DEFINE TOP OF MODULE	PAGE 82

SNAP-II MAP-100 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980  
 MODIFIED FOR GTF SYLVANIA BY S. TERRACE

MODULES INCLUDED:

NAME	FCR #
VFUT	136
VMIV	143
VSMAI	144
VCOS	146
FFTN	204
FFTNA	205
FFTIN	206
FFTINA	207

DEFINE SYMBOLS FOR ARRAY FUNCTION ASSEMBLIES

FROM THE SNAP-II EXECUTIVE --- REL. 3.05 --- 5/22/79

```

000008FR (00027) *
00000245 (00028) *
000004FA (00029) *
00000463 (00030) *
00000420 (00031) *
00000240 (00032) *
0000024R (00033) *
000005W2 (00034) *
0000000C (00035) *
00000000 (00036) *
00000010 (00037) *
0000000A (00038) *
0000000F (00039) *
00000009 (00040) *
00000004 (00041) *
0001FFC0 (00042) *
000043) *
00000604 (00044) *
    
```

MODIFIED FOR NEW START ADDRESS

00000686 (00045)  
00000587 (00046)  
00000000 (00047)  
00000000 (00048) \*  
00000702 (00049)  
000021FC (00050)  
00000000 (00051) \*  
00000794 (00052)  
00000000 (00053) \*  
00001AFA (00054)  
00000000 (00055) \*  
0000079A (00056)  
00000000 (00057)  
00000004 (00058)  
00000005 (00059)  
00000011 (00060)  
00000020 (00061)  
00000000 (00062) \*  
00000001 (00063)  
00000000 (00064) \*  
00001R7F (00065)  
00001R8F (00066)  
00000000 (00067) \*  
0000FF00 (00068)  
000000FF (00069)  
00000000 (00070)  
00000000 (00071) \*  
000007AF (00072)  
00000382 (00073)  
000003CF (00074)  
0001FFCF (00075)  
00000000 (00076) \*  
000007R4 (00077)  
000021FF (00078)  
00000298 (00079)  
00000000 (00080) \*  
00000002 (00081)  
00000000 (00082) \*  
000007HA (00083)  
00000000 (00084) \*  
00000000 (00085) \*  
00000000 (00086)

RCTSAT = S6R6  
RCTSHA = S5R2  
RITSGO = S0  
CLKSGGCI = S792  
CSPUSNOS = S21FC  
DAYS = S794  
ERRORS = S1AFA  
FETSCHSZ = S79A  
FLGSCIP = S0  
FLGSGO = S4  
FLGSG1 = S5  
FLGSP1 = S11  
FLGSET = S20  
HS = 1  
LOADSAP = S1A7F  
LOADSAPI = S1RHF  
MSKSLRYT = SFF00  
MSKSRRYT = S00FF  
MSS = 0  
SHEFLMS5 = S7AF  
SVTS = S3R2  
SVTSUN1 = S3CF  
SYSFLGS = S1FFCF  
TFMS0 = S7R4  
TOPS = S21FE  
TOPSPTR = S2RR  
WS = S2  
ZPR0 = S7HA  
PIFCT



PAGE 3: SNAP-11 MAP-100 ARITH. MODULES - PRIC. BR1010.01 MAY 7, 1980

```
(00087) *
(00088) *
(00089) *
00288 00104678 (00090) *      ADDR  TOPSCTR(,1)      UPDATE TOP OF EXEC POINTER
(00091) *
(00092) *
(00093) *      DEFINE SYMBOLS FOR THIS MODULE
(00094) *
00000003 (00095) *      NM = 3
00004000 (00096) *      START = 54000
(00097) *      DEFINE START LOCATION FOR MODULE
(00098) *
(00099) *      FJFCT
```

(00100) \* SPECIAL NOTE: THESE MODULES ARE THE SAME AS RELEASE 2.1 EXCEPT  
 (00101) \* THE SPECIAL BINDING IN THE FFT'S AND SPECIAL FUNCTIONS HAVE BEEN  
 (00102) \* MODIFIED FOR RELEASE 3.0  
 (00103) \*  
 (00104) \*  
 (00105) \*  
 (00106) \*

ARITHMETIC FUNCTIONS DISPATCH TABLE

(00107) \*  
 (00108) \* EVERY ARRAY FUNCTION OCCUPIES ONE NODE IN THE TABLE.  
 (00109) \* THE NODE FOR ARRAY FCR #N WILL BE FOUND AT LOCATION  
 (00110) \* APTS = 3 \* NS \* (#N-128)

(00111) \*  
 (00112) \* EACH NODE CONSISTS OF 3 POINTERS AS FOLLOWS:  
 (00113) \* WORD 1 = ADDR APUSMODULE(R7,1)  
 (00114) \* WORD 2 = ADDR APSSMODULE(R7,1)  
 (00115) \* WORD 3 = ADDR CSPUSMODULE(.1,SSSV)

(00116) \* IF HIT 0 OF THE FCR CONTROL BITS = 0  
 (00117) \* NORMAL BINDING OCCURS AND CSPUSMODULE  
 (00118) \* IS EXECUTED FOR SPECIAL AP-DONE SUPPORT  
 (00119) \* ELSE CSPUSMODULE IS FOR SPECIAL BINDING  
 (00120) \* SSSV = SPECIAL SUPPORT SEMAPHORE VALUE  
 (00121) \*  
 (00122) \*  
 (00123) \*

(00124) \*  
 EJECT

STANDARD FORMAT FOR ALL ARRAY FCRL'S

FCR FORMAT (16 BIT WORD FORMAT SHOWN)

- (00125) \*
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- (00164) \*

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCR AND FUNCTION LIST FLAG (LSR)	
1	FCR NUMBER	CONTROL BITS
2	Y (0) AND (4)	SA
3	U (1) AND (9)	SR
4	V (2) AND (10)	SC
5	W (3) AND (11)	SD

OBJECT



U	00920	001F0000	F-0020R	ADDR	V1174RS(R7,1)	
	00922	001021FC	(00210)	ADDR	CSPUSNDS(.1,0)	
			(00211) *			
U	00924	001F0000	(00212)	ADDR	APUSMULT.(R7,1)	138 = VECTOR FLOAT (R-RIT)
	00926	001F0000	F-0020Q	ADDR	V1174CS(R7,1)	
	00928	001021FC	(00214)	ADDR	CSPUSNDS(.1,0)	
			(00215) *			
U	0092A	001F0000	F-00213	ADDR	VFIXS(R7,1)	139 = VECTOR FIX (H-HIT)
U	0092C	001F0000	F-00216	ADDR	V1174DS(R7,1)	
	0092F	001021FC	(00218)	ADDR	CSPUSNDS(.1,0)	
			(00219) *			
	00930	001F0000	(00220)	ADDR	APUSMULT.(R7,1)	140 = UNASSIGNED
	00932	001F0000	(00221)	ADDR	APSSMULT.(R7,1)	
	00934	001021FC	(00222)	ADDR	CSPUSNDS(.1,0)	
			(00223) *			
	00936	001F0000	(00224)	ADDR	APUSMULT.(R7,1)	141 = UNASSIGNED
	00938	001F0000	(00225)	ADDR	APSSMULT.(R7,1)	
	0093A	001021FC	(00226)	ADDR	CSPUSNDS(.1,0)	
			(00227) *			
	0093C	001F0000	(00228)	ADDR	APUSMULT.(R7,1)	142 = UNASSIGNED
	0093E	001F0000	(00229)	ADDR	APSSMULT.(R7,1)	
	00940	001021FC	(00230)	ADDR	CSPUSNDS(.1,0)	
			(00231) *			
	00942	001F0000	(00232)	ADDR	APUSMULT.(R7,1)	143 = UNASSIGNED
	00944	001F0000	(00233)	ADDR	APSSMULT.(R7,1)	
	00946	001021FC	(00234)	ADDR	CSPUSNDS(.1,0)	
			(00235) *			
	00948	001F4002	(00236)	ADDR	V54A1S(R7,1)	144 = VECTOR SCALAR MULT-ADD(1 VECTOR)
	0094A	001F4122	(00237)	ADDR	V1174S(R7,1)	
	0094C	001021FC	(00238)	ADDR	CSPUSNDS(.1,0)	
			(00239) *			
U	0094F	001F0000	F-00217	ADDR	V54A2S(R7,1)	145 = VECTOR SCALAR MULT-ADD(2 VECTOR)
U	00950	001F0000	F-00240	ADDR	V2134AS(R7,1)	
	00952	001021FC	(00242)	ADDR	CSPUSNDS(.1,0)	
			(00243) *			
U	00954	001F0000	F-00241	ADDR	VMULS(R7,1)	146 = VECTOR MULTIPLY
U	00956	001F0000	F-00244	ADDR	V2134AS(R7,1)	
	00958	001021FC	(00246)	ADDR	CSPUSNDS(.1,0)	
			(00247) *			
U	0095A	001F0000	F-00245	ADDR	VRCPS(R7,1)	147 = VECTOR RECIPROCAL
	0095C	001F4122	(00249)	ADDR	V1174S(R7,1)	
	0095E	001021FC	(00250)	ADDR	CSPUSNDS(.1,0)	
			(00251) *			
	00960	001F0000	(00252)	ADDR	APUSMULT.(R7,1)	148 = VECTOR DIVIDE

ADDRESS	OPERATION	OPERANDS	DESCRIPTION
00962	001F0000	(00253)	CURRENTLY NOT IMPLEMENTED
00964	001021FC	(00254)	
00966	001F0000	(00255) *	149 = UNASSIGNED
00968	001F0000	(00256)	
00970	001F0000	(00257)	150 = UNASSIGNED
00972	001021FC	(00258)	
00974	001F0000	(00259) *	151 = UNASSIGNED
00976	001F0000	(00260)	
00978	001F0000	(00261)	152 = VECTOR ABSOLUTE
00980	001021FC	(00262)	
00982	001F0000	(00263) *	153 = VECTOR ABSOLUTE SQUARED
00984	001F0000	(00264)	
00986	001F0000	(00265)	154 = VECTOR SQUARE
00988	001021FC	(00266)	
00990	001F0000	(00267) *	155 = VECTOR MAGNITUDE SQUARED
00992	001F0000	(00268)	
00994	001F0000	(00269)	156 = VECTOR MAGNITUDE
00996	001021FC	(00270)	
00998	001F0000	(00271) *	157 = VECTOR LOG
00999	001F0000	(00272)	
0099A	001F0000	(00273)	158 = UNASSIGNED
0099B	001021FC	(00274)	
0099C	001F0000	(00275) *	159 = UNASSIGNED
0099D	001F0000	(00276)	
0099E	001F0000	(00277)	
0099F	001F0000	(00278)	
009A0	001021FC	(00279) *	
009A2	001F0000	(00280)	
009A4	001F0000	(00281)	
009A6	001F0000	(00282)	
009A8	001F0000	(00283) *	
009AA	001F0000	(00284)	
009AC	001F0000	(00285)	
009AE	001021FC	(00286)	
009B0	001F0000	(00287) *	
009B2	001F0000	(00288)	
009B4	001F0000	(00289)	
009B6	001F0000	(00290)	
009B8	001F0000	(00291) *	
009BA	001F0000	(00292)	
009BC	001F0000	(00293)	
009BE	001021FC	(00294)	
009C0	001F0000	(00295) *	
009C2	001F0000	(00296)	

U	009A4	001F0000	(00297)	ADDR	APSSMULT.(R7,1)	
	009A6	001021FC	(00298)	ADDR	CSPUSNOS(,1,0)	
	009A8	001F0000	F-00289	ADDR	VCLIPS(R7,1)	160 = VECTOR CLIP
	009AA	001F4122	(00301)	ADDR	V1124S(R7,1)	
	009AC	001021FC	(00302)	ADDR	CSPUSNOS(,1,0)	
	009AE	001F0000	F-00300	ADDR	VTHRSHS(R7,1)	161 = VECTOR THRESHOLD
	009B0	001F4122	(00305)	ADDR	V1124S(R7,1)	
	009B2	001021FC	(00306)	ADDR	CSPUSNOS(,1,0)	
	009B4	001F0000	F-00304	ADDR	VLIMITS(R7,1)	162 = VECTOR LIMIT
	009B6	001F4122	(00309)	ADDR	V1124S(R7,1)	
	009B8	001021FC	(00310)	ADDR	CSPUSNOS(,1,0)	
	009BA	001F0000	F-00308	ADDR	VCOMP(SR7,1)	163 = VECTOR COMPARE
	009BC	001F0000	F-00312	ADDR	V2134S(R7,1)	
	009BE	001021FC	(00314)	ADDR	CSPUSNOS(,1,0)	
	009C0	001F0000	F-00315	ADDR	VTAGS(R7,1)	164 = VECTOR TAG
	009C2	001F4122	(00317)	ADDR	V1124S(R7,1)	
	009C4	001021FC	(00318)	ADDR	CSPUSNOS(,1,0)	
	009C6	001F0000	F-00319	ADDR	APUSNUL.(R7,1)	165 = UNASSIGNED
	009C8	001F0000	(00320)	ADDR	APSSNUL.(R7,1)	
	009CA	001021FC	(00321)	ADDR	CSPUSNOS(,1,0)	
	009CC	001F0000	(00323)	ADDR	APUSNUL.(R7,1)	166 = UNASSIGNED
	009CE	001F0000	(00324)	ADDR	APSSNUL.(R7,1)	
	009D0	001021FC	(00325)	ADDR	CSPUSNOS(,1,0)	
	009D2	001F0000	F-00326	ADDR	APUSNUL.(R7,1)	167 = UNASSIGNED
	009D4	001F0000	(00327)	ADDR	APSSNUL.(R7,1)	
	009D6	001021FC	(00328)	ADDR	CSPUSNOS(,1,0)	
	009D8	001F0000	F-00331	ADDR	SSUMS(R7,1)	168 = SCALAR SUM
	009DA	001F0000	F-00316	ADDR	S1011S(R7,1)	
	009DC	001021FC	(00332)	ADDR	CSPUSNOS(,1,0)	
	009DE	001F0000	F-00335	ADDR	SSUMMS(R7,1)	169 = SCALAR SUM OF ABSOLUTES
	009DF	001F0000	F-00333	ADDR	S1011S(R7,1)	
	009F0	001F0000	F-00336	ADDR	CSPUSNOS(,1,0)	
	009F2	001021FC	(00338)	ADDR	SSUMSUS(R7,1)	170 = SCALAR SUM OF SQUARES
	009F4	001F0000	F-00339	ADDR	CSPUSNOS(,1,0)	

U	009F6	001F0000	F-00340	ADDR	S1011S(R7,1)		
	009F8	001021FC	(00342)	ADDR	CSPUSNDS(,1,0)		
U	009FA	001F0000	F-00341	ADDR	SMAXS(R7,1)	171 = SCALAR MAXIMUM	
U	009FC	001F0000	F-00344	ADDR	S1002S(R7,1)		
	009FE	001021FC	(00346)	ADDR	CSPUSNDS(,1,0)		
U	009F0	001F0000	F-00345	ADDR	SMAYAS(R7,1)	172 = SCALAR MAXIMUM ABSOLUT	
U	009F2	001F0000	F-00348	ADDR	S1007S(R7,1)		
	009F4	001021FC	(00350)	ADDR	CSPUSNDS(,1,0)		
U	009F6	001F0000	F-00349	ADDR	SDITS(R7,1)	173 = SCALAR DOT PRODUCT	
U	009F8	001F0000	F-00352	ADDR	S2011S(R7,1)		
	009FA	001021FC	(00354)	ADDR	CSPUSNDS(,1,0)		
	009FC	001F0000	(00356)	ADDR	APUSNULI(R7,1)	174 = UNASSIGNED	
	009FE	001F0000	(00357)	ADDR	APSSNULI(R7,1)		
	00A00	001021FC	(00358)	ADDR	CSPUSNDS(,1,0)		
	00A02	001F0000	(00359)	ADDR	APUSNULI(R7,1)	175 = UNASSIGNED	
	00A04	001F0000	(00361)	ADDR	APSSNULI(R7,1)		
	00A06	001021FC	(00362)	ADDR	CSPUSNDS(,1,0)		
	00A08	001F0000	(00363)	ADDR	CVMULS(R7,1)	176 = COMPLEX VECTOR MULTIPLY	
U	00A0A	001F0000	F-00363	ADDR	C2124AS(R7,1)		
U	00A0C	001021FC	(00366)	ADDR	CSPUSNDS(,1,0)		
U	00A0E	001F0000	F-00365	ADDR	CCVMULS(R7,1)	177 = COMPLEX CONJ. VECTOR MULTIPLY	
U	00A10	001F0000	F-00369	ADDR	C2124AS(R7,1)		
	00A12	001021FC	(00370)	ADDR	CSPUSNDS(,1,0)		
	00A14	001F0000	F-00369	ADDR	CVRCPS(R7,1)	178 = COMPLEX VECTOR RECIPICAL	
U	00A16	001F0000	E-00372	ADDR	C1124AS(R7,1)		
	00A18	001021FC	(00374)	ADDR	CSPUSNDS(,1,0)		
U	00A1A	001F0000	F-00373	ADDR	CPHAPS(R7,1)	179 = COMPLEX POLAR CONVERSION	
U	00A1C	001F0000	F-00376	ADDR	C1124AS(R7,1)		
	00A1E	001021FC	(00378)	ADDR	CSPUSNDS(,1,0)		
	00A20	001F0000	(00379)	ADDR	APUSNULI(R7,1)	180 = COMPLEX RECTANGULAR CONVERSION	
	00A22	001F0000	(00381)	ADDR	APSSNULI(R7,1)	CURRENTLY NOT IMPLEMENTED	
	00A24	001021FC	(00382)	ADDR	CSPUSNDS(,1,0)		
	00A26	001F0000	(00383)	ADDR	APUSNULI(R7,1)	181 = UNASSIGNED	
			(00384)				



00A2R	001F0000	(003M5)	ADDR	APSSNULL(R7,1)	
00A2A	001021FC	(003M6)	ADDR	CSPUSNDS(,1,0)	
		(003M7) *			
U	00A2C	001F0000	F-00377	CXNULL(R7,1)	1R2 = COMPLEX EXPONENTIAL MULTIPLY
U	00A2F	001F0000	F-0038R	C2120S(R7,1)	
	00A30	001021FC	(00390)	CSPUSNDS(,1,0)	
		(00391) *			
U	00A32	001F0000	F-00389	CXNULL(R7,1)	1R3 = COMPLEX EXPON. MULTIPLY BY REAL
U	00A34	001F0000	F-00392	C1120S(R7,1)	
	00A36	001021FC	(00394)	CSPUSNDS(,1,0)	
		(00395) *			
U	00A3R	001F0000	F-00393	VPOLYS(R7,1)	1R4 = VECTOR POLYNOMIAL
U	00A3A	001F0000	F-00396	V2116AS(R7,1)	
U	00A3C	00100000	F-00397	SHMSVPLY(,1,0)	
		(00399) *			
U	00A3F	001F0000	F-0039H	VRAMPS(R7,1)	1R5 = VECTOR RAMP
	00A40	001F4122	(00401)	V1174S(R7,1)	
	00A42	001021FC	(00402)	CSPUSNDS(,1,0)	
		(00403) *			
	00A44	001F4070	(00404)	VCUSS(R7,1)	1R6 = VECTOR COSTINE
	00A46	001E41F6	(00405)	V2134RS(R7,1)	
	00A4R	001021FC	(00406)	CSPUSNDS(,1,0)	
		(00407) *			
U	00A4A	001F0000	F-00400	VPOWS(R7,1)	1R7 = VECTOR POWER
U	00A4C	001F0000	F-0040R	V1117AS(R7,1)	
	00A4F	001021FC	(00410)	CSPUSNDS(,1,0)	
		(00411) *			
	00A50	001F0000	(00412)	APUSNULL(R7,1)	1R8 = UNASSIGNED
	00A52	001E0000	(00413)	APSSNULL(R7,1)	
	00A54	001021FC	(00414)	CSPUSNDS(,1,0)	
		(00415) *			
	00A56	001F0000	(00416)	APUSNULL(R7,1)	1R9 = UNASSIGNED
	00A5R	001F0000	(00417)	APSSNULL(R7,1)	
	00A5A	001021FC	(0041R)	CSPUSNDS(,1,0)	
		(00419) *			
	00A5C	001F0000	(00420)	APUSNULL(R7,1)	1R9 = UNASSIGNED
	00A5F	001E0000	(00421)	APSSNULL(R7,1)	
	00A60	001021FC	(00422)	CSPUSNDS(,1,0)	
		(00423) *			
	00A62	001F0000	(00424)	APUSNULL(R7,1)	1R1 = UNASSIGNED
	00A64	001F0000	(00425)	APSSNULL(R7,1)	
	00A66	001021FC	(00426)	CSPUSNDS(,1,0)	
		(00427) *			
U	00A6R	001F0000	F-00409	DDIFFS(R7,1)	1R2 = DISCRETE DIFFERENTIATION

U	00A6A	001F0000	F-0042H	ADDR	01113S(R7,1)	
	00A6C	001021FC	(00430)	ADDR	CSPUSNOS(,1,0)	
			(00431) *			
U	00A6E	001F0000	F-00429	ADDR	FIETGS(R7,1)	
U	00A70	001F0000	F-00432	ADDR	01113S(R7,1)	
	00A72	001021FC	(00434)	ADDR	CSPUSNOS(,1,0)	
			(00435) *			
U	00A74	001F0000	F-00433	ADDR	DF1127S(R7,1)	
U	00A76	001F0000	F-00436	ADDR	01116S(R7,1)	
	00A78	001021FC	(00438)	ADDR	CSPUSNOS(,1,0)	
			(00439) *			
U	00A7A	001F0000	F-00437	ADDR	DCVMS(R7,1)	
U	00A7C	001F0000	F-00440	ADDR	D2116S(R7,1)	
U	00A7E	00100000	F-00441	ADDR	SAMSDCVW(,1,0)	
			(00443) *			
U	00A80	001F0000	(00444)	ADDR	APUSNUI.L.(R7,1)	
U	00A82	001F0000	(00445)	ADDR	APSSNUI.L.(R7,1)	
U	00A84	001021FC	(00446)	ADDR	CSPUSNOS(,1,0)	
			(00447) *			
U	00A86	001F0000	(00448)	ADDR	APUSNUI.L.(R7,1)	
U	00A88	001F0000	(00449)	ADDR	APSSNUI.L.(R7,1)	
U	00A8A	001021FC	(00450)	ADDR	CSPUSNOS(,1,0)	
			(00451) *			
U	00A8C	001F0000	(00452)	ADDR	APUSNUI.L.(R7,1)	
U	00A8E	001F0000	(00453)	ADDR	APSSNUI.L.(R7,1)	
U	00A90	001021FC	(00454)	ADDR	CSPUSNOS(,1,0)	
			(00455) *			
U	00A92	001F0000	(00456)	ADDR	APUSNUI.L.(R7,1)	
U	00A94	001F0000	(00457)	ADDR	APSSNUI.L.(R7,1)	
U	00A96	001021FC	(00458)	ADDR	CSPUSNOS(,1,0)	
			(00459) *			
U	00A98	001F0000	F-00442	ADDR	FSCRS(R7,1)	
U	00A9A	001F0000	F-00460	ADDR	F11CS(R7,1)	
U	00A9C	001021FC	(00462)	ADDR	CSPUSNOS(,1,0)	
			(00463) *			
U	00A9E	001F0000	F-00461	ADDR	FRX4S(R7,1)	
U	00AA0	001F0000	F-00464	ADDR	F21HS(R7,1)	
U	00AA2	001021FC	(00466)	ADDR	CSPUSNOS(,1,0)	
			(00467) *			
U	00AA4	001F0000	F-00465	ADDR	FFOSS(R7,1)	
U	00AA6	001F0000	F-00468	ADDR	F11AS(R7,1)	
U	00AA8	001021FC	(00470)	ADDR	CSPUSNOS(,1,0)	
			(00471) *			
U	00AAA	001F0000	F-00469	ADDR	FSC4S(R7,1)	

193 = DISCRETE INTEGRATION

194 = DISCRETE FILTER(2-POLYFS, 2-ZEROS)

195 = DISCRETE CONVOLUTION MULTIPLY

196 = UNASSIGNED

197 = UNASSIGNED

198 = UNASSIGNED

199 = UNASSIGNED

200 = FOURIER SCRAMBLE

201 = FOURIER RADIX-4 TRANSFORM

202 = FOURIER EVEN-ODD SEPERATE

203 = FOURIER SCRAMBLE - EVEN PWRS OF 2

U	00AAC	001F0000	F=00472	ADDR	F11CS(R7,1)	
	00AAE	001021FC	(00474)	ADDR	CSPUSNDS(,1,0)	
			(00475) *			204 = UNASSIGNED
	00AR0	001F0000	(00476)	ADDR	APUSNULL(R7,1)	
	00AR2	001F0000	(00477)	ADDR	AI SMULT(R7,1)	
	00AR4	001021FC	(00478)	ADDR	CSPUSNDS(,1,0)	
			(00479) *			205 = UNASSIGNED
	00AR6	001F0000	(00480)	ADDR	APUSMULT(R7,1)	
	00AR8	001F0000	(00481)	ADDR	APSSMULT(R7,1)	
	00AHA	001021FC	(00482)	ADDR	CSPUSNDS(,1,0)	
			(00483) *			206 = UNASSIGNED
	00AHC	001F0000	(00484)	ADDR	APUSMULT(R7,1)	
	00AHF	001F0000	(00485)	ADDR	APSSMULT(R7,1)	
	00AC0	001021FC	(00486)	ADDR	CSPUSNDS(,1,0)	
			(00487) *			207 = UNASSIGNED
	00AC2	001F0000	(00488)	ADDR	APUSMULT(R7,1)	
	00AC4	001F0000	(00489)	ADDR	APSSMULT(R7,1)	
	00AC6	001021FC	(00490)	ADDR	CSPUSNDS(,1,0)	
			(00491) *			208 = FORWARD FFT(COMPLEX-COMPLEX)
	00AC8	001F0000	(00492)	ADDR	APUSMULT(R7,1)	
	00ACA	001F0000	(00493)	ADDR	APSSMULT(R7,1)	
	00ACC	00100000	F=00473	ADDR	FF3SSSM(,1,0)	
			(00495) *			209 = INVERSE FFT(COMPLEX-COMPLEX)
	00ACF	001F0000	(00496)	ADDR	APUSMULT(R7,1)	
	00AD0	001F0000	(00497)	ADDR	APSSMULT(R7,1)	
	00AD2	00100000	F=00494	ADDR	IFF3SSSM(,1,0)	
			(00499) *			210 = FORWARD FFT-REAL-COMPLEX
	00AD4	001F0000	(00500)	ADDR	APUSMULT(R7,1)	
	00AD6	001F0000	(00501)	ADDR	APSSMULT(R7,1)	
	00AD8	00100000	F=00498	ADDR	RF3SSSM(,1,0)	
			(00503) *			211 = INVERSE FFT-REAL-COMPLEX
	00ADA	001F0000	(00504)	ADDR	APUSMULT(R7,1)	
	00ADC	001F0000	(00505)	ADDR	APSSMULT(R7,1)	
	00ADE	00100000	F=00502	ADDR	TRF3SSSM(,1,0)	
			(00507) *			212 = UNASSIGNED
	00AE0	001F0000	(00508)	ADDR	APUSMULT(R7,1)	
	00AE2	001F0000	(00509)	ADDR	APSSMULT(R7,1)	
	00AF4	001021FC	(00510)	ADDR	CSPUSNDS(,1,0)	
			(00511) *			213 = UNASSIGNED
	00AE6	001F0000	(00512)	ADDR	APUSMULT(R7,1)	
	00AER	001F0000	(00513)	ADDR	APSSMULT(R7,1)	
	00AEA	001021FC	(00514)	ADDR	CSPUSNDS(,1,0)	
			(00515) *			214 = UNASSIGNED
	00AEC	001F0000	(00516)	ADDR	APUSMULT(R7,1)	

00AF6	001F0000	(00517)	ADDR	APSSMULL(R7,1)	215 = UNASSIGNED
00AF0	001021FC	(00518)	ADDR	CSPUSMOS(,1,0)	
00AF2	001F0000	(00520)	ADDR	APIUSMULL(R7,1)	216 = UNASSIGNED
00AF4	001F0000	(00521)	ADDR	APSSMULL(R7,1)	
00AF6	001021FC	(00522)	ADDR	CSPUSMOS(,1,0)	217 = UNASSIGNED
00AF8	001F0000	(00523)	ADDR	APIUSMULL(R7,1)	
00AFA	001F0000	(00524)	ADDR	APSSMULL(R7,1)	218 = UNASSIGNED
00AFB	001021FC	(00526)	ADDR	CSPUSMOS(,1,0)	
00AFE	001F0000	(00528)	ADDR	APIUSMULL(R7,1)	219 = UNASSIGNED
00B00	001F0000	(00529)	ADDR	APSSMULL(R7,1)	
00B02	001021FC	(00530)	ADDR	CSPUSMOS(,1,0)	220 = UNASSIGNED
00B04	001F0000	(00531)	ADDR	APIUSMULL(R7,1)	
00B06	001F0000	(00532)	ADDR	APSSMULL(R7,1)	221 = UNASSIGNED
00B08	001021FC	(00534)	ADDR	CSPUSMOS(,1,0)	
00B0A	001F0000	(00535)	ADDR	APIUSMULL(R7,1)	222 = UNASSIGNED
00B0C	001F0000	(00536)	ADDR	APSSMULL(R7,1)	
00B0E	001021FC	(00538)	ADDR	CSPUSMOS(,1,0)	223 = UNASSIGNED
00B10	001F0000	(00539)	ADDR	APIUSMULL(R7,1)	
00B12	001F0000	(00540)	ADDR	APSSMULL(R7,1)	224 = UNASSIGNED
00B14	001021FC	(00542)	ADDR	CSPUSMOS(,1,0)	
00B16	001F0000	(00543)	ADDR	APIUSMULL(R7,1)	225 = UNASSIGNED
00B18	001F0000	(00544)	ADDR	APSSMULL(R7,1)	
00B1A	001021FC	(00546)	ADDR	CSPUSMOS(,1,0)	
00B1C	001F0000	(00547)	ADDR	APIUSMULL(R7,1)	226 = UNASSIGNED
00B1E	001F0000	(00548)	ADDR	APSSMULL(R7,1)	
00B20	001021FC	(00550)	ADDR	CSPUSMOS(,1,0)	227 = UNASSIGNED
00B22	001F0000	(00551)	ADDR	APIUSMULL(R7,1)	
00B24	001F0000	(00552)	ADDR	APSSMULL(R7,1)	228 = UNASSIGNED
00B26	001021FC	(00554)	ADDR	CSPUSMOS(,1,0)	
00B28	001F0000	(00555)	ADDR	APIUSMULL(R7,1)	229 = UNASSIGNED
00B2A	001F0000	(00556)	ADDR	APSSMULL(R7,1)	
00B2C	001021FC	(00558)	ADDR	CSPUSMOS(,1,0)	230 = UNASSIGNED
00B2E	001F0000	(00559)	ADDR	APIUSMULL(R7,1)	

00H 0	001F0000	(00561)	ADDR	APSSNULL(R7,1)	
00H 12	001021FC	(00562)	ADDR	CSPUSNDS(,1,0)	
00H 14	001F0000	(00563) *	ADDR	APUSNULL(R7,1)	226 = UNASSIGNED
00H 16	001F0000	(00564)	ADDR	APSSNULL(R7,1)	
00H 18	001021FC	(00565)	ADDR	CSPUSNDS(,1,0)	
00H 1A	001F0000	(00566)	ADDR	APUSNULL(R7,1)	
00H 1C	001F0000	(00567)	ADDR	APSSNULL(R7,1)	227 = UNASSIGNED
00H 1E	001021FC	(00568)	ADDR	CSPUSNDS(,1,0)	
00H 20	001F0000	(00569)	ADDR	APUSNULL(R7,1)	
00H 22	001021FC	(00570)	ADDR	APSSNULL(R7,1)	228 = UNASSIGNED
00H 24	001F0000	(00571) *	ADDR	CSPUSNDS(,1,0)	
00H 26	001F0000	(00572)	ADDR	APUSNULL(R7,1)	
00H 28	001F0000	(00573)	ADDR	APSSNULL(R7,1)	229 = UNASSIGNED
00H 2A	001021FC	(00574)	ADDR	CSPUSNDS(,1,0)	
00H 2C	001F0000	(00575) *	ADDR	APUSNULL(R7,1)	
00H 2E	001F0000	(00576)	ADDR	APSSNULL(R7,1)	230 = UNASSIGNED
00H 30	001F0000	(00577)	ADDR	APUSNULL(R7,1)	
00H 32	001021FC	(00578)	ADDR	CSPUSNDS(,1,0)	
00H 34	001F0000	(00579) *	ADDR	APUSNULL(R7,1)	
00H 36	001F0000	(00580)	ADDR	APSSNULL(R7,1)	231 = UNASSIGNED
00H 38	001F0000	(00581)	ADDR	APUSNULL(R7,1)	
00H 3A	001021FC	(00582)	ADDR	CSPUSNDS(,1,0)	
00H 3C	001F0000	(00583) *	ADDR	APUSNULL(R7,1)	
00H 3E	001F0000	(00584)	ADDR	APSSNULL(R7,1)	232 = UNASSIGNED
00H 40	001F0000	(00585)	ADDR	APUSNULL(R7,1)	
00H 42	001021FC	(00586)	ADDR	CSPUSNDS(,1,0)	
00H 44	001F0000	(00587) *	ADDR	APUSNULL(R7,1)	
00H 46	001F0000	(00588)	ADDR	APSSNULL(R7,1)	233 = UNASSIGNED
00H 48	001F0000	(00589)	ADDR	APUSNULL(R7,1)	
00H 4A	001021FC	(00590)	ADDR	CSPUSNDS(,1,0)	
00H 4C	001F0000	(00591) *	ADDR	APUSNULL(R7,1)	
00H 4E	001F0000	(00592)	ADDR	APSSNULL(R7,1)	234 = UNASSIGNED
00H 50	001F0000	(00593)	ADDR	APUSNULL(R7,1)	
00H 52	001021FC	(00594)	ADDR	CSPUSNDS(,1,0)	
00H 54	001F0000	(00595) *	ADDR	APUSNULL(R7,1)	
00H 56	001F0000	(00596)	ADDR	APSSNULL(R7,1)	235 = UNASSIGNED
00H 58	001F0000	(00597)	ADDR	APUSNULL(R7,1)	
00H 5A	001021FC	(00598)	ADDR	CSPUSNDS(,1,0)	
00H 5C	001F0000	(00599)	ADDR	APUSNULL(R7,1)	
00H 5E	001F0000	(00600)	ADDR	APSSNULL(R7,1)	236 = UNASSIGNED
00H 60	001F0000	(00601)	ADDR	APUSNULL(R7,1)	
00H 62	001021FC	(00602)	ADDR	CSPUSNDS(,1,0)	
00H 64	001F0000	(00603) *	ADDR	APUSNULL(R7,1)	
00H 66	001F0000	(00604)	ADDR	APSSNULL(R7,1)	

00R72	001F0000	(00605)	ADDR	APSSMULT.(R7,1)	
00R74	001021FC	(00606)	ADDR	CSPUSNUS(,1,0)	
00R76	001F0000	(00608)	ADDR	APUSMULT.(R7,1)	237 = UNASSIGNED
00R78	001F0000	(00609)	ADDR	APSSMULT.(R7,1)	
00R7A	001021FC	(00610)	ADDR	CSPUSNUS(,1,0)	
00R7C	001F0000	(00612)	ADDR	APUSMULT.(R7,1)	238 = UNASSIGNED
00R7E	001F0000	(00613)	ADDR	APSSMULT.(R7,1)	
00R80	001021FC	(00614)	ADDR	CSPUSNUS(,1,0)	
00R82	001F0000	(00615)	ADDR	APUSMULT.(R7,1)	239 = UNASSIGNED
00R84	001F0000	(00617)	ADDR	APSSMULT.(R7,1)	
00R86	001021FC	(00618)	ADDR	CSPUSNUS(,1,0)	
00R88	001F0000	(00619)	ADDR	APUSMULT.(R7,1)	240 = UNASSIGNED
00R8A	001F0000	(00621)	ADDR	APSSMULT.(R7,1)	
00R8C	001021FC	(00622)	ADDR	CSPUSNUS(,1,0)	
00R8E	001F0000	(00623)	ADDR	APUSMULT.(R7,1)	241 = UNASSIGNED
00R90	001F0000	(00624)	ADDR	APSSMULT.(R7,1)	
00R92	001021FC	(00625)	ADDR	CSPUSNUS(,1,0)	
00R94	001F0000	(00627)	ADDR	APUSMULT.(R7,1)	242 = UNASSIGNED
00R96	001F0000	(00628)	ADDR	APSSMULT.(R7,1)	
00R98	001021FC	(00630)	ADDR	CSPUSNUS(,1,0)	
00R9A	001F0000	(00631)	ADDR	APUSMULT.(R7,1)	243 = UNASSIGNED
00R9C	001F0000	(00632)	ADDR	APSSMULT.(R7,1)	
00R9E	001021FC	(00633)	ADDR	CSPUSNUS(,1,0)	
00RA0	001F0000	(00635)	ADDR	APUSMULT.(R7,1)	244 = UNASSIGNED
00RA2	001F0000	(00636)	ADDR	APSSMULT.(R7,1)	
00RA4	001021FC	(00637)	ADDR	CSPUSNUS(,1,0)	
00RA6	001F0000	(00639)	ADDR	APUSMULT.(R7,1)	245 = UNASSIGNED
00RA8	001F0000	(00640)	ADDR	APSSMULT.(R7,1)	
00RAA	001021FC	(00641)	ADDR	CSPUSNUS(,1,0)	
00RAC	001F0000	(00643)	ADDR	APUSMULT.(R7,1)	246 = UNASSIGNED
00RAE	001F0000	(00644)	ADDR	APSSMULT.(R7,1)	
00RAH	001021FC	(00645)	ADDR	CSPUSNUS(,1,0)	
00RAJ	001F0000	(00646)	ADDR	APUSMULT.(R7,1)	247 = UNASSIGNED
00RAK	001021FC	(00647)	ADDR	CSPUSNUS(,1,0)	
00RAL	001F0000	(00648)	ADDR	APUSMULT.(R7,1)	

00RH4 001F0000 (00649)	ADDR	APSSNULL(R7,1)	
00RH6 001021FC (00650)	ADDR	CSPUSNDS(,1,0)	
00RH8 001F0000 (00651) *			248 = UNASSIGNED
00RRA 001F0000 (00652)	ADDR	APIUSNULL(R7,1)	
00RRA 001F0000 (00653)	ADDR	APSSNULL(R7,1)	
00RRC 001021FC (00654)	ADDR	CSPUSNDS(,1,0)	
00RRE 001F0000 (00655) *			249 = UNASSIGNED
00RHF 001F0000 (00656)	ADDR	APIUSNULL(R7,1)	
00RHC 001F0000 (00657)	ADDR	APSSNULL(R7,1)	
00RC2 001021FC (00658)	ADDR	CSPUSNDS(,1,0)	
00RC4 001F0000 (00659) *			250 = UNASSIGNED
00RC6 001F0000 (00660)	ADDR	APIUSNULL(R7,1)	
00RC8 001F0000 (00661)	ADDR	APSSNULL(R7,1)	
00RCR 001021FC (00662) *	ADDR	CSPUSNDS(,1,0)	
00RCA 001F0000 (00663)	ADDR	APIUSNULL(R7,1)	
00RCC 001F0000 (00664)	ADDR	APSSNULL(R7,1)	
00RCF 001021FC (00665)	ADDR	CSPUSNDS(,1,0)	
00RCE 001F0000 (00666) *			251 = UNASSIGNED
00RCA 001F0000 (00667)	ADDR	APIUSNULL(R7,1)	
00RD0 001F0000 (00668)	ADDR	APSSNULL(R7,1)	
00RD2 001F0000 (00669)	ADDR	CSPUSNDS(,1,0)	
00RD4 001021FC (00670) *	ADDR		252 = UNASSIGNED
00RD6 001F0000 (00671)	ADDR	APIUSNULL(R7,1)	
00RD8 001F0000 (00672)	ADDR	APSSNULL(R7,1)	
00RDA 001021FC (00673) *	ADDR	CSPUSNDS(,1,0)	
00RDB 001F0000 (00674)	ADDR	APIUSNULL(R7,1)	
00RDC 001F0000 (00675) *	ADDR	APSSNULL(R7,1)	
00RDE 001F0000 (00676)	ADDR	CSPUSNDS(,1,0)	
00RDF 001F0000 (00677)	ADDR	APIUSNULL(R7,1)	
00RDH 001021FC (00678) *	ADDR	APSSNULL(R7,1)	
00RDI 001F0000 (00679)	ADDR	CSPUSNDS(,1,0)	
00RDE 001F0000 (00680)	ADDR	APIUSNULL(R7,1)	
00RDF 001F0000 (00681)	ADDR	APSSNULL(R7,1)	
00RDH 001021FC (00682) *	ADDR	CSPUSNDS(,1,0)	
00RDI 001F0000 (00683) *			253 = UNASSIGNED
00RDE 001F0000 (00684)	ADDR	APIUSNULL(R7,1)	
00RDF 001F0000 (00685)	ADDR	APSSNULL(R7,1)	
00RDH 001021FC (00686)	ADDR	CSPUSNDS(,1,0)	
00RDI 001F0000 (00687) *			254 = UNASSIGNED
00RDE 001F0000 (00688)	ADDR	APIUSNULL(R7,1)	
00RDF 001F0000 (00689)	ADDR	APSSNULL(R7,1)	
00RDH 001021FC (00690) *	ADDR	CSPUSNDS(,1,0)	
00RDI 001F0000 (00691)	ADDR	APIUSNULL(R7,1)	
00RDE 001F0000 (00692)	ADDR	APSSNULL(R7,1)	
00RDF 001021FC (00693) *	ADDR	CSPUSNDS(,1,0)	
00RDI 001F0000 (00694)	ADDR	APIUSNULL(R7,1)	
00RDE 001F0000 (00695)	ADDR	APSSNULL(R7,1)	
00RDF 001021FC (00696)	ADDR	CSPUSNDS(,1,0)	
00RDI 001F0000 (00697) *			255 = UNASSIGNED
00RDE 001F0000 (00698)	ADDR	APIUSNULL(R7,1)	
00RDF 001F0000 (00699)	ADDR	APSSNULL(R7,1)	
00RDH 001021FC (00700) *	ADDR	CSPUSNDS(,1,0)	

FCH #143

000000942 #1=AFDTSORC+3\*2\*(143-12R)

ADDR VMVS(R7,1)

ADDR V1124S(R7,1)

ADDR CSPUSNDS(,1,0)

PAGE 18: SNAP-11 MAP-300 ARITH. MODULES - PROC. #430101.03 MAY 7, 1980  
DISPATCH TABLE ENTRIES FOR FFTN, FFTNR, FFTNI, FFTNIR

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(00689) DISPATCH TABLE ENTRIES FOR FFTN, FFTNR, FFTNI, FFTNIR
(00690) ?
(00691) ? FFTN AND FFTNI HAVE FULL BINDING
(00692) ? *TNR AND FFTNIR ONLY BIND YRASE AND URASE
(00693) ?
      FFTNS=204
      FFTNHS=205
      FFTNIS=206
      FFTNIRS=207
(00694) ?
(00695) ?
(00696) ?
(00697) ?
(00698) ?
(00699) ?
(00700) ?
(00701) ? #I=AFDTSORG+3*#S*(FFTHS-12R)          FCH #204
      ADDR CSM2S(R7, 1)
(00702) ? ADDR CSM2S(R7, 1)
(00703) ? ADDR CSM2S(R7, 1)
(00704) ? ADDR SRSFFT(, 1, 0)
(00705) ?
(00706) ? #I=AFDTSORG+3*#S*(FFTHS-12R)          FCH #205
      ADDR CSM2S(R7, 1)
(00707) ? ADDR CSM2S(R7, 1)
(00708) ? ADDR CSM2S(R7, 1)
(00709) ? ADDR SRSFFT(, 1, 0)
(00710) ?
(00711) ? #I=AFDTSORG+3*#S*(FFTHS-12R)          FCH #206
      ADDR ISM2S(R7, 1)
(00712) ? ADDR ISM2S(R7, 1)
(00713) ? ADDR CSM2S(R7, 1)
(00714) ? ADDR SRSFFT(, 1, 0)
(00715) ?
(00716) ? #I=AFDTSORG+3*#S*(FFTHS-12R)          FCH #207
      ADDR ISM2S(R7, 1)
(00717) ? ADDR ISM2S(R7, 1)
(00718) ? ADDR CSM2S(R7, 1)
(00719) ? ADDR SRSFFT(, 1, 0)
(00720) *
(00721) *
(00722) *
00004000 (00722) *

```

#I. = START SFT PC TO START LOCATION FOR MODULE



APU3-VSMA1 VECTOR SCALAR MULTIPLY-ADD  
 1 VECTOR BINDS TO APS3-V1124

PC	OPCODE	OPERANDS	START ON WORD BOUNDARY
PC	OPCODE	OPERANDS	START ADDRESS
PC	OPCODE	OPERANDS	SIZE
PC	OPCODE	OPERANDS	START OF APU MODULE
PC	OPCODE	OPERANDS	SET START ADDRESS
04000	0000		
04001	000+		
0401E			
04002	0HEFF0REF		
04004	0HE70RF7		
04006	0HEF0000		
04008	00000REF		
0400A	84718471		
0400C	473C473C		
0400E	0HE90000		
04010	00000REF9		
04012	84F084F0		
04014	471C471C		
04016	901C0002		
04018	20320000		
0401A	00000000		
0401C	10000000		
0401E	0000000E		
0401F			
04020	0HEF0000		
04022	0HEF0000		
04024	00000REF		
04026	84718471		
04028	473C473C		
0402A	0HE90000		
0402C	00000REF9		
0402E	84F084F0		
04030	471C471C		
04032	901C0002		
04034	20320000		
04036	00000000		
04038	0HEFF0REF		
0403A	0HE70RF7		
0403C	0HEF0000		
0403E	00000REF		
04040	84718471		
04042	473C473C		
04044	0HE90000		
04046	00000REF9		
04048	84F084F0		
0404A	471C471C		
0404C	901C0002		
0404E	20320000		
04050	00000000		
04052	0HEFF0REF		
04054	0HE70RF7		
04056	0HEF0000		
04058	00000REF		
0405A	84718471		
0405C	473C473C		
0405E	0HE90000		
04060	00000REF9		
04062	84F084F0		
04064	471C471C		
04066	901C0002		
04068	20320000		
0406A	00000000		
0406C	0HEFF0REF		
0406E	0HE70RF7		
04070	0HEF0000		
04072	00000REF		
04074	84718471		
04076	473C473C		
04078	0HE90000		
0407A	00000REF9		
0407C	84F084F0		
0407E	471C471C		
04080	901C0002		
04082	20320000		
04084	00000000		
04086	0HEFF0REF		
04088	0HE70RF7		
0408A	0HEF0000		
0408C	00000REF		
0408E	84718471		
04090	473C473C		
04092	0HE90000		
04094	00000REF9		
04096	84F084F0		
04098	471C471C		
0409A	901C0002		
0409C	20320000		
0409E	00000000		
040A0	0HEFF0REF		
040A2	0HE70RF7		
040A4	0HEF0000		
040A6	00000REF		
040A8	84718471		
040AA	473C473C		
040AC	0HE90000		
040AE	00000REF9		
040B0	84F084F0		
040B2	471C471C		
040B4	901C0002		
040B6	20320000		
040B8	00000000		
040BA	0HEFF0REF		
040BC	0HE70RF7		
040BE	0HEF0000		
040C0	00000REF		
040C2	84718471		
040C4	473C473C		
040C6	0HE90000		
040C8	00000REF9		
040CA	84F084F0		
040CC	471C471C		
040CE	901C0002		
040D0	20320000		
040D2	00000000		
040D4	0HEFF0REF		
040D6	0HE70RF7		
040D8	0HEF0000		
040DA	00000REF		
040DC	84718471		
040DE	473C473C		
040E0	0HE90000		
040E2	00000REF9		
040E4	84F084F0		
040E6	471C471C		
040E8	901C0002		
040EA	20320000		
040EC	00000000		
040EE	0HEFF0REF		
040F0	0HE70RF7		
040F2	0HEF0000		
040F4	00000REF		
040F6	84718471		
040F8	473C473C		
040FA	0HE90000		
040FC	00000REF9		
040FE	84F084F0		
04100	471C471C		
04102	901C0002		
04104	20320000		
04106	00000000		
04108	0HEFF0REF		
0410A	0HE70RF7		
0410C	0HEF0000		
0410E	00000REF		
04110	84718471		
04112	473C473C		
04114	0HE90000		
04116	00000REF9		
04118	84F084F0		
0411A	471C471C		
0411C	901C0002		
0411E	20320000		
04120	00000000		
04122	0HEFF0REF		
04124	0HE70RF7		
04126	0HEF0000		
04128	00000REF		
0412A	84718471		
0412C	473C473C		
0412E	0HE90000		
04130	00000REF9		
04132	84F084F0		
04134	471C471C		
04136	901C0002		
04138	20320000		
0413A	00000000		
0413C	0HEFF0REF		
0413E	0HE70RF7		
04140	0HEF0000		
04142	00000REF		
04144	84718471		
04146	473C473C		
04148	0HE90000		
0414A	00000REF9		
0414C	84F084F0		
0414E	471C471C		
04150	901C0002		
04152	20320000		
04154	00000000		
04156	0HEFF0REF		
04158	0HE70RF7		
0415A	0HEF0000		
0415C	00000REF		
0415E	84718471		
04160	473C473C		
04162	0HE90000		
04164	00000REF9		
04166	84F084F0		
04168	471C471C		
0416A	901C0002		
0416C	20320000		
0416E	00000000		
04170	0HEFF0REF		
04172	0HE70RF7		
04174	0HEF0000		
04176	00000REF		
04178	84718471		
0417A	473C473C		
0417C	0HE90000		
0417E	00000REF9		
04180	84F084F0		
04182	471C471C		
04184	901C0002		
04186	20320000		
04188	00000000		
0418A	0HEFF0REF		
0418C	0HE70RF7		
0418E	0HEF0000		
04190	00000REF		
04192	84718471		
04194	473C473C		
04196	0HE90000		
04198	00000REF9		
0419A	84F084F0		
0419C	471C471C		
0419E	901C0002		
041A0	20320000		
041A2	00000000		
041A4	0HEFF0REF		
041A6	0HE70RF7		
041A8	0HEF0000		
041AA	00000REF		
041AC	84718471		
041AE	473C473C		
041B0	0HE90000		
041B2	00000REF9		
041B4	84F084F0		
041B6	471C471C		
041B8	901C0002		
041BA	20320000		
041BC	00000000		
041BE	0HEFF0REF		
041C0	0HE70RF7		
041C2	0HEF0000		
041C4	00000REF		
041C6	84718471		
041C8	473C473C		
041CA	0HE90000		
041CC	00000REF9		
041CE	84F084F0		
041D0	471C471C		
041D2	901C0002		
041D4	20320000		
041D6	00000000		
041D8	0HEFF0REF		
041DA	0HE70RF7		
041DC	0HEF0000		
041DE	00000REF		
041E0	84718471		
041E2	473C473C		
041E4	0HE90000		
041E6	00000REF9		
041E8	84F084F0		
041EA	471C471C		
041EC	901C0002		
041EE	20320000		
041F0	00000000		
041F2	0HEFF0REF		
041F4	0HE70RF7		
041F6	0HEF0000		
041F8	00000REF		
041FA	84718471		
041FC	473C473C		
041FE	0HE90000		
04200	00000REF9		
04202	84F084F0		
04204	471C471C		
04206	901C0002		
04208	20320000		
0420A	00000000		
0420C	0HEFF0REF		
0420E	0HE70RF7		
04210	0HEF0000		
04212	00000REF		
04214	84718471		
04216	473C473C		
04218	0HE90000		
0421A	00000REF9		
0421C	84F084F0		
0421E	471C471C		
04220	901C0002		
04222	20320000		
04224	00000000		
04226	0HEFF0REF		
04228	0HE70RF7		
0422A	0HEF0000		
0422C	00000REF		
0422E	84718471		
04230	473C473C		
04232	0HE90000		
04234	00000REF9		
04236	84F084F0		
04238	471C471C		
0423A	901C0002		
0423C	20320000		
0423E	00000000		
04240	0HEFF0REF		



```

(00R05) *
(00R06) *CONTINUE WITH A7=20 WHERE -1<2H<0
(00R07) *
A11 04042 08F608F6 MOV(10A,A6) A6=C
A12 04044 02C002C0 R(A6) W=C
(00R10) *
(00R11) * DECREMENTATION LOOP ON C
(00R12) *
A13 04046 08960896 MOV(R,A6) A6=C
A14 04048 911F0017 JUMPS(VCS4,T1) GO TO #4 OF C<<
(00R15) *
A15 0404A 40C040C0 SUB(A6,A5) R=C-1
A16 0404C 10000013 JUMP(VCS3) REPEAT DECR PROCESS
(00R19) * INCREMENTATION LOOP ON C
(00R20) *
A17 0404E 45D645D6 MOV(A6),ADD(A6,A5) A6=C, R=C+1
A18 04050 08R008R0 MOV(R,MUL) COMPLETE T1 FLAG UPDATE
A19 04052 911F0017 JUMPS(VCS4,T1) REPEAT IF C+1 NEGATIVE
(00R24) *
(00R25) * CONTINUE FOR A6=C, WHERE -1<C<<0
(00R26) *
A1A 04054 4FC048C0 SUB(A6,A7)\SUB(A6,A0) R=-1<C-2H<1;R=-1<C-R<1
A1B 04056 51D651D6 MOV(A6),ADD(A6,A5) A6=C-2H,C-H;R=0<C-2R<1,R=0<C-R<1
A1C 0405H 08960896 MOV(R,A6) A6=0<C-2H<1,0<C-R<1;
(00R31) *
(00R32) * END OF INITIALIZATION*
(00R33) *
(00R34) *
(00R35) * INPUT/OUTPUT PARAMETERS AND COMPUTE ARGUMENT OF COSINE
(00R36) * ENTER LOOP WITH K=0
(00R37) *
A1D 0405A 47C847C8 MOV(M0),ADD(A6,A7) M0=R23'; R1=(4K+1)R+C
A1E 0405C 64328432 MOV(A2),MUL(M0,M5) A2=P12'; P13'=R23'+U(4K-4), R23'+U(4K-3)
A1F 0405E 08F08FF MOV(10A,M7) M7=A
A20 04060 08F0000 MOV(A0),ADD(T(A0,A5) A0=R1, R2=R1+T(1)=0<-(4KK)R+C),((4K+1)R+C)<1
A21 04062 08F0000 NOP \ MOV(10A,M3) M3=V(2K)
A22 04064 00008F8 MOV(A0),ADD(A1,A2) M3=V(4K+1)
A23 04066 47304230 MOV(M5),MUL(M1,M7) A0=R2,R24'=C0+P12'
A24 04068 85F085F0 MOV(M4),ADD(A0,A7) M5=P13'; P1=V(4K)*A,V(4K+1)*A
A25 0406A 470C470C MOV(A6),ADD(A0,A7) M4=R24', R3=R2+R(=(4K+2)R+C),((4K+3)R+C)
A26 0406C 51D651D6 MOV(A6),ADD(T(A6,A5) A6=R3, R4=R3+T(1)=0<-(4K+2)H+C),((4K+3)R+C)<1
A27 0406E 84918491 MOV(A1),MUL(M1,M4) A1=P1; P14'=U(4K+2)*R24'+U(4K+3)*R24'

```

```

A2H 04070 4C164C16 (00R49)
A29 04072 08F80000 (00R50)
A2A 04074 000008FH (00H51)
A2B 04076 41164110 (00H52)
A2C 04078 85985F9 (00H53)
A2D 0407A 50105D10 (00H54)
A2E 0407C 4C104C10 (00H55)
A2F 0407E 85384531 (00H56)
A30 04080 32101210 (00H57)
A31 04082 48704H70 (00H58)
A32 04084 84DC84DC (00H60)
A33 04086 08F808F8 (00H61)
A34 04088 4CC84CCF (00H63)
A35 0408A 41104110 (00H64)
A36 0408C 857C857C (00H65)
A37 0408E 911C0061 (00H66)
A38 04090 50105D10 (00R73)
A39 04092 08C808C8 (00R74)
A3A 04094 4C104C10 (00H75)
A3B 04096 08AF08AF (00H77)
A3C 04098 84608460 (00R78)
A3D 0409A 12101210 (00R79)
A3E 0409C 08FC08FC (00R81)
A3F 0409E 48704H70 (00R82)
A40 040A0 08F08F08 (00R83)
A41 040A2 08H088H0 (00R85)
A42 040A4 85D285D2 (00R87)
A43 040A6 08F08F08 (00R89)
A44 040A8 40404040 (00R90)
A45 040AA 08AH08AH (00R91)
A46 040AC 85808580 (00R92)

A6=R4, R5=R2-1/2 (-1/2<R5<1/2)
M3=V(2K+1)
M3=V(4K+1)
A0=M5, M6=R2+P1
M1=P14; P2=V(4K+7)*A, V(4K+3)*A
A0=R6, R7=R6+T(1)
A0=R7, R8=R7-1/2 (=CARG(4K)), (=CARG(4K+1))
A1=R2; P15=Y(4K+4), Y(4K-3)
A0=R3, HEANS(R8) 0<CARG(2K)21/2
0<CARG(4K+1)<1/2
A0=R9, R10=1/4, -P9, -1/4<SARG(4K)<1/4
-1/4<SARG(4K+1)<1/4
O0=Y(4K-4), Y(4K-3); P6=Y(4K-2), Y(4K-1)
M2=SARG(4K) (=X(4K)), SARG(4K+1) (=X(4K+1))
M7=X(4K), X(4K+1); R11=R8-1/2
A0=R11, R12=R11+P2
O0=Y(4K-2), Y(4K-1); P3=X(4K)^2, X(4K+1)^2
EXIT

FFTCOEFFICIENTS AND EVALUATE POLYNOMIAL APPROX.
VIA HORNER METHOD

MOV(A0), ADDI(T(A0, A5))
MOV(T0, M0)
MOV(A0), SUB(CA0, A4)
MOV(P, M7)
MUL(M0, M7)
MOV(A0), ARS(A0)
MOV(IOA, M4)
MOV(A0), SUB(A3, A0)
MOV(R, M6)
MOV(R, M3)
MOV(A2), MUL(M3, M6)
MOV(IOA, A0)
ADD(A2, A0)
MOV(P, M3)
MUL(M3, M4)

A0=R12, R13=R12+T(1)
M0=C4
A0=R13, R14=R13-1/2 (=CARG(4K+2)
CARG(4K+3))
M7=X(4K)^2, X(4K+1)^2
P4=C4*X(4K)^2, C4*X(4K+1)^2
A0=R14, R15=ARS(R14) (0<CARG(4K+2)
CARG(4K+3)<1/2)
M4=C4
A0=R15, R16=1/4-R15 (-1/4<SARG(4K+2)
SARG(4K+3)<1/4)
M6=SARG(4K+2) (=X(4K+2))
SARG(4K+3) (=X(4K+3))
M3=X(4K+2), X(4K+3)
A2=C4*X(4K)^2, C4*X(4K+1)^2, P5=X(4K+2)^2
X(4K+3)^2
A0=C3
R17=C3+C4*X(4K)^2, C3+C4*X(4K+1)^2
M3=X(4K+2)^2, X(4K+3)^2
P6=C4*X(4K+2)^2, X(4K+3)^2

```



PAGE 242 SNAP-11 MAP-100 ANITH. MODULES - PROG. BR30101.03 MAY 7, 1980  
APU3-VCDS VECTOR COSINE

040F0 94AMRC42 (00937) DATA -41.34167750 C1  
040F2 3741FC1 (0093H) DATA 6.283185272 C0  
(00939) \*

```

(00940) * APU3-VFLT
(00941) *
(00942) * VECTOR VECTOR FLOAT(VFLT, VFLTH)
(00943) *
(00944) * RINDS TO APS4-V1124A FOR VFLT AND V1124C FOR VFLTR
(00945) *
(00946) * FO. YENUMM(II)+H
(00947) *
(00948) *
(00949) *
(00950) *
(00951) *
(00952) *
(00953) *
(00954) *
(00955) *
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(00974) *
(00975) *
(00976) *
(00977) *
(00978) *
(00979) *
(00980) *
(00981) *
  
```

040F4 0000  
 040F5 0012  
 00000000  
 040F6 08FF08FF  
 040F7 08F708F7  
 00000002  
 040FA 08F20000  
 040FB 000008F2  
 040FC 325C325C  
 040FD 08880888  
 04102 84718471  
 04104 47204720  
 04106 08F30000  
 04108 000008F3  
 0410A 327C327C  
 0410C 08F008F0  
 0410F 84F084F0  
 04110 47004700  
 04112 201C0002  
 04114 20320000  
 04116 00000000  
 04118 10000000  
 00000017  
 0411A

START ON WORD BOUNDARY  
 START ADDRESS  
 SIZE  
 START OF APU MODULE  
 M7=A  
 A7=H  
 A2=HEX/  
 A2=DEF.  
 OUT=MF',R=NORM(UEFF)=UE  
 M0=UE.  
 A1=PD',PE=UE+H  
 R0'=R0'+R  
 A3=U00/  
 A3=U00  
 OUT=RO',R=NORM(U00)=U0  
 M1=U0  
 A0=PE,PD=U0+H  
 PE=PE+H  
 HALT  
 CLEAR(MA) \ NOP  
 NOP  
 JUMP(0)  
 VFLTSSZ=#A-VFLTSSA  
 END VFLTSSZ  
 EVEN

```

(00982) * APS3-V1124
(00983) *
(00984) *
(00985) * INPUT STREAM - SA, SR:DK; FI
(00986) * OUTPUT STREAM - O1,O2,O3,O4; YK; EO
(00987) *
(00988) *
(00989) * START OF HEADER BLOCK
(00990) *
(00991) *
(00992) *
(00993) *
(00994) *
(00995) *
(00996) *
(00997) *
(00998) *
(00999) *
(01000) *
(01001) *
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(01011) *
(01012) *
(01013) *
(01014) *
(01015) *
(01016) *
(01017) *
(01018) *
(01019) *
(01020) *
(01021) *
(01022) *
(01023) *
(01024) *
(01025) *
  
```

```

START ON WORD BOUNDARY
PTR TO CONSTR INSTR BLOCK
PTR TO SCALAR BLOCK
NUMBR OF SCALARS
MODULE SIZE
PTR TO CHAIN ANCHOR
END OF WORD BOUNDARY

SET OUTPUT PC
TURN ON APU

GEN SA ADDR
GEN SB ADDR
LOAD VECTOR-U BASE ADDR
LOAD VECTOR-U SIZE-1
SET BR0 TO U BASE-SPACING
ENTER LOOP AT TEST

GEN U-ELEMENT ADDRESS

LOOP UNTIL CNTR NFG
SET UP CLEAN-UP LOOP
  
```

```

      0411A 0000416C (00992)
      0411C 00004126 (00993)
      0411F 0002 (00994)
      0411F 0062 (00995)
      04120 00004164 (00996)
      04120 00004164 (00997)
      04120 00004164 (00998)
      04120 00004164 (00999)
      04120 00004164 (01000)
      04120 00004164 (01001)
      04120 00004164 (01002)
      04120 00004164 (01003)
      04120 00004164 (01004)
      04120 00004164 (01005)
      04120 00004164 (01006)
      04120 00004164 (01007)
      04120 00004164 (01008)
      04120 00004164 (01009)
      04120 00004164 (01010)
      04120 00004164 (01011)
      04120 00004164 (01012)
      04120 00004164 (01013)
      04120 00004164 (01014)
      04120 00004164 (01015)
      04120 00004164 (01016)
      04120 00004164 (01017)
      04120 00004164 (01018)
      04120 00004164 (01019)
      04120 00004164 (01020)
      04120 00004164 (01021)
      04120 00004164 (01022)
      04120 00004164 (01023)
      04120 00004164 (01024)
      04120 00004164 (01025)
  
```



A0F 0413F 1C8A9006	(01026) *	ADD(RW0,[9],TF)	GEN LAST 1-3 U-ADDR
A0F 04140 1F290F81	(01027) #6	SUBL(RW2,1),JUMPP(#6)	LOOP UNTIL FINISHED
A10 04142 20200031	(01029) *	CLFAR(RI)	HAIT INPUT
A11 04144 22000020	(01030) V1124S7	NUP(0)	
	(01032) *		
	(01033) *		
	(01034) *	OUTPUT PROGRAM	
	(01035) *		
A12 04146 24300032	(01036) V1124S3	SET(RA)	TURN-ON API
A13 04148 26C20794	(01037)	LOAD(RW0,DMYS(1),TF)	GEN DUMMY-1 ADDRESS
A14 0414A 28C20794	(01038)	LOAD(RW0,DMYS(1),TF)	
A15 0414C 2AC20794	(01039)	LOAD(RW0,DMYS(1),TF)	
A16 0414E 2CC20794	(01040)	LOAD(RW0,DMYS(1),TF)	GEN DUMMY-4 ADDR
A17 04150 2F400012	(01041)	LOAD(RW0,[0])	LOAD VECTOR-Y BASE ADDR
A18 04152 30500000	(01042)	LOAD(RW1,MSS)	LOAD VECTOR-Y SIZE-1
A19 04154 32020000	(01043)	SUB(RW0,MSS)	SET RW0 TO Y BASE-SPACING
A1A 04156 34111F79	(01044)	ADDL(RW1,1),JUMP(V1124S8)	ENTER LOOP AT TEST
	(01045) *		
	(01046) *	OUTPUT ADDRESS GENERATION LOOP	
	(01047) *		
A1R 04158 368A0000	(01048) #4	ADD(RW0,[8],TF)	GEN Y-ELEMENT ADDR
A1C 0415A 388A0002	(01049)	ADD(RW0,[8],TF)	
A1D 0415C 3A8A0002	(01050)	ADD(RW0,[8],TF)	
A1E 0415E 3C8A0002	(01051)	ADD(RW0,[8],TF)	
A1F 04160 3F111H44	(01052) *	SUBL(RW1,4),JUMPP(#4)	LOOP UNTIL NEG
	(01054) *		
A20 04162 401123FH	(01055)	ADDL(RW1,3),JUMPN(V1124S0)	SET UP CLEAN-UP LOOP
	(01056) *		
A21 04164 428A0006	(01057) #9	ADD(RW0,[8],TF)	GEN LAST 1-3 Y-ADDR
A22 04166 441121H1	(01058)	SUBL(RW1,1),JUMPP(#9)	LOOP UNTIL FINISHED
	(01059) *		
A23 04168 46200030	(01060) V1124S0	CLFAR(R0)	HAIT OUTPUT
A24 0416A 48000020	(01061)	NUP(0)	
	(01062) *		
	(01063) *		
	(01064) V1124SA=#C		ASSIGN VALUE TO CHAIN ANCHOR
	(01065) *		
00004164	(01066)		
	(01067) *	FND #A-1	END OF MODULE
0416C	(01068) *		
	(01069) *	STORAGE BUFFER FOR CONSTRUCTED INSTRUCTIONS	

PAGE 28: SNAP-11 MAP-100 ARITH. MODULES - PROC. #R30101.03 MAY 7, 1960  
APS3-V1124

(01070) \*  
(01071) \*  
0416C 00000000 (01072) V11746I DATA 12F'0.0'  
...  
(01073) \*  
(01074) \*  
00000062 (01075) V117462=01-V1124S

COMPUTE MODULE SIZE

041000 AUTO. MODULES - PROG. #R30101.03 MAY 7, 1980  
 0-V1124A APS PGM FOR VEET

APS3-V1124A APS PGM FOR VEET

(01076) \*  
 (01077) \*

(01078) \* INPUT STREAM - SA, SB; UK FIXED LONG; FI  
 (01080) \* OUTPUT STREAM - DI, D2, D3, D4; VK; ED

(01081) \*  
 (01082) \*

(01083) \*  
 (01084) \*  
 (01085) \*  
 (01086) \*  
 (01087) \*

START ON WORD BOUNDARY  
 PTR TO CONSTR INSTR BLOCK  
 PTR TO SCALAR BLOCK  
 NUMBER OF SCALARS  
 MODULE SIZE  
 PTR TO CHAIN ANCHOR  
 END OF WORD BOUNDARY

EVEN  
 ADDR V1124AS1  
 ADDR V1124AS2\*V1124ASS  
 DATA 2  
 DATA V1124AS7  
 ADDR V1124ASA  
 EVEN

(01094) \*  
 (01095) \*

(01096) V1124AS HEGTN APS(V1124A)

(01097) \*  
 (01098) \*

(01099) \* INPUT PROGRAM

(01100) \*  
 (01101) #1  
 (01102) \*

SFT OUTPUT PC  
 TURN ON OUTPUT

JSM(V1124AS3,P2)  
 SET(P0)

(01103) \*  
 (01104) V1124ASS

GEN SA ADDRESS - COEF A  
 GEN SR ADDR, SAVE  
 LOAD VECTOR-U BASE ADDR-LONG  
 LOAD VECTOR-U SIZE-1  
 SFT HR0 TO U BASE-SPACING  
 ENTER LOOP AT TEST

LOAD(HR0,MSS(1),TF)  
 LOAD(HR0,MSS(1),TF)  
 LOAD(HR0,11)  
 LOAD(HR0,MSS)  
 SUB(HR0,MSS)  
 ADD(HR0,1),JUMP(V1124AS5)

(01105) \*  
 (01106) \*  
 (01107) \*  
 (01108) \*  
 (01109) \*

(01110) \* INPUT ADDRESS GENERATION LOOP

(01111) \*  
 (01112) #2

GEN U-ELEMENT ADDR FIXED-LONG

ADD(HR0,[9],TF)  
 ADD(HR0,[9],TF)  
 ADD(HR0,[9],TF)  
 ADD(HR0,[9],TF)

(01113) \*  
 (01114) \*  
 (01115) \*  
 (01116) \*

(01117) \*  
 (01118) V1124ASS SUME(HR2,4),JUMPP(#2)

LOOP UNTIL CNTR NEG

(01119) \*

04104 00004106  
 04106 00004190  
 04108 0002  
 04109 0062  
 0410A 000041CF

A00 0410C 00201240  
 A01 0410E 02300030

A02 04100 04C20000  
 A03 04102 06F20000  
 A04 04104 08401000  
 A05 04106 0A600000  
 A06 04108 0C020000  
 A07 0410A 0E240C79

A08 0410C 110A9000  
 A09 0410E 130A9002  
 A0A 04110 150A9002  
 A0B 04112 170A9002

A0C 04114 18200004

A00 041A6 1A2910E4 (01120)	ADDL(CHW2,3),JUMPN(V1124AS7)	SET UP CLEAN-UP LOOP
A01 041A7 100A0006 (01121)	ADD(RW0,[R],TF)	GEN LAST 1-3 D-ADDR
A02 041A8 1A2900E4 (01122)	SUBL(CHW2,1),JUMPP(86)	LOOP UNTIL FINISHED
A03 041A9 20200031 (01123)	CLEAR(RT)	HALT INPUT
A04 041AA 20000020 (01124)	MDP(O)	
A05 041AB 20000020 (01125)	OUTPUT PROGRAM	
A06 041AC 2A300032 (01126)	SET(RA)	TURN-ON API
A07 041AD 28C20793 (01127)	LOAD(RW0,DVYS(1),TF)	GEN DUMMY-1 ADDR
A08 041AE 28C20794 (01128)	LOAD(RW0,DVYS(1),TF)	GEN DUMMY-2 ADDR
A09 041AF 2A300032 (01129)	LOAD(RW0,DVYS(1),TF)	
A10 041B0 2F400012 (01130)	LOAD(RW0,[O])	GEN DUMMY-4 ADDR
A11 041B1 30S00000 (01131)	LOAD(RW1,MS)	LOAD VECTOR-Y BASE ADDR
A12 041B2 32020000 (01132)	SUB(RW0,MS)	LOAD VECTOR-Y SIZE-1
A13 041B3 4411E79 (01133)	ADDL(CHW1,1),JUMPP(V1124ASR)	SET RW0,TO PHASE-SPACING
A14 041B4 4411E79 (01134)	OUTPUT GENERATION LOOP	ENTER LOOP AT TEST
A15 041B5 3A3A0008 (01135)	ADD(RW0,[R],TF)	
A16 041B6 3A3A0002 (01136)	ADD(RW0,[R],TF)	GEN Y-ELEMENT ADDR
A17 041B7 3C3A0002 (01137)	ADD(RW0,[R],TF)	
A18 041B8 3E11384 (01138)	SUBL(CHW1,4),JUMPP(84)	LOOP UNTIL CMTR NEG
A19 041B9 401123E4 (01139)	ADDL(CHW1,3),JUMPN(V1124AS0)	SET UP CLEAN-UP LOOP
A20 041C0 423A0006 (01140)	ADD(RW0,[R],TF)	GEN LAST 1-3 Y-ADDR
A21 041C1 44112141 (01141)	SUBL(CHW1,1),JUMPP(89)	LOOP UNTIL FINISHED
A22 041C2 46200030 (01142)	CLEAR(RD)	HALT OUTPUT
A23 041C3 48000020 (01143)	MDP(O)	
A24 041C4 000041CF (01144)	V1124ASA=8C	ASSIGN VALUE TO CHAIN ANCHOR
A25 041C5 000041CF (01145)	END BA-1	END OF MODULE

PAGE 31: SNAP-11 MAP-100 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980  
APSA-V1124A APS PGM FOR VFLT

(01164) \* STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS

(01165) \*

(01166) \*

04106 00000000 (01167) V1124AS1 DATA 124'0.0'

...

(01168) \*

(01169) \*

00000062 (01170) V1124AS2=#1-V1124AS

COMPUTE MODULE SIZE

(01171) *	APS3-V2134R	APS PGM FOR VCU5
(01172) *		
(01173) *		
(01174) *	INPUT STREAM - SN,SC2 SA,V(4K),...V(4K+1),C4...C0,U(4K),...U(4K+1)FFI	
(01175) *	OUTPUT STREAM - D1,D2,D3,D4; VK; FI	
(01176) *		
(01177) *		
(01178) *	START OF HEADER BLOCK	
(01179) *		
(01180) *	VFVN	START ON WORD BOUNDARY
(01181) *	ADDR V2134HS1	PTR TO CONSTR INSTR BLOCK
(01182) *	ADDR V2134HS+2+V2134RSS	PTR TO SCALAR BLOCK
(01183) *	DATA 3	NUMBER OF SCALARS
(01184) *	DATA V2134HSZ	MODULE SIZE
(01185) *	ADDR V2134RSA	PTR TO CHAIN ANCHOR
(01186) *	VFVN	END OF WORD BOUNDARY
(01187) *		
(01188) *		
(01189) *	V2134RS	BEGIN APS(V2134R)
(01190) *		
(01191) *		
(01192) *	INPUT PROGRAM	
(01193) *		
(01194) *		
(01195) *	JSN(V2134HS1,P2)	SET OUTPUT PC
(01196) *	SFT(R0)	TURN ON APU
(01197) *		
(01198) *	V2134RSS	GFT SA ADDR SAVE
(01199) *	LOAD(RR3,MSS(1))	GEN SH ADDR
(01200) *	LOAD(RR0,MSS(1),TF)	GEN SC ADDR
(01201) *	MOVR(RW3,RR3)	SA ADDR IN RW3
(01202) *	LOAD(RR0,(1))	LOAD VECTOR-U BASE ADDR
(01203) *	LOAD(RR2,MSS)	LOAD VECTOR-U SIZE-1
(01204) *	SUB(RR0,MSS)	SFT RR0 TO U BASE-SPACING
(01205) *	LOAD(RR1,(2))	LOAD VECTOR-V BASE ADDR
(01206) *	LOAD(RR2,MSS)	LOAD VECTOR-V SIZE-1
(01207) *	SUB(RR1,MSS)	SFT RR1 TO V BASE-SPACING
(01208) *		
(01209) *		
(01210) *	INPUT ADDRESS GENERATION LOOP	
(01211) *		
(01212) *	MOVR(RR3,RR3,TF)	GEN SA ADDR
(01213) *	ADD(RR1,(10),TF)	GEN V-ELEMENT ADDRESS
(01214) *	ADD(RR1,(10),TF)	

A0F 04214 1F9A002 (01215)	ADD(HR1,10),TF	GEN V-ELEMENT ADDR
A10 04216 209A002 (01216)	ADD(HR1,10),TF	
A11 04218 229200A (01217)	LOAD(RW1,VCUSUS(1),TF)	GEN VCSC4 ADDR
A12 0421A 2493003A (01218)	ADD(HR1,2,TF)	
A13 0421C 2694003A (01219)	ADD(HR1,2,TF)	
A14 0421E 2895003A (01220)	ADD(HR1,2,TF)	
A15 04220 2A96003A (01221)	ADD(HR1,2,TF)	GEN VCSCO ADDRESS
A16 04222 2C9A00C (01222)	ADD(HR0,19),TF	GEN II-ELEMENT ADDR
A17 04224 2E9A9002 (01223)	ADD(HR0,19),TF	
A18 04226 309A9002 (01224)	ADD(HR0,19),TF	
A19 04228 329A9002 (01225)	ADD(HR0,19),TF	
A1A 0422A 34290C94 (01226)	SURE(HR2,4),JUMPP(#2)	LOOP UNTIL FINISHED
	(01227) *	
A1H 0422C 36200031 (01228)	CLEAR(H1)	
A1C 0422E 38000020 (01249)	NDP(0)	
	(01230) *	
	OUTPUT PROGRAM	
	(01231) *	
	(01232) *	
A1D 04230 3A300032 (01233)	V2134RS3 SET(RA)	TURN-ON APU
A1E 04232 3C320794 (01234)	LOAD(RW0,DMYS(1),TF)	GEN DUMMY-1 ADDR
A1F 04234 3E320794 (01235)	LOAD(RW0,DMYS(1),TF)	
A20 04236 40C20794 (01236)	LOAD(HW0,DMYS(1),TF)	
A21 04238 42C20794 (01237)	LOAD(HW0,DMYS(1),TF)	
A22 0423A 44A00012 (01238)	LOAD(HW0,10)	LOAD VECTOR-Y BASE ADDR
A23 0423C 46500000 (01239)	LOAD(RW1,MSS)	LOAD(VECTOR-Y SIZE-1
A24 0423E 48020000 (01240)	SUB(HW0,MSS)	SET RW0 TO Y BASE-SPACING
A25 04240 4A112A79 (01241)	ADD(HW1,1),JUMP(V2134RS8)	ENTER LOOP AT TEST
	(01242) *	
	(01243) *	
	(01244) *OUTPUT ADDRESS GENERATION LOOP	
	(01245) *	
A26 04242 4C9A00H 84 (01246)	ADD(RW0,1H),TF	GEN Y-ELEMENT ADDRESS
A27 04244 4E9A002 (01247)	ADD(HW0,1H),TF	
A28 04246 509A002 (01248)	ADD(HW0,1H),TF	
A29 04248 529A002 (01249)	ADD(HW0,1H),TF	
	(01250) *	
A2A 0424A 541126H4 (01251)	SURE(HW1,4),JUMPP(#4)	LOOP UNTIL CNTR NEG
	(01252) *	
A2B 0424C 56112FEH (01253)	ADD(HW1,1),JUMP(V2134RS0)	SET UP CLEAN-UP LOOP
	(01254) *	
A2C 0424E 589A006 89 (01255)	ADD(HW0,1H),TF	GEN LAST 1-3 Y-ADDR
A2D 04250 5A112CH1 (01256)	SURE(HW1,1),JUMPP(#9)	LOOP UNTIL FINISHED
	(01257) *	
A2E 04252 5C200030 (01258)	V2134RS0 CLEAR(R0)	HALT OUTPUT

PAGE 34: SNAP-II MAP-300 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980  
APS PGM FOR VCONS  
APS3-V2134R

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A2F 04254 5F000020 (01259) NOP(0)
      (01260) *
      (01261) *
      (01262) *
      0000924F (01263) V2134R6A=0C      ASSIGN VALUE TO CHAIN ANCHOR
      (01264) *
      04256      (01265)      END BA=1      END OF MODULE
      (01266) *
      (01267) *
      (01268) * STORAGE BUICK FOR CONSTRUCTED INSTRUCTIONS
      (01269) *
      04256 00000000 (01271) V2134R5I DATA 16F'0.0F
      ...
      (01272) *
      (01273) *
      00000000 (01274) V2134R5Z=BL-V2134RS      COMPUTE MODULE SIZE

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SNAP-11 MAP-300 ARITH. MODULES - PRPG. 8R10101.03 MAY 7, 1980  
MAP-300 EXTENDED ARRAY FUNCTIONS - PRPG. 8R10102.03 - MAY 7, 1980

(01275) 'MAP-300 EXTENDED ARRAY FUNCTIONS - PRPG. 8R10102.03 - MAY 7, 1980  
(01276) \*  
(01277) \*  
(01278) \*

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(01279) *NOT-IN-PLACE FFT FOR ONE AND TWO DIMENSIONS
(01280) *
(01281) *
(01282) *FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS
(01283) *
(01284) * PERFORMS AN N POINT FFT OR IFFT ON DATA STORED IN
(01285) * THE U BUFFER USING THE V BUFFER AS THE
(01286) * COSINE TABLE AND THE W BUFFER AS A
(01287) * WORKING BUFFER AND LEAVES THE RESULT IN THE
(01288) * Y BUFFER. THIS ROUTINE CANNOT BE DONE IN
(01289) * PLACE (I.E. W AND U CANT BE THE SAME BUF-
(01290) * FER). THE NUMBER OF POINTS IN THE FFT,
(01291) * N, MUST BE A POWER OF TWO AND NOT GREATER
(01292) * THAN 1024.
(01293) *
(01294) *
(01295) * THE BUFFER DESCRIPTIONS ARE:
(01296) * Y BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(01297) * U BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(01298) * V BUFFER (10-39) REAL 32-BIT FLOATING POINT
(01299) * W BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(01300) *
(01301) * THE COSINE TABLE ENTRIES ARE:
(01302) * C(K)=COS(2*PI*K/C/SIZE)
(01303) * WHERE C/SIZE IS A MULTIPLE OF N
(01304) *
(01305) ?
(01306) **ENTRY TO SPECIAL WINDING MODULE FOR 2-D FFT SETUP
(01307) **
(01308) ** ENTER WITH M1 POINTING TO FCN NUMBER
(01309) ** R2 POINTING TO DISPATCH TABLE
(01310) **
(01312) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01313) ***
(01314) ***
(01315) **RPTFT01 MOVRR R5,M1 ; COPY FCN BLOCK POINTER
(01316) ** FVFN ; TAG REPEATED FFT CALL
(01317) ** MOVLM 2,FFTSTYP ; GFT LOG2(FFT SIZE)
(01318) ** MOVRR R7,2*HS(45) ; FORCE TO BE <= 15
(01319) ** ANDIR R7,SF ; SAVE FOR LATER PERMANENT STORAGE
(01320) ** MOVRR R7,LOG2SZ ; POINT TO PARAMETER STORAGE AREA
(01321) ** MOVRR R7,SMPSPC ; SET LOOP COUNTER FOR 4 TRANSFERS
(01322) **

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(01323) **
(01324) **RPTFT02 PUPXI R5,R4 ; LOAD PASSED PARAMETER INTO R4
(01325) **EVEN
(01326) **ANDIR R4,SE ; FORCE ALL TO BE <= 15
(01327) **MOVBR R1,RPTFT03 ; GET COPY OF SHIFT INSTRUCTION
(01328) **TUNAR R4,R1,SEFFO ; OR IN SHIFT COUNT
(01329) **MOVBR R4,RPTFT03 ; STORE SHIFT INSTRUCTION
(01330) **MOVIR R4,1 ; INJ R4 TO 2**0
(01331) **RPTFT03 LLS R4,0 ; COMPUTE 2**N
(01332) **PUSHX1 R7,R4 ; SAVE IN PARAMETER AREA
(01333) **DJP R6,RPTFT02 ; LOOP OVER ALL 4 PARAMETERS
(01334) **PUSHX1 R7,R4 ; NEED 2 COPIES OF FFT SPACING
(01335) **EVEN
(01336) **
(01337) ** COMPUTE MAXSMP = (MAXIMUM SAMPLE INDEX) - 1
(01338) **
(01339) ** MAXSMP = (SAMPLE_SPACING)*(FFT_SIZE - 1) +
(01340) ** (NUMBER_OF_FFT'S - 1)*(FFT_SPACING)
(01341) ** = SA*(SD-1) + (SC-1)*SD
(01342) **
(01343) ** MOVBR R5,FFTSIZ ; GET SIZE
(01344) ** MOVBR R7,NEFT ; GET NUMBER OF FFT'S
(01345) ** DECR R5,1 ; CORRECT TO (FFTSIZ-1)
(01346) ** DECR R7,1 ; CORRECT TO (NEFT-1)
(01347) ** MOVBR R7,NEFT ; SAVE DECREMENTED COUNT FOR LOOPING
(01348) ** MULBR R5,SMPSPC ; FORM 1ST PRODUCT
(01349) ** MULBR R7,INSPC ; FORM 2ND PRODUCT
(01350) ** ADDR R5,R7 ; FORM SUM
(01351) ** LRS R5,1 ; COMMON CORRECTION FOR DOUBLING BY MU
(01352) ** MOVBR R5,MAXSMP ; SAVE FOR LATER
(01353) ** HOP RPTFT04 ; HOP INTO BINDING CODE
(01354) **EVEN
(01355) *
(01356) ***** FND REPEAT FFT CODE INSERTION *****
(01357) *

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(01358) *ENTRY TO SPECIAL BINDING MODULE FOR VECTOR FFT SETUP
(01359) ?
(01360) ? DOES ALL BINDING NOT NEEDED EVERY TIME
(01361) ? AN FFT IS DONE.
(01362) ?
(01363) ? ENTER WITH R1 POINTING TO FCN NUMBER
(01364) ? R2 POINTING TO DISPATCH TABLE
(01365) ?
(01366) ?
(01367) FFTSSFT = #L
(01368) ** MOVZM FFTSTYP ; TAG 'MIXED' FFT
04276 F0420002 MOVZM R4, 2*HS(M1) GET U BUFFER ID
04278 9A40FF00 ANDIR R4, MSKSIJVT MASK OUT RIGHT HALF
0427A 3C47 LPS R4, 7 SFT FOR FULL WORD INDEX
0427H 0M00 EVEN SKIP TO EVEN BOUNDARY
0427C F05806R7 MOVZM R5, ACTSAT+HS(R4) R5= U BUFFER ATTRIBUTES
0427E F050A39D MOVZM R5, PWR25 STORE FOR LATER REFERENCE
(01375) ?
(01376) *BIND ALL USIZE=1'S
(01377) ?
(01378) ?
(01379) *
(01380) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01381) *
(01382) ** MOVZM R7,FFTSTYP ; GET FFT TYPE TAG INDEX
(01383) ** JMP #PPTF05(R7) ; JUMP TO NEXT SECTION OF CODE
(01384) ?
(01385) ** PPTF05 ADDR RPTF07 ( , 1) ; 'MIXED' FFT
(01386) ** ADDR RPTF06 ( , 1) ; REPEATED FFT
(01387) ?
(01388) ** RPTF06 MOVZM R7,LOG2SZ ; GET LOG2(FFT SIZE)
(01389) ** MOVZM R7,PWR25 ; STORE FOR BINDING CODE USAGE
(01390) ** CMPR R6,MAXSMP ; CHECK INPUT BUFFER SIZE
(01391) ** JMP #RHS,LT ; JUMP IF BUFFER TOO SMALL
(01392) ** MOVZM R7,INSPC ; GET FFT INPUT VECTOR SPACING
(01393) ** MULR R7,R5 ; SCALE BY BUFFER SAMPLE SPACING
(01394) ** LRS R7,1 ; CORRECT FOR FACTOR OF 2 BY MUL
(01395) ** MOVZM R7,INSPC ; SAVE FOR USE IN POST-SUPPORT MODULE
(01396) ** MULR R5,SMPSPC ; COMPUTE INPUT SAMPLE SPACING
(01397) ** LRS R5,1 ; CORRECT FACTOR OF 2 FROM MUL
(01398) ** EVEN
(01399) ** MOVZM R6,FFTSIZ ; GET CORRECT FFT SIZE
(01400) ** MOVZM R7,R6 ; GET 2ND COPY
(01401) ** DFCR R7,1 ; NEED TO BIND (FFT_SIZE - 1) 6 PLACES

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(01402) ** MOVRR R7,CSMS + WS*CR4A03 + HS
(01403) ** MOVRR R7,CSMS + WS*CSMLOS + HS
(01404) ** MOVRR R7,CSMS + WS*CR4A3S + HS
(01405) ** MOVRR R7,CSMS + WS*CR4A9S + HS
(01406) ** MOVRR R7,CSMS + WS*CR4A9S + HS
(01407) ** MOVRR R7,CSMS + WS*CR4A13S + HS
(01408) ** HOP
(01409) ** REPRTOR
(01410) ** EVEN
(01411) ***** END REPEAT FFT CODE INSERTION *****
(01412) *
042R2 F06045F7 (01413) REPRT07 MOVRR R6, CSMS*WS*CR4A03+HS STORE AIL, USIZE-1'S
042R4 F06045A9 (01414) MOVRR R6, CSMS*WS*CSMLOS+HS ...
042R6 F06045D7 (01415) MOVRR R6, CSMS*WS*CR4A3S+HS ...
042R8 F0604631 (01416) MOVRR R6, CSMS*WS*CR4A9S+HS ...
(01417) *
(01418) *
(01419) * SCALAR A ID IS REALLY THE NUMBER OF MINI-BUFFERS
(01420) * WITHIN THE U BUFFER.
(01421) * FOR EXAMPLE:
(01422) * IF U BUFFER HAS 1024 COMPLEX POINTS BUT IS
(01423) * REALLY 4 GROUPS OF 256 POINTS THEN SA ID
(01424) * SHOULD BE SET TO 4.
(01425) * IF U BUFFER HAS 1024 POINTS AND IS REALLY
(01426) * 16 GROUPS OF 64 POINTS THEN SA ID SHOULD BE
(01427) * 16.
(01428) * NORMAL OPERATION WHERE THE U BUFFER CONTAINS
(01429) * ONE BUFFER TO BE FFTED MEANS SA ID SHOULD
(01430) * BE 1.
(01431) * SA ID MUST BE A POWER OF 4. (I.E. 1, 4,
(01432) * 16, 64, 256)
(01433) *
042R8 F0420001 (01434) MOVRR R4, HS(R1) POINT TO SCALAR A ID (SAID)
042R9 F04000FF (01435) ANDIR R4, MSKSRHT MASK IT OUT
042R1 F0407C (01436) MOVRR R7, R6 PUT USIZE-1 IN R7
042R2 F0408 (01437) XXIS SRRC 0, R4 SKIP SHIFTING NEEDED
042R3 F0409 (01438) HOP XX2S
042R4 F040A (01439) LPS R7,1 ; DIVIDE BY 2
042R5 F040B (01440) LPS R4, 1 AND SHIFT TILL BIT APPEARS
042R6 F040C (01441) HOP XX1S
042R7 F040D (01442) EVEN
042R8 F0704649 (01443) XX2S MOVRR R7, CSMS*WS*CR4A9S+HS STORE FFTSIZE-1
042R9 F070460F (01444) MOVRR R7, CSMS*WS*CR4A13S+HS ...
(01445)

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04298 2661 (01466) INCR R6, 1 R6<=N(USIZE)
04299 403A (01467) RPTATOM MOVRR R3, R5 R3<=USEP
0429A 345C (01448) MULRR R5, R6 R5<=2(USEP)(USIZE)
0429B 3C52 (01449) LMS R5, 2 R5<=0.5(USEP)(USIZE)
(01450) ?
(01451) ?HIND ALL. OUI'S
(01452) ?
0429C 9040439D (01453) MOVRR R4,HUSTR0,-1 ; POINT TO TARGE OF HU BINDING LOC'S
0429E 304E (01454) POPXT R4,R7 ; GET NEXT BINDING ADDR
0429F 0270 (01455) TEST R7 ; CHECK FOR END OF TABLE
042A0 801042AA (01456) JMP HUSDN,F0 ; ZERO MARKS END OF TABLE
042A1 100F0000 (01457) CMH 0,D(1R7) ; RESET MSR OF DELTA FIELD
042A4 F05F0001 (01458) MOVRR R5,1(R7) ; STORE LOW 16 BITS
042A6 1H10 (01459) SKPL NF ; IF NOT 0, DELTA IS OK
042A7 D20F0000 (01460) SMH 0,D(1R7) ; PRODUCT WAS 2**18 BEFORE CORRECTION
042A9 210C (01461) HOP HUSLIP ; LOOP FOR ALL HU BINDING
(01462) ?
042AA 0250 (01463) HUSDN R5 ; CHECK PRODUCT
042AB 1H10 (01464) SKPL NF ; IF NOT 0, DELTA IS OK
042AC F0510000 (01465) MOVRR R5,S10000 ; SET UP CORRECT DELTA FOR 2**18
042AE 3C51 (01467) LRS R5, 1 R5<=HU/2 (QU)
042AF 0H00 (01468) EVEN TU TO EVEN BOUNDARY
(01469) ?
(01470) ?HIND ALL. OUI'S
(01471) ?
042B0 F0504577 (01472) MOVRR R5, CSMS+R5*CSM1,3S+HS STORE OUI'S
042B2 4F56 (01473) SUBRR R5, R3 R5<=OUI-USEP
042B3 0H00 (01474) EVEN
042B4 F050456F (01475) MOVRR R5, CSMS+R5*CSM1,1S+HS STORE OUI-USEP
(01476) ?
(01477) ?
  
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PAGE 413 SNAP-11 MAP-100 ARITH. MODULES - PROG. 040101.03 MAY 7, 1980  
 ENTRY TO SPECIAL WINDING MODULE FOR VECTOR FFT SETUP

042M6 405C	(01478)	MOVW R5, R6	R5<=N
042M7 0R00	(01479)	FVFN	
042M8 04500003	(01480)	MOVLW R5, 3	R5<=4*(3N/2)
	(01481) ?		
	(01482) ?	BRIND 00=0715	
	(01483) ?		
042M9 F05045R5	(01484)	MOVW R5, CSMS+WS*SCRM0+HS	STORE ALL D0'S
042M0 F05045H0	(01485)	MOVW R5, CSMS+WS*SCRM2S+HS	...
042M1 F05045H0	(01486)	MOVW R5, CSMS+WS*SCRM4B+HS	...
042M2 F05045Q1	(01487)	MOVW R5, CSMS+WS*SCRM6S+HS	...
042M3 3C51	(01488)	LRS R5, 1	R5<=00/2 (01)
042M4 0R00	(01489)	FVFN	
042M5 F05045R7	(01490)	MOVW R5, CSMS+WS*SCRM1S+HS	STORE ALL D1'S
042M6 F05045H0	(01491)	MOVW R5, CSMS+WS*SCRM5S+HS	...
042M7 3C51	(01492)	LRS R5, 1	R5<=01/2 (02)
042M8 0R00	(01493)	FVFN	
042M9 F05045H0	(01494)	MOVW R5, CSMS+WS*SCRM3S+HS	STORE D2
042M0 3C51	(01495)	LRS R5, 1	R5<=02/2 (03)
042M1 0R00	(01496)	FVFN	
042M2 F05045Q5	(01497)	MOVW R5, CSMS+WS*SCRM7S+HS	STORE D3
042M3 3C51	(01498)	LRS R5, 1	R5<=03/2 (04)
042M4 0R00	(01499)	FVFN	
042M5 F05045H0	(01500)	MOVW R5, CSMS+WS*SCRM9S+HS	STORE D4
042M6 3C51	(01501)	LRS R5, 1	R5<=04/2 (05)
042M7 0R00	(01502)	FVFN	
042M8 F05045A1	(01503)	MOVW R5, CSMS+WS*SCRM7S+HS	STORE D5
042M9 3C51	(01504)	LRS R5, 1	R5<=05/2 (06)
042M0 0R00	(01505)	FVFN	
042M1 F05045A7	(01506)	MOVW R5, CSMS+WS*SCRM10S+HS	STORE D6
042M2 3C51	(01507)	LRS R5, 1	R5<=06/2 (07)
042M3 0R00	(01508)	FVFN	
042M4 F05045A0	(01509)	MOVW R5, CSMS+WS*SCRM7+HS	STORE D7
	(01510) ?		
	(01511)		
	(01512)		

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PAGE 42: SNAP-II MAP-100 ARITH. MODULES - PROG. BR10101.03 MAY 7, 1960  
ENTRY TO SPECIAL RINDING MODULE FOR VECTOR FFT SETUP

042E0 305C	(01513)	MOVRR R5, R6	RSC=N (US1ZF)
042F1 3A52	(01514)	LIS R5, 2	RSC=46N
042F2 2653	(01515)	INCR R5, 4	RSC=46N+4
042F3 0800	(01516)	EVEN	
	(01517) ?		
	(01518)	PFCT	



AD-A091 663

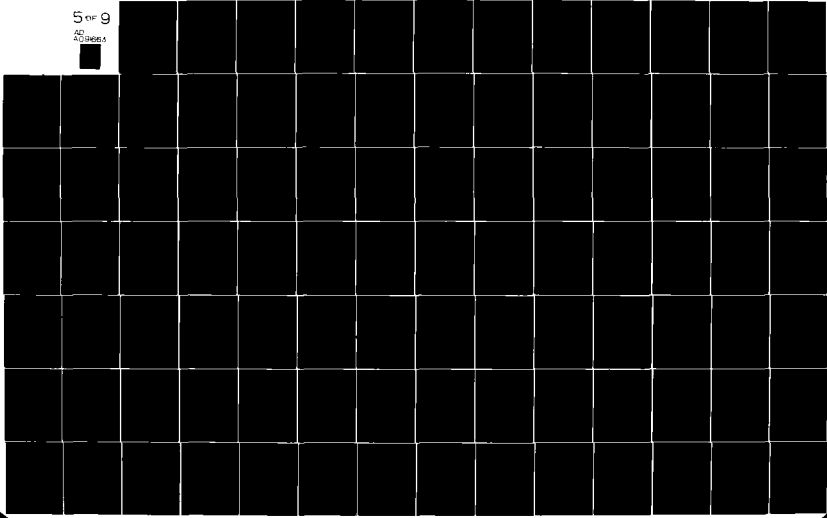
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

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NL

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AD  
Access



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(01519) ?
(01520) PRINT A(L, 4N+4 AND 4N+12'S
(01521) ;
042E4 F05045F9 MOVW R5, CSMS+MS+CR4A25+HS STORE 4N+8'S
042E6 F050462F MOVW R5, CSMS+MS+CR4A3+HS ***
042E8 7658 INCW R5, R R5<=4*N+12
042E9 0800 FVEN
042FA F05045H5 MOVW R5, CSMS+MS+CSM00+HS STORE 4N+12
042FC F0420001 MOVW R4, HS(R1) LOAD Y BUFFER ID
042FE 9A40FF00 ANDIR R4, MSKSRHT MASK LEFT HALF
042F0 3C17 IRS R4, 7 CREATE FULL WORD INDEX
042F1 0800 FVEN
(01531) ?
(01532) BIND YSEFP
(01533) ;
042F2 C0580604 MOVW R5, RCTSAD(R4) R5<=YSEFP, R6<=YSIZE-1
(01535) *
(01536) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01537) *
(01538) ** MOVW R7, FFTSTYP ; GET FFT TYPE TAG
(01539) ** JMP @RPTF10(R7) ; JUMP TO NEXT SECTION OF CODE
(01540) ;
(01541) ** RPTF10 ADDR RPTF11(, 1) ; 'MIXED' FFT
(01542) ** ADDR RPTF10(, 1) ; REPEATED FFT
(01543) ?
(01544) ** RPTF10 MOVW R7, OUTSPC ; GET FFT OUTPUT VECTOR SPACING
(01545) ** MULW R7, R5 ; SCALE BY BUFFER SAMPLE SPACING
(01546) ** IRS R7, 1 ; CORRECT FOR FACTOR OF 2 BY MUL
(01547) ** MOVW R7, OUTSPC ; SAVE FOR USE IN POST-SUPPORT
(01548) ** CMW R6, MAXSMP ; CHECK IF OUTPUT VECTOR IS LARGE ENOUGH
(01549) ** JMP FRSS, J1T ; ERROR IF TOO SMALL
(01550) ** MULW R5, SMPSPC ; COMPUTE OUTPUT SAMPLE SPACING
(01551) ** IRS R5, 1 ; CORRECT FOR FACTOR OF 2 BY MUL
(01552) ** FVEN
(01553) ** MOVW R5, CSMS + MS+CR4A12$ + R5 ; STORE YSEP
(01554) ** MOVW R6, FFTSIZ. ; GET SIZE OF OUTPUT VECTOR FOR 1 PASS
(01555) ** HOP RPTF12 ; CONTINUE BINDING
(01556) ** FVEN
(01557) ?
(01558) ** FRRSICR = 79 ; IMPROPER BUFFER ERROR FLAG
(01559) ** FRSS MOVW R7, FRSICR ; SET IMPROPER BUFFER ERROR
(01560) ** JMP ERRORS ; GO TO EXFC ERROR PROCESSING
(01561) *
(01562) ***** END REPEAT FFT CODE INSERTION *****

```

PAGE 44: SNAP-11 MAP-100 ARITH. MODULES - PROG. #H10101.03 MAY 7, 1980  
ENTRY TO SPECIAL BINDING MODULE FOR VECTOR FFT SETUP

042F4 F05045F1	(01563) *	MOVW R5, (CSMS+8*CR4A12S+H6	STORE YSFP
042F6 2661	(01564)	RPTFT11 INCR R6, 1	R6<=YSIZE
042F7 4R5C	(01565)	MULRR R5, R6	R5<=2*YSIZE*YSFP
042F8 0250	(01566)	RPTFT12 TEST R5	TEST CONDITION CODES FROM MULTIPLY
042F9 3C51	(01567)	LRS R5, 1	R5<=YSIZE*YSFP
	(01568)	EVEN	
	(01569)		
	(01570) ?		
	(01571)	FJECT	

PAGE 45: SNAP-11 MAP-300 ARITH. MODULES - PRIG. #R10101.03 MAY 7, 1960  
 ENTRY TO SPECIAL HANDLING MODULE FOR VECTOR FFT SETUP

```

(01572) ?
(01573) ?
(01574) ?
(01575) ?
(01576) ?
(01577) ?
(01578) ?
(01579) ?
(01580) ?
(01581) ?
(01582) ?
(01583) ?
(01584) ?
(01585) ?
(01586) ?
(01587) ?
(01588) ?
(01589) ?
(01590) ?
(01591) ?
(01592) ?
(01593) ?
(01594) ?

0429A 0000450F CMR 0,CSMS + WS*CR4AUF ; RESET MSR OF DELTA FIELD
0429C 1H30 SKPL GK ; LEAVE MSR CLEAR WHEN PRODUCT POS
0429D 0200450F SMR 0,CSMS + WS*CR4AUF ; MSR SHOULD BE SET
0429F R1104306 JMP YSPSZ,NF ; IF NOT 0, ALL IS WELL
04301 C000450F MOVZM CSMS + WS*CR4AUF + HS ; PRODUCT IS 2**18 -- SET DELTA = 0
04303 9050M000 MOVIR R5,$ROUND ; YSEF*YSIZE/4 = 2**16
04305 2004 NOP YSPSZ4 ; GO CONTINUE HANDLING
04306 F050450F MOVHM R5, CSMS+WS*CR4AUF+HS STORE YSEF*YSIZE
04308 3C52 LRS R5, 2 R5<=YSIZE*YSEF/4
04309 0A00 EVEN

(01585) ?
(01586) ?
(01587) ?
(01588) ?
(01589) ?
(01590) ?
(01591) ?
(01592) ?
(01593) ?
(01594) ?

0430A F050450D MOVHM R5, CSMS+WS*CR4A118+HS STORE YSEF*YSIZE/4
0430C F0420003 MOVHM R4, 3*HS(R1) LOAD C BUFFER ID
0430F 9A40FF00 ANDIR R4, MSKSLR1T
04310 3C47 LRS R4, 7
04311 0A00 EVEN
FUFCT
  
```

CREATE FULL WORD INDEX

```

(01595) ?
(01596) ?RIND CHASE
(01597) ?
04312 0060582 (01598) MOVMI R6, RCTSHA(R4)
04314 0050461F (01599) MOVMI R5, CSMS+WS*CR4A4
ANDIR R6, SF
04316 9460000F (01600) TORWK R6, R5, SFFFO
04318 766AFFFO (01601) MOVMI R6, R(P5)
0431A 846A0000 (01602) MOVMI R5, ACTSAD(R4)
0431C 00500604 (01603) INCR R6, 1
0431E 2661 (01604) MULRR R5, R6
0431F 485C (01605) LRS R5, 3
04320 3C53 (01606) FVFN
04321 0800 (01607)
(01608) ?
(01609) ?RIND ALL HPI'S
(01610) ?
04322 F0504625 (01611) MOVMI R5, CSMS+WS*CR4A5S+HS
04324 F0504629 (01612) MOVMI R5, CSMS+WS*CR4A6S+HS
04326 F050462D (01613) MOVMI R5, CSMS+WS*CR4A7S+HS
(01614) ?
(01615) ?SIANG=HPI/SSI, RIGHT NOW ONLY RADIX 2 AND RADIX 4
(01616) ?HAVE BEEN IMPLEMENTED. SO ALL THAT IS NECESSARY
(01617) ?TO CALCULATE SSI IS TO KNOW IF USIZE IS AN EVEN OR
(01618) ?ODD POWER OF TWO. THIS IS DONE BY CHECKING THE
(01619) ?POWER OF TWO ENTRY FOR THE U BUFFER IN THE ACTSAT
(01620) ?TABLE. IF BIT 0 IS ON, THEN IT'S AN ODD POWER.
(01621) ?
04328 F010024D (01622) MOVMI R3, APSRSL
0432A 0400439D (01623) SMRC 0, PWR2S
0432C 2006 (01624) HOP SHMS1
0432D F0845C7 (01625) MOVLM 4, CSMS+WS*CR4A01+HS
0432F F084A39C (01626) MOVLM FIGSG1, GSFIGS+HS
04331 3C52 (01627) LRS R5, 2
04332 2005 (01628) HOP SHMS2
04333 F08445C7 (01629) SHMS1
04335 F84A439C (01630) MOVLM FIGSG1+FIGSSET, GSFIGS+HS
04337 3C51 (01631) LRS R5, 1
(01632) ?
(01633) SHMS7
(01634) ?
(01635) ?RIND SIANG TO STANG'S
(01636) ?
04338 F0504657 (01637) MOVMI R5, CSMS+WS*ANG11S+HS
0433A 3C52 (01638) LRS R5, 2
LOAD C HASE ADDRESS
POINT TO LOAD CHASE INST.
AND OUT LOW FOUR BITS
ON IN NEW FOUR BITS
AND STORE HACK IN INST.
R5<=CSFP, R6<=CSIZE-1
R6<=CSIZE
R5<=2*(CSFP)(CSIZE)
R5<=0.25*(CSFP)(CSIZE) (HPI)
STORE ALL HPI'S
...
...
FETCH POINTER
SKIP IF EVEN POWER OF TWO
... ODD POWER
SET AND RIND SSI = 4
RESET G1
SIANG=HPI/4
SET AND RIND SSI = 2
SFT G1
SIANG=HPI/2

```

PAGE 47: SNAP-II MAP-100 ARITH. MODULES - PRIC. #H30101.03 MAY 7, 1980  
ENTRY TO SPECIAL HANDING MODULE FOR VECTOR FFT SETUP

0433H 0R00 (01639)	FVEN
0433C F0504659 (01640)	MOVVM R5, CSMS+WS*ANGL7S+HS STORE S2ANG
0433E 3C52 (01641)	IRS R5, 2
0433F 0R00 (01642)	FVEN
04340 F050465H (01643)	MOVVM R5, CSMS+WS*ANGL3S+HS STORE S3ANG
04342 3C52 (01644)	IRS R5, 2
04343 0R00 (01645)	FVEN
04344 F050465D (01646)	MOVVM R5, CSMS+WS*ANGL4S+HS STORE S4ANG
04346 3C52 (01647)	IRS R5, 2
04347 0R00 (01648)	FVEN
04348 F050465F (01649)	MOVVM R5, CSMS+WS*ANGL5S+HS STORE S5ANG
0434A 3C52 (01650)	IRS R5, 2
0434H 0R00 (01651)	FVEN
0434C F0504661 (01652)	MOVVM R5, CSMS+WS*ANGL6S+HS STORE S6ANG
0434E 3C52 (01653)	IRS R5, 2
0434F 0R00 (01654)	FVEN
04350 F0504663 (01655)	MOVVM R5, CSMS+WS*ANGL7S+HS STORE S7ANG

```

(01656) ; SPECIAL BINDING FOR WBASE, YBASE, AND URASE
(01657) ;
(01658) ; THIS SECTION DOES THE FAST BINDING FOR
(01659) ; CHANGES IN WBASE, YBASE, AND URASE ONLY.
(01660) ;
(01661) SHMSFT EVEN START ON EVEN BOUNDARY
(01662) MOVNR R4, 4*HS(R1) GET W BUFFER ID
(01663) ANDIR R4, MSKSLAYT MASK THE LEFT HALF
(01664) IORS R4, 7 CREATE FULL WORD INDEX
(01665) EVEN
(01666) ;
(01667) ;BIND ALL WBASE'S
(01668) ;
(01669) MOVNPL R6, RCTSHA(R4) LOAD WBASE ADDRESS IN R6, R7
(01670) MOVIR R5, CSMS+MS*CSMO1S POINT TO LOAD INSTRUCTION
(01671) ANDIR R6, SF ONLY LOW FOUR BITS
(01672) TORWK R6, R5, SFFF0 'OR'ED INTO LOAD INST.
(01673) MOVNML R6, 0(R5) STORE ALL WBASE'S
(01674) ANDIR R6, SF MASK OUT OLD LOAD INST.
(01675) MOVIR R5, CSMS+MS*CR4A1S POINT TO NEXT LOAD WBASE
(01676) TORWK R6, R5, SFFF0 OR WBASE INTO IT
(01677) MOVNML R6, 0(R5) STORE WBASE
(01678) ;
(01679) ;BIND URASE
(01680) ;
(01681) MOVNR R4, 2*HS(R1) LOAD U BUFFER ID
(01682) ANDIR R4, MSKSLAYT MASK THE LEFT HALF
(01683) IORS R4, 7 CREATE FULL WORD INDEX
(01684) EVEN
(01685) MOVNML R6, RCTSHA(R4) LOAD URASE ADDRESS IN R6, R7
(01686) MOVIR R5, CSMS+MS*CSML6S POINT TO LOAD URASE INST.
(01687) ANDIR R6, SF ONLY LOW ORDER FOUR BITS
(01688) TORWK R6, R5, SFFF0 'OR' INTO INST.
(01689) MOVNML R6, 0(R5) STORE URASE
(01690) *
(01691) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01692) *
(01693) ** ADDR R7, INSPC ; RUMP TO NEXT INPUT VECTOR
(01694) ** ADC R6 ; COMPLETE THE RUMP
(01695) ** EVEN
(01696) ** MOVNML R6, LDSURS ; SAVE NEXT <LOAD URASE> INSTRUCTION
(01697) *
(01698) ***** END REPEAT FFT CODE INSERTION *****
(01699) *

```

PAGE 49: SNAP-11 MAP-300 ARITH. MODULES - PROG. #R10101.03 MAY 7, 1980  
 SPECIAL HANDLING FOR YBASE, YBASE, AND YBASE

```

(01700) F
(01701) #HND YBASE
(01702) ;
0437A F0420001      MOVRL R4, HS(R1)      LOAD Y BUFFER TO
0437C 9A40FF00      ANDIR P4, MSHSLHNT   MASK LEFT HALF
0437E 3C47          LRS R4, 7           CREATE FULL WORD INDEX
0437F 0800          EVEN
04380 C66805R2     MOVRL R6, RCTSHA(R4)   LOAD YBASE ADDRESS IN R6, R7
04382 905045DA     MOVIR R5, CSMS*#S*CH410S POINT TO LOAD INST.
04384 9A60000F     ANDIR R6, SF         MASK LOW FOUR BITS
04386 766AFFFF     TORWK R6, R5, SFFF0   OR INTO INST.
04388 446A0000     MOVRL R6, 0(R5)     STORE YBASE
(01717) *
(01713) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01714) *
(01715) **          ADDR  R7,OUTSPC      ; RUMP TO NEXT OUTPUT VECTOR
(01716) **          ADC    R6          ; COMPLETE THE RUMP
(01717) **          EVEN
(01718) **          MOVRL  R6,INDSYHS   ; SAVE NEXT <LOAD YBASE> INSTRUCTION
(01719) *
(01720) ***** END REPEAT FFT CODE INSERTION *****
(01721) *

```



```

(01722) ; SET PENDING SLOTS TO PROPER VALUES
(01723) ;
0438A C63400FH (01724) PUSHMIL R3, AFDTSORG(R2) STORE APU MODULE BUS ORIGIN
(01725)
0438C 90711FFF (01726) MOVIR R7, -WS
0438F C4H765FF (01727) PUSHMIL R3, B-WS(P3) STORE APU MODULE START AND SIZE
04390 C63400FA (01728) PUSHMIL R3, AFDTSORG+WS(R2) STORE APS MODULE BUS ORIGIN
(01729) ;
(01730) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01731) ;
(01732) ** MOVIR R4, FFTSTYP ; GET FFT TYPE TAG
(01733) ** XCT RPTFT0(R4) ; PUSH C8PU POST-SUPPORT MODULE ADDR
(01734) ** PUSHMIL R3, AFDTSORG+2*WS(R2)
(01735) ;
(01736) ***** END REPEAT FFT CODE INSERTION *****
(01737) ;
04392 2632 (01738) INCR R3, 2
04393 7771 (01739) DFOP R7, 1
(01740)
04394 C4H76FFC (01741) PUSHMI R3, 8-2*WS(R3) STORE APS MODULE SIZE
(01742) ** MOVIR R4, -3*HS(R3) GET SPECIAL SUPPORT SEMAPHORE
(01743) ** MOVWK R4, R4, MSKSLAYT
(01744) ** LRS R4, R
(01745) ** PUSHXI R3, R4 STORE SPECIAL SUPPORT SEMAPHORE
(01746) ** MOVIR R5, R1 POINT TO SCALAR A ID
(01747) ** INCR R5, HS STORE SCALAR A IDENTIFIER
(01748) ** MOVWK R4, R5, MSKSRBYT
(01749) ** PUSHXII R3, R4 STORE FLAG G1
(01750) INCR R3, 4
04396 2634 (01751) PUSHMI R3, GSF1GS+HS
04397 C430439C (01752) INCR R3, 4
(01753) ** PUSHMI R3, ZERO SET FOR NO PRF
(01754) ;
(01755) ;
04399 80000F63 (01756) JMP APSHNDRO AND EXIT
(01757) ** DATA STORAGE FOR SPECIAL FFT BINDING
(01758) **
(01759) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01760) ***
(01761) ** EXECUTION LIST FOR POST-SUPPORT ADDR QUEUING
(01762) **
(01763) **
(01764) **
(01765) ** RPTFT70 PUSHMIL R3, AFDTSORG+2*WS(R2) ; C8PU PRE-SUPPORT ADDR FOR 'MIXED' FF
  
```

```

(01766) **      PUSHML R3, RPTFT21      ; POST-SUPPORT ADDR FOR REPEAT FFT
(01767) **
(01768) **RPTFT21 ADDR      RPTFT31( ,1,SF)      ; POST-SUPPORT ENTRY ADDR
(01769) **
(01770) **
(01771) **FFFTSTYP DATA      MSS      ; FFT TYPE TAG
(01772) **LOG2SZ DATA      MSS      ; TEMPORARY STORAGE OF LOG2(FFT-SIZE)
(01773) **MAXSMP DATA      MSS      ; MAXIMUM SAMPLE INDEX - 1
(01774) **
(01775) **      EVEN      ; MAKE THIS BLOCK EVEN FOR LATER TRANS
(01776) **SMPSPC DATA      MSS      ; INPUT SAMPLE SPACING
(01777) **FFTSIZ DATA      MSS      ; SINGLE FFT SIZE
(01778) **NFFT DATA      MSS      ; NUMBER OF FFT'S TO DO
(01779) **INSPC DATA      MSS      ; INPUT FFT VECTOR SPACING
(01780) **OUTSPC DATA      MSS      ; OUTPUT FFT VECTOR SPACING
(01781) **
(01782) **      DATA      MSS      ; NEED FULL WORD BOUNDARY
(01783) **
(01784) **LDSUNS DATA      F'0.0'      ; ROUND <LOAD URASE> INSTR
(01785) **LDSYMS DATA      F'0.0'      ; ROUND <LOAD YRASE> INSTR
(01786) *
(01787) * ***** END REPEAT FFT CODE INSERTION *****
(01788) *
(01789) GSEIGS DATA      $4, $25      ; CODES FOR CONTROLLING FLAGS GO & G1
(01790) PWR2$ DATA      MSS      STORAGE FOR POWER OF TWO
(01791) ?
(01792) RUSTRI. DATA      CSMS+MS*CSML      ; TABLE OF ADDRS FOR BINDING HU
(01793) DATA      CSMS+MS*CSMF
(01794) DATA      CSMS+MS*CSML2S
(01795) DATA      CSMS+MS*CSML4S
(01796) DATA      CSMS+MS*CSML5S
(01797) DATA      0
(01798) **      REPEAT FFT POST-SUPPORT MODULE
(01799) **
(01800) **      EVEN
(01801) **RPTFT31 MOVIR      R7, RPTFT-2
(01802) **      MOVIR      R6, 3
(01803) **      RMOVEI, R7, R6, RPTSPRM
(01804) **      MOVZM      APSASSS
(01805) **      HOP      RPTFT33
(01806) **
(01807) **RPTFT30 RCT      ; RETURN FROM INTERRUPT
(01808) **      EVEN      ; GET TO FULL WORD BOUNDARY
  
```

0439H 0004  
 0439C 0025  
 0439D 0000  
 0439F 456C  
 0439F 4570  
 043A0 4574  
 043A1 4578  
 043A2 457F  
 043A3 0000

```

(01809) **RPTFT13 MOVIR H7,RPTSURS-2 ; POINT BEFORE <LOAD URASE> INSTRUCTIO
(01810) ** MOVIR R6,1 ; 2 FULL WORDS TO TRANSFER TO APS
(01811) ** LPRDCL H7,R6,APSR ; WRITE TO APS
(01812) ** MOVIM FLGSET+FLGSR1,SYSSFLGS ; SFT R1 -- TURN ON AP
(01813) ** DECM RPTSCNT ; COUNT THIS FFT
(01814) ** SKP ; SKIP THE HOP IF DONE
(01815) ** HOP RPTFT12 ; GO COMPUTE NEXT SET OF BASE ADDR'S
(01816) **;
(01817) ** RET ; RELEASE THE CSPI
(01818) ** EVEN ; GET TO FULL WORD ROUNDARY
(01819) ** JMP APSDOME ; ALL DONE -- LET EXEC CONTINUE
(01820) **;
(01821) **RPTFT13 MOVIRL R4,RPTSURS ; GET <LOAD URASE> INSTRUCTION
(01822) ** MOVIRL R6,RPTSURS ; GET <LOAD URASE> INSTRUCTION
(01823) ** ADDR R5,RPTSOSP ; ADD SPACING TO NEXT VECTOR BASE
(01824) ** ADC R4 ; COMPLETE THE COMPUTATION
(01825) ** ADDR R7,RPTSISP ; ADD SPACING TO NEXT VECTOR BASE
(01826) ** ADC R6 ; COMPLETE THE COMPUTATION
(01827) ** MOVIRL R4,RPTSURS ; STORE THE INSTRUCTION BACK
(01828) ** MOVIRL R6,RPTSURS ; STORE THE INSTRUCTION BACK
(01829) ** HOP RPTFT10 ; GO BACK TO LOOP START FOR RET
(01830) **;
(01831) ** EVEN ;
(01832) **;
(01833) **RPTSPRM ; DATA TABLE FOR UPDATING APS CODE
(01834) **RPTSCNT DATA M55 ; CURRENT COUNT FOR FFT
(01835) **RPTSISP DATA M55 ; CURRENT SPACING TO NEXT INPUT VECTOR
(01836) **RPTSOSP DATA M55 ; CURRENT SPACING TO NEXT OUTPUT VECTO
(01837) ** DATA M55 ; POSITION HOLDER
(01838) **RPTSURS DATA F'0.0' ; CURRENT INPUT VECTOR BASE ADDR
(01839) **RPTSURS DATA F'0.0' ; CURRENT OUTPUT VECTOR BASE ADDR

```



PAGE 54: SNAP-II MAP-300 ARITH. MODULES - PROG. #830101.03 MAY 7, 1980  
FFT - AP PROGRAMS

AOR 043BC 00000000 (018R5) NOP  
AOC 043BE 1000002H (018H6) JUMP(CR4FS)

(018R4) ;  
(018R5)  
(018H6)  
(018R7) ;

```

(01888) ; SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD
(01889) ; SCRAMBLE AND FORWARD RADIX 4 STAGE OF FFT,G1 CLEAR
(01890) ;
(01891) ; FUNCTION
(01892) ; THE FIGHT SUCCESSIVE INPUTS ARE PROVIDED TO LOOP
(01893) ; R00,R01,I01,I00,I02,I03,R03,R02
(01894) ; THE INTERMEDIATE RADIX TWO RESULTS ARE CALCULATED
(01895) ; S0=U0+U1,S1=U0-U1,S2=U2+U3,S3=U2-U3
(01896) ; THE OUTPUTS THEN BEING GIVEN BY
(01897) ; Y0=S0+S2,Y1=S1-JS3,Y2=S0-S2,Y3=S1+JS3
(01898) ; THE ACTUAL OUTPUT SEQUENCE BEING
(01899) ; RY0,RY1,IY0,IY1,IY2,IY3,RY2,RY3
(01900) ;
(01901) ; APU INITIALIZATION
(01902) ;
(01903) ;
(01904) CSM4F      MOV(10A,A0)      A0=R00\R00
(01905)           MOV(10A,A1)      A1=R01\R01
(01906)           MOV(10A,A3)      A3=I01\I03
(01907)           ADD(A0,A1)\SUR(A0,A1)
(01908)           MOV(10A,A2)      A2=I00\I00
(01909)           JUMP(CSM4FS)
(01910) ;
(01911) ;
(01912) ; CSM4F, APU INNER LOOP
(01913) ;
(01914) #1        MOV(00),ADD(A5,A6)\MOV(00),SUR(A5,A7) 00=RY0\RY1
(01915)           MOV(10A,A0)      A0=R00
(01916)           MOV(00),SUR(A5,A6)\MOV(00),ADD(A5,A7) 00=IY0\IY1
(01917)           MOV(10A,A1)      A1=R01
(01918)           MOV(00),SUR(A4,A7)\MOV(00),SUR(A4,A6) 00=IY2\IY3
(01919)           MOV(10A,A3)      A3=I01
(01920)           MOV(00),ADD(A0,A1)\MOV(00),SUR(A0,A1) 00=RY2\RY3
(01921)           MOV(10A,A2)      A2=I00
(01922)           CSM4FS
(01923)           MOV(A4),ADD(A7,A3)\MOV(A4),SUR(A2,A3)  A4=RS0\MS1
(01924)           MOV(10A,A0)      A0=I02
(01925)           MOV(10A,A1)      A1=I03
(01926)           MOV(A5),ADD(A0,A1)\MOV(A5),SUR(A0,A1)  A5=IS0\IS1
(01927)           MOV(10A,A3)      A3=R03
(01928)
(01929)
(01930)
(01931)

```

PAGE 56: SNAP-11 MAP-100 APITH, MODULES - PROG. BR30101.03 MAY 7, 1980  
SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD

A20 043F6 08F20HF2 (01932)	MOV(10A,A2)	A2=RU2
(01933)		
A21 043F8 43564H56 (01934)	MOV(A6),ADD(A7,A3)\MOV(A6),SUR(A2,A3)	A6=IS2\IS3
(01935)		
A22 043FA 47974697 (01936)	MOV(A7),ADD(A4,A7)\MOV(A7),ADD(A4,A6)	A7=RS2\RS3
(01937)		
A23 043FC 90160013 (01938)	JUMPC(81,FWI)	
(01939)		
A24 043FF 46HC4FRC (01940)	MOV(00),ADD(A5,A6)\MOV(00),SUR(A5,A7)	00=RY0\RY1
A25 043F0 4PHC47HC (01941)	MOV(00),SUR(A5,A6)\MOV(00),ADD(A5,A7)	00=LY0\LY1
A26 043F2 4F9C4F9C (01942)	MOV(00),SUR(A4,A7)\MOV(00),SUR(A4,A6)	00=LY2\LY3
A27 043F4 089C089C (01943)	MOV(R,00)	00=RY2\RY3

(01944) ; SUCCESSIVE RADIX-4 STAGES, FORWARD  
(01945) ;  
(01946) PUSES APS PROGRAM CR4A  
(01947) ;  
(01948) ; MATHEMATICS  
(01949) ;  
(01950) ; W=A+HF+CF2+DF3=PA+O  
(01951) ; X=A-JRF-CF2+JDF3=ER-S  
(01952) ; Y=A-RF+CF2-DF3=P=O  
(01953) ; Z=A+JRF-CF2-JDF3=ER+S  
(01954)  
(01955) ; P=A+CF2, R=A-CF2  
(01956) ; Q=F(R+DF2), S=JE(H-DF2)  
(01957)  
(01958) ; PR=AR+CRCOS2X+CTISIN2X  
(01959) ; PT=AT+CTCOS2X-CRSIN2X  
(01960) ; PR=AR-CHCOS2X-C1SIN2X  
(01961) ; PT=AT-C1COS2X+CHSIN2X  
(01962) ; OR=ARCOSX+H1SINX+DRCOS3X+DISIN3X  
(01963) ; O1=RTCOSX-RRSINX+DICUS3X-DRSIN3X  
(01964) ; SR=RSINX-R1COSX+DICUS3X-DRSIN3X  
(01965) ; S1=RCOSX+R1SINX-DRCOS3X-DISIN3X  
(01966) ;  
(01967) ; SINX STORED IN M1\M5  
(01968) ; SIN2X STORED IN M6\M2  
(01969) ; SIN3X STORED IN M7\M3  
(01970) ; COSX STORED IN M5\M1  
(01971) ; COS2X STORED IN M2\M6  
(01972) ; COS3X STORED IN M3\M7  
(01973)  
(01974) FJFCT



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(01975) JCR4F=PIPF.LINE STARTUP
(01976)
(01977)
A28 043F6 202A202A CR4FS CLEAR(AF2)
A29 043F8 20292029 CR4FSA CLEAR(AFI)
A30 043FA 202H202H CR4FSA CLEAR(AFO)
A2R 043FC 1A8D168D K(1)
A2C 043FE 080908F0 MOV(ZERO,M1)\MOV(1QA,A0)
A2D 04400 08F0080D MOV(1QA,A0)\MOV(ZERO,M5)
(01981)
(01982)
(01983)
(01984)
(01985)
(01986)
A2F 04402 08D008H9 MOV(R,M5)\MOV(R,M1)
A31 04404 08A008FF MOV(R,M2)\MOV(R,M6)
A32 04406 08H008FF MOV(R,M3)\MOV(R,M7)
(01989)
(01990)
A31 04408 08F1080A MOV(1QA,A1)\MOV(ZERO,M2)
A32 0440A 080E08F1 MOV(ZERO,M6)\MOV(1QA,A1)
A33 0440C 080E080H MOV(ZERO,M7)\MOV(ZERO,M3)
(01993)
(01994)
A34 0440E 08F808FF MOV(1QA,M0)
A35 04410 84A08440 MUL(M0,M6)
A36 04412 0A200720 NEG(A1)\R(A1)
(01997)
(01998)
A37 04414 10000053 JUMP(CR4FF)
(01999)
(02000)
(02001) JCR4F:CSINE ENTRY
(02002) ?
A38 04416 08E008F9 MOV(1QA,M5)\MOV(1QA,M1)
A39 04418 08F908FF MOV(1QA,M1)\MOV(1QA,M5)
A3A 0441A 08A008A0 MOV(P,NUCL)
A3B 0441C 08F008FA MOV(1QA,M6)\MOV(1QA,M2)
A3C 0441E 08EA08FF MOV(1QA,M2)\MOV(1QA,M6)
A3D 04420 08E008FF MOV(1QA,M3)\MOV(1QA,M7)
A3E 04422 08F008FF MOV(1QA,M7)\MOV(1QA,M3)
(02010) ?
(02011) JCR4F=BITTFFELY
(02012)
A3F 04424 08F808FF MOV(1QA,M0)
A40 04426 4A744474 MOV(A4),MULN(M0,M7)\MOV(A4),MUL(M0,M7)
A41 04428 4A954455 MOV(A5),SQR(A4,A2)\MOV(A5),ADD(A2,A4)
A42 0442A 08E008FC MOV(1QA,M4)
A43 0442C 4A724472 MOV(A2),SQR(A3,A2)
(02018) ?
A0=DI\DR
SINX=0=M1\M5
COSX=1=M5\M1
COS2X=1=M2\M6
COS3X=1=M3\M7
A1=RI\BR
SIN2X=0=M6\M2
SIN3X=0=M7\M3
M0=CR
P=CRSIN2X\CRCOS2X
R=-RI\BR
COSX TO M5\M1
SINX TO M1\M5
SIN2X TO M6\M2
COS2X TO M2\M6
COS3X TO M3\M7
SIN3X TO M7\M3
DR TO M0
A4=CTCOS2X\CISIN2X
A5=DI\DR
DI TO M4
A2=CTCOS2X-CRSIN2X
\CISIN2X+CRCOS2X
    
```

A44	0442E	R540A590	(02021)	MOV(A0),MUL(M3,M4)\MOV(A0),MULN(M3,M4)	A0=DRSIN3X\DRCOS3X
A45	04430	427R427R	(02022)	MOV(EX0),ADD(A1,A2)	FXER\RR
A46	04432	0A560R56	(02023)	MOV(EX1,A6)	A6=RR\RI
A47	04434	0RFR0RFR	(02024)	MOV(10A,M0)	RI TO M0
A48	04436	47D447D4	(02025)	MOV(A4),ADD(A6,A7)	A4=PI\PR
			(02026)		
			(02027)		
A49	0443R	R431A431	(0202R)	MOV(A1),MUL(M0,M5)\MOV(A1),MULN(M0,M5)	A1=DRCOS3X\DISIN3X
A4A	0443A	4R3C403C	(02024)	MOV(00),SUR(A1,A0)\MOV(10),ADD(A1,A0)	00=ZR\ZI
A4R	0443C	0RRC0RRC	(02030)	MOV(10A,M4)	RR TO M4
A4C	0443F	4D904D90	(02031)	MOV(A0),SUR(A4,A5)	A0=DRCOS3X-DRSIN3X
			(02032)		\DISIN3X+DRCOS3X
			(02033)		
			(02034)		
			(02035)		
A4D	04440	A491R491	(02036)	MOV(A1),MULN(M1,M4)\MOV(A1),MUL(M1,M4)	A1=RICOSX\RSINX
A4E	04442	4RDC4RDC	(02037)	MOV(00),SUR(A6,A7)	00=YI\YR
A4F	04444	0RFR0RFR	(0203R)	MOV(10A,M0)	CR TO M0
A50	04446	4S4C4S4C	(02039)	MOV(00),ADD(A4,A5)	00=KR\XI
			(02040)		
			(02041)		
A51	0444R	A452R452	(02042)	MOV(A2),MULN(M0,M6)\MOV(A2),MULN(M0,M6)	A2=ARSINX\RCOSX
A52	0444A	4R5C473C	(02043)	MOV(00),SUR(A2,A1)\MOV(00),ADD(A1,A2)	00=WR\WI
A53	0444C	0RRC0RRC	(02044)	MOV(10A,M4)	CI TO M4
A54	0444F	41114R31	(02045)	MOV(A1),ADD(A0,A1)\MOV(A1),SUR(A1,A0)	A1=RRSINX-RICOSX
			(02046)		\RRCOSX+RSINX
			(02047)		
			(0204R)		
A55	04450	R512A512	(02049)	MOV(A2),MUL(M2,M4)\MOV(A2),MULN(M2,M4)	A2=CRSTN2X\CRCOS2X
A56	04452	0RFR4037	(02050)	MOV(10A,A3)\MOV(A7),ADD(A1,A0)	A3=AI \ A7=SI
A57	04454	4R170R53	(02051)	MOV(A7),SUR(A0,A1)\MOV(10A,A1)	A7=SR \ A3=AR
A5R	04456	0Q2R0Q3F	(02052)	JUMPC(CR4FH,AF0),CLEAR	AF1:STAGE DONE
A59	0445R	0Q240Q3R	(02053)	JUMPC(CR4FC,AF1),CLEAR	AF0:COSINES USED
			(02054)		
			(02055)		
A5A	0445A	0RR40RR4	(02056)	CR4F-PIPELINE: CLEAR(IP	A4=CTCOS2X\CISTN2X
			(02057)	MOV(P,A4)	
			(0205R)		
			(02059)		
A5R	0445C	4A954A55	(02060)	MOV(A5),SUR(A4,A2)\MOV(A5),ADD(A2,A4)	A5=OI\OR
A5C	0445E	4A72A472	(02061)	MOV(A2),SUR(A3,A2)	A2=CTCOS2X-CRSIN2X
			(02062)		\CISTN2X+CRCOS2X





PAGE 62: SNAP-11 MAP-300 ARITH. MODULES - PROG. BR10101.03 MAY 7, 1980  
SCRAMBLE AND FIRST RADIX-7 STAGE, REVERSE

(02134) ; SCRAMBLE AND FIRST RADIX-4 STAGE, REVERSE  
 (02135) ; SCRAMBLE AND FIRST RADIX-4 STAGE OF IFFT,G1 CLEAR

(02136) ; FUNCTION

(02137) ; THE EIGHT SUCCESSIVE INPUTS ARE PROVIDED TO LOOP  
 (02138) ; R00,R01,R02,R03,R04,R05,R06,R07  
 (02139) ; THE INTERMEDIATE RADIX TWO RESULTS ARE CALCULATED  
 (02140) ; S0=S0+R1,S1=R0-R1,S2=R0+R1,S3=R0-R1  
 (02141) ; THE OUTPUTS THEN BEING GIVEN BY  
 (02142) ; Y0=S0+S2,Y1=S1+S3,Y2=S0-S2,Y3=S1-S3  
 (02143) ; THE ACTUAL OUTPUT SEQUENCE BEING  
 (02144) ; RY0,RY1,RY2,RY3,RY4,RY5,RY6,RY7,RY8,RY9

(02145) ; APU INITIALIZATION

(02146) ;

(02147) ;

(02148) ;

(02149) ;

(02150) CSM41

(02151) MOV(10A,A0)

(02152) MOV(10A,A1)

(02153) MOV(10A,A3)

(02154) ADD(A0,A1)\SUR(A0,A1)

(02155) MOV(10A,A2)

(02156) ; JUMP(CSM41S)

(02157) ;

(02158) ; CSM41, APU INNER LOOP

(02159) ;

(02160) B1

(02161) MOV(00),ADD(A5,A6)\MOV(00),ADD(A5,A7)

(02162) MOV(10A,A0)

(02163) MOV(00),SUR(A5,A6)\MOV(00),SUR(A5,A7)

(02164) MOV(10A,A1)

(02165) MOV(00),SUR(A4,A7)\MOV(00),ADD(A4,A6)

(02166) MOV(10A,A3)

(02167) MOV(00),ADD(A0,A1)\MOV(00),SUR(A0,A1)

(02168) MOV(10A,A2)

(02169) ;

(02170) ;

(02171) ;

(02172) CSM41S

(02173) MOV(A4),ADD(A2,A3)\MOV(A4),SUR(A2,A3)

(02174) MOV(10A,A0)

(02175) MOV(10A,A1)

(02176) MOV(A5),ADD(A0,A1)\MOV(A5),SUR(A0,A1)

(02177) MOV(10A,A3)

A0=R00\R00  
 A1=R01\R01  
 A3=R01\R01  
 A2=R00\R00

00=RY0\RY1  
 A0=R00

00=RY0\RY1  
 A1=R01

00=RY2\RY3  
 A3=R01

00=RY2\RY3  
 A2=R00

A4=RS0\RS1  
 A0=R02

A1=R03

A5=IS0\IS1  
 A3=R03

PAGE 64: SNAP-II MAP-300 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980  
SCRAMBLE AND FIRST RADIX-4 STAGE, REVERSE

A20 044C2 08F208F2 (02178)	MOV(10A,A2)	A2=RU2
A21 044C4 43564856 (02180)	MOV(A6),ADD(A2,A3)\MOV(A6),SUB(A2,A3)	A6=IS2\IS3
A22 044C6 47974897 (02182)	MOV(A7),ADD(A4,A7)\MOV(A7),SUB(A4,A6)	A7=RS2\RS3
A23 044C8 90160013 (02184)	JUMPC(R1,FMI)	
A24 044CA 46HC478C (02185)	MOV(00),ADD(A5,A6)\MOV(00),ADD(A5,A7)	00=RY0\RY1
A25 044CC 4FHC488C (02187)	MOV(00),SUB(A5,A6)\MOV(00),SUB(A5,A7)	00=IY0\IY1
A26 044CE 4F9C498C (02189)	MOV(00),SUB(A4,A7)\MOV(00),ADD(A4,A6)	00=IY2\IY3
A27 044D0 089C089C (02189)	MOV(R,00)	00=RY2\RY3

(02190) ; SUCCESSIVE RADIX-4 STAGES, REVERSE  
 (02191) ;  
 (02192) ;USFS APS PROGRAM CR4A  
 (02193) ;  
 (02194) ;MATHEMATICS  
 (02195) ;  
 (02196) ; W=A+R+CF2+DF3=PYO  
 (02197) ; X=A+JAF-CF2-JDF3=R+S  
 (02198) ; Y=A-R+CF2-DF3=P-O  
 (02199) ; Z=A-JAF-CF2+JDF3=R-S  
 (02200)  
 (02201) ; P=A+CF2,P=A-CF2  
 (02202) ; Q=F(R+DF2),S=JF(R-DF2)  
 (02203)  
 (02204) ; PR=AR+CRCS2X-CISIN2X  
 (02205) ; PJ=AJ+CI COS2X+CRSIN2X  
 (02206) ; PH=AP-CKCOS2X+CSIN2X  
 (02207) ; HI=AI-CICOS2X-CRSIN2X  
 (02208) ; GR=RCOSX-HISINX+DPCOS3X-DISIN3X  
 (02209) ; OI=RICOSX+RHSINX+DICUS3X+DRSIN3X  
 (02210) ; SH=RICOSX-RHSINX+DJCOS3X-DRSIN3X  
 (02211) ; SI=RPCOSX-HISINX-DRCOS3X+DISIN3X  
 (02212) ;  
 (02213) ; SINX STORED IN M1\M5  
 (02214) ; SIN2X STORED IN M6\M7  
 (02215) ; SIN3X STORED IN M7\M3  
 (02216) ; COSX STORED IN M5\M1  
 (02217) ; COS2X STORED IN M2\M6  
 (02218) ; COS3X STORED IN M3\M7  
 (02219)  
 (02220) ;EJECT



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(02221) ;CR41-PIPELINE STARTUP
(02222)
(02223)
A28 04402 202A202A CR41S CLEAR(AF2)
A29 04404 202A202A CR41SA CLEAR(AF1)
A2A 04406 202R202R (02226) CLEAR(AE0)
A2H 0440R 165016H0 (0222H) K(1)
A2C 0440A 0R090R0E0 (02229) MOV(ZFR0,M1)\MOV(10A,A0)
A2D 0440C 0R000R0D (02230) MOV(10A,A0)\MOV(ZFR0,M5)
(02231)
(02232)
A2E 0440F 0R0D0R0H9 (02233) MOV(R,M5)\MOV(R,M1)
A2F 04410 0R0A0R0R4 (02234) MOV(R,M2)\MOV(R,M6)
A30 04412 0R0R0R0R4 (02235) MOV(R,M3)\MOV(R,M7)
(02236)
A31 04414 0R010R0A (02237) MOV(10A,A1)\MOV(ZFR0,M2)
A32 04416 0R0E0R0F1 (02238) MOV(ZFR0,M6)\MOV(10A,A1)
A33 04418 0R0F0R0H (02239) MOV(ZFR0,M7)\MOV(ZFR0,M3)
(02240)
A34 0441A 0R0R0R0R4 (02241) MOV(10A,M0)
A35 0441C 0R000R0A0 (02242) MUL(M0,M6)
A36 0441E 0A2000720 (02243) NFG(A1)\K(A1)
(02244)
A37 0441F 10000053 (02245) JUMP(CR41E)
(02246)
(02247) ;CR41-CUSINE ENTRY
(02248) ;
A38 04412 0R0D0R0F9 (02249) MOV(10A,M5)\MOV(10A,M1)
A39 04414 0R0E0R0F0 (02250) MOV(10A,M1)\MOV(10A,M5)
A3A 04416 0R0A0R0A0 (02251) MOV(P, NULL)
A3B 04418 0R0F0R0FA (02252) MOV(10A,M6)\MOV(10A,M2)
A3C 0441A 0R0A0R0FA (02253) MOV(10A,M2)\MOV(10A,M6)
A3D 0441C 0R0R0R0R4 (02254) MOV(10A,M3)\MOV(10A,M7)
A3E 0441E 0R0F0R0FA (02255) MOV(10A,M7)\MOV(10A,M3)
(02256) ;
(02257) ;CR41-RUTTFELY
(02258)
A3F 04500 0R0R0R0R4 (02259) MOV(10A,M0)
A40 04502 0A14H474 (02260) MOV(A4),MUL(M0,M7)\MOV(A4),MUL(M0,M7)
A41 04504 4A954455 (02261) MOV(A5),SQR(A4,A2)\MOV(A5),ADD(A2,A4)
A42 04506 0R0C0R0FC (02262) MOV(10A,M4)
A43 04508 4A124A72 (02263) MOV(A2),SQR(A3,A2)
(02264) ;
(02271)
A0=01\DR
SINX=0=M1\M5
COSX=1=M5\M1
COS2X=1=M2\M6
COS3X=1=M3\M7
A1=R1\RR
SIN2X=0=M6\M2
SIN3X=0=M7\M3
M0=CR
P=CRSIN2X\CRCOS2X
R=-R1\RR
COSX TO M5\M1
SINX TO M1\M5
SIN2X TO M6\M2
COS2X TO M2\M6
COS3X TO M3\M7
SIN3X TO M7\M3
DR TO M0
A4=COS2X\-CISIN2X
A5=01\OR
DI TO M4
A2=COS2X+CRSIN2X
\-CISIN2X+CRCOS2X

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(022655)
(022661)
A44 0450A 8520M590
(022667)
A45 0450C 4274127R (022668)
A46 0450E 08560R56 (022693)
A47 04510 08F00MFR (022700)
A48 04512 48444FD4 (022711)
(022712)
(022713)
A49 04514 8411H431 (022714)
A4A 04516 484C403C (022715)
A4B 04518 08F00MFR (022716)
A4C 0451A 40904D90 (022717)
(022718) ;
(022719)
(022740)
(022741)
A4D 0451C 8491H491 (022742)
A4E 0451E 470C47DC (022743)
A4F 04520 08F00MFR (022744)
A50 04522 459C459C (022746)
(022747)
(022748)
A51 04524 8452R452 (022749)
A52 04526 405C423C (022750)
A53 04528 08F00MFR (022751)
A54 0452A 41114831 (022752) ;
(022753)
(022754)
A55 0452C 8512H512 (022755)
A56 0452E 08F34037 (022756)
A57 04530 49170RF4 (022757)
A58 04532 9028003F (022758)
A59 04534 9029003R (022759)
(022800)
(023011)
A5A 04536 08H40RH4 (023012) ; CR41-PIPELINE CLEARUP
(023013)
(023041)
(02305)
A5B 04538 4A954455 (02306)
A5C 0453A 4A72A472 (02307)
(02308) ;
(022655)
MOV(A0), MUL(M3, M4) \ MOV(A0), MUL(M3, M4)
MOV(EX0), ADD(A3, A2)
MOV(EX1), A6
MOV(10A, M0)
MOV(A4), SUB(A6, A7)
(022712)
MOV(A1), MUL(M0, M5) \ MOV(A1), MUL(M0, M5)
MOV(00), SUB(A1, A0) \ MOV(00), ADD(A1, A0)
MOV(10A, M4)
MOV(A0), SUB(A4, A5)
(022718) ;
MOV(A1), MUL(M1, M4) \ MOV(A1), MUL(M1, M4)
MOV(00), ADD(A6, A7)
MOV(10A, M0)
MOV(00), ADD(A4, A5)
(022748)
MOV(A2), MUL(M0, M6) \ MOV(A2), MUL(M0, M6)
MOV(00), SUB(A2, A1) \ MOV(00), ADD(A1, A2)
MOV(10A, M4)
MOV(A1), ADD(A0, A1) \ MOV(A1), SUR(A1, A0)
(022921) ;
MOV(A2), MUL(M2, M4) \ MOV(A2), MUL(M2, M4)
MOV(10A, A3) \ MOV(A7), ADD(A1, A0)
MOV(A7), SUR(A0, A1) \ MOV(10A, A3)
JUMPC(CR41F, AF0), CLEAR
JUMPC(CR41C, AF1), CLEAR
(023011)
CR41-PIPELINE CLEARUP
MOV(P, A4)
(02306)
MOV(A5), SUR(A4, A2) \ MOV(A5), ADD(A2, A4)
MOV(A2), SUR(A3, A2)
(02308) ;
A0=-DRSIN3X\DRCOS3X
FX=RI\RR
A6=HR\RI
HI TO M0
A4=PI\PR
A1=DICOS3X\DISIN3X
OQ=ZRVZI
RR TO M4
A0=DICOS3X+DRSIN3X
\DISIN3X+DRCOS3X
A1=DICOSX\RSINX
OQ=VI\VR
CR TO M0
OQ=XR\XI
A2=-RRSINX\BRCOSX
OQ=WR\WI
CI TO M4
A1=-RRSINX-RICOSX
\BRCOSX-RISINX
A2=-CRSIN2X\CRCOS2X
A3=AI \ A7=SI
A7=SR \ A3=AR
AF1STAGE DONE
AF0;COSINES USED
A4=CTCOS2X\DISIN2X
A5=OI\OR
A7=CTCOS2X+CRSIN2X
\DISIN2X+CRCOS2X

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0455C 00004664	(02340) ;	APS PROGRAMS	
0455F 00004564	(02341) ;		
04560 0000	(02342) ;		
04561 0100	(02343) ;	START OF HEADER BLOCK	
04562 00000000	(02344) ;		
	(02345) ;	EVEN	
	(02346) ;	ADDR CSMS1	
	(02347) ;	ADDR CSMS+2*CSMSS	
	(02348) ;	DATA 0	
	(02349) ;	DATA CR4AS7	
	(02350) ;	ADDR CR4ASA	
	(02351) ;	EVEN	
	(02352) ;		
	(02353) ;		

START ON WORD BOUNDARY  
 PTR TO CONSTR INSTR BLOCK (NONE)  
 PTR TO SCALAR BLOCK (NONE)  
 NO SCALARS  
 MODULE SIZE  
 PTR TO CHAIN ANCHOR  
 END OF BOUNDARY

(02354) ; CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)  
 (02355) ; APS PROGRAM PROVIDING SCRAMBLE FOR COMPLEX FFT  
 (02356) ; MAY BE USED WITH THE MAP-300 APU PROGRAMS  
 (02357) ; CSM2(Y,U), CSM4(Y,U), CSM4(Y,U)  
 (02358) ;  
 (02359) ; RESTRICTIONS  
 (02360) ; IN PLACE OPERATION NOT PERMITTED  
 (02361) ; BUFFER SIZES MUST BE 1024 OR LESS  
 (02362) ; Y BUFFER MUST BE COMPACT 32 BIT FLOATING  
 (02363) ;  
 (02364) ;  
 (02365) ; BINDING PARAMETERS  
 (02366) ;  
 (02367) ; N# OF POINTS IN FFT (USIZE)  
 (02368) ; HU=USEP\*N/2  
 (02369) ; QU=HU/2  
 (02370) ; APS-INPUT ADDRESS SEQUENCE  
 (02371) ; HU(K), RUC(K+N/2), IU(K+N/2), IU(K), IU(K+N/4)  
 (02372) ; IU(K+3N/4), RUC(K+N/4), HU(K+N/4), FOR UC=K+N/4  
 (02373) ; THUS PROVIDING REVERSAL OF TWO BITS  
 (02374) ;  
 (02375) ; APS-OUTPUT ADDRESS SEQUENCE  
 (02376) ; RW(J), RW(J+1), IU(J), IU(J+1), RW(J+2),  
 (02377) ; RW(J+3), IU(J+2), IU(J+3)  
 (02378) ; WHEN JE BIT REVERSAL OF K  
 (02379) ; THE DIFFERENCE, D(K) = J(K+1)-J(K) IS  
 (02380) ; PROVIDED BY THE P3 SUBROUTINE  
 (02381) ;  
 (02382) ;  
 (02383) ; CSMS REGIN APS(CSM)  
 (02384) ;  
 (02385) ; CSMS JSM(CSMU,P2)  
 (02386) ;  
 (02387) ; CSML6S LOAD(HRO,MSS,I,TF)  
 (02388) ; CSML0S LOAD(HRI,MSS)  
 (02389) ; JUMP(CSMF,PA),SET  
 (02390) ;  
 (02391) ; CSML SUR(HRO,MSS,TF)  
 (02392) ;  
 (02393) ; CSML1S SUR(HRO,MSS,TF)  
 (02394) ; CSML ADD(HRO,MSS,TF)  
 (02395) ; ADD(HRO,2,TF)  
 (02396) ; CSML2S SUR(HRO,MSS,TF)  
 (02397) ;  
 A00 04564 00707540 INPT RUC(0) [URASE ROUND]  
 A01 04566 02000000 [USIZE-1 ROUND]  
 A02 04568 04500000 TURN ON APU  
 A03 0456A 06300672 INPT RUC(N/4) [HU ROUND]  
 A04 0456C 08820000 INPT RUC(N/2) [HU ROUND]  
 A05 0456E 0A820000 INPT RUC(N/2) [HU ROUND]  
 A06 04570 0C8A0000 INPT RUC(N/2) [HU ROUND]  
 A07 04572 0E8A0002 INPT IU(K) [HU ROUND]  
 A08 04574 10820000 INPT IU(K) [HU ROUND]

PAGE 71: SNAP-II MAP-100 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980  
CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)

A09 04576 128A0000 (02398) CSML3S	ADD(RR0,MSS,TF)	INPT IU(K*4) (OU ROUND)
A0A 04578 148A0000 (02399) CSML4S	ADD(RR0,MSS,TF)	INPT IU(K*3N/4) (HU ROUND)
A0H 0457A 16820002 (02400)	SUR(RP0,2,TF)	INPT RU(K*3N/4)
	(02401) ?	
A0C 0457C 18190484 (02402)	SURI(RR1,4),JUMPP(CSM1)	
	(02403) ?	
A0D 0457E 1A820000 (02404) CSML5S	SUR(RK0,MSS,TF)	INPT RU(LAST) (HU ROUND)
A0E 04580 1C305977 (02405)	JUMP(CR4AF,WI),SFT	
	(02406) ?	

(02407) ? CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)  
 (02408) ?  
 (02409) ? DATA POINT ADDRESS AT ENTRY 4\*(J(K)-N)+WBASE  
 (02410) ? DATA OUTPUT ADDRESS GENERATED (4J(K+1))+WBASE  
 (02411) ?  
 (02412) ? ITERATION EQUATION, J=J(K)=K, HIT REVERSED  
 (02413) ? 4J(K+1)=4\*(J(K)-N)+DM  
 (02414) ? WBASE = NUMBER OF TRAILING 1'S IN (K)

(02415) ?  
 (02416) ? BINDING CONSTANTS  
 (02417) ?  
 (02418) ? P0=4\*(3\*N/2)  
 (02419) ? P1=D0/2  
 (02420) ? P2=D1/2  
 (02421) ? P3=D2/2  
 (02422) ? P4=D3/2  
 (02423) ? P5=D4/2  
 (02424) ? P6=D5/2  
 (02425) ? P7=D6/2  
 (02426) ?  
 (02427) ?

LOADS P1 FOR CR4A

A0F 04582 1F101840	(02428) ?	JSN(ANGLE, P1)	(D0 ROUND)
A10 04584 20FF0000	(02429) ?	ADD(HW0, MSS, TF, C)	(D1 ROUND)
A11 04586 22FF0000	(02430) ?	ADD(HW0, MSS, TF, C)	(D2 ROUND)
A12 04588 24FF0000	(02431) ?	ADD(HW0, MSS, TF, C)	(D3 ROUND)
A13 0458A 26FF0000	(02432) ?	ADD(HW0, MSS, TF, C)	(D4 ROUND)
A14 0458C 28FF0000	(02433) ?	ADD(HW0, MSS, TF, C)	(D5 ROUND)
A15 0458E 2AFF0000	(02434) ?	ADD(HW0, MSS, TF, C)	(D6 ROUND)
A16 04590 2CFF0000	(02435) ?	ADD(HW0, MSS, TF, C)	(D7 ROUND)
A17 04592 2E201AF8	(02436) ?	JUMPS(SCRM4, AF0), CLEAR	
A18 04594 30FF0000	(02437) ?	ADD(HW0, MSS, TF, C)	(D3 ROUND)
A19 04596 32301068	(02438) ?	JUMP(SCRM0, AF0), SET	
A1A 04598 342010F9	(02439) ?	JUMPS(SCRM5, AF1), CLEAR	
A1B 0459A 36FF0000	(02440) ?	ADD(HW0, MSS, TF, C)	(D4 ROUND)
A1C 0459C 38301069	(02441) ?	JUMP(SCRM0, AF1), SET	
A1D 0459E 3A2020FA	(02442) ?	JUMPS(SCRM6, AF2), CLEAR	
A1E 045A0 3CFF0000	(02443) ?	ADD(HW0, MSS, TF, C)	(D5 ROUND)
A1F 045A2 3E30106A	(02444) ?	JUMP(SCRM0, AF2), SET	

PAGE 73: SNAP-II MAP-100 ARITH. MODULES - PROG. BR10101.03 MAY 7, 1980  
CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)

A20 045A4 402023FH (02451) SCRMK	JUMPS(SCHM,AF3),CIFAR	(06 ROUND)
A21 045A6 42HF0000 (02452) SCRM10S	ADD(RW0,MSS,TH,C)	
A22 045A8 4430106H (02453)	JUMP(SCHMD,AF3),SFT	
	(02454) ?	
A23 045AA 46HF0000 (02455) SCRM7	ADD(RW0,MSS,TH,C)	(07 ROUND)
A24 045AC 48001060 (02456)	JUMP(SCHMD)	
	(02457) ?	





```

(02482) ; SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)
(02483) ; PROGRAM ASSUMES SCRAMBLE COMPLETED
(02484) ;
(02485) ;
(02486) ; 17/14/77
(02487) ; SCRAMBLE LEAVES ADS AS FOLLOWS
(02488) ; INPUT - LAST COMMAND, JUMPICR4AP, M1, SET
(02489) ; INPUT-P1 LOCATED AT ANGLE
(02490) ; OUTPUT - ACTIVE, IN P2
(02491) ;
(02492) ; SS=STAGE SEPARATION, DATA SEPARATION A H C D
(02493) ; STARTING VALUES(4)APF
(02494) ; SSI=1, SCRAMBLE ONLY PRECEDING N=4*M
(02495) ; SSI=2, SCRAMBLE PLUS RADIX2 PRECEDING N=2*4*M
(02496) ; SSI=3, SCRAMBLE PLUS RADIX3 PRECEDING N=3*4*M
(02497) ; SSI=4, SCRAMBLE PLUS RADIX4 PRECEDING N=4*4*M
(02498) ; SSI=6, SCRAMBLE PLUS RADIX6 PRECEDING N=6*4*M
(02499) ;
(02500) ;
(02501) ; COSINE TABLE IS A REAL BUFFER WITH CONTENTS:
(02502) ; CT(K)=COS(2*PI*K/CSIZE)
(02503) ; RESTRICTION.
(02504) ; CSIZE MUST BE MULTIPLE OF N, FFT SIZE
(02505) ;
(02506) ; COSINE TABLE BINDING PARAMETERS
(02507) ;
(02508) ;
(02509) ; HPI=CSEP*(CSIZE/4) 90 DEG SEPARATION
(02510) ; REGISTER USAGE
(02511) ; RRO/RWO
(02512) ; RR1/RR1
(02513) ; RW7
(02514) ; RR3
(02515) ; RR2
(02516) ; RR3
(02517) ; FJFCT
  
```

DATA ADDRESSES  
 # OF USES OF A COSINE, COUNTER  
 # OF COSINES USED, COUNTER  
 STAGE SEPARATION  
 SEP BETWEEN COSINES WITHIN A SET  
 SEP BETWEEN SPTS OF COSINES

```

(02518)
A31 045C6 62700000 (02519) CR4A01 LOAD(RW3,MSS) (SS1 ROUND)
(02520) ?
(02521) ? CR4A-APS OUTPUT PROGRAM-STAGE INITIALIZATION
(02522) ?
(02523) ?
A32 045C8 64100024 (02524) CR4A02 ADDR(RW3,RW3)
A33 045CA 6631002F (02525) ADDR(RW3,RW3)
A34 045CC 68400000 (02526) CR4A1S LOAD(RW0,MSS) SET SEPARATION FOR STAGE
A35 045CF 6A010034 (02527) SURL(RW0,4) SET RW0=RW-4 (WRASE ROUND)
A36 045D0 6C210016 (02528) MOVX(RW2,RW3) SET WRASE-4
A37 045D2 6E090010 (02529) MOVX(RW0,RW0)
A38 045D4 70200037 (02530) CLEAR(W1)
(02531) ?
(02532) ? TEST FOR LAST STAGE
(02533) ? IF SD, CHANGE OUTPUT TO Y BUFFER INSTEAD
(02534) ? DF THE W BUFFER
(02535) ?
A39 045D6 72500000 (02536) CR4A3S LOAD(RW1, MSS) (USIZE-1 ROUND)
A3A 045D8 741149A6 (02537) SURR(RW1, RW3), JUMPP(CR4A03) JUMP IF NOT LAST STAGE
(02538) ?
(02539) ? LAST STAGE OUTPUT ADDRESSES
(02540) ?
A3B 045DA 76400000 (02541) CR4A10S LOAD(RW0, MSS) (WRASE ROUND)
A3C 045DC 78500000 (02542) CR4A11S LOAD(RW1, MSS) (YSEP*YSIZE/4 ROUND)
A3D 045DE 7ADA0000 (02543) CR4A0F ADD(RW0, MSS) (YSEP*YSIZE ROUND)
A3E 045F0 7C810022 (02544) SURR(RW0,RW1,TF) ZR
A3F 045F2 7E9A0002 (02545) ADD(RW0,2,TF) ZI
A40 045F4 80A10022 (02546) SURR(RW0,RW1,TF) YI
A41 045F6 82B20002 (02547) SURR(RW0,2,TF) YR
A42 045F8 84C30002 (02548) SURR(RW0,RW1,TF) XR
A43 045FA 86D40002 (02549) ADD(RW0,2,TF) XI
A44 045FC 88E50002 (02550) SURR(RW0,RW1,TF) WI
A45 045FE 8AF60002 (02551) SURR(RW0,2,TF) WR
(02552) ?
(02553) ?
(02554) ?
(02555) ? TEST FFT DONE
(02556) ?
A46 045F0 8C0A0000 (02557) CR4A12S ADD(RW0,MSS) (YSEP ROUND)
A47 045F2 8E203DAA (02558) JUMPC(CR4A0F,AF2),CLEAR
A48 045F4 90203470 (02559) JUMPC(CR4A1S,R0),CLEAR
(02560) ?
A49 045F6 92500000 (02561) CR4A03 LOAD(RW1,MSS) SET BUTTERFLY COUNT (USIZE-1 ROUND)

```

	MAP-300	ADDRESS	OPERATION	STARTING ADDRESS (4N+4 ROUND)
A4A	045F8	940A0000	(02562) CR4A78 ADD(RW0, MSS)	
			(02563)	
			(02564)	
A4H	045FA	94810026	(02565) CR4A04 SUBR(RW0, RW3, TF)	ZR
A4C	045FC	948A0002	(02566) ADD(RW0, 2, TF)	ZI
A4D	045FE	94810026	(02567) SUBR(RW0, RW3, TF)	YI
A4E	04600	94820002	(02568) SUB(RW0, 2, TF)	YR
A4F	04602	94810026	(02569) SUBR(RW0, RW3, TF)	XP
A50	04604	A08A0002	(02570) ADD(RW0, 2, TF)	XI
A51	04606	A2810026	(02571) SUBR(RW0, RW3, TF)	WI
A52	04608	A3820002	(02572) SUB(RW0, 2, TF)	WR
			(02573) ?TEST FOR NEW COSINES, ELSE OUTPUT NEXT 4 POINTS	
			(02574) ?	
A53	0460A	A61148A6	(02575) SUBR(RW1, RW3), JUMPP(CR4A04)	
			(02576) ?TEST STAGE DONE, ELSE GET NEW COSINES	
			(02577) ?	
A54	0460C	A82049AA	(02578) JUMPC(CR4A03, AF2), CLEAR	
			(02579) ?	
			(02580) ?STOPPING SHORT CHECK IS HERE	
			(02581) ?	
A55	0460E	AA500000	(02582) CR4A13S LOAD(RW1, MSS)	(FFTSIZE-1 ROUND)
A56	04610	AC1132A6	(02583) SUBR(RW1, RW3), JUMPP(CR4A02)	
A57	04612	AF203470	(02584) JUMP(CR4A15, R0), CLEAR	HALT APS OUTPUT
			(02585) ?	
			(02586) ?	

PAGE 10: SWAP-11 MAP-300 APITH. MODULES - PROG. B0101.03 MAY 7, 1980  
 SUCCESSIVE RADIX-4 STAGES (INPUT - P0)

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(02581) ; SUCCESSIVE RADIX-4 STAGES (INPUT - P0)
(02584)
(02589) CR4A1 SFT(W1)
(02590) ; STAGE INITIALIZATION
(02591) ;
(02592) CR4A2 LOAD(RR2,0)
(02593) CR4A3 MOVR(RR3,RR2,C)
(02594) ;
(02595) ; ANGLE SUBROUTINE,P1,INSERTS COSINE SEPARATION
(02596) ; FOR STAGE INTO RR3
(02597)
(02598)
(02599)
(02600)
(02601) CR4A7 SUBL(RR2,1),JUMP(CR4A3)
(02602) ; COSINE ENTRY
(02603) ;
(02604) CR4A4 LOAD(RR1,MSS,I)
(02605) CR4A5 ADDR(RR2,RR3)
(02606)
(02607)
(02608)
(02609) CR4A6 ADDR(RR1,RR2,TF)
(02610) CR4A5 ADDR(RR1,MSS,TF)
(02611) CR4A6 SUB(RR1,MSS,FF)
(02612) CR4A7 ADDR(RR1,RR2,TF)
(02613) CR4A3 ADD(RR0,MSS,TF)
(02614) CR4A8 LOAD(RR1,MSS)
(02615)
(02616) ; CR4A-APS BUTTERFLY INPUT
(02617) ;
(02618)
(02619) CR4A4 SUBR(RR0,RR3,TF)
(02620) ADDR(RR0,2,TF)
(02621) SUBR(RR0,RR3,TF)
(02622) SUBL(RR0,2,TF)
(02623) SUBR(RR0,RR3,TF)
(02624) ADDR(RR0,2,TF)
(02625) SUBR(RR0,RR3,TF)
(02626)
(02627) ; TEST IF NEW COSINE NEEDED, ELSE INPUT NEXT 4 POINTS
(02628) ;
(02629)
(02630) SUBR(RR1,RR3),JUMP(CR4A5)
SFT(AP0)
  
```

TELL AP0 COSINES COMING

PAGE 79: SNAP-11 MAP-100 ARITH. MODELS - PROG. #830103.03 MAY 7, 1980  
SUCCESSIVE RADIX-4 STAGES (INPUT - 00)

```
(02631) ; TEST IF STAGE DONE, ELSE GET NEW COSINES
(02632)
A70 04644 F0715C44 (02633)      SHRL(RW2,4),JUMPP(CR4A2)      TELL API STAGE DONE
A71 04646 F2300029 (02634)      SFT(API)
(02635) ;***** CHANGE VALUE HERE TO STOP SHORT *****
(02636) ;TO STOP SHORT 1 STAGE, REND USIZP/4-1 HERE
A72 04648 F4500060 (02637)      CR4A5  LOAD(RR1,MSS)      LEFTSIZE-1 ROUND)
A73 0464A F6490032 (02638)      SHRL(RR0,2,TF)          LAST DATA POINT
(02639)
(02640) ; TEST IF FFT DONE, ELSE START NEXT STAGE
(02641)
A74 0464C F81958A6 (02642)      SHRR(RR1,RW3),JUMPP(CR4A1)  GO HACK
A75 0464E FA205071 (02643)      JUMP(CR4AF,RT),CLEAR      HALT APS INPUT
(02644)
A76 04650 FC890032 (02645)      CR4A5  SHRL(RR0,2,TF)      AR
A77 04652 FF006760 (02646)      JUMP(CR4A4)              BACK TO BUTTERFLY
(02647)
```

PAGE 80: SNAP-II MAP-100 ARITH. MODULES - PROG. RR40101.03 MAY 7, 1980  
 ANGULAR SEPARATION SUBROUTINE (INPUT - P1)

(02648) ? ANGULAR SEPARATION SUBROUTINE (INPUT - P1)  
 (02649) ? STANG=HPI/SSI  
 (02650) ? S2ANG=S1ANG/4  
 (02651) ? STANG=2ANG/4  
 (02652) ? S4ANG=S1ANG/4  
 (02653) ? S5ANG=S4ANG/4  
 (02654) ? S6ANG=S5ANG/4  
 (02655) ? STANG=S6ANG/4  
 (02656)  
 (02657)

A78 04654 F3F0000 ANGLE SFT(R0) (S1ANG ROUND)  
 A79 04656 F2F0000 ANGLE15 AFD(HR3,MSS,C) (S2ANG ROUND)  
 A7A 04658 F4F0000 ANGLE25 ADD(HR3,MSS,C) (S3ANG ROUND)  
 A7B 0465A F6F0000 ANGLE35 ADD(HR3,MSS,C) (S4ANG ROUND)  
 A7C 0465C F8F0000 ANGLE45 ADD(HR3,MSS,C) (S5ANG ROUND)  
 A7D 0465E FAF0000 ANGLE55 ADD(HR3,MSS,C) (S6ANG ROUND)  
 A7E 04660 FC3F0000 ANGLE65 ADD(HR3,MSS,C) (S7ANG ROUND)  
 A7F 04662 FE3F0000 ANGLE75 ADD(HR3,MSS,C)  
 00000000 (02666) ?  
 04664 (02667) CRRASA=BC ASSIGN VALUE TO CHAIN  
 (02668) ? FND #A-1 END OF MODULE  
 00004664 (02670) CSMS1 #L = #L + 2#0  
 00000100 (02672) CRRASZ = #L - CSMS  
 (02673) ?  
 (02674) ?

VMUV

	(02675) *	VMUV	
	(02676) *	AP ROUTINES FOR VMUV	
	(02677) *		
	(02678) *		
	(02679) *		
	(02680) *	API03-VMUV	
	(02681) *	RINDS TO APS3-VI124	
	(02682) *	FO. Y=U	
	(02683) *		
	(02684) *		
	(02685) *		
	(02686) *		
	(02687) *		
04664 0000	(02688)	START ON WORD BOUNDARY	
04665 0000	(02689)	START ADDRESS	
	(02690) *	SIZE	
	(02691) VMUVS	START OF API MODULE	
	(02692) #A=0		
	(02693) *		
A00 04666 08FC0000	(02694) VMUVSSA	DUMMY SCALAR 1 TO DUMMY OUT 1	
A01 04668 08DC0000	(02695)	DUMMY SCALAR 1 TO DUMMY OUT 2	
A02 0466A 08FC0000	(02696)	DUMMY SCALAR 1 TO DUMMY OUT 3	
A03 0466C 08FC0000	(02697)	DUMMY SCALAR 2 TO DUMMY OUT 4	
	(02698) *		
	00000004	LOOP TO MOVE U INTO Y	
A04 0466E 04FC0000	(02700)	HALT	
A05 04670 901C0004	(02701)		
A06 04672 20327032	(02702)		
	(02703) *		
A07 04674 00000000	(02704)		
A08 04676 10000000	(02705)	IF RESTART WITHOUT RELOAD	
	00000000	VMUVSSZ=#A-VMUVSSA	
0467H	(02707)	END	



PAGE 92: SNAP-II MAP-300 ARITH. MODULES - PRIC. #H30101.03 MAY 7, 1980  
DEFINE TOP OF MODULE

(02708) DEFINE TOP OF MODULE  
(02709) \*  
0000467H (02710) TUESDAY=81. DEFINE TOP LOCATION OF MODULE  
(02711) \*  
(02712) \*  
(02713) \*  
0467H END

AFDTSUNC:	00048 (00027)	(00165)	(00684)	(00701)	(00706)	(00711)	(00716)	(01724)	(01728)
ANGLE1:	00079 (01637)	(02659)							
ANGLE2:	0007A (01640)	(02660)							
ANGLE3:	0007B (01643)	(02661)							
ANGLE4:	0007C (01646)	(02662)							
ANGLE5:	0007D (01649)	(02663)							
ANGLE6:	0007E (01652)	(02664)							
ANGLE7:	0007F (01655)	(02665)							
ANGLE:	0007H (02428)	(02658)							
APSASS:	00245 (00028)								
APSHNDV:	00FA (00029)								
APSRNDP0:	00F3 (00030)								
APSRNDP1:	00F2 (00031)	(01756)							
APSRSL:	0024B (00032)	(01622)							
APSSSC:	00248 (00033)								
APSDJNF:	00F42 (00034)								
APSG0:	0000C (00035)								
APSH1:	0000P (00036)								
APSHRF:	00010 (00037)								
APSSAID:	0000A (00038)								
APSSCLR:	0000F (00039)								
APSSS:	00009 (00040)								
APSSHW0:	00004 (00041)								
APSSMULT:	00000 (00169)	(00189)	(00193)	(00197)	(00201)	(00221)	(00225)	(00229)	(00233)
	(00257)	(00261)	(00265)	(00293)	(00297)	(00321)	(00325)	(00329)	(00357)
	(00381)	(00385)	(00413)	(00417)	(00421)	(00425)	(00445)	(00449)	(00453)
	(00477)	(00481)	(00485)	(00489)	(00493)	(00497)	(00501)	(00505)	(00509)
	(00517)	(00521)	(00525)	(00529)	(00533)	(00537)	(00541)	(00545)	(00549)
	(00557)	(00561)	(00565)	(00569)	(00573)	(00577)	(00581)	(00585)	(00589)
	(00597)	(00601)	(00605)	(00609)	(00613)	(00617)	(00621)	(00625)	(00633)
	(00637)	(00641)	(00645)	(00649)	(00653)	(00657)	(00661)	(00665)	(00673)
	(00677)	(00681)							
APSW:	1FFC0 (00042)								
APUSMULT:	00000 (00168)	(00188)	(00192)	(00196)	(00200)	(00212)	(00220)	(00228)	(00232)
	(00252)	(00256)	(00260)	(00264)	(00292)	(00296)	(00320)	(00324)	(00356)
	(00360)	(00380)	(00384)	(00417)	(00416)	(00420)	(00424)	(00444)	(00452)
	(00456)	(00476)	(00480)	(00484)	(00488)	(00492)	(00496)	(00500)	(00508)
	(00512)	(00516)	(00520)	(00524)	(00528)	(00532)	(00536)	(00540)	(00548)
	(00552)	(00556)	(00560)	(00564)	(00568)	(00572)	(00576)	(00580)	(00588)
	(00592)	(00596)	(00600)	(00604)	(00608)	(00612)	(00616)	(00620)	(00628)
	(00632)	(00636)	(00640)	(00644)	(00648)	(00652)	(00656)	(00660)	(00668)
	(00672)	(00676)	(00680)						
RCTSD:	00604 (00044)	(01374)	(01534)	(01603)					
RCTSAT:	00686 (00045)	(01373)							

RCFSRA:	00582 (00046)	(01598)	(01669)	(01685)	(01707)
RETSG0:	00000 (00047)				
U C1120S:	00000 (00393)				
U C1124AS:	00000 (00373)				
U C1124MS:	00000 (00377)				
U C2120S:	00000 (00389)				
U C2124AS:	00000 (00365)	(00369)			
U CVMUUS:	00000 (00368)				
CLMSG0G1:	00792 (00049)				
U CPOLARS:	00000 (00376)				
CR4SSZ:	00060 (01848)	(02084)			
CR4ASA:	00000 (02350)	(02867)			
CR4ASZ:	00100 (02349)	(02872)			
CR4A1:	00058 (02589)	(02842)			
CR4A1S:	00034 (01675)	(02526)	(02559)	(02584)	
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 CSMS4IS: 0001H (02155) (02172)  
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 CSMSI.15: 00005 (01475) (02393)  
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 CSMSI.45: 0000A (01795) (02399)  
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 SCHM6: 00020 (02447) (02451)  
 SCHM7: 00016 (01487) (02436)  
 SCHM8: 00023 (01509) (02455)  
 SCHM9: 00018 (01497) (02439)  
 SCHM10: 00014 (01500) (02441)  
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 SMTLSRS: 00781 (00072)  
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 V1124S5: 0000C (01014) (01023)  
 V1124S7: 00010 (01025) (01030)  
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 V2134R5R: 0002A (01241) (01251)  
 V2134R5A: 0424F (01185) (01263)  
 V2134R51: 04256 (01181) (01271)  
 V2134R5S: 00002 (01182) (01198)  
 V2134R5Z: 00000 (01184) (01274)  
 U V3112AS: 00000 (00409)  
 VCS2: 00009 (00786) (00793) (00796)  
 VCS3: 00013 (00813) (00817)  
 VCS4: 00017 (00814) (00821) (00823)  
 VCS5: 0001A (00827)  
 VCS6: 0001D (00838)  
 VCS7: 00061 (00866) (00927)  
 VCS8: 0000F (00807) (00804)  
 U VCLIPS: 00000 (00300)  
 U VCUMPS: 00000 (00312)  
 VCDS: 04020 (00404) (00771)  
 VCOSSSA: 00000 (00768) (00777) (00927)  
 VCOSSSZ: 00065 (00769) (00927) (00924)  
 VCOSCS: 040FA (00934) (01217)  
 U VFIXS: 00000 (00208) (00216)  
 VFILTS: 040F6 (00204) (00952)  
 VFLTSSA: 00000 (00949) (00955) (00978)  
 VFLTSZ: 00012 (00950) (00978) (00980)  
 U VLIMITS: 00000 (00304)  
 U VLIMITS: 00000 (00288)  
 U VMAGS: 00000 (00284)  
 U VMAGSOS: 00000 (00280)





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(00001) \* SNAP-II IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A

(00002) \*

(00003) \*

(00004) \* RELEASE 0.0 -- FIRST DELIVERED VERSION

(00005) \*

(00006) \* RELEASE 1.0A

(00007) \* 1. CORRECTED HEAD SCROLL REGISTERS FCB. PR #4000 11/20/79

(00008) \* 2. CORRECTED SPECIAL WINDING MODULE FOR ADMMRS. PR #4001 11/20/79

(00009) \* 3. PR #4002 NOT INCLUDED, ONLY FOR PFV 12 CSPU. MUST PATCH OBJECT FILE.

(00010) \* 4. FIXED START ADDRESSES FOR IOS-3. PR #4003 11/20/79.

(00011) \* 5. ADDED IOS-4 SUPPORT. PR #4008 7/13/80

(00012) \*

(00013) \*

(00014) \*

(00015) \*

(00016) \* DEFINE SYMBOLS FOR SNAP-II EXECUTIVE REL. 3.05 5/22/79

(00017) \*

(00018) \*

000001F5 (00019) ADDRFBCH = 229

000001F6 (00020) AFDTSORG = SFRF

000001F7 (00021) APSHNDR = SFFA

(00022) \*

000001G4 (00023) HCTSAD = \$604

000001G5 (00024) RCTSAT = \$686

000001G6 (00025) RCTSHA = \$5R2

000001G7 (00026) HCTSSIZ = 64

000001G8 (00027) HITSRW = 11

000001G9 (00028) HITSORD = 6

000001H0 (00029) HITSORDT = 9

(00030) \*

000002H6 (00031) CSMS1161 = \$2H6

000003Z2 (00032) CSMS105 = \$322

00000405 (00033) CSMSSTUH = \$405

(00034) \*

000001I4 (00035) ERRSINS = 20

00001AFA (00036) FRDPS = S1AFA

(00037) \*

000007ER (00038) FDT5 = STFR

(00039) \*

00000001 (00040) HS = 1

(00041) \*

0000003F (00042) IDEVSA3 = \$3F

00001R50 (00043) IDIV5 = \$1R50

00000001 (00044) TLVLS1 = 1

ROTATE BUFFER FCB #  
AP DISPATCH TABLE ORIGIN  
AP BINDER ROUTINE

BUFFER UNDEFINED BIT

INTEGER DIVIDF ROUTINE

```

00000002 (00045) ILVLS7 = 7
00000003 (00046) ILVLS3 = 3
00000502 (00047) ISVTS = $502
00000040 (00048) ISVTS17 = 17H
000049) *
0000F00 (00050) MKSHRY1 = SFF00
000000FF (00051) MKSHRYT = S00FF
00000000 (00052) MSS = 0
000053) *
00001C12 (00054) SFTSSMR = $1C12
000000FF (00055) SNAPSINI = SFFH
000056) *
00000784 (00057) TMS0 = $784
00000785 (00058) TMS1 = $785
00000786 (00059) TMS7 = $786
00000288 (00060) TMSPTR = $288
000061) *
00000002 (00062) WS = 7
000063) *
000064) *
000065) *-----DEFINE START LOCATION FOR IOS PACKAGE
00004900 (00067) START = $4900
000068) *
000069) *
00000288 (00070) #L = TMSPTR
000071) *
00288 0010515A (00072) ADDR TDESCUR(,1) UPDATE TOP OF EXEC POINTER
000073) *

```

TEMPORARY STORAGE

TOP OF EXEC POINTER

DEFINE START LOCATION

UPDATE TOP OF EXEC POINTER

(00074) \* FOR DISPATCH TABLE FOR I/O ROUTINES

(00075) \*  
 (00076) \*  
 (00077) \*  
 (00078) \*  
 (00079) \*  
 (00080) \*  
 (00081) \*  
 (00082) \*  
 (00083) \*  
 (00084) \*  
 (00085) \*  
 (00086) \*  
 (00087) \*  
 (00088) \*  
 (00089) \*  
 (00090) \*  
 (00091) \*  
 (00092) \*  
 (00093) \*  
 (00094) \*  
 (00095) \*  
 (00096) \*  
 (00097) \*  
 (00098) \*  
 (00099) \*  
 (00100) \*  
 (00101) \*  
 (00102) \*  
 (00103) \*

00080 0000406C (00077) BL = FDTs + 2 \* 66  
 00081 0000409C (00079) ADDR LOSS  
 00082 000040F4 (00080) ADDR RMSS  
 00083 00004066 (00081) ADDR MPAAAS

00084 00000074 (00083) BL = FDTs + 2 \* 70  
 00085 0000409A (00084) ADDR RMS  
 00086 00004082 (00085) ADDR WSPS

00087 0000007A (00088) BL = FDTs + 2 \* 73  
 00088 00004032 (00090) ADDR ADAMSSSP  
 00089 0000407C (00091) ADDR ADAMS16S

00090 00000046 (00097) \*  
 00091 001F5102 (00098) \*  
 00092 001F511E (00099) \*  
 00093 00105080 (00100) \*  
 00094 001021 \*  
 00095 00103 \*  
 00096 00103 \*

(00094) \* FOR DISPATCH TABLE ENTRY FOR ADMRA

BL = AFDTORG + 6 \*(ADMNFCR - 128)

ADDR ADMRAPI (R7, 1)  
 ADDR ADMRAPI (P7, 1)  
 ADDR ADMRA ( , 1, 0)

66=LOAD I/O SCROLL  
 67=RUN I/O SCROLL  
 68=RUN ADM AND ADM

70=READ SCROLL REGISTERS  
 71=WRITE SCROLL REGISTERS

73=SINGLE OR DUAL CHANNEL SAMPLING  
 74=ADAM-16 ROUTINES

(00104) \* IUS INTERRUPT TABLES  
 (03105) \*  
 (00106) \* SYSTEM STATWORDS FOR IUS DEVICES  
 (00107) \*  
 (00108) \*  
 (00109) \*  
 (00110) \*  
 (00111) \*  
 (00112) \*  
 (00113) \*  
 (00114) \*  
 (00115) \*  
 (00116) \*  
 (00117) \*  
 (00118) \*  
 (00119) \*  
 (00120) \*  
 (00121) \*  
 (00122) \*  
 (00123) \*  
 (00124) \*  
 (00125) \*  
 (00126) \*  
 (00127) \*  
 (00128) \*  
 (00129) \*  
 (00130) \*  
 (00131) \*  
 (00132) \*  
 (00133) \*  
 (00134) \*  
 (00135) \*  
 (00136) \*  
 (00137) \*  
 (00138) \*  
 (00139) \*  
 (00140) \*

00000206 (00110) RL = CWSI161

00206	49F21014	(00113)	CSW	CWSSTDA,016SINT1	IUS-2 DEVICE 16: LINE 1
00208	4A01014	(00114)	CWSI162 CSW	CWSSTDA,016SINT2	IUS-2 DEVICE 16: LINE 2
0020A	4A01014	(00115)	CWSI163 CSW	CWSSTDA,016SINT3	IUS-2 DEVICE 16: LINE 3
0020C	4A1A1014	(00116)	CWSI171 CSW	CWSSTDA,017SINT1	IUS-2 DEVICE 17: LINE 1
0020E	4A2A1014	(00117)	CWSI172 CSW	CWSSTDA,017SINT2	IUS-2 DEVICE 17: LINE 2
00200	4A321014	(00118)	CWSI173 CSW	CWSSTDA,017SINT3	IUS-2 DEVICE 17: LINE 3
00202	4A4A1014	(00119)	CWSI181 CSW	CWSSTDA,018SINT1	IUS-2 DEVICE 18: LINE 1
00204	4A5A1014	(00120)	CWSI182 CSW	CWSSTDA,018SINT2	IUS-2 DEVICE 18: LINE 2
00206	4A6A1014	(00121)	CWSI183 CSW	CWSSTDA,018SINT3	IUS-2 DEVICE 18: LINE 3
00208	4A7A1014	(00122)	CWSI191 CSW	CWSSTDA,019SINT1	IUS-2 DEVICE 19: LINE 1
0020A	4A8A1014	(00123)	CWSI192 CSW	CWSSTDA,019SINT2	IUS-2 DEVICE 19: LINE 2
0020C	4A9A1014	(00124)	CWSI193 CSW	CWSSTDA,019SINT3	IUS-2 DEVICE 19: LINE 3
0020E	4AA1014	(00125)	CWSI201 CSW	CWSSTDA,020SINT1	IUS-2 DEVICE 20: LINE 1
00200	4AB1014	(00126)	CWSI202 CSW	CWSSTDA,020SINT2	IUS-2 DEVICE 20: LINE 2
00202	4AC1014	(00127)	CWSI203 CSW	CWSSTDA,020SINT3	IUS-2 DEVICE 20: LINE 3
00204	4AD1014	(00128)	CWSI211 CSW	CWSSTDA,021SINT1	IUS-2 DEVICE 21: LINE 1
00206	4AE1014	(00129)	CWSI212 CSW	CWSSTDA,021SINT2	IUS-2 DEVICE 21: LINE 2
00208	4AF1014	(00130)	CWSI213 CSW	CWSSTDA,021SINT3	IUS-2 DEVICE 21: LINE 3
0020A	4B0A1014	(00131)	CWSI221 CSW	CWSSTDA,022SINT1	IUS-2 DEVICE 22: LINE 1
0020C	4B1A1014	(00132)	CWSI222 CSW	CWSSTDA,022SINT2	IUS-2 DEVICE 22: LINE 2
0020E	4B2A1014	(00133)	CWSI223 CSW	CWSSTDA,022SINT3	IUS-2 DEVICE 22: LINE 3
00200	4B3A1014	(00134)	CWSI231 CSW	CWSSTDA,023SINT1	IUS-2 DEVICE 23: LINE 1
00202	4B4A1014	(00135)	CWSI232 CSW	CWSSTDA,023SINT2	IUS-2 DEVICE 23: LINE 2
00204	4B5A1014	(00136)	CWSI233 CSW	CWSSTDA,023SINT3	IUS-2 DEVICE 23: LINE 3

FUNCT

INITIAL VALUES FOR SYSTEM STATEWORDS FOR IUS DEVICES

00327	49F21014	(00141) *	CS#	CWSSTDR,D16SINT1	IUS-2 DEVICE 16: LINE 1
		(00142) *	CS#	CWSSTDR,D16SINT2	IUS-2 DEVICE 16: LINE 2
00328	4A0F1014	(00143) *	CS#	CWSSTDR,D16SINT3	IUS-2 DEVICE 16: LINE 3
00329	4A161014	(00144) *	CS#	CWSSTDR,D17SINT1	IUS-2 DEVICE 17: LINE 1
0032A	4A241014	(00145) *	CS#	CWSSTDR,D17SINT2	IUS-2 DEVICE 17: LINE 2
0032B	4A321014	(00146) *	CS#	CWSSTDR,D17SINT3	IUS-2 DEVICE 17: LINE 3
0032C	4A3A1014	(00147) *	CS#	CWSSTDR,D18SINT1	IUS-2 DEVICE 18: LINE 1
0032D	4A481014	(00148) *	CS#	CWSSTDR,D18SINT2	IUS-2 DEVICE 18: LINE 2
0032E	4A561014	(00149) *	CS#	CWSSTDR,D18SINT3	IUS-2 DEVICE 18: LINE 3
0032F	4A5F1014	(00150) *	CS#	CWSSTDR,D19SINT1	IUS-2 DEVICE 19: LINE 1
00330	4A6C1014	(00151) *	CS#	CWSSTDR,D19SINT2	IUS-2 DEVICE 19: LINE 2
00331	4A7A1014	(00152) *	CS#	CWSSTDR,D19SINT3	IUS-2 DEVICE 19: LINE 3
00332	4A81014	(00153) *	CS#	CWSSTDR,D20SINT1	IUS-2 DEVICE 20: LINE 1
00333	4A8F1014	(00154) *	CS#	CWSSTDR,D20SINT2	IUS-2 DEVICE 20: LINE 2
00334	4A961014	(00155) *	CS#	CWSSTDR,D20SINT3	IUS-2 DEVICE 20: LINE 3
00335	4AA41014	(00156) *	CS#	CWSSTDR,D21SINT1	IUS-2 DEVICE 21: LINE 1
00336	4AAE1014	(00157) *	CS#	CWSSTDR,D21SINT2	IUS-2 DEVICE 21: LINE 2
00337	4AB41014	(00158) *	CS#	CWSSTDR,D21SINT3	IUS-2 DEVICE 21: LINE 3
00338	4AC21014	(00159) *	CS#	CWSSTDR,D22SINT1	IUS-2 DEVICE 22: LINE 1
00339	4AC71014	(00160) *	CS#	CWSSTDR,D22SINT2	IUS-2 DEVICE 22: LINE 2
0033A	4ACD1014	(00161) *	CS#	CWSSTDR,D22SINT3	IUS-2 DEVICE 22: LINE 3
0033B	4AD1014	(00162) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
0033C	4AD61014	(00163) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
0033D	4AE41014	(00164) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
0033E	4AF21014	(00165) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
0033F	4AF71014	(00166) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00340	4AFD1014	(00167) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
00341	4B0A1014	(00168) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
00342	4B0F1014	(00169) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00343	4B161014	(00170) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
00344	4B1C1014	(00171) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
00345	4B221014	(00172) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00346	4B291014	(00173) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
00347	4B2F1014	(00174) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
00348	4B361014	(00175) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00349	4B3C1014	(00176) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
0034A	4B421014	(00177) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
0034B	4B491014	(00178) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
0034C	4B4F1014	(00179) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
0034D	4B561014	(00180) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
0034E	4B5C1014	(00181) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
0034F	4B621014	(00182) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3
00350	4B691014	(00183) *	CS#	CWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
00351	4B6F1014	(00184) *	CS#	CWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00352	4B761014	(00185) *	CS#	CWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3

INITIALIZE IUS BUSY FLAGS  
 ENTERED FROM SNAP-II SCHEDULER

SET PC TO ENTRY IN SCHEDULER

000000FF #I. = SNAPSINI. # b

JMP JOSSINI.

000000FF	(00179) *	JMP	JOSSINI.
000000FF	(00180) *		
000000FF	(00181) *		
000000FF	(00182) *		
000000FF	(00183) *		
000000FF	(00184) *		



PAGE 6: SNAP-II IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A  
IUS INTERRUPT TABLES

00004900 (001R5) #L = START  
(001R6) \*  
(001R7) \*

RESET START LOCATION FOR MODULE

LOAD SCROLL ERROR CODES

CODE	DESCRIPTION	DATA
(00188) *	TRANSFER DIRECTION ILLEGAL	
(00189) *	1	
(00190) *	2	
(00191) *	3	
(00192) *	4	
(00193) *	5	
(00194) *	6	
(00195) *	7	
(00196) *	8	
(00197) *	9	
(00198) *	10	
(00199) *	11	
(00200) *		
(00201) *		
(00202) *		
(00203) *		
(00204) *		
(00205) *		
(00206) *		
(00207) *		
(00208) *		
(00209) *		
(00210) *		
(00211) *		
(00212) *		
(00213) *		
(00214) *		
(00215) *		
(00216) *		

LOAD SCROLL ERROR CODES

ERROR MEANING

1 TRANSFER DIRECTION ILLEGAL  
 2 MID 0 ILLEGAL ( OUT OF RANGE OR UNDEFINED )  
 3 MID 1 ILLEGAL ( OUT OF RANGE OR UNDEFINED )  
 4 MID 2 ILLEGAL ( OUT OF RANGE OR UNDEFINED )  
 5 MID 3 ILLEGAL ( OUT OF RANGE OR UNDEFINED )  
 6 SIZE(MID 0) \*MF. INTEGRAL MULTIPLE OF # CHANNELS  
 7 IF DOUBLE BUFFERED MODE, AND SIZE(MID 0) \*ME. SIZE(MID 2)  
 8 1ST REGISTER # TO WRITE IS OUT OF RANGE  
 9 # REGISTERS TO WRITE IS OUT OF RANGE  
 10 INTEGER SCALAR ID OF OFFSET .GT. MAXIMUM INTEGER ID  
 11 WINDING CHAIN IN ERROR

SCROLL ERROR INDICATOR

0 BUSY FLAG PER IIS DEVICE  
 0 => FREE, 1 => BUSY

ACQUISITION MODE PER IIS DEVICE

0,0,0,0,0,0,0,0

OBJECT

PAGE 8: SNAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A  
LOAD SCHEDULE ERROR CODES

(00217) *					
(00218) *		INITIALIZE IOS BUSY FLAGS			
(00219) *					
049F1 R4001C12 (00220) IOSINIT. CALL		R0, SFTSSMR		REPLACE INSTRUCTION IN SCHEDULER	
(00221) *					
049F3 1F200007 (00222) LARPT		R2, 7		SET TO CLEAR 8 WORDS	
049F5 CC044901 (00223) MOVZM		IOSFLG(R2)			
(00224) *					
049F7 R0000R0 (00225) JMP		SNAP\$INI+R		RETURN TO SCHEDULER	
(00226) *					
049F9 0R00 (00227) EVFM					

I/O WAIT ROUTINE

(00228) ! I/O WAIT ROUTINE  
(00229) \*  
(00230) \*  
(00231) \* ENTERED WITH R7 = SCROLL ID  
(00232) \*  
(00233) \*

049FA DC0F49C1 (00234) WAITSTOS SMHS 0, IUSPFG-16(R7) IF I/O FREE,  
049FC 0F70 (00235) RETURN RETURN

049FD 0190004F (00237) WAITSO WAITS IDEVS63, IIVIS1 IF BUSY, WAIT FOR I/O DONE

049FF 2106 (00239) HOP WAITSTOS REPEAT TEST

049F0 0F70 (00241) RETURN  
049F1 0800 (00242) EVEN

```

(00241) *
(00244) *
(00245) *
(00246) !
(00247) *
(00248) D16SINT1 MOVIR R7, 0          POINT TO VALUES FOR FIRST INTERRUPT
(00249) CALL  R0, IOSSINT             GO TO INTERRUPT PROCESSING SUBROUTINE
(00250) PFT
(00251) FVFN
(00252) MOVIR R7, 1          POINT TO VALUES FOR SECOND INTERRUPT
(00253) CALL  R0, IOSSINT             GO TO INTERRUPT PROCESSING SUBROUTINE
(00254) PFT
(00255) FVFN
(00256) JMP   D16SINT1            REPEAT LOOP ON NEXT INTERRUPT
(00257) *
(00258) *
(00259) !
(00260) *
(00261) *
(00262) D16SINT2 MOVIR R7, 16         POINT TO VALUES FOR FIRST INTERRUPT
(00263) CALL  R0, IOSSINT             GO TO INTERRUPT PROCESSING SUBROUTINE
(00264) PFT
(00265) FVFN
(00266) MOVIR R7, 17         POINT TO VALUES FOR SECOND INTERRUPT
(00267) CALL  R0, IOSSINT             GO TO INTERRUPT PROCESSING SUBROUTINE
(00268) PFT
(00269) FVFN
(00270) JMP   D16SINT2            REPEAT LOOP ON NEXT INTERRUPT
(00271) *
(00272) *
(00273) !
(00274) *
(00275) *
(00276) D16SINT3 MOVIR R7, 0          POINT TO VALUES FOR INTERRUPT 16
(00277) CALL  R0, IOSSINT             GO TO INTERRUPT COMPLETION SUBROUTINE
(00278) PFT
(00279) FVFN
(00280) JMP   D16SINT3
(00281) *
(00282) *
  
```



```

(00320) !
(00321) *
04A3A 90700004 (00322) DIRSINT1 MOVIP R7, 4 POINT TO VALUES FOR FIRST INTERRUPT
04A3C 86004017 (00323) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE.
04A3E 0E70 (00324) RFT
04A3F 0800 (00325) EVFN
04A40 90700005 (00326) MOVIP R7, 5 POINT TO VALUES FOR SECOND INTERRUPT
04A42 86004017 (00327) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE.
04A44 0E70 (00328) RFT
04A45 0800 (00329) EVFN
04A46 8000403A (00330) JMP DIRSINT1 REPEAT LOOP ON NEXT INTERRUPT
(00331) *
(00332) *
(00333) !
(00334) *
(00335) *
04A48 90700014 (00336) DIRSINT2 MOVIP R7, 20 POINT TO VALUES FOR FIRST INTERRUPT
04A4A 86004017 (00337) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE.
04A4C 0E70 (00338) RFT
04A4D 0800 (00339) EVFN
04A4F 90700015 (00340) MOVIP R7, 21 POINT TO VALUES FOR SECOND INTERRUPT
04A50 86004017 (00341) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE.
04A52 0E70 (00342) RFT
04A53 0800 (00343) EVFN
04A54 80004048 (00344) JMP DIRSINT2 REPEAT LOOP ON NEXT INTERRUPT
(00345) *
(00346) *
(00347) !
(00348) *
(00349) *
04A56 90700004 (00350) DIRSINT3 MOVIP R7, 4 POINT TO VALUES FOR INTERRUPT 18
04A58 86004024 (00351) CALL R0, IOSSINT GO TO INTERRUPT COMPLETION SUBROUTINE.
04A5A 0E70 (00352) RFT
04A5B 0800 (00353) EVFN
04A5C 80004056 (00354) JMP DIRSINT3
(00355) *
(00356) *
EJECT
  
```

```

(00357) ! ; DEVICE NUMBER 19 - LINE 1
(00358) *
04A5F 90700006 (00359) D19SINT1 MOVIR R7, 6 POINT TO VALUES FOR FIRST INTERRUPT
04A60 86004812 (00360) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04A62 0F70 (00361) RPT
04A63 0800 (00362) EVFN
04A64 90700007 (00363) MOVIR R7, 7 POINT TO VALUES FOR SECOND INTERRUPT
04A66 86004812 (00364) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04A68 0F70 (00365) RPT
04A69 0800 (00366) EVFN
04A6A 80004A5F (00367) JMP D19SINT1 REPEAT LOOP ON NEXT INTERRUPT
(00368) *
(00369) *
(00370) ! ; DEVICE NUMBER 19 - LINE 2
(00371) *
(00372) *
04A6C 90700016 (00373) D19SINT2 MOVIR R7, 22 POINT TO VALUES FOR FIRST INTERRUPT
04A6E 86004812 (00374) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04A70 0F70 (00375) RPT
04A71 0800 (00376) EVFN
04A72 90700017 (00377) MOVIR R7, 23 POINT TO VALUES FOR SECOND INTERRUPT
04A74 86004812 (00378) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04A76 0F70 (00379) RPT
04A77 0800 (00380) EVFN
04A78 80004A6C (00381) JMP D19SINT2 REPEAT LOOP ON NEXT INTERRUPT
(00382) *
(00383) *
(00384) ! ; DEVICE NUMBER 19 - LINE 3
(00385) *
(00386) *
04A7A 90700006 (00387) D19SINT3 MOVIR R7, 6 POINT TO VALUES FOR INTERRUPT 16
04A7C 86004824 (00388) CALL R0, IOSSINTC GO TO INTERRUPT COMPLETION SUBROUTINE
04A7E 0F70 (00389) RPT
04A7F 0800 (00390) EVFN
04A80 80004A7A (00391) JMP D19SINT3
(00392) *
(00393) *
EJFCT
  
```



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(00394) !
(00395) *
04AR2 90700008 (00396) D20SINT1 MOVIR R7, R          POINT TO VALUES FOR FIRST INTERRUPT
04AR4 86004R12 (00397) CALL. R0, IOSSINT  GO TO INTERRUPT PROCESSING SUBROUTINE
04AR6 0F70 (00398) RPT
04AR7 0800 (00399) FVFN
04AR8 90700009 (00400) MOVIR R7, 9          POINT TO VALUES FOR SECOND INTERRUPT
04ABA 86004R12 (00401) CALL. R0, IOSSINT  GO TO INTERRUPT PROCESSING SUBROUTINE
04ABC 0F70 (00402) RPT
04ABD 0800 (00403) FVFN
04ABE 80004R2 (00404) JMP D20SINT1        REPEAT LOOP ON NEXT INTERRUPT
(00405) *
(00406) *
(00407) !
(00408) *
(00409) *
04A90 90700018 (00410) D20SINT2 MOVIR R7, 24         POINT TO VALUES FOR FIRST INTERRUPT
04A92 86004R12 (00411) CALL. R0, IOSSINT  GO TO INTERRUPT PROCESSING SUBROUTINE
04A94 0F70 (00412) RPT
04A95 0800 (00413) FVFN
04A96 90700019 (00414) MOVIR R7, 25         POINT TO VALUES FOR SECOND INTERRUPT
04A98 86004R12 (00415) CALL. R0, IOSSINT  GO TO INTERRUPT PROCESSING SUBROUTINE
04A9A 0F70 (00416) RPT
04A9B 0800 (00417) FVFN
04A9C 80004R0 (00418) JMP D20SINT2        REPEAT LOOP ON NEXT INTERRUPT
(00419) *
(00420) *
(00421) !
(00422) *
(00423) *
04A9F 90700008 (00424) D20SINT3 MOVIR R7, R          POINT TO VALUES FOR INTERRUPT 20
04AA0 86004R24 (00425) CALL. R0, IOSSINTC GO TO INTERRUPT COMPLETION SUBROUTINE
04AA2 0F70 (00426) RPT
04AA3 0800 (00427) FVFN
04AA4 80004R4 (00428) JMP D20SINT3
(00429) *
(00430) *
  
```

	(00431) !		DEVICE NUMBER 21 - LINE 1		
	(00432) *				
04AA4 9070001A	(00433) !	D21SINT1	MOVIR R7, 10	POINT TO VALUES FOR FIRST INTERRUPT	
04AA8 R6004R12	(00434) *		CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE	
04AAA 0F70	(00435) *		RPT		
04AA8 0R00	(00436) *		EVEN		
04AAC 9070000H	(00437) *		MOVIR R7, 11	POINT TO VALUES FOR SECOND INTERRUPT	
04AAF R6004R12	(00438) *		CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE	
04AR0 0F70	(00439) *		RPT		
04AK1 0R00	(00440) *		EVEN		
04AR2 R0004R4	(00441) *		JMP D21SINT1	REPEAT LOOP ON NEXT INTERRUPT	
	(00442) *				
	(00443) *				
	(00444) !		DEVICE NUMBER 21 - LINE 2		
	(00445) *				
	(00446) *				
04AR4 9070001A	(00447) !	D21SINT2	MOVIR R7, 26	POINT TO VALUES FOR FIRST INTERRUPT	
04AR6 R6004R12	(00448) *		CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE	
04A8H 0F70	(00449) *		RPT		
04AR9 0R00	(00450) *		EVEN		
04ARA 9070001R	(00451) *		MOVIR R7, 27	POINT TO VALUES FOR SECOND INTERRUPT	
04ARC R6004R12	(00452) *		CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE	
04AHF 0F70	(00453) *		RPT		
04AHF 0R00	(00454) *		EVEN		
04AC0 R0004R4	(00455) *		JMP D21SINT2	REPEAT LOOP ON NEXT INTERRUPT	
	(00456) *				
	(00457) *				
	(00458) !		DEVICE NUMBER 21 - LINE 3		
	(00459) *				
	(00460) *				
04AC2 9070000A	(00461) !	D21SINT3	MOVIR R7, 10	POINT TO VALUES FOR INTERRUPT 21	
04AC4 R6004R24	(00462) *		CALL R0, IOSSINTC	GO TO INTERRUPT COMPLETION SUBROUTINE	
04AC6 0F70	(00463) *		RPT		
04AC7 0R00	(00464) *		EVEN		
04AC8 R0004AC2	(00465) *		JMP D21SINT3		
	(00466) *				
	(00467) *		FJFCT		

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(00468) !
(00469) *
04ACA 9070000C (00470) D22SINT1 MOVIR R7, 12 POINT TO VALUES FOR FIRST INTERRUPT
04ACC 86004H17 (00471) CALL R0, IUSISINT GO TO INTERRUPT PROCESSING SUBROUTINE
04ACD 0F70 (00472) RFT
04ACE 0800 (00473) EVEN
04AD0 90700000 (00474) MOVIR R7, 13 POINT TO VALUES FOR SECOND INTERRUPT
04AD2 86004H17 (00475) CALL R0, IUSISINT GO TO INTERRUPT PROCESSING SUBROUTINE
04AD4 0F70 (00476) RFT
04AD5 0800 (00477) EVEN
04ADA 80004ACA (00478) JMP D22SINT1 REPEAT LOOP ON NEXT INTERRUPT
(00479) *
(00480) *
(00481) !
(00482) *
(00483) *
04ADH 9070001C (00484) D22SINT2 MOVIR R7, 28 POINT TO VALUES FOR FIRST INTERRUPT
04ADA 86004H17 (00485) CALL R0, IUSISINT GO TO INTERRUPT PROCESSING SUBROUTINE
04ADC 0F70 (00486) RFT
04ADD 0800 (00487) EVEN
04ADF 9070001D (00488) MOVIR R7, 29 POINT TO VALUES FOR SECOND INTERRUPT
04AE0 86004H17 (00489) CALL R0, IUSISINT GO TO INTERRUPT PROCESSING SUBROUTINE
04AE2 0F70 (00490) RFT
04AE3 0800 (00491) EVEN
04AE4 80004ADR (00492) JMP D22SINT2 REPEAT LOOP ON NEXT INTERRUPT
(00493) *
(00494) *
(00495) !
(00496) *
(00497) *
04AF6 9070000C (00498) D22SINT3 MOVIR R7, 12 POINT TO VALUES FOR INTERRUPT 22
04AF8 86004H24 (00499) CALL R0, IUSISINT3 GO TO INTERRUPT COMPLETION SUBROUTINE
04AFA 0F70 (00500) RFT
04AFB 0800 (00501) EVEN
04AFC 80004AF6 (00502) JMP D22SINT3
(00503) *
(00504) *
FINCT
  
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(00505) !
(00506) *
04AF4 9070000F (00507) D23SINT1 MOVIR R7, 14 POINT TO VALUES FOR FIRST INTERRUPT
04AF0 86004812 (00508) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04AF2 0E70 (00509) RET
04AF3 0800 (00510) EVEN
04AF4 9070000F (00511) MOVIR R7, 15 POINT TO VALUES FOR SECOND INTERRUPT
04AF6 86004812 (00512) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04AF8 0E70 (00513) RET
04AF9 0800 (00514) EVEN
04AFA 800048FC (00515) JMP D23SINT1 REPEAT LOOP ON NEXT INTERRUPT
(00516) *
(00517) *
(00518) !
(00519) *
(00520) *
04AFC 9070001F (00521) D23SINT2 MOVIR R7, 30 POINT TO VALUES FOR FIRST INTERRUPT
04AFF 86004812 (00522) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04B00 0E70 (00523) RET
04B01 0800 (00524) EVEN
04B02 9070001F (00525) MOVIR R7, 31 POINT TO VALUES FOR SECOND INTERRUPT
04B04 86004812 (00526) CALL R0, IOSSINT GO TO INTERRUPT PROCESSING SUBROUTINE
04B06 0E70 (00527) RET
04B07 0800 (00528) EVEN
04B08 800048FC (00529) JMP D23SINT2 REPEAT LOOP ON NEXT INTERRUPT
(00530) *
(00531) *
(00532) !
(00533) *
(00534) *
04B0A 9070000F (00535) D23SINT3 MOVIR R7, 14 POINT TO VALUES FOR INTERRUPT 23
04B0C 86004824 (00536) CALL R0, IOSSINTC GO TO INTERRUPT COMPLETION SUBROUTINE
04B0E 0E70 (00537) RET
04B0F 0800 (00538) EVEN
04B10 8000480A (00539) JMP D23SINT3
(00540) *
(00541) *
04B11 FJFCT
  
```

	(00542) *	!	GENERAL SUBROUTINES TO PROCESS IUS INTERRUPTS			
	(00543) *					
04H12 F05F40AC	(00544) *	TOSSINT	MOVRR R5, IUSRSHW(R7)	GET READ/WRITE INDEX		
04H14 406F	(00545)		MOVRR R6, R7	COMPUTE LOGICAL BUFFER INDEX		
04H15 3C61	(00546)		LPS R6, 1			
04H16 4C7C	(00547)		ADDRR R7, R6			
04H17 F06F40AC	(00548)	*	MOVRR R6, IUSR1(R7)	IF HID LEGAL,		
04H19 1A10	(00549)		SKPL F0Z			
04H1A F20A4046	(00550)		XCT IUSRTHI+WS(R5)	FLAG CURRENT BUFFER AVAILABLE		
04H1C F07F4060	(00551)		MOVRR R7, IUSR1+1(R7)	IF HID LEGAL,		
04H1E 1A10	(00552)		SKPL F0Z			
04H1F F20A4046	(00553)		XCT IUSRTHI+WS(R5)	FLAG NEXT BUFFER BUSY		
04H21 1200003F	(00554)	*	AINR IUSR63, IUSR1	SIGNAL I/O OPERATION COMPLETE		
04H23 0F70	(00555)		RETURN			
	(00556)		EVEN			
	(00557)	*				
	(00558)					
	(00559)					
	(00560)					
	(00561)	*				
	(00562)	*				
	(00563)	*				
04H24 402F	(00564)	TOSSINTC	MOVRR R2, R7	SAVE INDEX FOR LATER USE		
04H25 F05F40AC	(00565)		MOVRR R5, IUSRSHW(R7)	GET READ/WRITE INDEX		
04H27 406F	(00566)		MOVRR R6, R7	COMPUTE INDEX TO LOGICAL BUFFERS ID'S		
04H28 3C61	(00567)		LPS R6, 1			
04H29 4C7C	(00568)		ADDRR R7, R6			
04H2A C03F4084	(00569)		EVEN			
04H2C C06F4060	(00570)		MOVRR R3, IUSR1+24(R7)	GET LOGICAL BUFFER IDS (1 AND 3)		
	(00571)		MOVRR R6, IUSR1(R7)	GET LOGICAL BUFFER IDS (0 AND 2)		
	(00572)	*				
04H2F 0260	(00573)		R6	IF HID LEGAL,		
04H31 1A10	(00574)		F0Z			
04H30 F20A4046	(00575)		XCT IUSRTHI+WS(R5)	FLAG BUFFER 0 AVAILABLE		
	(00576)	*				
04H32 406F	(00577)		MOVRR R6, R7	IF HID LEGAL,		
04H33 1A10	(00578)		SKPL F0Z			
04H34 F20A4046	(00579)		XCT IUSRTHI+WS(R5)	FLAG BUFFER 2 AVAILABLE		
	(00580)	*				
04H36 4066	(00581)		MOVRR R6, R3	IF HID LEGAL,		
04H37 1A10	(00582)		SKPL F0Z			
04H38 F20A4046	(00583)		XCT IUSRTHI+WS(R5)	FLAG BUFFER 1 AVAILABLE		
	(00584)	*				
04H3A 406F	(00585)		MOVRR R6, R4	IF HID LEGAL,		

PAGE 19: SNAP-II I/O PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A  
 INTERRUPT ROUTINES FOR I/O-SCHEDULE BUFFER MANAGEMENT

04R3R 1A10	(00546)	SKPG	F02		
04R3C 820A4H46	(00587)	XCT	I0SHMTHI+WS(45)	FLAG BUFFER 3 AVAILABLE	
04R3F 3C21	(00549)	IPS	R2, 1		
04R3F CC0449D1	(00590)	MOVZP	I0SSF1C(42)	CLEAR I/O DEVICE BUSY FLAG	
04R41 1200003F	(00592)	AINI	I0EVS63, I1V151	SIGNAL I/O OPERATION COMPLETE	
04R43 0F70	(00593)	RETURN			
	(00594)	EVEN			
	(00595)				
	(00546)	EXIT			

PAGE 20: SWAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A  
 INTERRUPT ROUTINES FOR IOS-SCROLL BUFFER MANAGEMENT

ADDRESS	OPERATION	INDEX	DESCRIPTION
04R43	02650686	(00597) *	HIT/STROB, HCTSATR(7) READ DEVICE
04R46	006C0686	(00599) *	HIT/STROB, HCTSATR(6)
04R48	031E0686	(00600) IUSHSW1	SMR
04R4A	531C0686	(00601) *	CMR
		(00602) *	SMR
		(00603) *	CMR
		(00604) *	HIT/STROB, HCTSATR(6)
		(00605) *	HIT/STROB, HCTSATR(7) READ DEVICE
		(00606) *	HIT/STROB, HCTSATR(6)
		(00607) *	HIT/STROB, HCTSATR(7) WRITE DEVICE
		(00608) *	HIT/STROB, HCTSATR(6)
		(00609) *	HIT/STROB, HCTSATR(7) WRITE DEVICE
		(00610) *	HIT/STROB, HCTSATR(6)
		(00611) *	HIT/STROB, HCTSATR(7) WRITE DEVICE
		(00612) IUSHSW1	DATA
04R4C	0000		0.0
04R4D	0000		0.0
04R4E	0000		0.0
04R4F	0000		0.0
04R50	0000		0.0
04R51	0000		0.0
04R52	0000		0.0
04R53	0000		0.0
04R54	0000		0.0
04R55	0000		0.0
04R56	0000		0.0
04R57	0000		0.0
04R58	0000		0.0
04R59	0000		0.0
04R5A	0000		0.0
04R5B	0000		0.0
04R5C	0000	(00620) *	DATA
04R5D	0000	(00621) *	DATA
04R5E	0000	(00622) *	DATA
04R5F	0000	(00623) *	DATA
04R60	0000	(00624) *	DATA
04R61	0000	(00625) *	DATA
04R63	0000	(00626) *	DATA
04R64	0000		0.0
04R65	0000		0.0
04R66	0000		0.0
04R67	0000		0.0

PAGE 71: SNAP-11 IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # R40001.01A  
INTERRUPT ROUTINES FOR IOS-SCROLL BUFFER MANAGEMENT

04868 0000	(00627)	DATA	0,0	DEVICE 22 - LINE 2
04869 0000	(00628)	DATA	0,0	DEVICE 23 - LINE 2
0486A 0000	(00629) *			
	(00630)			PIKCT



(00631) *	LOGICAL BUFFER IDS * 2 FOR I/O INTERRUPTS			
(00632) *				
(00633) *				
(00634) *	SFT THE HALFWORD TRIPLET TO RUF0, RUF2, RUF0			
(00635) *				
(00636) *				
(00637) INSRJ	DATA	0,0,0	DEVICE 16 - LINE 1	
(00638)	DATA	0,0,0	DEVICE 17 - LINE 1	
(00639)	DATA	0,0,0	DEVICE 18 - LINE 1	
(00640)	DATA	0,0,0	DEVICE 19 - LINE 1	
(00641)	DATA	0,0,0	DEVICE 20 - LINE 1	
(00642)	DATA	0,0,0	DEVICE 21 - LINE 1	
(00643)	DATA	0,0,0	DEVICE 22 - LINE 1	
(00644)	DATA	0,0,0	DEVICE 23 - LINE 1	
(00645) *				
(00646) *	SFT THE HALFWORD TRIPLET TO RUF1, RUF3, RUF1			
(00647) *				
(00648)	DATA	0,0,0	DEVICE 16 - LINE 2	
(00649)	DATA	0,0,0	DEVICE 17 - LINE 2	
(00650)	DATA	0,0,0	DEVICE 18 - LINE 2	
(00651)	DATA	0,0,0	DEVICE 19 - LINE 2	
04R6C 0000				
04R6D 0000				
04R6E 0000				
04R6F 0000				
04R70 0000				
04R71 0000				
04R72 0000				
04R73 0000				
04R74 0000				
04R75 0000				
04R76 0000				
04R77 0000				
04R7H 0000				
04R79 0000				
04R7A 0000				
04R7H 0000				
04R7C 0000				
04R7D 0000				
04R7F 0000				
04R7F 0000				
04R80 0000				
04R81 0000				
04R82 0000				
04R83 0000				
04R84 0000				
04R85 0000				
04R86 0000				
04R87 0000				
04R88 0000				
04R89 0000				
04R8A 0000				
04R8B 0000				
04R8C 0000				
04R8D 0000				
04R8E 0000				

PAGE 23: SWAP-U IOS PACKAGE --- APR 14, 1980 --- PROGRAM # 940001.01A  
INTERRUPT ROUTINES FOR IOS-SCHILL, BUFFER MANAGEMENT

0488F 0000				
04890 0000	(00652)	DATA	0,0,0	DEVICE 20 - LINE 2
04891 0000				
04892 0000				
04893 0000	(00653)	DATA	0,0,0	DEVICE 21 - LINE 2
04894 0000				
04895 0000				
04896 0000	(00654)	DATA	0,0,0	DEVICE 22 - LINE 2
04897 0000				
04898 0000				
04899 0000	(00655)	DATA	0,0,0	DEVICE 23 - LINE 2
0489A 0000				
0489B 0000				

LDIS5 MODULE TO PROCESS THE LOAD IOS SCROLL FCR

FCR FORMAT (16 BIT WORD FORMAT SHOWN)	WORD	LEFT BYTE	RIGHT BYTE
0	0	POINTER TO NEXT FCR AND FUNCTION LIST FLAG(LSH)	
1	1	66	RID
2	2		SCROLL IDENT
3	3		SCROLL TYPE
4	4	0	0
5	5	0	0

OBJECT

LOGICAL BUFFER FORMAT FOR IOS PROGRAM

(00696) !  
 (00697) \*  
 (00698) \*  
 (00699) \*  
 (00700) \*  
 (00701) \*  
 (00702) \*  
 (00703) \*  
 (00704) \*  
 (00705) \*  
 (00706) \*  
 (00707) \*  
 (00708) \*  
 (00709) \*  
 (00710) \*  
 (00711) \*  
 (00712) \*  
 (00713) \*  
 (00714) \*  
 (00715) \*  
 (00716) \*  
 (00717) \*  
 (00718) \*  
 (00719) \*  
 (00720) \*  
 (00721) \*  
 (00722) \*  
 (00723) \*  
 (00724) \*  
 (00725) \*  
 (00726) \*  
 (00727) \*  
 (00728) \*  
 (00729) \*  
 (00730) \*  
 (00731) \*  
 (00732) \*  
 (00733) \*  
 (00734) \*  
 (00735) \*  
 (00736) \*  
 (00737) \*

HN CONTENTS  
 --  
 0 N = SCROLL PROGRAM SIZE IN HALFWORDS  
 1 H1D1 : H1D0  
 2 - N+1 SCROLL PROGRAM IN HALFWORDS  
 N+2 0 IF MAP -> SCROLL TRANSFER, ELSE 2 (ADAM=2, ADM=0)  
 N+3 # CHANNELS : ACQUISITION MODE  
 N+4 H1D3 : H1D2  
 N+5 # = NUMBER OF SCROLL REGISTERS TO LOAD  
 N+6 # OF FIRST SCROLL REGISTER TO LOAD  
 N+7 - N+M+6 VALUES TO LOAD INTO SCROLL REGISTERS  
 N+M+7 INTEGER SCALAR ID OF OFFSET  
 N+M+8 BUFFER 0 CHAIN ANCHOR  
 N+M+9 BUFFER 1 CHAIN ANCHOR  
 N+M+10 BUFFER 2 CHAIN ANCHOR  
 N+M+11 BUFFER 3 CHAIN ANCHOR

ACQUISITION MODE:  
 0 => DISCRETE OR BURST  
 .NE. 0 => CONTINUOUS  
 1 => DOUBLE BUFFERED  
 2 => CIRCULAR

-----  
 FJECT  
 -----

(00738) *	REGISTER USAGE		
(00739) *			
(00740) *			
(00741) *	R1	POINTER TO FCR # IN FCR BLOCK	
(00742) *	R2	SCROLL PROGRAM SIZE = N	
(00743) *	R3	PA = IOS LOGICAL BUFFER BASE ADDRESS	
(00744) *	R4	SCROLL INDEX ( 16 => 0, 17 => 7, ... , 23 => 14 )	
(00745) *			
(00746) *			
(00747) *			
0489C 707200FF	MOVWK	R7, R1, *MKSRRHT	EXTRACT LOGICAL BUFFER ID FROM FCR
0489E 3A71	LIS	R7, 1	CONVERT TO WORD INDEX
0489F 0400	EVEN		
048A0 F03F05H3	MOVMP	R3, HCTSHA+HS(R7)	GET LOGICAL BUFFER START ADDRESS
048A2 F0420001	MOVMP	R4, 1(R1)	GET SCROLL ID FROM FCR
048A4 407H	MOVRR	R7, R4	
048A5 F40049FA	CALL	R0, WAITSIOS	WAIT TILL SCROLL DEVICE FREE
048A7 C00049D0	MOVZM	FHSVAL	INITIALIZE ERROR INDICATOR
048A9 6076	MOVMP	R2, R3	GET IOS PROGRAM SIZE
048AA R42007R4	MOVRL	R7, TEMSD	SAVE RA & N
048AC 4056	MOVRR	R5, R3	
048AD 4C54	ADDR	R5, R2	COMPUTE PNTR TO SCROLL PROGRAM END ( RA+N )
048AE F06A0003	MOVRR	R6, 3(R5)	GET ACQUISITION MODE
048B0 4A6000FF	ANDIR	R6, MKSRRHT	
048B2 F06R49C9	MOVRR	R6, ACUSLIST-16(R4)	SAVE ACQUISITION MODE FOR RIN SCROLL
048B4 9C49FFFA	ADDIR	R4, -32(R4)	
048B6 F05A0002	MOVRR	R5, 2(R5)	CONVERT SCROLL TO SCROLL INDEX GET TRANSFER DIR. FOR BUFFER CONTROL
(00771) *			
(00772) *	TEST IF TRANSFER DIRECTION LEGAL ( 0 OR 2 )		
(00773) *			
(00774) *	SKP	NEZ	
048B8 1910	HOP	LDSSC	0 => OK
048B9 2005	CMPIR	R5, 2	
048BA 92500002	SKPT	FO	2 => OK
048BC 1A10	JMP	IOSSFR1	OTHERWISE, ERROR
048BD R0004CFC			
(00779) *			
048BF 3A51	LIS	R5, 1	CONVERT TO 0 OR 4
048C0 F05R4R4C	MOVRR	R5, IOSSRWI(R4)	MOVE READ/WRITE FLAG INTO TABLE

04HC2 F05R4H4D	(007R2)	MOVPM	R5, IUSKSRWI+1(R4)		
	(007R3) *				
04HC4 F07R0001	(007R4)	MOVNR	R7, RS(R3)	EXTRACT LOGICAL BUFFER IDENTIFIERS	(RID 0) * 2
04HC6 506F00FF	(007R5)	MOVNR	R6, R7, MSKSRHT		
04HC8 3A61	(007R6)	LIS	R6, 1		
04HC9 3C77	(007R7)	LPS	R7, 7		(RID 1) * 2
	(007R8) *			CHECK RID 0	
04HCA 405C	(007R9)	MOVNR	R5, R6		
04HCB 86604CRC	(007R0)	CALL	R6, RINDSCH	ERROR ENCOUNTERED?	
04HCD 0250	(007R1)	TEST	R5	YES, EXIT	
04HCE 80304C6E	(007R2)	JMP	IUSSEH2, LTZ	CHECK RID 1	
	(007R3) *				
04HD0 405F	(007R4)	MOVNR	R5, R7		
04HD1 86604CRC	(007R5)	CALL	R6, RINDSCH		
04HD3 0250	(007R6)	TEST	R5		
04HD4 80304CC7	(007R7)	JMP	IUSSEH3, LTZ	COMPUTE INDEX INTO TRIPLET RID TABLE	
	(007R8) *				
04HD6 405H	(007R9)	MOVNR	R5, R4		
04HD7 3C51	(00R00)	LPS	R5, 1		
04HDR 4C58	(00R01)	ADDR	R5, R4		
04HD9 0R00	(00R02)	EVEN			
04HDA F06A4H6C	(00R03)	MOVNR	R6, IUSLRI(R5)	STORE RID 0 FOR INTERRUPT LINE 1	
04HDC F06A4H6F	(00R04)	MOVNR	R6, IUSLRI+2(R5)		
	(00R05) *				
04HDE F07A4H84	(00R06)	MOVNR	R7, IUSLRI+24(R5)	STORE RID 1 FOR INTERRUPT LINE 2	
04HE0 F07A4H86	(00R07)	MOVNR	R7, IUSLRI+26(R5)		
	(00R08) *				
04HF2 90760004	(00R09)	MOVIR	R7, 4(R3)	COMPUTE POINTER TO BUFFER 2 & 3 IDS	
04HF4 4C7A	(00R10)	ADDR	R7, R2	( R4+4 )	
04HF5 607E	(00R11)	MOVWP	R7, R7	GET BUFFER 2 & 3 IDS	
	(00R12) *				
04HF6 506F00FF	(00R13)	MOVNR	R6, R7, MSKSRHT	EXTRACT THEM	
04HF8 3A61	(00R14)	LIS	R6, 1	(RID 2) * 2	
04HF9 3C77	(00R15)	LPS	R7, 7	(RID 3) * 2	
	(00R16) *				
04HFA F06A4H6D	(00R17)	MOVNR	R6, IUSLRI+1(R5)	STORE RID 2 FOR INTERRUPT LINE 1	
04HFC F07A4H85	(00R18)	MOVNR	R7, IUSLRI+25(R5)	STORE RID 3 FOR INTERRUPT LINE 2	
	(00R19) *				
04HFE 405C	(00R20)	MOVNR	R5, R6	CHECK RID 2	
04HFF 86604CRC	(00R21)	CALL	R6, RINDSCH	ERROR ENCOUNTERED?	
04HF1 0250	(00R22)	TEST	R5	YES, EXIT	
04HF2 80304CC5	(00R23)	JMP	IUSSEH4, LTZ	CHECK RID 3	
	(00R24) *				
04HF4 405F	(00R25)	MOVNR	R5, R7		

```

04HF5 R6604CR (00R26) CALL R6, RIUSCHK
04HF7 0250 (00R27) TEST R5 ERROR ENCOUNTERED?
04HF8 H0304CR (00R28) JMP IUSSPR5, IZ YES, EXIT
(00R29) *
(00R30) * TEST IF SIZE OF RID 0 = INTEGRAL MULTIPLE OF # CHANNELS
(00R31) *
(00R32) *
(00R33) *
(00R34) *
(00R35) *
(00R36) *
(00R37) *
(00R38) *
(00R39) *
(00R40) *
(00R41) *
(00R42) *
(00R43) *
(00R44) *
(00R45) *
(00R46) *
(00R47) *
(00R48) *
(00R49) * SFT UP LOOP -- R6 = # CHANNELS, R7 = BUFFER SIZE
(00R50) *
(00R51) #1
(00R52) *
(00R53) *
(00R54) *
(00R55) *
(00R56) *
(00R57) *
(00R58) *
(00R59) * TEST IF SIZE(RID 0) = SIZE(RID 2) IF DOUBLE BUFFERED
(00R60) *
(00R61) IUSSS
(00R62) *
(00R63) *
(00R64) *
(00R65) *
(00R66) *
(00R67) *
(00R68) *
(00R69) *
  
```

```

R6, RIUSCHK
R5
IUSSPR5, IZ
( HA*N )
EXTRACT # CHANNELS
EXTRACT RID 0
GET BUFFER SIZE-1
SAVE FOR LATER USE
SKIP TEST IF NULL,
R7, R6
SUBTRACT # CHANNELS
DIFF. = 0 => NO ERROR
> 0 => CONTINUE
IUSSPR6
R6, ICP5)
R6, MSKSPRYT
R6, I
IUSSSH, NE
R7, 4CP5)
R7, MSKSPRYT
R7, I
R7, ACTSAD+HS(R7)
  
```

```

MOVPR R5, R3
ADDRP R5, R2
MOVNR R6, ICP5)
IUS R6, H
MOVNR R7, ICR3)
ANDIR R7, MSKSPRYT
IUS R7, I
MOVNR R7, ACTSAD+HS(R7)
INCR R7, I
MOVNR R7, TEMS7
TEST R6
JMP IUSSSA, IZ
SFT UP LOOP -- R6 = # CHANNELS, R7 = BUFFER SIZE
SUBRR R7, R6
JMP IUSSSA, F0Z
JMP R1, GTZ
FRP, SIZE NOT A MULTIPLE OF # CHANNELS
JMP IUSSPR6
TEST IF SIZE(RID 0) = SIZE(RID 2) IF DOUBLE BUFFERED
MOVNR R6, ICP5)
ANDIR R6, MSKSPRYT
CMPIR R6, I
JMP IUSSSH, NE
MOVNR R7, 4CP5)
ANDIR R7, MSKSPRYT
IUS R7, I
MOVNR R7, ACTSAD+HS(R7)
  
```

```

EXTRACT ACQUISITION MODE
IF DOUBLE BUFFERED MODE, DO TEST
OTHERWISE, CONTINUE
EXTRACT RID 2
GET SIZE-1 OF RID 2
  
```

```

04C22 2671 (00870) INCR R7, 1
(00871) *
04C23 2700746 (00872) CMPMR R7, TMS2 ? SIZE OF RID 0?
04C25 81104CF (00873) JMP IUSERR, NF NO, ERROR
(00874) *
04C27 10520002 (00875) IDSSR GET SCROLL TYPE FROM FCH
04C29 92500003 (00876) MOVMR R5, 2(R1) DISPATCH ON SCROLL TYPE
04C2B 80104C98 (00877) CMPIR R5, 3 JUMP IF IOS=3
04C2D 92500004 (00878) CMPIN R5, 4 TEST IF IOS=4
04C2F 80104CA6 (00879) JMP IUSO2, FO JUMP IF IOS=4
(00880) *
(00881) *
(00882) *
(00883) *
(00884) *
(00885) *
(00886) *
04C31 90560006 (00887) MOVIR R5, 6(R3) COMPUTE POINTER TO SCROLL DATA - 1
04C33 4C54 (00888) ADDR R5, R2 ( HA+M+6 )
04C34 406A (00889) MOVMR R6, R5 GET FIRST SCROLL REGISTER ID
04C35 F07HEFFF (00890) MOVMR R7, -(R5) GET M = # OF SCROLL REGISTERS TO LOAD
04C37 80104C4A (00891) JMP IUSO0, F0Z BYPASS LOAD IF NONE, ELSE
04C39 80304CD4 (00892) JMP IUSERR9, I.TZ # REG < 0 => ERROR
(00893) *
(00894) *
(00895) *
(00896) *
(00897) *
(00898) *
04C3H 0260 (00899) TEST R6 CHECK 1ST REGISTER
04C3C 80304CD1 (00900) JMP IUSERR, I.TZ < 0 => ERROR
04C3F 9260000F (00901) CMPIR R6, 15 > 15 => ERROR
04C40 80204CD1 (00902) JMP IUSERR, GT CHECK # + 1ST REGISTER
(00903) *
04C42 4C7C (00904) ADDR R7, R6 CHECK # + 1ST REGISTER
04C43 92700010 (00905) CMPIR R7, 16 > 16 => ERROR
04C45 80204CD4 (00906) JMP IUSERR, GT RESTORE R7 = M
(00907) *
04C47 4F7C (00908) SUBRR R7, R6 RESTORE R7 = M
(00909) *
(00910) *
04C4H 86604D8C (00911) CALL R6, WSR51 LOAD THE SCROLL REGISTERS
(00912) *
(00913) *---- R5 ASSUMED = (RA+M+6+M)
  
```



```

(00914) *
04C4A 2721 INCR R7, 1 ADJUST COUNT
04C4B 2651 INCR R3, 1 AND START ADDRESS
04C4C R20H4C7C XCT LDSSCR12(R4) EXECUTE PROPER BLOCK MOVE
(00918) *
(00919) *----- BIND THE LOGICAL BUFFERS
(00920) *
(00921) * SPT UP FOR CALLS TO I02SHDR ROUTINE
(00922) *
04C4F 0170 CLR R7 SET UP NULL OFFSET VALUE
04C4F 2651 INCR R5, 1 POINT INTEGER SCALAR ID OF OFFSET
04C50 616A MOVWR R6, R5 GET SCALAR ID
(00926) *
04C51 R0304C59 (00927) JMP LDSSNULL, I1Z IF < 0, => NULL
04C53 92600080 (00928) CMPJR R6, JSVTS17 IF > MAX => ERROR
04C55 R1304C07 (00929) JMP IUSSER10, GE
(00930) *
04C57 F07C0502 (00931) MOVWR R7, JSVTS(R6) LEGAL ID, GET OFFSET VALUE
04C59 F0704F30 (00932) MOVWR R7, I0RDSOFF STORE FOR I02SHDR ROUTINE
(00933) *
04C5A C02007H4 (00934) MOVWPL R2,TFMS0 RESTORE R2=N, R3=NA
04C5B 4C26 (00935) ADDR R2, R3 (NA + N )
04C5F F0740004 (00936) MOVWR R7, 4(R2) R7 = BID 3 : 2 FOR LATER USE
04C60 F04H4C7D (00937) MOVWR R4, LDSSCR12+H$(R4) SET UP SCROLL LOAD ADDRESS
(00938) *
04C62 F0260001 (00939) MOVWR R2, 1(R3) GET BID 1 : 0
04C64 506400FF (00940) MOVKR R6, R2, MSKSRHVT R6 = BID 0
04C66 2651 (00941) INCR R5, 1 R5 = POINTER TO BINDING CHAIN ANCHOR
04C67 2632 (00942) INCR R3, 2 R3 = POINTER TO SCROLL PROGRAM
(00943) *-----
04C68 R6104DCR (00944) CALL R1, I02SHDR BIND BUFFER 0
(00945) *
04C6A 2651 (00946) INCR R5, 1 STEP TO NEXT CHAIN ANCHOR
04C6B 5064F00 (00947) MOVKR R6, R2, MSKSLHVT GET BID 1
04C6D 3C6A (00948) LRS R6, R RIND BUFFER 1
(00949) *-----
04C6E R6104DCR (00950) CALL R1, I02SHDR BIND BUFFER 1
(00951) *
04C70 2651 (00952) INCR R5, 1 STEP TO NEXT CHAIN ANCHOR
04C71 506F00FF (00953) MOVKR R6, R7, MSKSRHVT GET BID 2
(00954) *-----
04C73 R6104DCR (00955) CALL R1, I02SHDR BIND BUFFER 2
(00956) *
04C75 2651 (00957) INCR R5, 1 STEP TO NEXT CHAIN ANCHOR

```

PAGE 31: SNAP-II IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A  
 LDSS MODULE TO PROCESS THE LOAD IOS SCROLL, FCH

04C76 506FFFD0 (00958)	MOVKB	R6, R7, MKSRIHYT	GET RID 3
04C78 306H	IRS	R6, R	
04C79 46104DCR (00960) *----	CALL	R1, I02SHDR	HIND BUFFFF 3
(00962) *			
(00963) *			
04C7B 0470	RETURN		
(00964)	EVEN		
(00965)			
(00966) *			
(00967) *			
(00968) *	IOS-7 LOAD INSTRUCTIONS		
(00969) *			
(00970) *			
04C7C 0535F000 (00971) I05SRI,7	MOVE	R3, R2, S1F000	LOAD SCROLL 16
04C7E 0535F200 (00972)	MOVE	R3, R2, S1F200	LOAD SCROLL 17
04C80 0535F400 (00973)	MOVE	R3, R2, S1F400	LOAD SCROLL 18
04C82 0535F600 (00974)	MOVE	R3, R2, S1F600	LOAD SCROLL 19
04C84 0535F800 (00975)	MOVE	R3, R2, S1F800	LOAD SCROLL 20
04C86 0535FA00 (00976)	MOVE	R3, R2, S1FA00	LOAD SCROLL 21
04C88 0535FC00 (00977)	MOVE	R3, R2, S1FC00	LOAD SCROLL 22
04C8A 0535FE00 (00978)	MOVE	R3, R2, S1FE00	LOAD SCROLL 23
(00979) *			
(00980) *			
(00981)	EJECT		



PROGRAM # 840001.01A  
 TO PROCESS THE LOAD IOS SCROLL FOR

```

04C100 1 LOAD IOS-3 SCROLL.
04C101 *
04C102 *
04C103 *
04C104 *
04C105 *
04C106 *
04C107 *
04C108 *
04C109 *
04C110 *
04C111 *
04C112 *
04C113 *
04C114 *
04C115 *
04C116 *
04C117 *
04C118 *
04C119 *
04C120 *
04C121 *
04C122 *
04C123 *
04C124 *
04C125 *
04C126 *
04C127 *
04C128 *
04C129 *
04C130 *
04C131 *
04C132 *
04C133 *
04C134 *
04C135 *
  
```

ADJUST REPEAT COUNT

EXECUTE PROPER BLOCK MOVE

IOS-3 LOAD INSTRUCTIONS

LOAD SCROLL 16  
 LOAD SCROLL 17  
 LOAD SCROLL 18

EJECT

04CAB 3C21	(01036)	LDSS02	IMS	R2, 1	ADJUST REPEAT COUNT
04CA7 2121	(01037)		DECR	R2, HS	
04CAN R20R4CAC	(01038)		XCT	LDSSCR14(R4)	EXECUTE PROPER LPROCL
04CAA 0470	(01039)		RETURN		
04CAH 0M00	(01040)		EVEN		
	(01041) *				
04CAC 0635FF1F	(01042)	LDSSCR14	LPROCL	R1, R2, S1FF1F	LOAD SCROLL 16
04CAF 0635FF3F	(01043)		LPROCL	R1, R2, S1FF3F	LOAD SCROLL 17
04CR0 0635FF5F	(01044)		LPROCL	R1, R2, S1FF5F	LOAD SCROLL 18
04CH2 0635FF7F	(01045)		LPROCL	R1, R2, S1FF7F	LOAD SCROLL 19
04CM4 0635FF9F	(01046)		LPROCL	R1, R2, S1FF9F	LOAD SCROLL 20
04CN6 0635FFBF	(01047)		LPROCL	R1, R2, S1FFBF	LOAD SCROLL 21
04CR8 0635FFDF	(01048)		LPROCL	R1, R2, S1FFDF	LOAD SCROLL 22
04CHA 0635FFFF	(01049)		LPROCL	R1, R2, S1FFFF	LOAD SCROLL 23
	(01050) *				
	(01051)		EJECT		



MNSS MODULE TO PROCESS THE RUN IUS SCROLL FCH

- (01083) \* MNSS
- (01084) \*
- (01085) \*
- (01086) \*
- (01087) \*
- (01088) \*
- (01089) \*
- (01090) \*
- (01091) \*
- (01092) \*
- (01093) \*
- (01094) \*
- (01095) \*
- (01096) \*
- (01097) \*
- (01098) \*
- (01099) \*
- (01100) \*
- (01101) \*
- (01102) \*
- (01103) \*
- (01104) \*
- (01105) \*
- (01106) \*
- (01107) \*
- (01108) \*
- (01109) \*
- (01110) \*
- (01111) \*
- (01112) \*
- (01113) \*
- (01114) \*
- (01115) \*
- (01116) \*
- (01117) \*
- (01118) \*
- (01119) \*
- (01120) \*
- (01121) \*
- (01122) \*

FCH FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCH AND FUNCTION LIST FLAG(LSB)	
1	67	0
2	SCROLL IDENT	
3	SCROLL TYPE	
4	SCROLL START ADDRESS	
5	0	0

OBJECT

04CF4 F0720001	(01121)	RNSS	MOVW	R7, 1(R1)	GPT SCROLL ID
04CF6 H60039FA	(01124)		CALL	R0, WAITSI05	WAIT TILL SCROLL DEVICE FREE
04CF8 H6104006	(01125)		CALL	R1, RNSSTNT	SET UP BUFFERS AND INTERRUPTS
04CFA 9C7F4FFD	(01126)	*	ADDI	R7, -2(R7)	CONVERT TO SCROLL INDEX
04CFC 0D60	(01127)		CLR	R6	SET UP REGISTER FOR START SCROLL
04CFD 0800	(01129)		EVFN		
04CFE C0420002	(01130)		MOVWRL	R4, 2(R1)	R4 = IOS TYPE ; R5 = START LITERAL
04CF0 92400003	(01131)		CMPJP	R4, 3	DISPATCH ON SCROLL TYPE
04CF2 H0104040	(01132)		JMP	RNSS01, F0	JUMP IF IOS-3
04CF4 92400004	(01133)	*	CMPJP	R4, 4	TEST FOR IOS-4
04CF6 H010404A	(01135)		JMP	RNSS02, F0	JUMP IF IOS-4
04CF8 3A5H	(01138)	*	LI.S	R5, R	MOVE START ADDRESS TO LEFT BYTE
04CF9 F00F403D	(01140)		MOVW	R5, RNSSI052(R7)	START THE SCROLL
04CFB 3C71	(01141)		IRS	R7, 1	
04CFC F02F4009	(01142)		MOVW	R2, ACOSLIST(R7)	GPT ACQUISITION MODE FLAG
04CFE 1810	(01143)	*	SKP	F0Z	SKIP IF DISCRETE SAMPLING
04CF8 0F70	(01145)		RETURN		PLSP, EXIT
04CFD 0F70	(01146)		EVFN		
04D00 90600009	(01147)	*	MOVW	R6, 0	POINT TO SYNCSTOP REGISTER
04D02 3A71	(01149)		LI.S	R7, 1	
04D03 F0AF403D	(01150)		MOVW	R2, RNSSI052(R7)	THEN REFERENCE IT TO SFT SYNCSTOP
04D05 0F70	(01151)		RETURN		
04D06 0F70	(01152)	*	EVFN		
04D07 0F70	(01153)	*	JFCT		
04D08 0F70	(01154)	*	JFCT		





04D30	0010F1FF	(01190) * RNS\$IOS2	ADDR	\$1FFF(R6,1)	START SCROLL 16
04D32	0010F1FF	(01191)	ADDR	\$13FF(R6,1)	START SCROLL 17
04D34	0010F5FF	(01192)	ADDR	\$15FF(R6,1)	START SCROLL 18
04D36	0010F7FF	(01193)	ADDR	\$17FF(R6,1)	START SCROLL 19
04D38	0010F9FF	(01194)	ADDR	\$19FF(R6,1)	START SCROLL 20
04D3A	0010FBFF	(01195)	ADDR	\$1BFF(R6,1)	START SCROLL 21
04D3C	0010FDFF	(01196)	ADDR	\$1DFF(R6,1)	START SCROLL 22
04D3E	0010FFFF	(01197)	ADDR	\$1FFF(R6,1)	START SCROLL 23
		(01198) * (01199) *			
		(01200) *	START IOS-3 TYPE SCROLL		
04D40	F00F1044	(01201) RNS\$O1	MOVEM R5, RNS\$IOS3(R7)		START THE SCROLL
04D42	0F70	(01202)	RETURN		
04D43	0800	(01203)	EVEN		
		(01204) *			
		(01205) *	START ADDRESSES FOR IOS-3		
		(01206) *			
04D44	0010FFDC	(01207) RNS\$IOS3	ADDR	\$1FDC(R6,1)	START SCROLL 16
04D46	0010FFFC	(01208)	ADDR	\$1FFC(R6,1)	START SCROLL 17
04D48	0010FFFC	(01209)	ADDR	\$1FFC(R6,1)	START SCROLL 18
		(01210) *			
		(01211) *	START IOS-4 TYPE SCROLL		
		(01212) *			
04D4A	3A5C	(01213) RNS\$O2	I.LS R5, 12		MOVE START ADDR TO LEFT MOST DIGIT
04D4B	0800	(01214)	EVEN		
04D4C	F00F4050	(01215)	MOVEM R5, RNS\$IOS4(R7)		START THE SCROLL
04D4E	0F70	(01216)	RETURN		
04D4F	0800	(01217)	EVEN		
		(01218) *			
		(01219) *	START ADDRESSES FOR IOS-4		
		(01220) *			
04D50	0010FF1A	(01221) RNS\$IOS4	ADDR	\$1FF1A(R6,1)	START SCROLL 16
04D52	0010FF3A	(01222)	ADDR	\$1FF3A(R6,1)	START SCROLL 17
04D54	0010FF5A	(01223)	ADDR	\$1FF5A(R6,1)	START SCROLL 18
04D56	0010FF7A	(01224)	ADDR	\$1FF7A(R6,1)	START SCROLL 19
04D58	0010FF9A	(01225)	ADDR	\$1FF9A(R6,1)	START SCROLL 20
04D5A	0010FFBA	(01226)	ADDR	\$1FFBA(R6,1)	START SCROLL 21
04D5C	0010FFDA	(01227)	ADDR	\$1FFDA(R6,1)	START SCROLL 22
04D5E	0010FFFA	(01228)	ADDR	\$1FFFA(R6,1)	START SCROLL 23
		(01229) *			
		(01230) *	FNCT		

PAGE 40: SNAP-11 I/O PACKAGE ---- APR 18, 1960 ---- PROGRAM # R40001.01A  
RMS5 MODULE TO PROCESS THE RUN I/O SCREEN FCH

04060	12400000	(01235)	RMS5FINT	FINT	MSS, TLVLS1
04062	12400000	(01236)	FINT	FINT	MSS, TLVLS2
04064	12400000	(01237)	FINT	FINT	MSS, TLVLS3
		(01238)			
		(01239)			FVFN
		(01231)			
		(01232)			
		(01233)			
		(01234)			
		(01235)			
		(01236)			
		(01237)			
		(01238)			
		(01239)			

HAAS MODULE TO PROCESS THE RUN ADAM AND AUM FCH

(01240) \* HAAS  
 (01241) \*  
 (01242) \*  
 (01243) \*  
 (01244) \* FCH FORMAT (16 BIT WORD FORMAT SHOWN)  
 (01245) \*  
 (01246) \*  
 (01247) \*  
 (01248) \*  
 (01249) \*  
 (01250) \*  
 (01251) \*  
 (01252) \*  
 (01253) \*  
 (01254) \*  
 (01255) \*  
 (01256) \*  
 (01257) \*  
 (01258) \*  
 (01259) \*  
 (01260) \*  
 (01261) \*  
 (01262) \*  
 (01263) \*  
 (01264) \*  
 (01265) \*  
 (01266) \*  
 (01267) \*  
 (01268) \*  
 (01269) \*  
 (01270) \*  
 (01271) \*  
 (01272) \*  
 (01273) \*  
 (01274) \*  
 (01275) \*  
 (01276) \*  
 (01277) \*  
 (01278) \*  
 (01279) \*

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCH AND FUNCTION LIST FLAG(LSB)	
1	6R	ADAMID
2	ADAMSA	ADMID
3	ADMSA	0
4	0	0
5	0	0

SUBJECT



PAGE 43: SNAP-11 IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A  
HAAS MIDDLE TO PROCESS THE RUN ADAM AND ADM FCR

04096 3C51	(01324)	MPRAAS0	IRS	R5, 1	
04090 P02A4Q09	(01325)		MOVW	R2, ACUSLIST(R5)	GFT ACQUISITION MODE FOR ADM
04092 H1104Q9	(01326)		JMP	MPRAAS1, MFZ	
	(01327) *				
04096 40600009	(01328)		MOVW	R6, 9	IF = 0,
04096 3A51	(01329)		LIS	R5, 1	
04097 P0RA4Q30	(01330)		MOVW	R7, @RNSST0S2(R5)	SFT SYNCSTOP REGISTER
	(01331) *				
04090 0F70	(01332)	MPRAAS1	RTURN		
	(01333)		EVEN		

MSRS MODULE TO PROCESS THE READ FROM SCROLL REGISTERS FCH

```

(01334) * MSRS
(01335) *
(01336) *
(01337) *
(01338) * FCH FORMAT (16 BIT WORD FORMAT SHOWN)
(01339) *
(01340) *
(01341) *
(01342) *
(01343) *
(01344) *
(01345) *
(01346) * 0
(01347) *
(01348) *
(01349) *
(01350) * 1
(01351) *
(01352) *
(01353) *
(01354) * 2
(01355) *
(01356) *
(01357) *
(01358) *
(01359) *
(01360) *
(01361) *
(01362) * 4
(01363) *
(01364) *
(01365) *
(01366) *
(01367) *
(01368) *
(01369) *
(01370) *
(01371) *
(01372) *
(01373) *

```

FCH FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCH AND FUNCTION LIST FLAG(LSR)	
1	/0	INTEGER SCALAR IDENTIFIER
2	SCROLL IDENTIFIER	
3	SCROLL TYPE	
4	STARTING REGISTER	
5	COUNT	

OBJECT

```

(01374) *
(01375) *
(01376) *
04D9A 707200FF (01377) RSRMS          MOVWA   R7, P1, RSRSMHYT    EXTRACT INTEGER TABLE IDENTIFIER
04D9C 9C700502 (01378)                ADDR   R7, ISVTS      CONVERT TO MAP ADDRESS
04D9E F0704001 (01379)                MOVWA  R7, RSRSMOV+HS  INSTALL IN BLACK MOVE INST.
04DA0 F0420001 (01380)                MOVPR  P4, ICHI)      GET SCROLL IDENT
04DA2 9C49FF60 (01381)                ADDR   R4, -32(R4)   CONVERT SCROLL ID TO WORD INDEX
(01382) *
04DA4 C0520003 (01383)                MOVWRL R5, ICHI)    GET STARTING REGISTER AND COUNT
04DA6 F07R4031 (01384)                MOVPR  R7, RSRSTOS2+HS(R4)  CONVERT REGISTER ID TO P5000 MEM. A
04DA8 2671     (01385)                INCR   R7, 1        ADJUST POINTER FOR RMOVF
04DA9 3A71     (01386)                LLS    R7, 1        FORCE HIT 18 TO ZERO
04DAA 3C71     (01387)                LRS    R7, 1        POINT TO REGISTER TO START THE READ
04DAB 4C7A     (01388)                ADDR   R7, R5       ADJUST REPEAT COUNT
04DAC 2761     (01389)                DECR   R6, 1        READ THE REGISTERS
04DAD W2004DR0 (01390)                XCT     RSRSMOV      RETURN FOR NEXT FCH
04DAF 0E70     (01391)                RETURN
(01392) *
(01393) *
04DB0 D57C0000 (01394) RSRSMOV RMOVF      BLOCK MOVE INST. FOR READ
(01395) EVEN
  
```



\*SFS \*MODULE TO PROCESS THE WRITE INTO SCROLL REGISTERS FCR

FCR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCR AND FUNCTION LIST FLAG(LSR)	
1	71	INTEGER SCALAR IDENTIFIER
2	SCROLL IDENTIFIER	
3	SCROLL TYPE	
4	STARTING REGISTER	
5	COUNT	

- (01396) \* MSFS
- (01397) \*
- (01398) \*
- (01399) \*
- (01400) \*
- (01401) \*
- (01402) \*
- (01403) \*
- (01404) \*
- (01405) \*
- (01406) \*
- (01407) \*
- (01408) \*
- (01409) \*
- (01410) \*
- (01411) \*
- (01412) \*
- (01413) \*
- (01414) \*
- (01415) \*
- (01416) \*
- (01417) \*
- (01418) \*
- (01419) \*
- (01420) \*
- (01421) \*
- (01422) \*
- (01423) \*
- (01424) \*
- (01425) \*
- (01426) \*
- (01427) \*
- (01428) \*
- (01429) \*
- (01430) \*
- (01431) \*
- (01432) \*
- (01433) \*
- (01434) \*
- (01435) \*

OBJECT

AD-A091 663

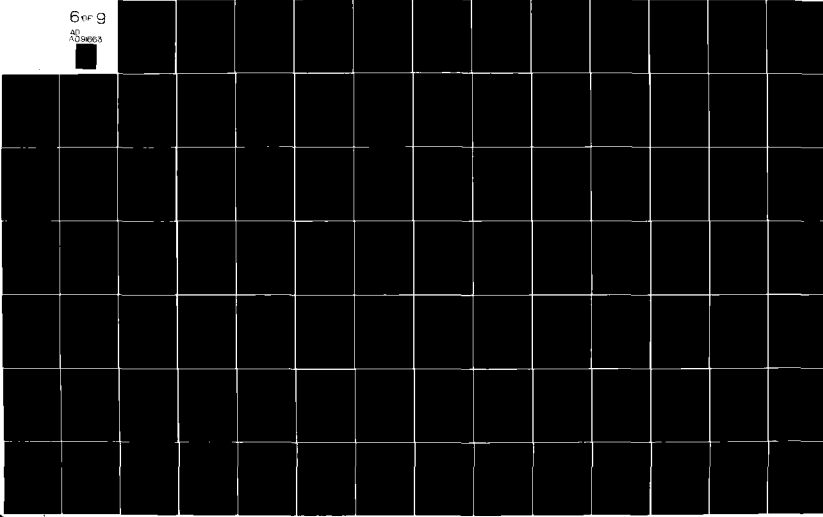
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

6 of 9

NO  
FORN DISSEM



PAGE 47: SWAP-11 I/O PACKAGE --- APR 14, 1980 --- PROGRAM # R00001.01A  
 MODULE TO PROCESS THE WRITE INTO SCROLL REGISTERS FOR

(01436) *					
(01437) *					
(01438) *					
04DH2 F0420001	WSRS	MOVW	R4, 1(R1)	GET SCROLL ID	
04DH4 9C49FFFD		ADDR	R4, -12(R4)	CONVERT SCROLL ID TO WORD INDEX	
(01441) *					
04DH6 705200FF		MOVW	R5, R1, WSRSHRIT	GET INTFGR TARGE ID	
04DH8 9C500501		ADDR	R5, 1SVFS-1	CONVERT TO MAP ADDRESS-1	
04DHA C0620003		MOVW	R6, 3(R1)	GET START REG. AND COUNT	
(01445) *					
(01446) *					
(01447) *					
(01448) *					
(01449) *					
(01450) *					
(01451) *					
(01452) *					
04DHC FC684031	WSRS1	ADDR	R6, WSSIOS2+HS(R4)	CONVERT REG. ID TO PSEUDO-MEM. ADDR.	
04DHE 2662		TNCR	R6, 2		
04DHF F0604DC7		MOVW	R6, WSRSHMOV+HS	STORE IN BLOCK MOVE INSTRUCTION	
04DC1 2771		DPCR	R7, 1	CONVERT COUNT TO NO. OF REPEATS	
04DC2 82004DC6		XCT	WSRSHMOV	WRITE INTO THE REGISTERS.	
04DC4 0F70		RETURN			
04DC5 0800		EVEN			
(01460) *					
04DC6 D55F0000	WSRSHMOV	RMVW	R5, R7, WSS+\$10000	BLOCK MOVE INSTRUCTION FOR WRITE	
(01462)					

ENTER HERE FROM L05\$ WITH REG 4-7 SET UP PROPERLY

R4 SCROLL INDEX  
 R5 POINTER - 1 TO DATA TO LOAD SCROLL REGISTER  
 R6 1ST SCROLL REGISTER  
 R7 # SCROLL REGISTERS TO LOAD

(01461) \* T02SDR IOS SCROLL PROGRAM BINDING SUBROUTINE  
 (01464) \*  
 (01465) \* THIS MODULE BINDS THE SPECIFIED ATTRIBUTES OF A LOGICAL BUFFER  
 (01466) \* TO THE IOS-2 PROGRAM. THE UNBOUND SCROLL PROGRAM MUST HAVE BEEN  
 (01467) \* LOADED PRIOR TO CALLING THIS PROGRAM.  
 (01468) \* THE CHAIN INFORMATION IS CONTAINED IN THE RIGHTMOST 15 BITS OF A  
 (01469) \* 32 BIT IOS-2 INSTRUCTION.  
 (01470) \*  
 (01471) \* ROUTINE ASSUMES WL = 0 => LONG (16 BIT)  
 (01472) \*  
 (01473) \* THE FORMAT OF THE CHAIN DESCRIPTOR IS:  
 (01474) \*  
 (01475) \* BITS: 0-7 BACKWARD (WORD) POINTER TO THE NEXT CHAIN ENTRY  
 (01476) \* BITS: 8-14 ATTRIBUTE SPECIFIER  
 (01477) \* VALUE: ATTRIBUTE  
 (01478) \* 0 BASE ADDRESS  
 (01479) \* 1 BASE ADDRESS - SEPARATION  
 (01480) \* 2 BUFFER SEPERATION  
 (01481) \* 3 BUFFER EXTENT  
 (01482) \* 4 BUFFER SIZE  
 (01483) \* 5 BUFFER SIZE - OFFSET  
 (01484) \* 6 OFFSET  
 (01485) \*  
 (01486) \* THIS MODULE IS CALLED WITH:  
 (01487) \*  
 (01488) \* P3 = BASE ADDRESS OF SCROLL PROGRAM ON BUS 1  
 (01489) \* P4 = SCROLL PROGRAM LOAD ADDRESS (LEAST SIGNIFICANT 16 BITS)  
 (01490) \* P5 = POINTER TO BINDING CHAIN ANCHOR  
 (01491) \* P6 = LOGICAL BUFFER ID  
 (01492) \*  
 (01493) \* TORSOFF CONTAINS INTEGER OFFSET  
 (01494) \*  
 (01495) \* -----  
 (01496) \*  
 (01497) \*  
 (01498) \* RID .IF. 0 OR  
 (01499) \* BINDING CHAIN ANCHOR .IT. 0  
 (01500) \*  
 (01501) \* => NO BINDING DONE.  
 (01502) \*  
 (01503) \* -----  
 (01504) \*  
 (01505) \* T02SDR MOVWP R5, R5 GET BINDING CHAIN ANCHOR  
 (01506) \* SKP GEZ

040CA 0470	(01507) *	RETURN	EXIT IF NULL
040CB 406C	(01508) *	ADDR R6, R6	CONVERT BUFFER TO WORD INDEX
040CC 1420	(01509)	SKP GTZ	
040CD 0470	(01510)	RETURN	EXIT IF NULL
	(01511) *		
	(01512) *		
	(01513) *---	REGISTER USAGE:	
	(01514) *		
	(01515) *	R1 INSTRUCTION TO BE ROUNDED	
	(01516) *	P2 ATTRIBUTE & THEN RIGHT HALF OF INSTRUCTION	
	(01517) *	P7 DISPLACEMENT TO NEXT INSTRUCTION	
	(01518) *		
	(01519) *	R5, 1 ADJUST BINDING POINTER	
040CF 3A51	(01519)	LIS	
040CF 0400	(01520)	FVFN	
040DD 40304F21	(01521)	MOVPM R1, I0MDSPT+HS	SFT POINTER TO IOS PROGRAM ON BUS 1
040DD 40404F1F	(01522)	MOVPM R4, I0MDSPT+HS	SET SCROLL ADDRESS
	(01523) *		
	(01524) *	GET NEXT INSTRUCTION TO BE ROUNDED	
	(01525) *	DISPATCH TO PROPER BINDING ROUTINE	
	(01526) *		
040DD 40904F20	(01527) *	I02SHDRN MOVPM R1, R10MDSPT	GET INSTRUCTION TO BE ROUNDED
040DD 507400FF	(01528)	MOVKR R7, R2, MCKSRHT	EXTRACT DISPLACEMENT TO NEXT INSTRUCT.
040DD 3A71	(01529)	LIS R7, 1	ADJUST DISPLACEMENT TO HALF WORD
040DD 3C27	(01530)	LRS R2, 7	POSITION ATTRIBUTE SPECIFIER AS WORD INDE
040DD 9A2000FF	(01531)	ANDIR R2, SF	RESET LEAST SIGNIFICANT BIT
040DD 80444F22	(01532)	JMP R102SHDRN(R2)	DISPATCH TO PROPER BINDING MODULE
	(01533) *		
	(01534) *		
	(01535) *---	BASE ADDRESS BINDING ---	
	(01536) *		
040DD 03C0582	(01537)	I02SHDRN MOVPM R3, HCTSRA(R6)	GET BASE ADDRESS ATTRIBUTES OF BUFFER
040DD 9A11FFFF	(01538)	ANDIR R1, SIFF8	CLEAR OUT BUS AND BIT 16
040DD 56160007	(01539)	INKR R1, R3, \$7	MERGE BUS AND BIT 16 FROM BUFFER
	(01540)	EVEN	
	(01541) *		
	(01542) *---	PATH FOR BINDING INTO IOS MEMORY ---	
	(01543) *---	AND FOR LOOPING TO NEXT INSTRUCTION	
	(01544) *		
040DE 4028	(01545) #0	MOVRR R2, R4	GET LOW ORDER BITS
	(01546) *		
040DE 84904F1F	(01547) #1	MOVPM R1, R10MDSPT	MOVE ROUNDED INSTRUCTION TO IOS2 MEM.
040DE 0270	(01548)	TFST P7	AND OTHER INSTRUCTION IN CHAIN?
040DE 1910	(01549)	SKP NEZ	
040DE 0470	(01550)	RETURN	NO, EXIT

040FA 4F5F	(01551) *	SUMMR	R5, R7	YES, COMPUTE DISPLACEMENT TO NEXT INST.
040FH 40304C1A	(01552) *	JMP	I05SR11, LTZ	WINDING CHAIN ERROR, EXIT
040FD 211A	(01553) *	HOP	I02SHDRN	OK, GET NEXT INST TO RE-ROUND
	(01554) *			
	(01555) *			
	(01556) *			
	(01557) *			
	(01558) *			
	(01559) *			
040FE C03C0592	(01560) *	I02SRDR1	MOVMBL R3, RCTSHA(R6)	GET BASE ADDRESS ATTRIBUTES OF BUFFER
040FF 9A11FFFH	(01561) *	ANDIR	R1, S1FFFF	CLEAR OUT RUS AND HIT 16
040F2 561A0006	(01562) *	I0RKR	R1, R3, S6	MERGE RUS ONLY
040F4 F44C0604	(01563) *	SUMMR	R4, RCTSD(R6)	SUBTRACT SEPARATION
040F6 0630	(01564) *	DAC	R3	
040F7 561A0001	(01565) *	I0RKR	R1, R3, 1	MERGE HIGH ORDER ADDRESS BIT
040F9 2116	(01566) *	HOP	#0	GO MOVE INST TO SCROLL MEMORY
	(01567) *			
	(01568) *			
	(01569) *			
	(01570) *			
040FA F03C0604	(01571) *	I02SHDR2	MOVMBL R3, RCTSD(R6)	GET BUFFER SEPARATION
040FC 50267FFF	(01572) *	MOVKR	R2, R3, S7FFF	MERGE 15 BIT SEPARATION
040FF 211A	(01573) *	HOP	#1	GO MOVE INST TO SCROLL MEMORY
040FF 0800	(01574) *			
	(01575) *			
	(01576) *			
	(01577) *			
04F00 C03C0604	(01578) *	I02SRDR3	MOVMBL R3, RCTSD(R6)	GET BUFFER SEPARATION AND SIZE
04F02 4R3R	(01579) *	MULRR	R3, R4	COMPUTE HALF-WORD DISPLACEMENT
04F03 FC4C0583	(01580) *	ADDMR	R4, RCTSHA+HS(R6)	ADD BUFFER BASE ADDRESS
04F05 50287FFF	(01581) *	MOVKR	R2, R4, S7FFF	MERGE 15 BIT LLIT FIELD
04F07 2123	(01582) *	HOP	#1	GO MOVE INST. TO SCROLL MEMORY
	(01583) *			
	(01584) *			
	(01585) *			
	(01586) *			
04F08 F04C0605	(01587) *	I02SRDR4	MOVMBL R4, RCTSD+HS(R6)	GET BUFFER SIZE-1
04F0A 2641	(01588) *	TNCR	R4, 1	
04F0B 50287FFF	(01589) *	MOVKR	R2, R4, S7FFF	MERGE 15 BIT LLIT FIELD
04F0D 2129	(01590) *	HOP	#1	GO MOVE INSTRUCTION TO SCROLL MEMORY
	(01591) *			
	(01592) *			
	(01593) *			
	(01594) *			

PAGE 51: SNAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # 840001.01A  
 IO2SHDR IOS SCROLL PROGRAM BINDING SUBROUTINE

04F0F F0304F30 (01595)	IO2SHDRS	MOVBR	R3, IOBDSOFF	GET OFFSET
04F10 F04C6605 (01596)		MOVBR	R4, ICI5AD*HS(R6)	GET BUFFER SIZE-1
04F12 2641 (01597)		INCR	R4, 1	
04F13 4F46 (01598)		SUBRR	R4, R3	SUBTRACT OFFSET
04F14 50267FFF (01599)		MOVBR	R2, R4, STFFF	MERGE 15 BIT LUT
04F16 2132 (01600)		HOP	#1	
04F17 0800 (01601)		EVFN		
			(01602) *	
			(01603) *	
			(01604) *	
			----- OFFSET BINDING ----	
04F18 F0304F30 (01605)	IO2SHDRS	MOVBR	R3, IOBDSOFF	GET OFFSET
04F1A 50267FFF (01606)		MOVBR	R2, R3, STFFF	MERGE 15 BIT LUT
04F1C 2138 (01607)		HOP	#1	
04F1D 0800 (01608)		EVFN		
			(01609) *	
			(01610)	
			EJECT	

GO MOVE INSTRUCTION TO SCROLL MEMORY

PAGE 52: SNAP-11 IOS PACKAGE ---- APR 14, 1980 ---- PROGRAM # 840001.01A  
 I02SDMR I05 SCROLL PROGRAM HINDING SUBROUTINE

04F20	000A0000	(01612)	I0HDSPT	ADDR	S1000(R5)	POINTER TO STORE INST IN SCROLL
04F21	000A0000	(01613)	I0HDSPT	ADDR	MSS(R5)	POINTER TO GET INST FROM SCROLL PROGRAM
04F22	0000400F	(01614)	I02SDMR	ADDR	I02SDMR0	HINDING DISPATCH TABLE
04F23	0000400F	(01615)		ADDR	I02SDMR1	
04F24	0000400F	(01616)		ADDR	I02SDMR2	
04F25	0000400F	(01617)		ADDR	I02SDMR3	
04F26	0000400F	(01618)		ADDR	I02SDMR4	
04F27	0000400F	(01619)		ADDR	I02SDMR5	
04F28	0000400F	(01620)		ADDR	I02SDMR6	
04F30	0000	(01621)	I0HDSOFF	DATA	MSS	OFFSET VALUE
04F31	0000	(01622)				
04F32	0000	(01623)				
04F33	0000	(01624)				



SNAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A  
 ADAMSSSP MODULE TO PROCESS THE ADAM SIMPLIFIED SAMPLING PLAN

ADAMSSSP MODULE TO PROCESS THE ADAM SIMPLIFIED SAMPLING PLAN

(01625) \*  
 (01626) \*  
 (01627) \*  
 (01628) \*  
 (01629) \*  
 (01630) \*  
 (01631) \*  
 (01632) \*  
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 (01641) \*  
 (01642) \*  
 (01643) \*  
 (01644) \*  
 (01645) \*  
 (01646) \*  
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 (01649) \*  
 (01650) \*  
 (01651) \*  
 (01652) \*  
 (01653) \*  
 (01654) \*  
 (01655) \*  
 (01656) \*  
 (01657) \*  
 (01658) \*  
 (01659) \*  
 (01660) \*  
 (01661) \*  
 (01662) \*  
 (01663) \*  
 (01664) \*

FOR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FOR AND FUNCTION LIST FLAG(LSB)	
1	73	ADAM NUMBER
2	LOGICAL BUFFER #1	LOGICAL BUFFER #0
3	CHANNEL #1	CHANNEL #0
4	COUNTER VALUE	
5	ACQUISITION MODE TRIG. & CLOCK PARAMETERS	

OBJECT

04F32 F0720002 (016A5)	ADAMSSS1	MOVMM	R7, WS(R1)	FIRST SET SINGLE OR DUAL SAMPLING
04F34 9A70F400 (016A6)	ANDIR	R7, MSKSHAYT		
04F36 1A10 (016A7)	SKP	F02		SKIP IF SINGLE CHANNEL,
04F37 90700007 (016A8)	MOVMM	R7, 2		FUSE, SET DUAL CHANNEL FLAG
04F39 0R00 (01670)	FVFN			
04F3A F0220002 (01671)	MOVMM	R2, WS(R1)		GET MIX ADDRESSES
04F3C 9A2000FF (01672)	ANDIR	R2, MSKSHAYT		FIRST MIX ADDRESS TO R3
04F3E 96200000 (01673)	TDIR	R2, S8000		SET ACQUIRE HIT
04F40 F06F4FD0 (01674)	MOVMM	R6, ADMSPMA(R7)		GET POINTER TO FMA IN IOS2 CODE
04F42 F0AF4FCC (01675)	MOVMM	R2, WADMSPLN(R7)		STORE FIRST MIX ADDRESS IN IOS2 PROG
04F44 0150 (01676)	CLH	R5		SET COUNTER FOR TWO PASSES
04F45 0R00 (01677)	FVFN			
04F46 F0220001 (01678)	ADAMSSS1	MOVMM	R2, HS(R1)	GET LOGICAL BUFFER ID
04F48 0250 (016A0)	TEST	R5		
04F49 1A10 (016A1)	SKP	F02		
04F4A 3C28 (016A2)	LMS	R2, H		
04F4B 4C5F (016A3)	ANDIR	R5, R7		CONSTRUCT POINTER FOR BUFFER 0 OR 1 INFO
04F4C 9A2000FF (016A4)	ANDIR	R2, MSKSHAYT		
04F4E 4C24 (016A5)	ANDIR	R2, R2		CONVERT TO WORD INDEX
04F4F 0R00 (016A6)	FVFN			
04F50 C0340582 (016A7)	MOVMM	R3, HCTSHA(R2)		GET BUFFER START ADDRESS
04F52 9A300007 (016A8)	ANDIR	R3, S7		CLEAR OUT UNWANTED BITS
04F54 F06A4FD0 (016A9)	MOVMM	R6, ADMSHA(R5)		CONSTRUCT POINTER TO BASE ADDR LOAD INST
04F56 90FF4FCC (01691)	MOVMM	R6, WADMSPLN(R7)		GET PNTR TO BASE ADDRESS LOC
04F58 763CFEFD (01692)	TDIR	R3, R6, SFFF0		CONSTRUCT PROPER BASE ADDRESS INSTRUCTION
04F5A 9A300000 (01693)	MOVMM	R3, 0(R6)		STORE IT IN IOS PROG
04F5C C0340604 (01694)	MOVMM	R3, HCTSHD(R2)		GET BUFFER SEPARATION AND SIZE
04F5E 2663 (01695)	INCR	R6, WS+HS		STEP TO FIRST SEPARATION LOC
04F5F 0R00 (01696)	FVFN			
04F60 F03C0000 (01697)	MOVMM	R3, 0(R6)		
04F62 F06A4FD0 (01698)	MOVMM	R6, ADMSEP(R5)		GET POINTER TO SEPERATION INFO
04F64 F0FF4FCC (01699)	MOVMM	R3, WADMSPLN(R7)		STORE IT
04F66 0270 (01700)	TEST	R7		
04F67 1910 (01701)	SKP	MFZ		SKIP IF DUAL CHANNEL SAMPLING
04F68 2003 (01702)	INP	ADAMSSS2		
04F69 2666 (01703)	INCR	R6, 3*WS		POINT TO NEXT LOC FOR SEPERATION
04F6A F0FF4FCC (01705)	MOVMM	R3, WADMSPLN(R7)		AND STORE IT
04F6C 483H (01707)	MULHM	R3, R4		COMPUTE DISPLACEMENT TO BUFFER END
04F6D 3C31 (0170H)	LPS	R3, 1		



04FAS 2161	(01753)	DECR	R6, 1	POINT TO IOS SIZE
04FAB 40F44CC	(01754)	MOVAP	R6, ADDRESSPLN(R7)	GET IOS SIZE
04FAR 2662	(01755)	INCR	R6, 2	POINT TO SLOT IN IOS BUFFER FOR ACO. MODE
04FAU 40F44CC	(01756)	MUVRM	R3, ADDRESSPLN(R7)	STORE RID 1 AS 'RID 2' FOR IOS
04FAS 2161	(01757)	*		
04FAR 2617	(01758)	INCR	R3, 2	POINT TO 1ST ADAM CONTROL REGISTER
04FAC 90F00002	(01759)	MOVIR	R6, 2	CONVERT ADAM DISPLACEMENT TO INDEX
04FAR 9C25F4F0	(01760)	ADDR	R2, -12(R2)	STORE ADAM COUNTER IN SCROLL REGISTER
04FAR 8944030	(01761)	POPRT	R1, BRSSIOS2(R2)	GET ACQUIS. MODE; TRIG/CLK PAR
04FAR 3018	(01762)	POPRT	R1, R4	EXTRACT TRIG/CLK SETTINGS
04FAR 503800FF	(01763)	MOVKH	R3, R4, MSKSHHT	
04FAS 2662	(01764)	INCR	R6, 2	STORE IN SCROLL REG 2
04FAR 40F44030	(01765)	MUVRM	R3, BRSSIOS2(R2)	SET-UP FOR LATER ADDRESS FETCH
04FAR 0F60	(01766)	CLR	R6	
04FAR 0800	(01767)	EVEN		
04FAR 3C48	(01768)	*		
04FAR 90104F43	(01769)	IRS	R4, R	EXTRACT ACQUISITION MODE
04FAR 4C4F4CC	(01770)	MOVIR	R1, ADDRESSCR+HS	POINT TO START OF DUMMY FCB
04FAR 4C4F4CC	(01771)	MOVAP	R3, ADDRESSPLN(R7)	POINT TO START OF SCROLL PROGRAM
04FAR 9F300002	(01772)	SUBIR	R3, WS	LOAD SCROLL WANTS POINTER TO SIZE
04FC1 6026	(01773)	*		
04FC2 4C26	(01774)	MOVWR	R2, R3	PUT ACQUISITION MODE
04FC3 F0440003	(01775)	ADDR	R2, R3	IN TRAILER ( RA+R3 )
04FC5 860048A2	(01776)	MUVRM	R4, 3(R2)	
04FC7 86004CF4	(01777)	CALL	R0, L0SSADAM	LOAD THE ADAM SCROLL
04FC9 3302	(01778)	CALL	R0, RNSS	EXECUTE SCROLL PROGRAM
04FCA 0F70	(01779)	POPXDL	R0, R1	RESTORE SAVED FCB POINTER
04FCB 0800	(01780)	RETURN		AND EXIT
	(01781)	EVEN		
	(01782)			
	(01783)			

TABLES FOR ADAM CODE SFT-UP

TABLE #	ADDRESS	DATA	DESCRIPTION
04743	001743	*	ADAMSSCS(P6,1)
04745	001745	*	ADAMSSCS(R6,1)
04746	001746	*	ASCSEMA*WS + HS, 0
04747	001747		ADCSH2AD*WS
04748	001748		ADCSH2AD*WS
04749	001749	*	ADCSH2AD*WS
04790	001790	ADMSPLN	ADMSPLN ADDR
04791	001791	ADDR	ADDR
04792	001792	*	ADMSPLN ADDR
04793	001793	ADMSMA	ADMSMA DATA
04794	001794		ADMSMA DATA
04795	001795	*	ADMSMA DATA
04796	001796	ADMSHA	ADMSHA DATA
04797	001797		ADMSHA DATA
04798	001798		ADMSHA DATA
04799	001799		ADMSHA DATA
04800	001800	*	ADMSHA DATA
04801	001801	ADMSFP	ADMSFP DATA
04802	001802		ADMSFP DATA
04803	001803		ADMSFP DATA
04804	001804		ADMSFP DATA
04805	001805	*	ADMSFP DATA
04806	001806	ADMSFND	ADMSFND DATA
04807	001807		ADMSFND DATA
04808	001808		ADMSFND DATA
04809	001809		ADMSFND DATA
04810	001810	*	ADMSFND DATA
04811	001811	ADMSMA	ADMSMA DATA
04812	001812		ADMSMA DATA
04813	001813	*	ADMSMA DATA
04814	001814	ADMSFCH	ADMSFCH DATA
04815	001815		ADMSFCH DATA
04816	001816		ADMSFCH DATA
04817	001817		ADMSFCH DATA
04818	001818		ADMSFCH DATA
04819	001819		ADMSFCH DATA

044CC 001C44A ADMSPLN ADDR ADAMSSCS(P6,1) POINTER TO SINGLE CHANNEL PLAN  
 044CF 001C430 ADDR ADAMSSCS(R6,1) POINTER TO DUAL CHANNEL PLAN

044D0 0003 ASCSEMA\*WS + HS, 0  
 044D1 0000  
 044D2 0003  
 044D3 0000

044D4 0008 ADMSHA DATA  
 044D5 0018 ADMSHA DATA  
 044D6 0008 ADMSHA DATA  
 044D7 0022 ADMSHA DATA

044D8 0011 ADMSFP DATA  
 044D9 0027 ADMSFP DATA  
 044DA 000F ADMSFP DATA  
 044DB 0029 ADMSFP DATA

044DC 0013 ADMSFND DATA  
 044DD 0029 ADMSFND DATA  
 044DE 0017 ADMSFND DATA  
 044DF 0031 ADMSFND DATA

044E0 0000 ADMSMA DATA  
 044E1 0027 ADMSMA DATA

044E2 0000 ADMSFCH DATA  
 044E3 0000 ADMSFCH DATA  
 044E4 0000 ADMSFCH DATA  
 044E5 0002 ADMSFCH DATA  
 044E6 0000 ADMSFCH DATA  
 044E7 0000 ADMSFCH DATA

DUMMY FCH FOR LOAD/RUN SCROLL CALLS  
 DUMMY FCH NUMBER  
 SCROLL NUMBER  
 SCROLL TYPE = IOS2  
 LITERAL START VALUE

```

(01R20) *          ADAM PROGRAM TO SUPPORT SINGLE CHANNEL SAMPLING
(01R21) *
(01R22) *
(01R23) *
(01R24) *
(01R25) *
(01R26) *          DATA  ACCESSIZP*MS, 0          SCROLL SIZE IN HW : LOGICAL AID 1:0
(01R27) *
(01R28) *
(01R29) *          ADAMSSCS BEGIN IUS2(ADAMSSCS)          ADAM PROGRAM FOR SINGLE CHANNEL SAMPLING
(01R30) *
(01R31) *          READD  SYNCSTOP, (6 *LS. 10) + (26 *LS. 5) + 4
(01R32) *
(01R33) *
(01R34) *          PR          SET SCROLL TO READ FROM PERIPHERAL
A00 04FFA 01300000 (01R34) *          RI = FIRST MUX ADDRESS
A01 04FFC 02400000 (01R35) *          SEND INITIAL MUX ADDRESS
A02 04FFB 03500000 (01R36) *          SF1          START ACQUISITION
A03 04FF0 04300100 (01R37) *
(01R38) *
A04 04FF2 05000000 (01R39) *          ASCSR1AD LOAD(R0, MSS)          R0 = BUFFER ONE START ADDRESS-SEP
A05 04FF4 06100000 (01R40) *          SUB(R0, MSS)
(01R41) *
A06 04FF6 07500010 (01R42) *          ADD(R1,S10,TP)          RELEASE SAMPLE HOLDS
A07 04FF8 08500010 (01R43) *          SUB(R1, S10)          ACQUIRE SAMPLE
A08 04FFA 091A0000 (01R44) *          ASCSR1SP ADD(R0, MSS, TM)          IF END OF BLOCK
A09 04FFC 0A2M0000 (01R45) *          ASCSR1ND JNF(R1, R0, MSS)
(01R46) *
A0R 04F00 0C302000 (01R47) *          INT1          INTERRUPT CSPU
A0C 04F02 0F341000 (01R48) *          SKIPC(SYNCSTOP)          STOP PROGRAM AT CSPU REQUEST
A0D 04F04 00000000
A0E 04F06 1R300400 (01R49) *          JUMP(ASCSSSTOP)
(01R50) *
A0F 04F08 10000000 (01R51) *          ASCSR2AD LOAD(R0, MSS)          R0 = BUFFER TWO START ADDRESS-SEP
A10 04F0A 11100000 (01R52) *          SUB(R0, MSS)
(01R53) *
A11 04F0C 12500010 (01R54) *          ADD(R1, S10, TP)          SEND NEXT MUX ADDRESS
A12 04F0E 13500010 (01R55) *          SUB(R1, S10)          ACQUIRE SAMPLE
A13 04F10 141A0000 (01R56) *          ASCSR2SP ADD(R0, MSS, TM)          IF END OF BLOCK
A14 04F12 162M0000 (01R57) *          ASCSR2ND JNF(R2, R0, MSS)
(01R58) *
A17 04F1H 1R302000 (01R59) *          INT1          INTERRUPT CSPU
A1R 04F1A 1R301000 (01R60) *          SKIPV(SYNCSTOP)          STOP PROGRAM AT CSPU REQUEST

```

PAGE 502 SNAP-11 IOS PACKAGE ---- APR 14, 1980 ---- PROGRAM # R40001.01A  
 ADAM PROGRAM TO SUPPORT SINGLE CHANNEL SAMPLING

```

A19 04F1C 00000000 (01861) .JUMP(ASCSTRAD) ELSE, CONTINUE SAMPLING
A1A 04F1F 04300000 (01862) *
A1B 04F20 1C400180 (01863) ASCSSTOP CF1 TURN ACQUISITION OFF
A1C 04F22 1D406400 (01864) STOP THEN STOP THE SCROLL
      00000010 (01865) *
      00000010 (01866) ASCSSIZE = 8A
04F24 (01867) *
      (01868) *
      (01869) *
04F26 (01870) * SFT UP TRAILER INFORMATION
      (01871) *
04F2A 0002 (01872) DATA 2
04F25 0000 (01873) DATA 0
04F26 0000 (01874) DATA 0
04F27 0000 (01875) DATA 0
04F28 0000 (01876) DATA 0
04F29 FFFF (01877) DATA -1
04F2A FFFF (01878) DATA -1,-1,-1,-1
04F2B FFFF
04F2C FFFF
04F2D FFFF (01879) EVEN
  
```

SET TRANSFER DIRECTION TO WRITE  
 # CHANNELS : ACQUISITION MODE  
 RID 3 : 2  
 # SCROLL REGISTERS TO LOAD  
 1ST SCROLL REGISTER TO LOAD  
 INTEGER SCALAR ID OF OFFSET = NULL  
 HINDING CHAIN ANCHORS = NULL

```

(01880) *      ADAM PROGRAM TO SUPPORT DUAL CHANNEL SAMPLING
(01881) *
(01882) *
(01883) *      DATA      ADCSSIZE*MS, 0      MODULE SIZE IN HW : LOGICAL, RID 1:0
(01884) *
(01885) *      ADAMSDCS REGTN IOS7(ADAMSDCS)      ADAM PROGRAM FOR DUAL CHANNEL SAMPLING
(01886) *
(01887) *
(01888) *      PR
A00 04F30 01300000      ADCSFMA LOAD(R1, MSS)      SPT TO READ FROM PERIPHERAL
A01 04F32 02400000      ADD(R1, 0, TP)      R1 = FIRST MUX ADDRESS
A02 04F34 03500000      SUB(R1, S10)      SEND INITIAL MUX ADDRESS
A03 04F36 04300100      JNE(R1, #1)      START ACQUISITION
(01892) *
A04 04F38 05000000      ADCSH2AD LOAD(R0, MSS)      R0 = BUFFER ONE START ADDRESS-SEP
A05 04F3A 06100000      SUB(R0, MSS)
(01895) *
(01896) * #1
A06 04F3C 07500000      ADCSSMA1 ADD(R1, MSS, TP)      SEND SECOND CHANNEL ADDRESS
A07 04F3E 081A0000      ADCSH2SP ADD(R0, MSS, TM)      ACQUIRE SAMPLE
A08 04F40 09500000      ADD(R1, MSS, TP)      SEND FIRST MUX ADDR : RELEASE S/H
A09 04F42 0A500010      SUB(R1, S10)
A0A 04F44 0B1A0000      ADD(R0, MSS, TM)      ACQUIRE SAMPLE
A0B 04F46 0C2R0000      ADCSH2ND JNE(R1, R0, MSS)      IF END OF BLOCK
(01903) *
A0D 04F4A 0F302000      INT1      INTERRUPT THE CSPU
A0E 04F4C 10341000      SKIPC(SYNCSSTOP)      STOP PROGRAM AT CSPU REQUEST
A0F 04F4E 00000000
A10 04F50 1F300400      JUMP(ADCSTUP)
(01907) *
A11 04F52 12000000      ADCSH2AD LOAD(R0, MSS)      R0 = BUFFER TWO START ADDRESS-SEP
A12 04F54 13100000      SUB(R0, MSS)
(01910) *
(01911) * #2
A13 04F56 14500000      ADCSSMA2 ADD(R1, MSS, TP)      SEND SECOND CHANNEL ADDRESS
A14 04F58 151A0000      ADCSH2SP ADD(R0, MSS, TM)      ACQUIRE SAMPLE
A15 04F5A 16500000      ADD(R1, MSS, TP)      SEND FIRST MUX ADDR : RELEASE S/H
A16 04F5C 17500010      SUB(R1, S10)
A17 04F5E 181A0000      ADD(R0, MSS, TM)      ACQUIRE SAMPLE
A18 04F60 1A2R0000      ADCSH2ND JNE(R2, R0, MSS)      IF END OF BLOCK
(01916) *
A19 04F62 00000000
(01918) *
A1R 04F66 1C302000      INT1      INTERRUPT CSPU
A1C 04F6A 1F301000      SKIPS(SYNCSSTOP)      STOP PROGRAM AT CSPU REQUEST
  
```



PAGE 01: SNAP=11 LOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A  
 ADAM PROGRAM TO SUPPORT DUAL CHANNEL SAMPLING

```

A10 04F6A 00000000 (01921) * JUMP(ADCSHAD) ELSE, CONTINUE SAMPLING
A1F 04F6C 04300000 (01922) *
A1E 04F6E 20300180 (01923) ADCSTOP CFI FIRST TURN OFF ACQUISITION
A20 04F70 21306400 (01924) STOP THEN STOP THE SCROLL
000000071 (01925) ADCSSIZE = BA
(01926) *
04F72 (01927) * END
(01928) *
(01929) * SET UP TRAILER INFORMATION
(01930) *
04F72 0002 (01931) * DATA 2
04F73 0000 (01932) * DATA 0
04F74 0000 (01933) * DATA 0
04F75 0000 (01934) * DATA 0
04F76 0000 (01945) * DATA 0
04F77 FFFF (01946) * DATA -1
04F78 FFFF (01937) * DATA -1,-1,-1,-1
04F79 FFFF
04F7A FFFF
04F7B FFFF
(01938) * EVEN
  
```

SET TRANSFER DIRECTION TO WRITE  
 # CHANNELS : ACQUISITION MODE  
 MID 1 : 2  
 # SCROLL REGISTERS TO LOAD  
 1ST SCROLL REGISTER TO LOAD  
 INTEGER SCALAR ID OF OFFSET = NULL  
 BINDING CHAIN ANCHORS = NULL

PAGE 67: SNAP-11 I/O PACKAGE ---- APR 18, 1980 ---- PROGRAM # 840001.01A  
 ADAMS165 - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. SAMP. FCR

ADAMS165 - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. SAMP. FCR

FCR FORMAT (16 BIT WORD FORMAT SHOWN)

(01939) *	WORD	LEFT BYTE	RIGHT BYTE
(01940) *	0	POINTER TO NEXT FCR AND FUNCTION LIST FLAG (LSB)	
(01941) *	1	74	ADAM NUMBER
(01942) *	2	LOGICAL BUFFER #1	LOGICAL BUFFER #0
(01943) *	3	0	0
(01944) *	4	COUNTER VALUE	
(01945) *	5	ACQUISITION MODE	TRIG. & CLOCK PARAMETER
(01946) *			
(01947) *			
(01948) *			
(01949) *			
(01950) *			
(01951) *			
(01952) *			
(01953) *			
(01954) *			
(01955) *			
(01956) *			
(01957) *			
(01958) *			
(01959) *			
(01960) *			
(01961) *			
(01962) *			
(01963) *			
(01964) *			
(01965) *			
(01966) *			
(01967) *			
(01968) *			
(01969) *			
(01970) *			
(01971) *			
(01972) *			
(01973) *			
(01974) *			
(01975) *			
(01976) *			

END

04E7C	F0220001	(01977)	ADAMSINS	MOVW	R2,	HS(R1)	GET BUFFER IOS
04E7E	9A2000FF	(01978)		ANDI	R2,	MSKSRHYT	GET BUFFER ID#0
04E80	4C24	(01979)		ADDR	R2,	R2	CONVERT TO RCT INDEX
04E81	0800	(01980)		FVEN			
04E82	C0340582	(01981)		MOVW	R3,	RCTSHA(R2)	GET BUFFER BUS NUMBER AND BASE ADDRESS
04E84	9A300006	(01982)		ANDI	R3,	S6	CLEAR OUT UNWANTED BITS
04E86	90604FF0	(01983)		MOVW	R6,	ATASHID	GET LOC. OF PTR TO BASE ADDR INST.
04E88	763CFF0	(01984)		LOGW	R3,	R6,	CONSTRUCT PROPER FORMAT TO BASE ADDR INST.
04E8A	R43C0000	(01985)		MOVW	R3,	0(R6)	STORE IN IOS PROG.
04E8C	C0340604	(01986)		MOVW	R3,	RCTSD(R2)	GET BUFFER SEPERATION AND SIZE
04E8E	2663	(01987)		INCR	R6,	S3	STEP TO SEPERATION LOC IN IOS PROG.
04E8F	0800	(01988)		FVEN			
04E90	F03C0000	(01989)		MOVW	R3,	0(R6)	STOP IN IOS PROG.
04E92	4838	(01990)		MULW	R3,	R4	COMPUTE DISPLACEMENT TO BUFFER END LOC.
04E93	3C31	(01991)		LPS	R3,	S1	GET BASE ADDR. IN PROPER IOS FMT.
04E94	FC340583	(01992)		ADDR	R3,	RCTSHA+HS(R2)	ADD IN BASE ADDRESS TO YIELD BUFFER END
04E96	F030503F	(01993)		MOVW	R3,	ATASH2+HS	STORE IN IOS PROG.
04E98	F0220001	(01994)	*	MOVW	R2,	HS(R1)	GET BUFFER IOS
04E9A	9A20FF00	(01995)		ANDI	R2,	MSKSRHYT	GET BUFFER ID #1
04E9C	0220	(01997)		TEST	P2		SEE IF BUFFER IS USED
04E9D	R0104FRH	(01998)		IMP	ADMSO,	E0Z	
04E9F	3C28	(02000)		LPS	R2,	S8	GET BUFFER ID IN PROPER FORMAT
04FA0	4C24	(02001)		ADDR	R2,	R2	IT IS, SO CONVERT TO RCT INDEX
04FA1	0800	(02002)		FVEN			
04FA2	C0340582	(02003)		MOVW	R3,	RCTSHA(R2)	GET BUFFER BUS NUMBER AND BASE ADDR.
04FA4	9A300006	(02004)		ANDI	R3,	S6	CLEAR OUT UNWANTED BITS
04FA6	9060504C	(02005)		MOVW	R6,	ATASH2D	GET LOC. OF POINTER TO BASE ADDR. INST.
04FA8	763CFF0	(02006)		LOGW	R3,	R6,	CONSTRUCT PROPER FORMAT FOR BASE ADDR.
04FAA	R43C0000	(02007)		MOVW	R3,	0(R6)	STORE IN IOS PROG.
04FAC	C0340604	(02008)		MOVW	R3,	RCTSD(R2)	GET BUFFER SEPERATION AND SIZE
04FAE	2663	(02009)		INCR	R6,	S3	STEP TO SEPERATION LOC IN IOS PROG.
04FAF	0800	(02010)		FVEN			
04FB0	F03C0000	(02011)		MOVW	R3,	0(R6)	STORE IN IOS PROG.
04FB2	4838	(02012)		MULW	R3,	R4	COMPUTE DISPLACEMENT TO BUFFER FND.
04FB3	3C31	(02013)		LPS	R3,	S1	PUT BASE ADDR. IN PROPER IOS FMT.
04FB4	FC340583	(02014)		ADDR	R3,	RCTSHA+HS(R2)	ADD IN BASE ADDRESS TO YIELD BUFFER END
04FB6	F0305093	(02015)	*	MOVW	R3,	ATASH2+HS	STORE IN IOS PROG.

DISTRIBUTE FCH PARAMETERS  
 (02017) \*  
 (02018) \*  
 (02019) \*  
 (02020) \*

```

04PRH 3602 (02021) ADMSC0 PUSH11 R0, R1 SAVE FCH POINTER
04PRH 2711 (02022) DECF R1, R5 PREPARE FOR POP OPERATIONS
04PRA 3014 (02023) POPX1 R1, R2 GET ADAM NUMBER
04PHH 0800 (02024) FVFN EVEN
04PHC 9A2000FF (02025) ANDIP R2, MSKSRHYT EXTRACT SCROLL IDENTIFIER
04PHE 40204FF4 (02026) MOVWM R2, ADMSDFCH+MS STORE IN DUMMY FCH
04PC0 40320001 (02028) MOVWM R3, 1(R1) GET RID 0
04FC2 503600FF (02029) MOVKB R4, R3, MSKSRHYT
04FC4 40404FFD (02030) MOVWM R4, ADMS16C+HS STORE IN I/O HEADER
04FC6 3C3H (02032) LPS R3, R
04FC7 90404FF (02033) MOVIR R4, ADMS16C GET RID 1
04FC9 5C404FFC (02034) ADDR R4, ADMS16C-2 COMPUTE POINTER TO 'RID 2'
04FCH 40380002 (02035) MOVWM R3, 2(R4) IN SCROLL BUFFER AREA
04PCD 2612 (02037) INCR R1, 2
04FCE 90600002 (02038) MOVIR R6, 2 POINT TO 1ST ADAM CONTROL REGISTER
04FD0 9C25FF40 (02039) ADDR R2, -32(R2) CONVERT ADAM DISPLACEMENT TO INDEX
04FD2 08944D30 (02040) POPM1 R1, MKNSS10S2(R2) STORE ADAM COUNTER IN SCROLL REGISTER 1
04FDA 301H (02042) POPX1 R1, R4
04FD5 503800FF (02043) MOVKB R3, R4, MSKSRHYT GET ACQUIS. MODE: TRIG/CLK PARA.
04FD7 2662 (02044) INCR R6, 2 EXTRACT TRIG. & CLOCK SETTINGS
04FDH 40844D30 (02045) MOVWM R3, MKNSS10S2(R2) STORE IN SCROLL REG 2
04FDA 3C4H (02047) LPS R4, R
04FDH 90104FF3 (02048) MOVIR R1, ADMSDFCH+HS EXTRACT ACQUISITION MODE
04FD0 4C304FF (02049) MOVAR R3, ADMS16C POINT TO START OF DUMMY FCH
04FDE 9F300002 (02050) SUBIR R3, R5 LOAD SCROLL. (R3 = START OF SCROLL BUFFER)
04FE1 6026 (02051) MOVWM R2, R3 PUT ACQUISITION MODE IN SCROLL BUFFER
04FE2 4C26 (02053) ADDR R2, R3 ( R4+R3 )
04FE3 40400003 (02054) MOVWM R4, 3(R2)
04FE5 86004A47 (02056) CALL R0, LDSSADAM LOAD THE ADAM WITH THE SCROLL PROGRAM
04FE7 86004CF4 (02057) CALL R0, KNSS EXECUTE SCROLL PROGRAM
04FE9 3302 (02059) POPX1L R0, R1 RESTORE SAVED FCH POINTER
04FFA 0F70 (02060) RETURN
04FEH 0800 (02061) FVFN
  
```

```

(02062) *          ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING
(02063) *
(02064) *
(02065) *          DATA  A16SIZ=WS, 0  MODULE SIZE IN HALF-WORDS : LOGICAL AID'S
(02066) *
(02067) *          BEGIN  IOS2(ADMS16C)  ADAM PROGRAM FOR 16 CHANNEL SAMPLING
(02068) *
(02069) *          OPAND  SYNSTOP, (6 .I.S. 10) + (26 .I.S. 5) + 4
(02070) *
(02071) *          PR      SET TO READ FROM ADAM
(02072) *          A16SPM=EL
(02073) *          LOAD    (R1, SR000)  SET ADAM REGISTER #1 TO READ FROM CHANNEL #00
(02074) *          ADD     (R1, 0, TP)  ACTIVATE CHANNEL #00
(02075) *          SF1    ENABLE ADAM DATA ACQUISITION
(02076) *
(02077) *
(02078) *          A16SRID=EL
(02079) *          A16S01A LOAD    (R0, MSS)  SET R0 TO CONTAIN THE MAP BUFFER MEMORY ADDR.,
(02080) *          SUB     (R0, MSS)  AND SEPARATION.
(02081) *
(02082) *          #1
(02083) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #01
(02084) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #00 INTO MAP MEMORY
(02085) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #02
(02086) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #01 INTO MAP MEMORY
(02087) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #03
(02088) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #02 INTO MAP MEMORY
(02089) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #04
(02090) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #03 INTO MAP MEMORY
(02091) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #05
(02092) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #04 INTO MAP MEMORY
(02093) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #06
(02094) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #05 INTO MAP MEMORY
(02095) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #07
(02096) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #06 INTO MAP MEMORY
(02097) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #08
(02098) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #07 INTO MAP MEMORY
(02099) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #09
(02100) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #08 INTO MAP MEMORY
(02101) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #10
(02102) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #09 INTO MAP MEMORY
(02103) *          ADD     (R1, 1, TP)  ACTIVATE CHANNEL #11
(02104) *          ADD     (R0, 1, TM)  PUT DATA FROM CHANNEL #10 INTO MAP MEMORY

```

```

A1D 05028 1F590001 (02105) ADD (R1, 1, TP) ACTIVATE CHANNEL #12
A1E 0502A 1F1A0001 (02106) ADD (R0, 1, TM) PUT DATA FROM CHANNEL #11 INTO MAP MEMORY
A1F 0502C 20590001 (02107) ADD (R1, 1, TP) ACTIVATE CHANNEL #13
A20 0502E 211A0001 (02108) ADD (R0, 1, TM) PUT DATA FROM CHANNEL #12 INTO MAP MEMORY
A21 05030 22590001 (02109) ADD (R1, 1, TP) ACTIVATE CHANNEL #14
A22 05032 231A0001 (02110) ADD (R0, 1, TM) PUT DATA FROM CHANNEL #13 INTO MAP MEMORY
A23 05034 24590001 (02111) ADD (R1, 1, TP) ACTIVATE CHANNEL #15
A24 05036 251A0001 (02112) ADD (R0, 1, TM) PUT DATA FROM CHANNEL #14 INTO MAP MEMORY
A25 05038 26590021 (02113) ADD (R1, S21, TP) SET CHANNEL # TO 00 AND ACTIVATE
A26 0503A 271A0001 (02114) ADD (R0, 1, TM) PUT DATA FROM CHANNEL #15 INTO MAP MEMORY
A27 0503C 28500030 (02115) * SUB (R1, S30) RESET S/H
A28 0503E 2A280000 (02117) * ATASHIF=H1
A29 05040 00000000 (02119) * JMP (R1, R0, MSS) LOOP AGAIN IF BUFFER NOT FILLED
A2A 05044 2C302000 (02120) * INT1 OTHERWISE, INTERRUPT THE CSPI
A2B 05046 2F341000 (02122) * SKIPC (SYNCSSTOP) AND STOP THE PROGRAM AT THE CSPI'S REQUEST
A2C 0504A 54100400 (02123) * JMP (AIRSSTOP)
A2D 0504E 31100000 (02129) * ATASH2D=H1
A2E 05050 32590001 (02132) * ADD (R1, 1, TP) ACTIVATE CHANNEL #01
A2F 05052 331A0001 (02133) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #00 INTO MAP MEMORY
A30 05054 34590001 (02134) * ADD (R1, 1, TP) ACTIVATE CHANNEL #02
A31 05056 351A0001 (02135) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #01 INTO MAP MEMORY
A32 05058 36590001 (02136) * ADD (R1, 1, TP) ACTIVATE CHANNEL #03
A33 0505A 371A0001 (02137) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #02 INTO MAP MEMORY
A34 0505C 38590001 (02138) * ADD (R1, 1, TP) ACTIVATE CHANNEL #04
A35 0505E 391A0001 (02139) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #03 INTO MAP MEMORY
A36 05060 3A590001 (02140) * ADD (R1, 1, TP) ACTIVATE CHANNEL #05
A37 05062 3B1A0001 (02141) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #04 INTO MAP MEMORY
A38 05064 3C590001 (02142) * ADD (R1, 1, TP) ACTIVATE CHANNEL #06
A39 05066 3D1A0001 (02143) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #05 INTO MAP MEMORY
A3A 05068 3E590001 (02144) * ADD (R1, 1, TP) ACTIVATE CHANNEL #07
A3B 0506A 3F1A0001 (02145) * ADD (R0, 1, TM) PUT DATA FROM CHANNEL #06 INTO MAP MEMORY
A3C 0506C 40590001 (02146) * ADD (R1, 1, TP) ACTIVATE CHANNEL #08
  
```

A40 0506F 411A0001 (02147)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #07 INTO MAP MEMORY
A41 05070 42590001 (02148)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #09
A42 05072 431A0001 (02149)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #08 INTO MAP MEMORY
A43 05074 43590001 (02150)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #10
A44 05076 451A0001 (02151)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #09 INTO MAP MEMORY
A45 05078 46590001 (02152)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #11
A46 0507A 471A0001 (02153)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #10 INTO MAP MEMORY
A47 0507C 48590001 (02154)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #12
A48 0507E 491A0001 (02155)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #11 INTO MAP MEMORY
A49 05080 4A590001 (02156)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #13
A4A 05082 4B1A0001 (02157)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #12 INTO MAP MEMORY
A4B 05084 4C590001 (02158)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #14
A4C 05086 4D1A0001 (02159)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #13 INTO MAP MEMORY
A4D 05088 4E590001 (02160)	ADD	(R1, 1, TP)	ACTIVATE CHANNEL #15
A4E 0508A 4F1A0001 (02161)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #14 INTO MAP MEMORY
A4F 0508C 50590021 (02162)	ADD	(R1, S21, TP)	SET CHANNEL # TO 00 AND ACTIVATE
A50 0508E 511A0001 (02163)	ADD	(R0, 1, TM)	PUT DATA FROM CHANNEL #15 INTO MAP MEMORY
A51 05090 52500030 (02164)	ADD	(R1, S30)	RESET S/H
A52 05092 54280000 (02165)	ADD		
A53 05094 00000000 (02166)	ADD		
A54 05096 00005092 (02167)	ADD		
A55 05098 56402000 (02168)	ADD		
A56 0509A 58301000 (02169)	ADD		
A57 0509C 00000000 (02170)	ADD		
A58 0509E 05300400 (02171)	ADD		
A59 050A0 5A300140 (02172)	ADD		
A5A 050A2 5B300780 (02173)	ADD		
A5B 050A4 5C306000 (02174)	ADD		
A5C 050A6 0000005C (02175)	ADD		
A5D 050A8 050A6 (02176)	ADD		
A5E 050AA 0000 (02177)	ADD		
A5F 050AC 0000 (02178)	ADD		
A60 050AE 0000 (02179)	ADD		
A61 050B0 0000 (02180)	ADD		
A62 050B2 0000 (02181)	ADD		
A63 050B4 0000 (02182)	ADD		
A64 050B6 0000 (02183)	ADD		
A65 050B8 0000 (02184)	ADD		
A66 050BA 0000 (02185)	ADD		
A67 050BC 0000 (02186)	ADD		
A68 050BE 0000 (02187)	ADD		
A69 050C0 0000 (02188)	ADD		

```

LOOP AGAIN IF BUFFER NOT FILLED

OTHERWISE, INTERRUPT THE CSPI
AND STOP THE PROGRAM AT THE CSPI'S REQUEST

FALSE, CONTINUE SAMPLING

TURN OFF ACQUISITION OF ADAM
THEN STOP THE SCROLL.

SFT TRANSFER DIRECTION TO WRITE
# CHANNELS : ACQUISITION MODE
WID 3 : 2
# SCROLL REGISTERS TO LOAD
1ST SCROLL REGISTER TO LOAD
  
```

```

SET UP TRAILER INFORMATION
DATA 2
DATA 0
DATA 0
DATA 0
DATA 0
  
```

PAGE 04: SNAP-11 IOS PACKAGE --- APR 14, 1980 --- PROGRAM # R40001.01A  
ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING

0500M FFFF (02189) DATA -1 INTEGER SCALAR TO OF OFFSET = NULL  
0500C FFFF (02190) DATA -1,-1,-1,-1 WINDING CHAIN ANCHORS = NULL  
0500D FFFF  
0500E FFFF  
0500F FFFF



(02191) \* ADDR# MODULE TO PROCESS ADAM ROTATE BUFFER FCB

000000003 (02192) \* (02193) \* (02194) \* (02195) \* (02196) \* (02197) \* (02198) \* (02199) \* (02200) \* (02201) \* (02202) \* (02203) \* (02204) \* (02205) \* (02206) \* (02207) \* (02208) \* (02209) \* (02210) \* (02211) \* (02212) \* (02213) \* (02214) \* (02215) \* (02216) \* (02217) \* (02218) \* (02219) \* (02220) \* (02221) \* (02222) \* (02223) \* (02224) \* (02225) \* (02226) \* (02227) \* (02228) \* (02229) \*

BM = 3

SET TO MAP-300 ASSEMBLY

OPADD TABS.(1 .LS. 14) + 19 DEFINE TAB PSEUDO OP

FCB FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCB AND FUNCTION LIST FLAG(LSR)	
1	229	
2	VALUE	ISA
3	UNDEF	
4	0	0
5	0	0

SNAP-11 I/O5 PACKAGE ---- APR 14, 1960 ---- PROGRAM # H40001.01A  
SPECIAL BINDING MODULE FOR ROTATE ADAM BUFFER

```

(02230) * SPECIAL BINDING MODULE FOR ROTATE ADAM BUFFER
(02231) *
(02232) * THE INPUT BUFFER 'U' IS SEPARATED INTO TWO 'BUFFERS'
(02233) * A AND B, WHERE
(02234) *
(02235) * A = 'BUFFER' STARTING WITH THE TRIGGER ADDRESS + 1
(02236) * AND EXTENDING TO THE LAST SAMPLE OF THE U BUFFER
(02237) *
(02238) * B = 'BUFFER' STARTING WITH THE FIRST SAMPLE OF THE
(02239) * U BUFFER AND EXTENDING TO THE SAMPLE INCLUDING THE
(02240) * TRIGGER ADDRESS
(02241) *
(02242) * THIS SPECIAL SUPPORT MODULE EXTRACTS THE TRIGGER ADDRESS
(02243) * FROM A PAIR OF CONSECUTIVE INTEGER SCALARS AND BINDS
(02244) * THE PERTINENT INFORMATION FOR 'BUFFERS' A AND B INTO THE
(02245) * APS MODULE
(02246) *
(02247) *
(02248) * CALL R1, APSNDW PERFORM STANDARD BINDING FIRST
(02249) *
(02250) * REG. 3 = HIGH ORDER BIT OF TRIGGER ADDRESS
(02251) * REG. 4 = LOW ORDER 16 BITS OF TRIGGER ADDRESS
(02252) *
(02253) * MOVBR R7, 1(CK1) INTEGER SCALAR 'A' ID
(02254) * ANDIR W7, WSKSRBYT
(02255) *
(02256) * MOVBRG R3, ISVTS(P7) GET TRIGGER VALUE
(02257) *
(02258) * INCR R4, 1 ADJUST TO 'ARASE'
(02259) * JMP ADMRRO, NCRN IF CARRY,
(02260) * SMW 0, R3 SET BIT 17 IN ADDRESS
(02261) *
(02262) * MOVBR ADMRRO R4, RARAS+1 PUT TRIGGER ADDRESS IN APS
(02263) * MOVBR R5, RARAS PICK UP 'ARASE' INSTRUCTION
(02264) * TORR R5, R3, 1 GET HIGH ORDER BIT
(02265) * MOVBR R5, RARAS STORE IN CONSTRUCTED BLOCK
(02266) *
(02267) * SMW 3, RARAS SET SHORT HIT IN LOAD INSTRUCTIONS
(02268) * SMW 3, RARAS-2
(02269) * SMW 3, RARAS-R
(02270) *
(02271) * MOV R3, 1 MOVE HIGH ORDER ADDRESS BIT TO POS 17
(02272) * TORR R3, R4, SFFFF CONCATENATE BIT 17 WITH ADDRESS
(02273) *

```

```

05006 F0220002 (0227A) MOVW R2, 7(R1)          GET URUF
05007 0A200000 (0227S) ABLEF R2, MSKSLAYT    GET URUF ID FROM LEFT HYTF
05008 3C27 (02276) LFS R2, 7          CONVERT TO RCT INDEX
05009 F0740542 (02277) MOVW R7, RCTSA(R2)    GET HIGH ORDER ADDRESS BIT
05010 50A00001 (02278) MOVW R6, R7, 1
05011 4E61 (02279) POP R6, 1          MOVE TO BIT POS 17
05012 F0540543 (02280) MOVW R5, RCTSA+HS(R2)    LOW ORDER 16 BITS
05013 56A00000 (02281) TORW R6, R5, SFFFF    CONCATENATE BIT 17 TO ADDRESS
05014 F0740604 (02283) MOVW R7, RCTSA(R2)    GET SEPARATION
05015 405C (02284) MOVW R5, R6          R5 = URASE
05016 F0540605 (02285) ADDRK R5, RCTSA+HS(R2)    COMPUTE BUFFER EXTENT
05017 4256 (02286) CMPLR R5, R3          TRIG BEYOND BUFFER RANGE?
05018 R13050FD (02287) JMP ADDRESS1, GEZ    JUMP IF NOT
05019 403C (02288) MOVW R3, R6          TRIG = URASE
05020 F0305159 (02290) MOVW R3, R3AAS+HS    STORE IN APS PROGRAM
05021 0E40 (02291) CLR R4
05022 56470000 (02292) TORW R4, R3, $10000    UPPER BIT SET?
05023 1810 (02293) SKP R0Z          SKIP IF NOT
05024 02005158 (02294) SMR 0, R3AAS    EQUSE, SET UPPER BIT
05025 4E66 (02296) ADDRST SUMW R6, R3    URASE = TRIG
05026 1820 (02297) SKP GTZ
05027 0860 (02298) MFC R6
05028 F0200784 (02299) MOVW R2, TEMSD    OFFSKT MUST BE NON - NEGATIVE
05029 86001850 (02300) CALL R0, IDIVS    SAVE R2 = RCT INDEX
05030 F0200784 (02301) MOVW R2, TEMSD    RSIZE := AHS(URASE-TRIG )/SEP
05031 F0540605 (02302) MOVW R5, RCTSA + HS(R2)    GET BUFFER SIZE - 1
05032 4E5F (02304) SUMW R5, R7          ASIZE - 1 := USIZE - BSIZE - 1
05033 F0505125 (02306) MOVW R5, RINDL,OK+3    ASIZE - 1
05034 2771 (02307) DECR R7, 1          BSIZE - 1
05035 F0705126 (02308) MOVW R7, RINDR,OK+9
05036 0E70 (02309) RETURN
    
```

(02111) \* APU - ROTATE ADAM BUFFFF  
 (02112) \*  
 (02113) \* THIS ROUTINE ASSUMES THAT DATA IS IN 16 BIT FORMAT -  
 (02114) \* EITHER LONG & FIXED OR SHORT & FLOATING PT.  
 (02115) \*  
 (02116) \* THERE IS ONE ADDRESSOR FOR BOTH DATA TYPES  
 (02117) \*  
 (02118) \* THE SPECIAL BINDING MODULE FORCES THE DATA MODE  
 (02119) \* TO BE SHORT, SINCE THERE IS NO ARITHMETIC OPERATION  
 (02120) \* PERFORMED ON THE DATA.  
 (02121) \*  
 (02122) \*  
 (02123) \*  
 (02124) \*  
 (02125) \*  
 (02126) \*  
 (02127) \*  
 (02128) \*  
 (02129) \*  
 (02130) \*  
 (02131) \*  
 (02132) \*  
 (02133) \*  
 (02134) \*  
 (02135) \*  
 (02136) \*  
 (02137) \*  
 (02138) \*  
 (02139) \*  
 (02140) \*  
 (02141) \*  
 (02142) \*  
 (02143) \*  
 (02144) \*  
 (02145) \*

050FF 0800 EVEN START ADDRESS  
 05100 0000 DATA ADDRESS SIZE OF MODULE  
 05101 000A DATA ADDRESSZ  
 (02127) \*  
 (02128) \*  
 (02129) \*  
 (02130) \*  
 (02131) \*  
 (02132) \*  
 (02133) \*  
 (02134) \*  
 (02135) \*  
 (02136) \*  
 (02137) \*  
 (02138) \*  
 (02139) \*  
 (02140) \*  
 (02141) \*  
 (02142) \*  
 (02143) \*  
 (02144) \*  
 (02145) \*

INSERT DUMMY NOP'S FOR 200/300  
 OBJECT COMPATIBILITY  
 ( # NOP'S = # REAL INSTR'S ABOVE)

END

```

(02146) * APS - ROTATE ADAM BUFFER
(02147) *
(02148) * SPECIAL BINDING MODULE MODIFIES BASE ADDRESSES FOR ALL
(02149) * THREE BUFFERS 'A', 'B', AND 'C' IN THE CONSTRUCTED
(02150) * INSTRUCTION BLOCK
(02151) * 1) SETS UP 'ARASE'
(02152) * 2) FORCES THE SHOOT BIT FOR THE LOAD INSTRUCTIONS
(02153) *
(02154) *
(02155) * INPUT STREAM - UK: FT
(02156) * OUTPUT STREAM - YK: FD
(02157) *
EVEN
ADDR RHPASSI
ADDR ADMRRAPS
DATA 0
DATA RHPASSZ
ADDR RHPASSA
EVEN
(02165) *
(02166) *
(02167) ADMRRAPS BEGIN APS(ROTREFAPS)
(02168) *
(02169) *
A00 0511E 00701040 (02170) JSM(RR00T, P2)
A01 05120 02300030 (02171) SET(R0) ENABLE OUTPUT
(02172) *
(02173) * INPUT PROGRAM
(02174) *
(02175) * HINDRLOK = #1.
(02176) *
A02 05122 04481000 (02177) LOAD(RR0, I1, S)
A03 05124 06500000 (02178) LOAD(RR1, MSS)
A04 05126 08020000 (02179) SHR(RR0, MSS)
(02180) *
A05 05128 0A681006 (02181) LOAD(RR2, I1, S)
A06 0512A 0C700006 (02182) LOAD(RR3, MSS)
A07 0512C 0E220000 (02183) SHR(RR2, MSS)
(02184) *
A08 0512E 108A0006 (02185) ATRANS
A09 05130 12190004 (02186) SHR(RR1, I), JUMPP(ATRANS)
(02187) *
A0A 05132 14490C08 (02188) ADDL(RR3, 0), JUMPP(ATRANS)
A0B 05134 16000E60 (02189) JUMPP(REFRNDM)
  
```

POINTER TO CONSTR. INST. BLOCK  
 POINTER TO SCALAR BLOCK ( NOT USED )  
 MODULE SIZE  
 CHAIN ANCHOR

SET OUTPUT PC

[ARASE] (TRIGGER)  
 [ASIZE] - 1  
 ARASE - SEPARATION  
 [RRASE] ( = URASE )  
 [RSIZE] - 1  
 RRASE - SEPARATION

```

(02300) *
A0C 05136 18A9608 (02391) WTRANS ADD(OR2, [9], TF)
A0D 05138 1A300C81 (02392) SUBL(RW1, 1), JUMPP(OUTTRANS)
(02393) *
A0E 0513A 1C200031 (02394) RNFADIN CLEAR(RI)
A0F 0513C 1F000020 (02395) NOP(0)
(02396) *
(02397) *
A10 0513E 20300037 (02398) RNDUIT SFT(NA)
(02399) *
A11 05140 2240000A (02400) LOAD(RW0, [0], S)
A12 05142 24500000 (02401) LOAD(RW1, MSS)
A13 05144 26020000 (02402) SUB(RW0, MSS)
(02403) *
A14 05146 288A0006 (02404) OUTTRANS ADD(RW0, [8], TF)
A15 05148 2A111481 (02405) SUBL(RW1, 1), JUMPP(OUTTRANS)
(02406) *
A16 0514A 2C200030 (02407) CLEAR(RI)
A17 0514C 2F000020 (02408) NOP(0)
(02409) *
00005146 (02410) RHAPSSA = RC
(02411) *
(02412) *
0514F (02413) END
(02414) *
(02415) *
(02416) *
0514F 00000000 (02417) RHAPSS1 DATA 6F'0.0'
...
(02418) *
00005158 (02419) RHANAS = BL - 2
(02420) *
0000001C (02421) RHAPSSZ = #1 - ADDRESS
(02422) *

```

POINT TO 'ARASE' SUBSTITUTION  
 MODULE SIZE

PAGE 75: SNAP-11 IUS PACKAGE ----- APR 18, 1980 ----- PROGRAM # H40001.01A  
CROSS REFERENCE

(02423) \* CROSS REFERENCE  
(02424) \*  
(02425) \*  
(02426) \* END OF MODULE  
(02427) \*  
(02428) \*  
0000515A (02429) \* TOP SCUR=81.  
(02430) \*  
(02431) \*  
(02432) \*  
0515A \*  
FND

CALCULATE END ADDRESS OF MODULE

A1S81A:	0005	(02079)	(02172)
A1S81D:	04F4	(01981)	(02078)
A1S81E:	0503	(01993)	(02180)
A1S81Z:	0504C	(02005)	(02177)
A1S82F:	0509Z	(02015)	(02167)
A1S82A:	04F0	(02072)	
A1S82Z:	0005C	(02065)	(02178)
A1S82UP:	00659	(02123)	(02174)
ACSL1ST:	04909	(00214)	(00767) (01142) (01317) (01325)
ADAMS16S:	04F7C	(00091)	(01977)
ADAMSDCS:	04F30	(01791)	(01885)
ADAMSSCS:	04F4A	(01790)	(01829)
ADAMSSS1:	04F46	(01679)	(01735)
ADAMSSS2:	04F6C	(01702)	(01707)
ADAMSSS3:	04F9Z	(01719)	(01733)
ADAMSSSP:	04F3Z	(00090)	(01665)
ADCS1AD:	00004	(01798)	(01893) (01921)
ADCS1MD:	0000P	(01808)	(01902)
ADCS1SP:	00007	(01803)	(01898)
ADCS2AD:	00011	(01799)	(01908)
ADCS2MD:	00018	(01809)	(01917)
ADCS2SP:	00014	(01804)	(01913)
ADCS2MA:	00001	(01794)	(01889)
ADCS2ZF:	00021	(01883)	(01925)
ADCSMA1:	00006	(01811)	(01897)
ADCSMA2:	00013	(01812)	(01917)
ADCSSTUP:	0001F	(01906)	(01923)
ADMS1AC:	04F5F	(02030)	(02033) (02034) (02049) (02067)
ADMSHA:	04F04	(01690)	(01796)
ADMSHMD:	04FDC	(01710)	(01806)
ADMSCU:	04F4R	(01998)	(02021)
ADMSD1CH:	04F2Z	(01746)	(01770) (01814) (02026) (02048)
ADMSFMA:	04F00	(01674)	(01793)
ADMSFDP:	04F04	(01698)	(01801)
ADMSFMA:	04F00	(01721)	(01811)
ADMSF1N:	04FCC	(01675)	(01691) (01699) (01705) (01711) (01714) (01716) (01727) (01731) (01750)
		(01754)	(01771) (01790)
ADMPHS:	050H0	(00101)	(02248)
ADMPHS0:	050M0	(02259)	(02262)
ADMPHS1:	050F0	(02287)	(02296)
ADMPHSA:	00000	(02325)	(02331) (02332) (02343)
ADMPHSSZ:	0000A	(02326)	(02343)
ADMPHAF5:	0511F	(00100)	(02360) (02367) (02421)
ADMPHAPH:	0510Z	(00099)	(02328)



ADPREFCN:	00065	(00019)	(00097)		
AFTSDRG:	008PM	(00020)	(00097)		
APSHDR2:	00FAA	(00021)	(02248)		
ASCMIAD:	00004	(01796)	(01819)	(01861)	
ASCMIAD:	00009	(01806)	(01845)		
ASCMIAD:	00004	(01801)	(01844)		
ASCSDAD:	0000F	(01797)	(01851)		
ASCSDAD:	00014	(01807)	(01857)		
ASCSDAD:	00013	(01802)	(01856)		
ASCSDAD:	00001	(01793)	(01835)		
ASCSDAD:	00010	(01826)	(01866)		
ASCSDAD:	0001H	(01849)	(01863)		
ATRANS:	0000H	(02385)	(02386)		
ACTSAD:	00604	(00023)	(00847)	(00869)	(01581) (01578) (01596) (01694) (01986)
		(02008)	(02283)	(02285)	(02303)
ACTSAT:	00686	(00024)	(00600)	(00601)	(00602) (00603) (01008)
ACTSNA:	00582	(00025)	(07511)	(01537)	(01560) (01580) (01688) (01709) (01715) (01981) (01992)
		(02003)	(02014)	(02277)	(02280)
ACTSSIZ:	00040	(00026)	(01004)		
ADSCHE:	04CMC	(00790)	(00795)	(00821)	(00998)
ADSPFT:	04C9A	(01000)	(01013)		
RINDRUK:	05122	(02306)	(02308)	(02375)	
ADTSAD:	0000H	(00027)	(01008)		
ADTSORD:	00006	(00028)	(00600)	(00601)	
ADTSLOWT:	00009	(00029)	(00602)	(00603)	
ADTRANS:	0000C	(02388)	(02391)	(02392)	
CSWS1161:	00286	(00031)	(00110)	(01180)	
CSWS1162:	0028H	(00114)	(01181)		
CSWS1163:	0028A	(00115)	(01182)		
CSWS1171:	0028C	(00116)			
CSWS1172:	0028F	(00117)			
CSWS1173:	002C0	(00118)			
CSWS1181:	002C2	(00119)			
CSWS1182:	002C4	(00120)			
CSWS1183:	002C6	(00121)			
CSWS1191:	002C8	(00122)			
CSWS1192:	002CA	(00123)			
CSWS1193:	002CC	(00124)			
CSWS1201:	002CF	(00125)			
CSWS1202:	002D0	(00126)			
CSWS1203:	002D2	(00127)			
CSWS1211:	002D4	(00128)			
CSWS1212:	002D6	(00129)			
CSWS1213:	002D8	(00130)			



CROSS REFERENCE

DEVS43:	0004F	(01993)	(01995)	(02014)	(02015)	(02022)	(02030)	(02048)	(02280)	(02285)	(02290)
DEVS:	01950	(00043)	(02300)	(00558)	(00592)	(01173)					
EVLS1:	00001	(00044)	(00237)	(00558)	(00592)	(01173)	(01215)				
EVLS2:	00002	(00045)	(01236)								
EVLS3:	00003	(00046)	(01237)								
IOSRPHL:	04044	(00552)	(00556)	(00575)	(00579)	(00583)	(00587)	(00600)			
IOSRPH1:	0404C	(00545)	(00565)	(00612)	(00781)	(00782)					
IOSLPH1:	0406C	(00550)	(00554)	(00570)	(00571)	(00637)	(00803)	(00804)	(00806)	(00807)	(00817)
IOSMOR:	0406H	(00934)	(00950)	(00955)	(00981)	(01505)					
IOSMOR0:	0406F	(01537)	(01614)								
IOSMOR1:	0406E	(01569)	(01615)								
IOSMOR2:	0406A	(01571)	(01616)								
IOSMOR3:	04060	(01578)	(01617)								
IOSMOR4:	0406H	(01587)	(01618)								
IOSMOR5:	0406E	(01595)	(01619)								
IOSMOR6:	0406A	(01605)	(01620)								
IOSMOR0:	04062	(01532)	(01614)								
IOSMOR1:	04004	(01577)	(01554)								
IOSMOR2:	0406E	(01522)	(01547)	(01611)							
IOSMOR3:	04060	(00932)	(01595)	(01605)	(01622)						
IOSMOR4:	0406C	(00778)	(01055)								
IOSMOR5:	04067	(00929)	(01073)								
IOSMOR6:	0406A	(01075)	(01553)								
IOSMOR7:	0406E	(00792)	(01057)								
IOSMOR8:	04062	(00797)	(01059)								
IOSMOR9:	04065	(00823)	(01061)								
IOSMOR0:	0406H	(00928)	(01063)								
IOSMOR1:	0406E	(00857)	(01065)								
IOSMOR2:	04061	(00900)	(00902)	(01067)							
IOSMOR3:	04064	(00927)	(00906)	(01071)							
IOSMOR4:	04060	(01056)	(01089)	(01060)	(01062)	(01064)	(01066)	(01068)	(01070)	(01072)	(01074)
IOSMOR5:	0406B	(01076)	(01078)								
IOSMOR6:	04061	(00213)	(00221)	(00234)	(00590)	(01163)					
IOSMOR7:	04061	(00181)	(00270)								
IOSMOR8:	04062	(00249)	(00253)	(00263)	(00286)	(00290)	(00300)	(00304)	(00323)	(00327)	
IOSMOR9:	00437	(00341)	(00364)	(00374)	(00378)	(00397)	(00401)	(00411)	(00415)		
IOSMOR0:	00434	(00438)	(00448)	(00452)	(00471)	(00475)	(00485)	(00489)	(00508)	(00512)	
IOSMOR1:	00527	(00526)	(00545)	(01169)	(01171)						
IOSMOR2:	04074	(00277)	(00319)	(00351)	(00388)	(00425)	(00462)	(00499)	(00516)	(00564)	

CROSS REFERENCE

ISV15:	00502 (00047)	(00931)	(01378)	(01443)	(02256)
ISV15S17:	00060 (00048)	(00928)			
LDSSCML12:	04C7C (00917)	(00947)	(00971)		
LDSSCML13:	04C4D (01922)	(01040)			
LDSSCML14:	04C4C (01048)	(01042)			
LDSS:	04B9C (00079)	(00748)			
LDSS0:	04C4A (00491)	(00915)			
LDSS01:	04C9H (00477)	(01020)			
LDSS02:	04C4B (00479)	(01046)			
LDSSA:	04C13 (00447)	(00852)	(00461)		
LDSSADAM:	04H42 (00753)	(01777)	(02056)		
LDSSH:	04C27 (00464)	(00475)			
LDSSC:	04BHF (00775)	(00740)			
LDSSMILL:	04C59 (00427)	(00932)			
MSS:	00000 (00052)	(01235)	(01236)	(01237)	(01394)
			(01238)	(01239)	(01612)
			(01240)	(01241)	(01622)
			(01242)	(01243)	(01835)
			(01244)	(01245)	(01839)
			(01246)	(01247)	(01851)
			(01248)	(01249)	(01852)
			(01250)	(01251)	(01856)
			(01252)	(01253)	(01857)
			(01254)	(01255)	(01893)
			(01256)	(01257)	(01894)
			(01258)	(01259)	(01908)
			(01260)	(01261)	(01909)
			(01262)	(01263)	(01912)
			(01264)	(01265)	(01913)
			(01266)	(01267)	(01914)
			(01268)	(01269)	(02128)
			(01270)	(01271)	(02129)
			(01272)	(01273)	(02168)
			(01274)	(01275)	(02378)
			(01276)	(01277)	(02379)
MPRAAS:	04D66 (00081)	(01292)			
MPRAAS0:	04DHF (01318)	(01324)			
MPRAAS1:	04D9Q (01326)	(01332)			
MSKSHHT:	0FF00 (00050)	(00947)	(00958)	(01295)	(01298)
			(00959)	(01299)	(01666)
			(00960)	(01300)	(01724)
			(00961)	(01301)	(01996)
			(00962)	(01302)	(02275)
			(00963)	(01303)	(00867)
			(00964)	(01304)	(00862)
			(00965)	(01305)	(00839)
			(00966)	(01306)	(00765)
			(00967)	(01307)	(00813)
			(00968)	(01308)	(00839)
			(00969)	(01309)	(00862)
			(00970)	(01310)	(00867)
			(00971)	(01311)	(00940)
			(00972)	(01312)	(00953)
			(00973)	(01313)	(00940)
			(00974)	(01314)	(01749)
			(00975)	(01315)	(01745)
			(00976)	(01316)	(01723)
			(00977)	(01317)	(01685)
			(00978)	(01318)	(01723)
			(00979)	(01319)	(01745)
			(00980)	(01320)	(01749)
			(00981)	(01321)	(01745)
			(00982)	(01322)	(01745)
			(00983)	(01323)	(01745)
			(00984)	(01324)	(01745)
			(00985)	(01325)	(01745)
			(00986)	(01326)	(01745)
			(00987)	(01327)	(01745)
			(00988)	(01328)	(01745)
			(00989)	(01329)	(01745)
			(00990)	(01330)	(01745)
			(00991)	(01331)	(01745)
			(00992)	(01332)	(01745)
			(00993)	(01333)	(01745)
			(00994)	(01334)	(01745)
			(00995)	(01335)	(01745)
			(00996)	(01336)	(01745)
			(00997)	(01337)	(01745)
			(00998)	(01338)	(01745)
			(00999)	(01339)	(01745)
			(01000)	(01340)	(01745)
			(01001)	(01341)	(01745)
			(01002)	(01342)	(01745)
			(01003)	(01343)	(01745)
			(01004)	(01344)	(01745)
			(01005)	(01345)	(01745)
			(01006)	(01346)	(01745)
			(01007)	(01347)	(01745)
			(01008)	(01348)	(01745)
			(01009)	(01349)	(01745)
			(01010)	(01350)	(01745)
			(01011)	(01351)	(01745)
			(01012)	(01352)	(01745)
			(01013)	(01353)	(01745)
			(01014)	(01354)	(01745)
			(01015)	(01355)	(01745)
			(01016)	(01356)	(01745)
			(01017)	(01357)	(01745)
			(01018)	(01358)	(01745)
			(01019)	(01359)	(01745)
			(01020)	(01360)	(01745)
			(01021)	(01361)	(01745)
			(01022)	(01362)	(01745)
			(01023)	(01363)	(01745)
			(01024)	(01364)	(01745)
			(01025)	(01365)	(01745)
			(01026)	(01366)	(01745)
			(01027)	(01367)	(01745)
			(01028)	(01368)	(01745)
			(01029)	(01369)	(01745)
			(01030)	(01370)	(01745)
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			(01033)	(01373)	(01745)
			(01034)	(01374)	(01745)
			(01035)	(01375)	(01745)
			(01036)	(01376)	(01745)
			(01037)	(01377)	(01745)
			(01038)	(01378)	(01745)
			(01039)	(01379)	(01745)
			(01040)	(01380)	(01745)
			(01041)	(01381)	(01745)
			(01042)	(01382)	(01745)
			(01043)	(01383)	(01745)
			(01044)	(01384)	(01745)
			(01045)	(01385)	(01745)
			(01046)	(01386)	(01745)
			(01047)	(01387)	(01745)
			(01048)	(01388)	(01745)
			(01049)	(01389)	(01745)
			(01050)	(01390)	(01745)
			(01051)	(01391)	(01745)
			(01052)	(01392)	(01745)
			(01053)	(01393)	(01745)
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			(01056)	(01396)	(01745)
			(01057)	(01397)	(01745)
			(01058)	(01398)	(01745)
			(01059)	(01399)	(01745)
			(01060)	(01400)	(01745)
			(01061)	(01401)	(01745)
			(01062)	(01402)	(01745)
			(01063)	(01403)	(01745)
			(01064)	(01404)	(01745)
			(01065)	(01405)	(01745)
			(01066)	(01406)	(01745)
			(01067)	(01407)	(01745)
			(01068)	(01408)	(01745)
			(01069)	(01409)	(01745)
			(01070)	(01410)	(01745)
			(01071)	(01411)	(01745)
			(01072)	(01412)	(01745)
			(01073)	(01413)	(01745)
			(01074)	(01414)	(01745)
			(01075)	(01415)	(01745)
			(01076)	(01416)	(01745)
			(01077)	(01417)	(01745)
			(01078)	(01418)	(01745)
			(01079)	(01419)	(01745)
			(01080)	(01420)	(01745)
			(01081)	(01421)	(01745)
			(01082)	(01422)	(01745)
			(01083)	(01423)	(01745)
			(01084)	(01424)	(01745)
			(01085)	(01425)	(01745)
			(01086)	(01426)	(01745)
			(01087)	(01427)	(01745)
			(01088)	(01428)	(01745)
			(01089)	(01429)	(01745)
			(01090)	(01430)	(01745)
			(01091)	(01431)	(01745)
			(01092)	(01432)	(01745)
			(01093)	(01433)	(01745)
			(01094)	(01434)	(01745)
			(01095)	(01435)	(01745)
			(01096)	(01436)	(01745)
			(01097)	(01437)	(01745)
			(01098)	(01438)	(01745)
			(01099)	(01439)	(01745)
			(01100)	(01440)	(01745)
			(01101)	(01441)	(01745)
			(01102)	(01442)	(01745)
			(01103)	(01443)	(01745)
			(01104)	(01444)	(01745)
			(01105)	(01445)	(01745)
			(01106)	(01446)	(01745)
			(01107)	(01447)	(01745)
			(01108)	(01448)	(01745)
			(01109)	(01449)	(01745)
			(01110)	(01450)	(01745)
			(01111)	(01451)	(01745)
			(01112)	(01452)	(01745)
			(01113)	(01453)	(01745)
			(01114)	(01454)	(01745)
			(01115)	(01455)	(01745)
			(01116)	(01456)	(01745)
			(01117)	(01457)	(01745)
			(01118)	(01458)	(01745)
			(01119)	(01459)	(01745)
			(01120)	(01460)	(01745)
			(01121)	(01461)	(01745)
			(01122)	(01462)	(01745)
			(01123)	(01463)	(01745)
			(01124)	(01464)	(01745)
			(01125)	(01465)	(01745)
			(01126)	(01466)	(01745)
			(01127)	(01467)	(01745)
			(01128)	(01468)	(01745)
			(01129)	(01469)	(01745)
			(01130)	(01470)	(01745)
			(01131)	(01471)	(01745)
			(01132)	(01472)	(01745)
			(01133)	(01473)	(01745)
			(01134)	(01474)	(01745)
			(01135)	(01475)	(01745)
			(01136)	(01476)	(01745)
			(01137)	(01477)	(01745)
			(01138)	(01478)	(01745)
			(		

SFTSSMR:	01C12 (00054)	(00226)	
SNAP\$IML:	00RKR (00055)	(00179)	(00225)
START:	04900 (00067)	(00185)	
TFMS0:	00704 (00057)	(00760)	(00934) (02299) (02302)
TFMS1:	00705 (00058)		
TFMS2:	00706 (00059)	(00844)	(00872)
TUP\$CUP:	0515A (00072)	(02429)	
TUP\$PTR:	00298 (00060)	(00070)	
WS:	00002 (00062)	(00552)	(00575) (00579) (00583) (00587) (01004) (01158) (01160) (01179)
		(01665) (01671)	(01695) (01704) (01722) (01728) (01746) (01772) (01793) (01794)
		(01796) (01797)	(01798) (01799) (01801) (01802) (01803) (01804) (01806) (01807)
		(01808) (01809)	(01811) (01812) (01826) (01883) (02026) (02050) (02065)
WATTS0:	049FD (00237)		
WATTS10S:	049FA (00234)	(00239)	(00755) (01124) (01301) (01305)
WSPS:	04DH2 (00086)	(01439)	
WSPS1:	04DMC (00911)	(01453)	
WSPSHMOV:	04DCb (01455)	(01457)	(014b1)

LINES WITH FROMS: 0 (MAP VERSTON R00101.10) F- 0

T A B L E O F C O N T E N T S

SYSTEM DEPENDENT VARIABLES (SNAP-100 REF. 3.5)	PAGE 2
DISPATCH TABLE ENTRIES FOR FF2R, FF2RR	PAGE 3
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SPECIAL BINDING FOR WBASE, VBASE, AND UBASE	PAGE 9
SET PRINTING SLOTS TO PROPER VALUES	PAGE 11
FFT - AP PROGRAMS	PAGE 12
APU PROGRAMS	PAGE 13
SCRAMBLE AND FIRST RADIX-2 STAGE, FORWARD	PAGE 14
SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD	PAGE 16
SUCCESSIVE RADIX-4 STAGES, FORWARD	PAGE 20
EVEN-ODD SEPARATE	PAGE 22
APS PROGRAMS	PAGE 23
CSM - COMPLEX FFT SCRAMBLE (P0 - INPUT)	PAGE 25
CSM-SCRAMBLE SUBROUTINE (P1 - OUTPUT)	PAGE 27
CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)	PAGE 28
SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)	PAGE 30
GENERATE OUTPUT ADDRESSES FOR EVEN-ODD SEPARATE	PAGE 31
SUCCESSIVE RADIX-4 STAGES (INPUT - P0)	PAGE 33
GENERATE INPUT ADDRESSES FOR EVEN-ODD SEPARATE	PAGE 34
ANGULAR SEPARATION SUBROUTINE (INPUT - P1)	PAGE 35
SYMBOL TABLE	

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(00001) ;FAST FOURIER TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980
(00002) ;
(00003) ; MODIFIED FOR GTE PENNSYLVANIA BY S. TERRACE
(00004) ;
(00005) ;MODIFICATIONS MADE TO CORRECT BUFFER PROBLEMS.....31 JANUARY 1979
(00006) ;
(00007) ; TWO REAL TRANSFORMS, Y=2.0*SIN(PI*(X+17)); X, Z REAL,
(00008) ; PERFORMS TWO REAL TRANSFORMS OF SIZE N ON
(00009) ; THE U BUFFER, ONE REAL FUNCTION, X, IS STORED IN
(00010) ; THE REAL PART OF U AND THE OTHER, Z, IS STORED IN
(00011) ; THE IMAGINARY PART OF U. RESULTS ARE LEFT IN THE
(00012) ; Y BUFFER WITH THE TRANSFORM OF X IN THE FIRST HALF
(00013) ; AND THE TRANSFORM OF Z IN THE SECOND HALF. SINCE
(00014) ; THE RESULT SAMPLES 0 AND N/2 ARE KNOWN TO HAVE
(00015) ; ONLY REAL PARTS, THEIR RESULTS ARE STORED IN W
(00016) ; ZERO WITH RX(0)=RX(0) AND IZ(0)=IZ(0) AND
(00017) ; RZ(0)=RZ(0) AND IZ(0)=IZ(0).
(00018) ; THE ALGORITHM PRODUCES TWICE THE CORRECT VALUE FOR THE SPECTRAL
(00019) ; OUTPUTS BEFORE THEY ARE WEIGHTED BY SCALAR A.
(00020) ;
(00021) ; THIS ROUTINE CANNOT BE DONE IN PLACE. I.E. THE
(00022) ; Y CANNOT BE THE SAME AS W AND U CANNOT BE THE SAME
(00023) ; AS W. BUT Y CAN BE THE SAME AS U.
(00024) ;
(00025) ;
(00026) ; THE BUFFER DESCRIPTIONS ARE:
(00027) ; Y BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(00028) ; U BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(00029) ; W BUFFER (10-39) REAL 32-BIT FLOATING POINT
(00030) ; W BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(00031) ;
(00032) ; THE COSINE TABLE ENTRIES ARE:
(00033) ; CT(K)=COS(2*PI*K/CSIZE)
(00034) ; WHERE CSIZE IS A MULTIPLE OF N
(00035) ;
(00036) ;

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SYSTEM DEPENDENT VARIABLES (SNAP-300 REL. 3.5)  
 (00031) \*SYSTEM DEPENDENT VARIABLES (SNAP-300 REL. 3.5)  
 (00038) ?  
 (00039) ?  
 (00040) ?  
 (00041) ?  
 (00042) ?  
 (00043) ?  
 (00044) ?  
 (00045) ?

UPDATED TO RELEASE 3.5 BY KEN WILMER, 4-OCTOBER-1979  
 CORRECTION MADE IN BINDING OF NO FOUR CASE WHEN USFPOUSIZE > UR = 20010  
 ALL THESE VARIABLES MUST BE REDEFINED FOR USE  
 WITH A NEW RELEASE OF SNAP EXEC

0000040M (00046) AFDTS = SMFH  
 0000040R (00047) AFDTSORG = AFDTS  
 0000020M (00048) TOPSPTR = \$200  
 00000400 (00049) MASKSHRT = \$FF00  
 000000F7 (00050) MASKSHRT = \$00FF  
 00000001 (00051) MS = 1  
 00000002 (00052) WS = 2\*MS  
 00000582 (00053) KCTSHA = \$582  
 00000604 (00054) ACTSAD = \$604  
 00000686 (00055) ACTSAT = \$686  
 00000000 (00056) MSS = 0  
 00000020 (00057) FLAGSPTR = \$20  
 00000004 (00058) FLAGGO = \$4  
 00000005 (00059) FLAGG1 = \$5  
 00000382 (00060) SVTS = \$382  
 00000240 (00061) APSKSL = \$240  
 0000070A (00062) ZPRD = \$70A  
 0001FFCF (00063) SVSSEFGS = \$1FFCF  
 00000F63 (00064) APSHNDRO = \$0F63  
 (00065) ?  
 0000020R (00066) #L = TOPSPTR  
 (00067) \*  
 0020H 001049C4 (00068) ADDR TOPSPTR  
 (00069) \*  
 (00070) \*  
 00000003 (00071) #M = 3  
 (00072) \*  
 00004690 (00074)

ARRAY FUNCTION DISPATCH  
 DISPATCH TABLE ORIGIN  
 POINTER TO TOP OF EXEC  
 MASK LEFT BYTE  
 MASK RIGHT BYTE  
 1 HALFWORD = 1 HALFWORD  
 1 FULLWORD = 2 HALFWORDS  
 BASE ADDRESS TABLE  
 ARRAY DEFINITION TABLE  
 HUFFER ATTRIBUTE TABLE  
 DUMMY ARGUMENT  
 SET FLAG HIT  
 FLAG G1  
 FLAG G1  
 SCALAR VALUE TABLE  
 ADDR OF ADDR OF BINDING SUPPORT LIST  
 ADDR OF ZERO

UPDATE: TOP OF EXEC POINTER  
 OFFINE: START LOCATION FOR MODULE  
 .START = \$4690



```

(00074) DISPATCH TABLE ENTRIES FOR FF2R, FF2RH
(00075) ;
(00076) ; FF2R HAS FULL BINDING
(00077) ; FF2RH ONLY BINDS YBASE AND XBASE AND XBASE
(00078) ;
00000006 (00079) FF2RS=214
00000001 (00080) FF2RHS=215
00000001 (00081) ;
000000FC (00082) #L=AFDTS+10MS*(FF2RS-12R)
00AF0 001F47AC (00083) ADDR CSM2S(R, 1)
00AF4 001F48C4 (00084) ADDR CSM2S(R, 1)
00AF0 00104690 (00085) ADDR FTSSET( 1, 0)
00000006 (00086) ;
000000AF2 (00087) #L=AFDTS+10MS*(FF2RHS-12R)
00AF2 001F47AC (00088) ADDR CSM2S(R, 1)
00AF4 001F48C4 (00089) ADDR CSM2S(R, 1)
00AF6 00104754 (00090) ADDR SRMSFFT( 1, 0)
(00091) ;
(00092) ;
00004690 (00093) #L = START SET PC TO START LOCATION OF MODULE

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SPECIAL BINDING MODULE FOR FFT SETUP

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(00093) SPECIAL BINDING MODULE FOR FFT SETUP
(00095) ;
(00096) ; DOES ALL BINDING NOT REPEATED EVERY TIME
(00097) ; AM FFT IS DONE.
(00098) ;
(00099) ; ENTER WITH K1 POINTING TO FCN NUMBER
(00100) ; K2 POINTING TO DISPATCH TABLE.
(00101) ;
(00102) ;
(00103) FFTSET MOVPM R4, 2*(HS(R1))      GET 0 BUFFER TO
(00104) ANDIR R4, MSKSUBT                MASK OUT RIGHT HALF
(00105) LRS R4, 7                        SET FOR FULL WORD INDEX
(00106) EVEN                               SKIP TO EVEN BOUNDARY
(00107) MOVPM R5, HCTSAT+HS(R4)          R5= 0 BUFFER ATTRIBUTES
(00108) MOVPM R5, PWP2S                    STORE FOR LATER REFERENCE
(00109) ;
(00110) ?BIND ALL USIZE=1'S AND USIZE=2
(00111) ;
(00112) MOVPM R5, HCTSAD(R4)              R5<=USEP, R6<=USIZE-1
(00113) MOVPM R6, CSMS*MS*CR4A03+HS      STORE ALL USIZE=1'S
(00114) MOVPM R6, CSMS*MS*CSAL05+HS      ...
(00115) MOVPM R6, CSMS*MS*CR4ARS+HS      ...
(00116) MOVPM R6, CSMS*MS*CR4AYS+HS      STORE: FFTSIZE=1
(00117) MOVPM R6, CSMS*MS*CR4A13S+HS      ...
(00118) DECR R6, 1
(00119) EVEN
(00120) MOVPM R6, CSMS*MS*F0S03+HS        BIND USIZE=2
(00121)
(00122) INCR R6, 2
(00123) MOVPM R3, R5
(00124) MOVPM R5, R6
(00125) LRS R5, 2
(00126) ;
(00127) ?BIND ALL HUI'S
(00128) ;
(00129) MOVPM R4, HUSTHU-1
(00130) HUIEP
(00131) TEST R7
(00132) JMP HUSTHU, R4
(00133) CMK 0,0(R7)
(00134) MOVPM R5,1(R7)
(00135) SKPL R4
(00136) SMR 0,0(R7)
(00137) HUIP
(00138) ; POINT TO TARGET OF HUI BINDING LOC'S
(00139) ; GET NEXT BINDING ADDR
(00140) ; CHECK FOR END OF TABLE
(00141) ; ZERO MARKS END OF TABLE
(00142) ; RESET MSK OF DELTA FIELD
(00143) ; STORE LOW 16 BITS
(00144) ; IF NOT 0, DELTA IS OK
(00145) ; PRODUCT WAS 2*1R BEFORE CORRECTION
(00146) ; LOOP FOR ALL HUI BINDING
(00147) ;

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```

(00138) ?
046C0 0250 (00139) HUSDN TEST R5 ; CHECK PRODUCT
046C1 1010 (00140) SKPL NF ; IF NOT 0, DELTA IS OK
046C2 FC510000 (00141) MOVAP R5,S10000 ; SET UP CORRECT DELTA FOR 2**18
046C3 3C51 (00142) ?
046C4 0800 (00143) LRS R5, 1 R5<=00/2 (00)
(00144) ? TO TO EVEN BOUNDARY
(00145) ?
(00146) ?
(00147) ?
046C7 F0504007 (00148) MOVPM R5, CSMS*MS*CSM13S+HS STORE: 00'S
046C8 4F56 (00149) SDRPR R5, R3 R5<=00-USEP
046C9 0800 (00150) EVEN
046CA F05040CF (00151) MOVPM R5, CSMS*MS*CSM11S+HS STORE: 00-USEP
046CB 405C (00152) MOVHR R5, R6 R5<=N
046CC 0800 (00153) EVEN
046CD 98500003 (00154) MULTR R5, 3 R5<=4*(3N/2)
(00155) ?
(00156) ?
(00157) ?
(00158) ?
046CC F05040F5 (00159) MOVPM R5, CSMS*MS*SCM0+HS STORE: ALL: 00'S
046CD F05040F9 (00160) MOVPM R5, CSMS*MS*SCM2S+HS ...
046CE F05040FF (00161) MOVPM R5, CSMS*MS*SCM4S+HS ...
046CF F05040FF (00162) MOVPM R5, CSMS*MS*SCM6S+HS ...
046D0 0800 (00163) EVEN R5<=00/2 (01)
046D1 F05040FF (00164) MOVPM R5, CSMS*MS*SCM1S+HS STORE: ALL: 01'S
046D2 3C51 (00165) MOVPM R5, CSMS*MS*SCM5S+HS ...
046D3 0800 (00166) LRS R5, 1 R5<=01/2 (02)
046D4 0800 (00167) EVEN
046D5 3C51 (00168) MOVPM R5, CSMS*MS*SCM3S+HS STORE: 02
046D6 0800 (00169) LRS R5, 1 R5<=02/2 (03)
046D7 3C51 (00170) MOVPM R5, CSMS*MS*SCM7S+HS STORE: 03
046D8 0800 (00171) LRS R5, 1 R5<=03/2 (04)
046D9 3C51 (00172) MOVPM R5, CSMS*MS*SCMRS+HS STORE: 04
046DA 0800 (00173) LRS R5, 1 R5<=04/2 (05)
046DB 3C51 (00174) MOVPM R5, CSMS*MS*SCMOS+HS STORE: 05
046DC 0800 (00175) LRS R5, 1 R5<=05/2 (06)
046DD 3C51 (00176) MOVPM R5, CSMS*MS*SCM10S+HS STORE: 06
046DE 0800 (00177) LRS R5, 1 R5<=06/2 (07)
046DF 3C51 (00178) MOVPM R5, CSMS*MS*SCM10S+HS
046E0 0800 (00179) LRS R5, 1
046E1 3C51 (00180) MOVPM R5, CSMS*MS*SCM10S+HS
046E2 0800 (00181) LRS R5, 1
    
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046FF 0800 (00182) FVFN
046FO F0504908 (00183) MOVW R5, CSMS*WS*SCM7+HS STORE 07
046F2 405C (00184) MOVW R5, R6 R5<=N (USIZE)
(00185) ?
(00186) ?HIND 2N'S AND 4N'S
(00187) ?
(00188) ?
(00189) ?
046F3 3A51 (00190) FLS R5, 1 R5<=2*N
FVFN
046F4 F05049A3 (00191) MOVW R5, CSMS*WS*FUS3+HS HIND 2N'S
046F6 F05049A5 (00192) MOVW R5, CSMS*WS*FUS07+HS ...
046FR 3A51 (00193) FLS R5, 1 R5<=4N
FVFN
046FA F05049A9 (00194) MOVW R5, CSMS*WS*FUS4+HS HIND 4N
046FC 2654 (00195) INCR R5, 4 R5<=4*N+4
046FD 0800 (00196) FVFN
(00197) ?
(00198) ?HIND ALL 4N*4 AND 4N*12'S
(00199) ?
046FE F05049A7 (00200) MOVW R5, CSMS*WS*CR4A2S+HS STORE 4N+4'S
04700 F0504978 (00201) MOVW R5, CSMS*WS*CR4A3+HS ...
04702 2658 (00202) INCR R5, 8 R5<=4*N+12
FVFN
04704 F05049A5 (00203) MOVW R5, CSMS*WS*CSM0L+HS STORE 4N+17
(00204) ?
(00205) ?
(00206) ?HIND SCALAR A
(00207) ?
04706 F0420001 (00208) MOVW R4, HS(R1) LOAD SCALAR A ID
0470R 9A4000FF (00209) ANDIP R4, MSKSRPT MASK IT OUT
0470A 9C480382 (00210) ADDIP R4, SVTS(R4) R4<=SVTS+WS*SAID
0470C F0404990 (00211) MOVW R4, CSMS*WS*FOS1+HS HIND SCALAR A
0470F F0420003 (00212) MOVW R4, 3*HS(R1) LOAD C BUFFER ID
04710 9A30FF00 (00213) ANDIP R4, MSKSRPT
04712 3C47 (00214) LRS R4, 7
04713 0800 (00215) FVFN
(00216) ?
(00217) ?HIND CHASR
(00218) ?
04714 C0680587 (00219) MOVW R6, HCTSMA(R4) LOAD C BASE ADDRESS
04716 9050496A (00220) MOVW R5, CSMS*WS*CR4A4 POINT TO LOAD CRASF INST.
0471R 9A60000F (00221) ANDIP R6, SF AND OUT LOW FOUR BITS
0471A 766AFF00 (00222) TORW R6, R5, SEFFO OR IN NEW FOUR BITS
0471C 846A0000 (00223) MOVW R6, 0(R5) AND STORE BACK IN INST.
0471E C0580604 (00224) MOVW R5, HCTSAD(R4) R5<=CSFP, R6<=CSIZF-1
04720 2661 (00225) INCR R6, 1 R6<=CSIZF

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04721 485C (00226) MOVW R5, R6 R5<=2*(CSFP)(CSIZE)
04722 3C54 (00227) LRS R5, 1 R5<=0.25*(CSFP)(CSIZE) (HPI)
04723 0800 (00228) FVEN
(00229) ?
(00230) ?HIND ALL HPI'S
(00231) ?
(00232) ? MOVW R5, CSMS+WS*CR4A4S+HS STORE ALL HPI'S
04724 F0504971 (00233) MOVW R5, CSMS+WS*CR4A4S+HS ...
04725 F0504975 (00234) MOVW R5, CSMS+WS*CR4A4S+HS ...
04726 F0504979 (00235) ?
(00236) ?SIANG=HPI/SSI, RIGHT NOW ONLY RADIX 2 AND RADIX 4
(00237) ?HAVE BEEN IMPLEMENTED, SO ALL THAT IS NECESSARY
(00238) ?TO CALCULATE SSI IS TO KNOW IF SIZE IS AN EVEN OR
(00239) ?ODD POWER OF TWO. THIS IS DONE BY CHECKING THE
(00240) ?POWER OF TWO ENTRY FOR THE 0 BUFFER IN THE BCSTAT
(00241) ?TABLE. IF BIT 0 IS ON, THEN IT'S AN ODD POWER.
(00242) ?
0472A F030024D (00243) MOVW R3, APSRSL. ? GET HINDING SUPPORT LIST POINTER
0472C D006478R (00244) SMBC 0, PWR2S SKIP IF EVEN POWER OF TWO
0472E 2006 (00245) HOP SHMS1 ... ODD POWER
0472F F0044927 (00246) MOVW 4, CSMS+WS*CR4A01+HS SET AND HIND SSI = 4
04731 F00A4781 (00247) MOVW F0GS01, GSFIGS+HS RADIX 4 FIRST STEP
03743 3C52 (00248) LRS R5, 2 SIANG=HPI/4
04734 2005 (00249) HOP SHMS2
04735 F0044927 (00250) SHMS1 MOVW 2, CSMS+WS*CR4A01+HS SET AND HIND SSI = 2
04737 F0A44781 (00251) MOVW F0GS01+FIGSSET, GSFIGS+HS RADIX 2 FIRST STEP
04739 3C51 (00252) LRS R5, 1 SIANG=HPI/2
(00253) SHMS2
(00254) ?
(00255) ?
(00256) ?HIND SIANG TO SIANG'S
(00257) ?
0473A F0504987 (00258) MOVW R5, CSMS+WS*ANG1.1S+HS STORE SIANG
0473C 3C52 (00259) LRS R5, 2
0473D 0800 (00260) FVEN
0473E F0504989 (00261) MOVW R5, CSMS+WS*ANG1.2S+HS STORE S2ANG
04740 3C52 (00262) LRS R5, 2
04741 0800 (00263) FVEN
04742 F050498R (00264) MOVW R5, CSMS+WS*ANG1.3S+HS STORE S3ANG
04744 3C52 (00265) LRS R5, 2
04745 0800 (00266) FVEN
04746 F050498D (00267) MOVW R5, CSMS+WS*ANG1.4S+HS STORE S4ANG
04748 3C52 (00268) LRS R5, 2
04749 0800 (00269) FVEN

```

PAGE 02 FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2K, FF2H MAY 7, 1980  
SPECIAL HANDLING MODES FOR FFT SETUP

0474A F050896F (00270)	MOVW R5, (SMS+R5*ANGL1.5S+HS	STORE S5ANG
0474C 3C52 (00271)	IRL R5, 2	
0474D 0M00 (00272)	FVFN	
0474E F05089C1 (00273)	MOVW R5, (SMS+R5*ANGL1.6S+HS	STORE SAANG
04750 3C52 (00274)	IRL R5, 2	
04751 0M00 (00275)	FVFN	
04752 F05089C1 (00276)	MOVW R5, (SMS+R5*ANGL1.7S+HS	STORE STANG



0478F F0420001	(00321) ?	
04790 2A40FF00	(00322) THIRD YBASE	
04792 3C37	(00323) :	
04793 0800		LOAD Y BUFFER 10
04794 C0680582		MASK LEFT HALF
04796 20504950		CREATE FULL WORD INDEX
04798 2A600004		LOAD YBASE ADDRESS IN R6, R7
0479A 7668FF00		POINT TO LOAD INST.
0479C 846A0000		MASK LOW FOUR BITS
		OR INTO INST.
		STORE YBASE

MOVW R4, HSP1)	
ANDR R4, MRSRHYT	
LES R4, J	
4VFN	
MOVW R6, RCTSHA(R4)	
MOVW R5, CSMS*MS*FUS01	
ANDR R6, SF	
TOPW R6, R5, SFF0	
MOVW R6, 0(R5)	



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(00333) ? SET PENDING SLOTS TO PROPER VALUES
(00334) ?
0470E C634000R PUSHMI R3, AFDTSORG(W?) STORE APS MODULE BUS ORIGIN
(00336)
047AD 90744FFF MOVER R7, -WS
047AD C6H74FFF PUSHMI R3, W-WS(R3) STORE APS MODULE START AND SIZE
047AD C634000A PUSHMI R3, AFDTSORG+WS(R2) STORE APS MODULE BUS ORIGIN
(00340) ** PUSHMI R3, AFDTSORG + 2*WS(R2) ? STORE CSFU SUPPORT ADDR
(00341) ** INCR R3, 2
047AD 2637 DECR R7, 1
047AD 7771 EVFN
(00343)
047AD C3R74FFC PUSHMI R3, W-2*WS(R3) STORE APS MODULE SIZE
(00345) ** PUSHMI R3, ZERO ; NO SPECIAL SUPPORT
(00346) ** MOVER R5, R1 POINT TO SCALAR A ID
(00347) ** INCR R5, HS
(00348) ** MOVER R4, R5, MSKSRVYT
(00349) ** PUSHMI R3, R4 STORE SCALAR A IDENTIFIER
047AA 2634 INCR R3, 4
047AD C33047H1 PUSHMI R3, GSF1GS+HS STORE FLAG G1
(00352) *
(00353) ** INCR R3, 2
(00354) ** PUSHMI R3, ZERO SKT FOR NO PRF
(00355) * AND EXIT
047AD 80004F63 JMP APSRDR0
(00357) ?
047AF 0800 EVFN
047AD 0004 GSF1GS DATA S4, S25 ; GO & G1 FLAG CONTROL STORAGE
047AD 0025
(00360) ?
047H2 4HCC DATA CSMS + WS*CSML
047H3 4HD0 DATA CSMS + WS*CSMF
(00362)
047H4 4HD4 DATA CSMS + WS*CSML2S
047H5 4HD8 DATA CSMS + WS*CSML4S
047H6 4HD0 DATA CSMS + WS*CSML5S
047H7 0000 DATA 0
(00367) ?
047RR 0000 DATA 0 ; STORAGE FOR POWER OF TWO

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PAGE 12: LAST FOURTH TRANSFORM ALGORITHM - FF2H, FF2H MAY 7, 1980  
FF - AP PROGRAMS

00000003	(00369) FFT - AP PROGRAMS
	(00370) ME3
	(00371) OPADD P=1, (1 .LS. 10)+(12 .LS. 5)+X'16'
	(00372) ? APU PROGRAMS
	(00373) ?
	(00374) ? BINDING SECTION FOR PFC
	(00375) ?
047H9 0000	EVFN
047HA 0000	DATA CSM/SSA
047HH 0000	DATA CM4SSZ
	EVFN
	(00380) ?
	(00381) ?

THIS IS A MAP-300 PROGRAM

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(00382) ; SCRAMBLE AND FIRST RADIX-2 STAGE, FORWARD
(00383) ; 03/08/78
(00384) ; SCRAMBLE AND FIRST STAGE OF FFT,G1 SET
(00385) ;
(00386) ; FUNCTION
(00387) ; LETTING FIGHT SUCCESSIVE INPUTS BEING DESIGNATED BY
(00388) ; R00,R01,I01,I00,I02,I03,R03,R02
(00389) ; THE FIGHT OUTPUTS ARE PROVIDED
(00390) ; R00+R01,R00-R01,I00+I01,I00-I01
(00391) ; I02+I03,I02-I03,R02+R03,R02-R03
(00392) ; FOR RELATIONSHIP OF INPUTS TO U(K), AND OUTPUTS TO Y(K),
(00393) ; SEE SUPPORTING APS PROGRAM, CSM
(00394) ;
(00395) CSM2S BEGIN APH(CSM2)
(00396)   WA=00
(00397) ;
(00398) CSM2SSA JUMP(CSM4F, G1)
(00399)   JUMP(CSM2F)
(00400) ;
(00401) CSM2L  MOV(R,00)           00=IY0\IY1
(00402) ;
(00403) CSM2F  MOV(IQA,A0)         A0=R00
(00404)   MOV(IQA,A1)         A1=R01
(00405)   ADD(A0,A1)\SUR(A0,A1)
(00406) ;
(00407)   MOV(IQA,A3)         A3=I01
(00408)   MOV(IQA,A2)         A2=I00
(00409)   MOV(I00),ADD(A2,A3)\MOV(00),SUR(A2,A3)
(00410) ;
(00411)   JUMP(CSM2L,FWI)
(00412) ;
(00413)   MOV(R,00)
(00414) ;
(00415)   NOP
(00416)   JUMP(CR4FS)
(00417) ;
  
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(00418) ; SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD
(00419) ; SCRAMBLE AND FORWARD RADIX 4 STAGE OF FFT.G1 CLEAR
(00420) ;
(00421) ; FUNCTION
(00422) ; THE FIFT SUCCESSIVE INPUTS ARE PROVIDED TO LOOP
(00423) ; R00,R01,I01,I00,I02,I03,R03,R02
(00424) ; THE INTERMEDIATE RADIX TWO RESULTS ARE CALCULATED
(00425) ; S0=0+01,S1=00+01,S2=02+03,S3=02-03
(00426) ; THE OUTPUTS THEN BEING GIVEN BY
(00427) ; Y0=S0+S2,Y1=S1+S3,Y2=S0-S2,Y3=S1+S3
(00428) ; THE ACTUAL OUTPUT SEQUENCE BEING
(00429) ; Y0,Y1,IY0,IY1,IY2,IY3,RY2,RY3
(00430) ;
(00431) ; APU INITIALIZATION
(00432) ;
(00433) ;
(00434) CSM4F MOV(10A,A0) A0=R00\R00
(00435) MOV(10A,A1) A1=R01\R01
(00436) MOV(10A,A3) A3=I01\I01
(00437) ADD(A0,A1)\SUB(A0,A1) A2=I00\I00
(00438) MOV(10A,A2)
(00439) JUMP(CSM4FS)
(00440) ;
(00441) ;
(00442) ; CSM4F, APU INNER LOOP
(00443) ;
(00444) #1 MOV(00),ADD(A5,A6)\MOV(00),SUR(A5,A7) 00=RY0\RY1
(00445) MOV(10A,A0) A0=R00
(00446) MOV(00),SUR(A5,A6)\MOV(00),ADD(A5,A7) 00=IY0\IY1
(00447) MOV(10A,A1) A1=R01
(00448) MOV(00),SUR(A4,A7)\MOV(00),SUR(A4,A6) 00=IY2\IY3
(00449) MOV(10A,A3) A3=I01
(00450) MOV(00),ADD(A0,A1)\MOV(00),SUR(A0,A1) 00=RY2\RY3
(00451) MOV(10A,A2) A2=I00
(00452) ;
(00453) CSM4FS MOV(A4),ADD(A2,A3)\MOV(A4),SUR(A2,A3) A4=RS0\RS1
(00454) MOV(10A,A0) A0=I02
(00455) MOV(10A,A1) A1=I03
(00456) MOV(A5),ADD(A0,A1)\MOV(A5),SUR(A0,A1) A5=IS0\IS1
(00457) MOV(10A,A3) A3=R03
(00458)
(00459)
(00460)
(00461)

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A20 047FC 08F208F2 (00462)	MOV(10A,A2)	A2=RU2
(00463)		
A21 047FF 43563856 (00464)	MOV(A6),ADD(A2,A3)\MOV(A6),SUB(A2,A3)	A6=TS2\IS3
(00465)		
A22 04800 47974697 (00466)	MOV(A7),ADD(A4,A7)\MOV(A7),ADD(A4,A6)	A7=RS2\RS3
(00467)		
A23 04802 90160013 (00468)	JUMPC(81,FWT)	
(00469)		
A24 04804 468C4F8C (00470)	MOV(100),ADD(A5,A6)\MOV(100),SUB(A5,A7)	00=RY0\RY1
(00471)	MOV(100),SUB(A5,A6)\MOV(100),ADD(A5,A7)	00=IY0\IY1
A25 04806 4F8C478C (00472)	MOV(100),SUB(A4,A7)\MOV(100),SUB(A4,A6)	00=IY2\IY3
(00473)	MOV(P,100)	00=RY2\RY3

```

(00474) ; SUCCESSIVE RADIX-4 STAGES, FORWARD
(00475) ;
(00476) ;USES APS PROGRAM CPOA
(00477) ;
(00478) ;MATHEMATICS
(00479) ;
(00480) ; W=A+HF+CF2+DF3+P+Q
(00481) ; X=A-JH+CF2+JDF3+R+S
(00482) ; Y=A-H+CF2-DF3+P-Q
(00483) ; Z=A+JH+CF2-JDF3+R+S
(00484) ;
(00485) ; P=A+CF2,H=A-CF2
(00486) ; Q=E+(H+DF2),S=JF+(H-DF2)
(00487) ;
(00488) ; PR=AH+HCOS2X+CSIN2X
(00489) ; PI=AI+CI COS2X-CRSIN2X
(00490) ; RP=AR-RCOS2X-CISIN2X
(00491) ; RI=AI-CICOS2X+CRSIN2X
(00492) ; QR=RCOSX+HSINX+DRCOS3X+DISIN3X
(00493) ; OI=RI COSX-RRSINX+DICOS3X-DRSIN3X
(00494) ; SR=RSINX-RCOSX+DICOS3X-DRSIN3X
(00495) ; SI=RCOSX+HSINX-DRCOS3X-DISIN3X
(00496) ;
(00497) ; SINX STORED IN M1X5
(00498) ; SIN2X STORED IN M2X2
(00499) ; SIN3X STORED IN M3X3
(00500) ; COSX STORED IN M5X1
(00501) ; COS2X STORED IN M2X6
(00502) ; COS3X STORED IN M3X7
(00503) ;
(00504) ;EJECT
    
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(00594) ?

```

A5F 04878 4278178 (00594)
A5F 0487A 08560856 (00595)
A60 0487C 47044704 (00596)
A61 0487E 4D9C0000 (00597)
A62 04880 000049C (00598)
A63 04882 4F0C0000 (00599)
A64 04884 000049C (00600)
A65 04886 704A704A (00601)
A66 04888 459C0000 (00602)
A67 0488A 000049C (00603)
A68 0488C 069C0000 (00604)
A69 0488E 000089C (00605)
A6A 04890 9008002A (00606)
      (00607)
      (00608)
      (00609)
      (00610)
      (00611) ?

```

(00594) ?

```

MOV(FX0),ADD(A3,A2)
MOV(FX1,A6)
MOV(A4),ADD(A6,A7)
MOV(00),SUB(A4,A5)\NOP
NOP\MOV(00),SUB(A4,A5)
MOV(00),SUB(A6,A7)\NOP
NOP\MOV(00),SUB(A6,A7)
SET(A42)
MOV(00),ADD(A4,A5)\NOP
NOP\MOV(00),ADD(A4,A5)
MOV(R,00)\NOP
NOP\MOV(R,00)
JUMPC(CR4F5A,A43)

```

(00594) ?

```

VCFSIN2X+CIRCUS2X
FX=RI\RR
A6=RR\RI
A4=PI\PR
00=ZR
00=ZI
00=YI
00=YR
APSZOTPT STAGE DN
00=XR
00=XI
00=WR
00=WI

```



PAGE 21: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2MR  
FF2R-000 SEPARATE

(00656)  
-(00657)  
(00658)  
(00659) ;

APS PROGRAMS

```

(00660) * APS PROGRAMS
(00661) ?
(00662) ?
(00663) ? START OF HEADER BLOCK
(00664) ?
(00665)
0488C 000044C4 (00666)
0488E 000048C4 (00667)
048C0 0000 (00668)
048C1 0100 (00669)
048C2 00000000 (00670)
(00671)
(00672) ?
(00673) ?

```

```

START ON WORD BOUNDARY
PTR TO CONSTR INSTR BLOCK(NONE)
PTR TO SCALAR BLOCK (NONE)
NO SCALARS
MODULE SIZE
PTR TO CHAIN ANCHOR
END OF BOUNDARY

```

```

EVEN
ADDR CSMS1
ADDR CSMS+2*CSMSS
DATA 0
DATA CR4ASZ
ADDR CR4ASA
EVEN

```

```

(00674) ? CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)
(00675) ? APS PROGRAM PROVIDING SCRAMBLE FOR COMPLEX FFT
(00676) ? MAY BE USED WITH THE MAD-300 APU PROGRAMS
(00677) ? CSM2(Y,U), CSMF(Y,U), CSMAT(Y,U)
(00678) ?
(00679) ? RESTRICTIONS
(00680) ? IN PLACE OPERATION NOT PERMITTED
(00681) ? BUFFER SIZES MUST BE 1024 OR LESS
(00682) ? Y BUFFER MUST BE COMPACT 32 BIT FLOATING
(00683) ?
(00684) ? BINDING PARAMETERS
(00685) ?
(00686) ?     N# OF POINTS IN FFT (USIZE)
(00687) ?     MU=USEP*N/2
(00688) ?     QU=HU/2
(00689) ?
(00690) ? APS-INPUT ADDRESS SEQUENCE
(00691) ? I(K), PU(K*N/2), IU(F*N/2), IU(K), IU(K*N/4)
(00692) ? I(K+3N/4), RU(K*N/4), RU(K*N/4)*1, FOR 0<=K<N/4
(00693) ? THIS PROVIDING REVERSAL OF TWO BITS
(00694) ?
(00695) ? APS-OUTPUT ADDRESS SEQUENCE
(00696) ? RW(J), RW(J+1), IW(J), IW(J+1), RW(J+2),
(00697) ? RW(J+3), IW(J+2), IW(J+3)
(00698) ? WHERE J= BIT REVERSAL OF K
(00699) ? THE DIFFERENCE, D(K) = J(K+1)-J(K) IS
(00700) ? PROVIDED BY THE P3 SUBROUTINE
(00701) ?
(00702) ?
(00703) ? CSMS     BEGIN APS(CSM)
(00704) ?
(00705) ? CSMS     JSM(CSM),P2)
(00706) ?
(00707) ? CSMS     IAD(HR0,MSS,I,TF)
(00708) ? CSMS     IAD(HR1,MSS)
(00709) ? CSMS     JUMP(CSMF,RA),SET
(00710) ?
(00711) ? CSML     SUR(HR0,MSS,TF)
(00712) ?
(00713) ? CSML     SUR(HR0,MSS,TF)
(00714) ? CSME     ADD(HR0,MSS,TF)
(00715) ? CSML     ADD(HR0,2,TF)
(00716) ? CSML     SUR(HR0,MSS,TF)
(00717) ?
  A00 048C4 00202540
  A01 048C6 02C00000
  A02 048C8 04500000
  A03 048CA 06400672
  A04 048CC 08420000
  A05 048CE 0A420000
  A06 048D0 0C4A0000
  A07 048D2 0E4A0002
  A08 048D4 10420000
  INPT RU(0) (URASE,ROUND)
  (USIZE-1,ROUND)
  TURN ON APU
  INPT RU(K+N/4) (HU,ROUND)
  INPT RU(K) (OU-USEP,ROUND)
  INPT RU(K+N/2) (HU,ROUND)
  INPT IU(K+N/2)
  INPT IU(K) (HU,ROUND)

```

PAGE 24: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RH

MAY 7, 1980

(SM - COMPLEX FFT SCRAMBLE (PO - INPUT))

```
A09 04ND6 12H0000 (00718) CSML35 ADD(MR0,KCS,TF)      IMPT IU(K+N/4) [OH ROUND]
A0A 04ND6 14H0000 (00714) CSML45 ADD(MR0,MSS,TF)      IMPT IU(K+3N/4) [HU ROUND]
A0B 04ND6 16H0002 (00720) SHR(MR0,2,TF)          IMPT RU(K+3N/4)
A0C 04ND6 181908H4 (00722) SHL(MR1,4),JUMPP(CSMU)
A0D 04ND6 1A020000 (00723) ?
A0E 04ND6 1A020000 (00724) CSML55 SHR(MR0,MSS,TF)      IMPT RU(LAST) [HU ROUND]
A0F 04ND6 1C304F77 (00725) JUMP(CP4AF,M1),SPT
A0G 04ND6 1C304F77 (00726) ?
```

```

(00727) ? CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)
(00728) ?
(00729) ? DATA POINT ADDRESS AT ENTRY 4*(J(K)-N1)+NRASE
(00730) ? DATA OUTPUT ADDRESS GENERATED (4*(K+1))+NRASE
(00731) ?
(00732) ? ITERATION LOCATION, J=(K)=K, HIT REVERSED
(00733) ? 4*(K+1)=4*(J(K)-N1)+M
(00734) ? WHERE M=NUMBER OF TRAILING 1'S IN (K)
(00735) ?
(00736) ? HINDING CONSTANTS
(00737) ?
(00738) ? D0=4*(1+N/2)
(00739) ? D1=D0/2
(00740) ? D2=D1/2
(00741) ? D3=D2/2
(00742) ? D4=D3/2
(00743) ? D5=D4/2
(00744) ? D6=D5/2
(00745) ? D7=D6/2
(00746) ?
(00747) ?
  
```

ADDRESS	OPERATION	OPERANDS	LOADS P1 FOR CR4
A0F 048F2 1F1078D	SCRW	JSN(ANGLE,P1)	
(00749) ?			
A10 048F4 208F0000	SCRW6	ADD(RW0,MSS,TF,C)	[D0 ROUND]
A11 048F6 228F0000	SCRW5	ADD(RW0,MSS,TF,C)	[D1 ROUND]
A12 048F8 248F0000	SCRW2S	ADD(RW0,MSS,TF,C)	[D0 ROUND]
A13 048FA 268F0000	SCRW3S	ADD(RW0,MSS,TF,C)	[D2 ROUND]
A14 048FC 288F0000	SCRW4S	ADD(RW0,MSS,TF,C)	[D0 ROUND]
A15 048FE 2A8F0000	SCRW5S	ADD(RW0,MSS,TF,C)	[D1 ROUND]
A16 04900 2C8F0000	SCRW6S	ADD(RW0,MSS,TF,C)	[D0 ROUND]
(00757) ?			
A17 048F2 2F201AFH		JUMPS(SCRW4,AF0),CLEAR	
A18 048F4 308F0000	SCRW7S	ADD(RW0,MSS,TF,C)	[D3 ROUND]
A19 048F6 3230106H		JUMP(SCRW0,AF0),SFT	
(00761) ?			
A1A 048F8 342010F9	SCRW4	JUMPS(SCRW5,AF1),CLEAR	
A1B 048FA 368F0000	SCRWRS	ADD(RW0,MSS,TF,C)	[D4 ROUND]
A1C 048FC 38301064		JUMP(SCRW0,AF1),SFT	
(00765) ?			
(00766) ?			
A1D 048FE 3A2020FA	SCRW5	JUMPS(SCRW6,AF2),CLEAR	
A1E 04900 3C8F0000	SCRWOS	ADD(RW0,MSS,TF,C)	[D5 ROUND]
A1F 04902 3F30106A		JUMP(SCRW0,AF2),SFT	
(00770) ?			





```

(00778) ? CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)
(00779) ?
(00780) ?
A25 0490F 4A306F40 (00781) CSM0 JSN(CSRM,P3)
A26 04910 4CC00000 (00782) CSM0S LOAD(RW0,MSS,I,TF) OTPT RW(0) (PHASE ROUND)
A27 04912 4F112953 (00783) #DIV(RW1,RP1),JUMP(CSMDE) RW1=MSIZE-1
A28 04914 50060000 (00784) ? [4N+12 ROUND]
(00785) CSM0L SUB(RW0,MSS,C)
(00786) ?
(00787) ? PRIOR TO JUMP TO P3,RW0=4J(K)-4N
(00788) ? SCRAMBLE SUBROUTINE OUTPUTS RW(J)
(00789) ? LEAVINC RW0=4J(K+1)
(00790) ?
A29 04916 52HA0004 (00791) CSM0F ADD(RW0,4,TF) OTPT RW(J+1)
A2A 04918 54R20002 (00792) SUB(RW0,2,TF) OTPT LW(J)
A2B 0491A 56RA0004 (00793) ADD(RW0,4,TF) OTPT LW(J+1)
(00794) ?
A2C 0491C 58RA0004 (00795) ADD(RW0,4,TF) OTPT LW(J+2)
A2D 0491F 5A8A0004 (00796) ADD(RW0,4,TF) OTPT LW(J+3)
A2E 04920 5CR20006 (00797) SUB(RW0,6,TF) OTPT RW(J+2)
A2F 04922 5FHA0004 (00798) ADD(RW0,4,TF) OTPT RW(J+3)
(00799) ?
A30 04924 60112884 (00800) ?
(00801) ?
  
```

```

(00802) ; SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)
(00803) ; PROGRAM ASSUMES SCRAMBLE COMPLETED
(00804) ;
(00805) ; 12/14/77
(00806) ; SCRAMBLE LEAVES APS AS FOLLOWS
(00807) ; INPUT - LAST COMMAND, JUMP(CR4AP, W1), SET
(00808) ; INPUT-P1 LOCATED AT ANGLE
(00809) ; OUTPUT - ACTIVE, IN P2
(00810) ;
(00811) ; S=STAGE SEPARATION, DATA SEPARATION A B C D
(00812) ; STARTING VALUES (/4)ARE
(00813) ; SSI=1, SCRAMBLE ONLY PRECEDING N=4*M
(00814) ; SSI=2, SCRAMBLE PLUS RADIX2 PRECEDING N=2*4*M
(00815) ; SSI=3, SCRAMBLE PLUS RADIX3 PRECEDING N=3*4*M
(00816) ; SSI=4, SCRAMBLE PLUS RADIX4 PRECEDING N=4*4*M
(00817) ; SSI=6, SCRAMBLE PLUS RADIX6 PRECEDING N=6*4*M
(00818) ;
(00819) ;
(00820) ; COSINE TABLE IS A REAL BUFFER WITH CONTENTS:
(00821) ; CT(K)=COS(2*PI*K/CSIZE)
(00822) ; RESTRICTION,
(00823) ; CSIZE MUST BE MULTIPLE OF N, FFT SIZE
(00824) ;
(00825) ; COSINE TABLE BINDING PARAMETERS
(00826) ; NPI=CSEP*(CSIZE/4) 90 DEG SEPARATION
(00827) ;
(00828) ; REGISTER USAGE
(00829) ; R0/RM0
(00830) ; R1/RM1
(00831) ; R2
(00832) ; R3
(00833) ; R4
(00834) ; R5
(00835) ; R6
(00836) ; R7
(00837) ; R8
    
```

DATA ADDRESSES  
 # OF USES OF A COSINE, COUNTER  
 # OF COSINES USED, COUNTER  
 STAGE SEPARATION  
 SEP BETWEEN COSINES WITHIN A SET  
 SEP BETWEEN SETS OF COSINES

```

(00840)
A31 04926 6200000 (00840) CH4A01 LOAD(RW3,MSS) (ISS1 ROUND)
(00841) ?
(00842) ?
(00843) ?
A32 04928 6410024 (00844) CH4A02 ADDR(RW1,RW1)
A33 0492A 6610024 (00845) ADDR(RW3,RW3)
A34 0492C 6840000 (00846) CP4A1S LOAD(RW0,MSS)
A35 0492E 6A210016 (00847) MOVW(RW2,RW3)
A36 04930 6C090010 (00848) MOVW(RW0,RW0)
A37 04932 6E200032 (00849) CLEAR(*1)
(00850)
A38 04934 70500000 (00851) CH4A03 LOAD(RW1,MSS)
A39 04936 720A0000 (00852) CH4A2S ADD(RW0,MSS)
(00853)
(00854)
A3A 04938 74810026 (00855) CH4A04 SUBH(RW0,RW3,TF)
A3B 0493A 768A0002 (00856) ADD(RW0,2,TF)
A3C 0493C 78810026 (00857) SUBH(RW0,RW3,TF)
A3D 0493E 7A820002 (00858) SUBH(RW0,2,TF)
A3E 04940 7C810026 (00859) SUBH(RW0,RW3,TF)
A3F 04942 7E8A0002 (00860) ADD(RW0,2,TF)
A40 04944 80810026 (00861) SUBH(RW0,RW3,TF)
A41 04946 82820002 (00862) SUBH(RW0,2,TF)
(00863) ?TEST FOR NEW COSINES, ELSE OUTPUT NEXT 4 POINTS
(00864) ?
A42 04948 84113A66 (00865) SUBH(RW1,RW3),JUMPP(CR4A04)
(00866) ?TEST STAGE DONE, ELSE GET NEW COSINES
(00867) ?
A43 0494A 862038AA (00868) JUMPC(CR4A03),AF2),CLEAR
(00869) ?
(00870) ?STOPPING SHORT CHECK IS HERE
(00871) ?
A44 0494C 88500000 (00872) CH4A13S LOAD(RW1, MSS)
A45 0494E 8A113266 (00873) SUBH(RW1, RW3), JUMPP(CR4A02)
(FFT SIZE-1 ROUND)
    
```

GENERATE OUTPUT ADDRESSES FOR EVEN-ODD SEPARATE

```

(00073) ;
(00075) ;
(00076) ;
(00077) ; W BUFFER CONTAINS FFT OF X*JZ
(00078) ; TWO REAL FFT'S ARE EXTRACTED AS FOLLOWS
(00079) ; X(K)=A*(w(K)+w'(N-K))
(00080) ; Z(K)=A*(w(K)-w'(N-K))
(00081) ; WHERE, w=CONJUGATE OF w
(00082) ;
(00083) ; FX(0)=PW(0)
(00084) ; IX(0)=RW(N/2)
(00085) ; RZ(0)=IW(0)
(00086) ; IZ(0)=IW(N/2)
(00087) ;
(00088) ; X(K) IS STORED IN Y(K)
(00089) ; Z(K) IS STORED IN Y(K+N/2)
(00090) ;
(00091) ; RX(K)=W(K)+W(N-K), IX(K)=IW(K)-IW(N-K)
(00092) ; RZ(K)=W(K)-W(N-K), IZ(K)=IW(K)+IW(N-K)
(00093) ;
(00094) ; THE OUTPUT ADDRESS SEQUENCE IS:
(00095) ; RX(0), RZ(0), IX(0), IZ(0),
(00096) ; RX(K), RZ(K), IX(K), IZ(K)
(00097) ;
(00098) ;
(00099) FDS01 LOAD(RW0, MSS, TF)
(00100) MOVH(RW1, RW0)
(00101) FDS02 ADD(RW1, MSS, TF)
(00102) FDS03 LOAD(RW2, MSS)
(00103) FDS04 ADD(RW0, 2, TF)
(00104) SUMH(RW2, 1), JUMPP(FDS04)
(00105) JUMP(CN4A15, RW), CLEAR
(00106)
(00107) ;
(00108) ;

```

```

<RX(0) (YBASE ROUND)
<RZ(0) (2N ROUND)
[USIZE-2 ROUND]
<X(K)
<Z(K)
HAUT APS OUTPUT

```

```

(00009) ; SUCCESSIVE RADIX-4 STAGES (INPUT - P0)
(00010)
A49 04960 9C300037 (00011) CR4A1 SET(WI)
(00012) ; STAGE INITIALIZATION
(00013) ;
A49 04962 9F600000 (00014) CR4A4 LOAD(RR2,0) SFT COSINE VALUE=0
A50 04964 A0300015 (00015) MOVW(RR3,RR2,C)
(00016) ;
(00017) ; ANGLE SUBROUTINE, P1, INSERTS COSINE SEPARATION
(00018) ; FOR STAGE INTO RR4
(00019)
(00020)
A51 04966 A2215071 (00021) SURL(RR2,1),JUMP(CR4A3) HW2=80F COSINES-1
(00022)
A52 04968 A4890037 (00023) CR4A7 SURL(RR0,2,TF) AR
(00024) ; COSINE ENTRY [CHASE ROUND]
(00025) ; RR2=NEXT ANGLE
(00026)
A53 0496A A6500000 (00027) CR4A8 LOAD(RR1,MSS,L) COSX
A54 0496C A829002F (00028) ADDR(RR2,RR1) SINX [HPT RC MD]
(00029) ADDR(RR1,RR2,TF) SIN2X
(00030) AC4A0000 (00031) CR4A5S ADD(RR1,MSS,TF) COS2X [HPT ROUND]
A57 04972 AF99002D (00032) CR4A6S SUB(RR1,MSS,TF) SIN3X [HPT ROUND]
A58 04974 A0270000 (00033) CR4A7S ADD(RR1,MSS,TF) SET RR0 TO 4N+COS# [4N+4 ROUND]
A59 04976 A299002D (00034) CR4A8S ADD(RR0,MSS) SET COSINE USE COUNTER [USIZE-1 ROUND]
A5A 04978 A49A0000 (00035) CR4A3 LOAD(RR1,MSS)
A5B 0497A A6DA0000 (00036) CR4A8S LOAD(RR1,MSS)
A5C 0497C A8500000 (00037)
(00038) ; CR4A-AFS BUTTERFLY INPUT
(00039) ;
(00040)
A5D 0497E AAM90026 (00041) CR4A4 SUBR(RR0,RR3,TF) DR
A5E 04980 AC89003A (00042) ADDL(RR0,2,TF) DI
A5F 04982 AF890026 (00043) SUBR(RR0,RR3,TF) AI
A60 04984 C0890032 (00044) SURL(RR0,2,TF) RR
A61 04986 C2890026 (00045) SUBR(RR0,RR3,TF) CR
A62 04988 C489003A (00046) ADDL(RR0,2,TF) CI
A63 0498A C6890026 (00047) SUBR(RR0,RR3,TF) AI
(00048)
(00049) ; TEST IF NEW COSINE NEEDED, ELSE INPUT NEXT 4 POINTS
(00050) ;
A64 0498C C81976A6 (00051) SURL(RR1,RR1),JUMP(CR4A5)
A65 0498E CA300028 (00052) SET(AE0)
    
```

TELL APU COSINES COMING

PAGE 32: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2R MAY 7, 1980  
SUCCESSIVE RADIX-4 STAGES (INPUT - P0)

```
(00954) ; TEST IF STAGE DONE, ELSE GET NEW COSINES  
(00954)  
AN6 04990 00215204 (00955)      SUBR(CW2,4),JUMPP(CR4A2)  
AN7 04992 00400029 (00956)      SET(AP1)          TELL APU STAGE DONE  
(00957) ;***** CHANGE VALUE HERE TO STOP SHORT *****  
(00958) ; TO STOP SHORT 1 STAGE, KIND USIZE/4-1 HERE  
AN8 04994 00500000 (00959) CR4A9S  LOAD(BR1,MSS) [FFTSIZE-1 BOUND]  
AN9 04996 02800032 (00960)      SUBR(CR0,2,FF)    LAST DATA POINT  
(00961)  
(00962) ; TEST IF FFT DONE, ELSE START NEXT STAGE  
(00963)  
ANA 04998 03194FA6 (00964)      SUBR(CR1,RW3),JUMPP(CR4A1)  GO BACK  
(00965) ;
```

GENERATE INPUT ADDRESSES FOR EVEN-ODD SEPARATE

```

(00966) ?
(00967) ?
(00968) ?
(00969) ? INPUT SEQUENCE:
(00970) ? SCALAR A,
(00971) ? PW(N/2), TW(N/2), FW(0), TW(0),
(00972) ? TW(N-K), PW(K), HW(N-K), HW(K)
(00973) ?
(00974) ?
(00975) ?
(00976) ?
(00977) ?
(00978) ?
(00979) ?
(00980) ?
(00981) ?
(00982) ?
(00983) ?
(00984) ?
(00985) ?
(00986) ?
(00987) ?
(00988) ?
(00989) ?
(00990) ?
(00991) ?
(00992) ?
(00993) ?
(00994) ?
(00995) ?
(00996) ?
(00997) ?
(00998) ?
(00999) ?
    
```

```

SET(AE1)
LOAD(HR0, MSS(1), TF)
LOAD(HR0, MSS)
MOVH(HR1, HR0)
ADD(HR0, MSS, TF)
ADD(HR0, 2, TF)
MOVH(HR0, HR1, TF)
ADD(HR1, MSS)
ADD(HR0, 2, TF)
SUB(HR1, 2, TF)
JUMP(FUS5)
    
```

```

SIGNAL, APH TO DD EDS
<SA (SCALAR A ROUND)
RW(0) (WRASE, ROUND)
<RW(N/2) (2N ROUND)
<TW(N/2)
<RW(0)
RW(N) (AN ROUND)
<WK)
<W(N-K)
    
```

AR

```

SUMH(HR0, 2, TF)
JUMP(CR4A4)
    
```

```

(00990) * ANGULAR SEPARATION SUBROUTINE (INPUT = P1)
(00991) * STANG=PI/SSI
(00992) * SZANG=SIANGZ4
(00993) * S1ANG=S1ANGZ4
(00994) * S4ANG=S4ANGZ4
(00995) * S5ANG=S5ANGZ4
(00996) * S6ANG=S6ANGZ4
(00997) * STANG=S6ANGZ4
(00998)
(00999)
A7M 049H3 F03F0000 ANGLE SET(PI)
A7N 049H6 F23F0000 ANGLE1S ADD(HR3,MSS,C)
A7A 049H8 F43F0000 ANGLE2S ADD(HR3,MSS,C)
A7R 049HA F63F0000 ANGLE3S ADD(HR3,MSS,C)
A7C 049HC F83F0000 ANGLE4S ADD(HR3,MSS,C)
A7D 049HE FA3F0000 ANGLE5S ADD(HR3,MSS,C)
A7E 049HU FC3F0000 ANGLE6S ADD(HR3,MSS,C)
A7F 049C7 FE3F0000 ANGLE7S ADD(HR3,MSS,C)
(01000)
(01001) *
(01002) * CRANS4=IC
(01003) * PND
(01004) *
(01005) *
(01006) *
(01007) *
(01008) *
(01009) *
(01010) *
(01011) *
(01012) *
(01013) * CSMS1 01=PI*2*0
(01014) *
(01015) * CRANS7=PI-CSMS
(01016) *
(01017) * OFFINE TOP OF MODULE
(01018) *
(000040C4 (01019) TOPSCUR = #1.
(01020) *

```

(S1ANG ROUND)  
 (SZANG ROUND)  
 (S3ANG ROUND)  
 (S4ANG ROUND)  
 (S5ANG ROUND)  
 (S6ANG ROUND)  
 (STANG ROUND)

ASSIGN VALUE TO CHAIN  
 #A-1 END OF MODULE



PAGE 35: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RB MAY 7, 1980  
SYMMOL, TABLE

049C4 (01021) SYMMOL, TABLE  
(01022) END

11

AFD1S:	0007H (00046)	(00047)	(000R2)	(000H7)
AFDTSURG:	0006H (00047)	(00335)	(00339)	
ANGLE1S:	00079 (0025H)	(01001)		
ANGLE2S:	0007A (00261)	(01003)		
ANGLE3S:	0007H (00263)	(01005)		
ANGLE4S:	0007C (00267)	(01004)		
ANGLE5S:	0007D (00270)	(01005)		
ANGLE6S:	0007E (00273)	(01006)		
ANGLE7S:	0007F (00276)	(01007)		
ANGLE8:	0007H (00278)	(01000)		
APSHNR00:	00F63 (00064)	(00356)		
APSH1:	0024D (00061)	(00243)		
ACTSAD:	00604 (00054)	(00112)	(00274)	
ACTSAT:	00606 (00055)	(00107)		
HCTSHA:	00582 (00053)	(00219)	(00290)	(00316)
CR4S2:	00080 (00378)	(00650)		
CR4A4:	00000 (00670)	(01010)		
CR4A5:	00100 (00669)	(01015)		
CR4A1:	0004F (00911)	(00464)		
CP4A1S:	00034 (00300)	(00846)	(00906)	
CP4A13S:	00040 (00117)	(00872)		
CH4A2:	00052 (00923)	(00455)		
CR4A2S:	00039 (00200)	(00852)		
CH4A3:	0005H (00201)	(00921)	(00935)	
CH4A4:	0005D (00943)	(00988)		
CH4A4S:	00053 (00220)	(00926)		
CH4A5:	00076 (00951)	(00987)		
CR4A5S:	00056 (00232)	(00930)		
CR4A6S:	0005H (00233)	(00932)		
CH4A7S:	0005A (00234)	(00934)		
CR4A8S:	0005C (00115)	(00936)		
CH4A9S:	0006H (00116)	(00959)		
CR4A9:	0004F (00725)	(00914)		
CR4A01:	00031 (00246)	(00250)	(00439)	
CR4A02:	00032 (00844)	(00873)		
CR4A03:	0003H (00113)	(00851)	(0086H)	
CP4A04:	0003A (00855)	(00865)		
CR4FF2:	00040 (00544)	(005H3)		
CR4FF:	00039 (00534)	(00564)		
CR4FFS:	00054 (00530)	(00575)		
CR4FSA:	0002H (00416)	(00508)		
CSMS:	04RC4 (00084)	(00089)	(00113)	(00114)
			(00115)	(00116)
			(00117)	(00120)
			(00160)	(00161)
			(00164)	(00165)
			(00168)	(00171)
			(00174)	(00177)

CSW51:	030C3	(00180)	(00183)	(00190)	(00191)	(00194)	(00200)	(00201)	(00204)	(00211)	(00220)
CSW52:	00000	(00232)	(00233)	(00234)	(00246)	(00250)	(00258)	(00261)	(00264)	(00267)	(00270)
CSW53:	0478C	(00273)	(00276)	(00291)	(00296)	(00300)	(00317)	(00329)	(00361)	(00362)	(00363)
CSW255A:	00000	(00364)	(00365)	(00667)	(00703)	(01015)					
CSW255B:	030C3	(00666)	(01013)								
CSW255C:	00000	(00667)	(00705)								
CSW255D:	0478C	(00083)	(00088)	(00195)							
CSW255E:	00000	(00377)	(00398)	(00648)	(00650)						
CSW255F:	00003	(00399)	(00403)								
CSW255G:	00002	(00401)	(00411)								
CSW255H:	00000	(00398)	(00434)								
CSW255I:	00014	(00439)	(00456)								
CSW255J:	00006	(00362)	(00709)	(00714)							
CSW255K:	00004	(00361)	(00711)	(00722)							
CSW255L:	00002	(00114)	(00708)								
CSW255M:	00005	(00151)	(00713)								
CSW255N:	00008	(00363)	(00716)								
CSW255O:	00009	(00148)	(00718)								
CSW255P:	0000A	(00364)	(00719)								
CSW255Q:	00000	(00365)	(00724)								
CSW255R:	00001	(00317)	(00707)								
CSW255S:	00025	(00705)	(00781)								
CSW255T:	00026	(00291)	(00782)								
CSW255U:	00029	(00783)	(00791)								
CSW255V:	00028	(00204)	(00785)	(00800)							
FUS1:	0004C	(00211)	(00976)								
FUS2:	0004D	(00296)	(00977)								
FUS3:	0006F	(00190)	(00979)								
FUS4:	00072	(00194)	(00982)								
FUS5:	00073	(00983)	(00985)								
FUS01:	00046	(00329)	(00989)								
FUS02:	00048	(00191)	(00901)								
FUS03:	00049	(00120)	(00902)								
FUS04:	0004A	(00903)	(00905)								
FF2MS:	00006	(00079)	(00082)								
FF2MS:	00007	(00080)	(00087)								
FF2MS:	04690	(00085)	(00103)								
FF2SSFT:	00004	(00058)									
FF2SSFT:	00005	(00059)	(00247)	(00251)							
FF2SSFT:	00020	(00057)	(00251)								
FF2SSFT:	04780	(00247)	(00251)	(00351)	(00359)						
GSFLGS:	00001	(00051)	(00052)	(00103)	(00107)	(00113)	(00114)	(00115)	(00116)	(00117)	(00120)
MS:		(00148)	(00151)	(00158)	(00159)	(00160)	(00161)	(00164)	(00165)	(00168)	(00171)
		(00174)	(00177)	(00180)	(00183)	(00190)	(00191)	(00194)	(00200)	(00201)	(00204)

HUSPN:	0464C	(00132)	(00139)	(00208)	(00211)	(00212)	(00232)	(00233)	(00234)	(00246)	(00247)	(00250)	(00251)
HUSLP:	0464D	(00130)	(00137)	(00258)	(00261)	(00264)	(00267)	(00270)	(00273)	(00276)	(00283)	(00312)	(00324)
HUSTHL:	037H2	(00129)	(00361)	(00351)									
MSS:	00000	(00056)	(00707)	(00704)	(00711)	(00713)	(00714)	(00716)	(00718)	(00719)	(00719)	(00724)	
		(00750)	(00751)	(00752)	(00753)	(00754)	(00755)	(00756)	(00759)	(00763)	(00763)	(00768)	
		(00772)	(00775)	(00782)	(00785)	(00839)	(00846)	(00851)	(00852)	(00872)	(00872)	(00899)	
		(00901)	(00902)	(00926)	(00930)	(00932)	(00934)	(00935)	(00936)	(00959)	(00959)	(00976)	
		(00977)	(00979)	(00982)	(01001)	(01002)	(01003)	(01004)	(01005)	(01006)	(01006)	(01007)	
MASKLWY:	0FF00	(00049)	(00104)	(00213)	(00284)	(00313)	(00325)						
MASKHWY:	000FF	(00050)	(00209)	(00368)									
PAR2S:	047H0	(00108)	(00244)										
SHMS1:	04735	(00245)	(00250)										
SHMS2:	0473A	(00249)	(00254)										
SHMSFE:	04754	(00900)	(00282)										
SCM:	0000F	(00748)	(00781)										
SCM0:	00010	(00158)	(00750)	(00760)	(00764)	(00769)	(00773)	(00776)					
SCM1S:	00011	(00164)	(00751)										
SCM10S:	00021	(00180)	(00772)										
SCM2S:	00012	(00159)	(00752)										
SCM3S:	00013	(00168)	(00753)										
SCM4:	0001A	(00754)	(00762)										
SCM4S:	00014	(00160)	(00754)										
SCM5:	0001D	(00762)	(00767)										
SCM5S:	00015	(00165)	(00755)										
SCM6:	00020	(00767)	(00771)										
SCM6S:	00016	(00161)	(00756)										
SCM7:	00023	(00183)	(00771)										
SCM7S:	00018	(00171)	(00759)	(00775)									
SCM8S:	0001H	(00174)	(00763)										
SCM9S:	0001F	(00177)	(00768)										
START:	04640	(00073)	(00093)										
SVTS:	00382	(00060)	(00210)										
SYSSFGS:	1F4CF	(00063)											
TOP SCUR:	049C4	(00068)	(01019)										
TOP SPTM:	002H8	(00048)	(00066)										
WS:	00002	(00052)	(00082)	(00087)	(00113)	(00114)	(00115)	(00116)	(00117)	(00120)	(00120)	(00148)	
		(00151)	(00158)	(00159)	(00160)	(00161)	(00164)	(00165)	(00168)	(00171)	(00171)	(00174)	
		(00177)	(00180)	(00183)	(00190)	(00191)	(00194)	(00200)	(00201)	(00204)	(00204)	(00211)	
		(00220)	(00232)	(00233)	(00234)	(00246)	(00250)	(00258)	(00261)	(00264)	(00264)	(00267)	
		(00270)	(00273)	(00276)	(00291)	(00296)	(00300)	(00317)	(00329)	(00337)	(00337)	(00338)	
		(00339)	(00344)	(00361)	(00362)	(00363)	(00364)	(00365)					

PAGE 39: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980  
SYMBOL TABLE

ZFNU: 007NA (00062)

LINES WITH ERRORS: 0 (MAP VERSION R0010).10) 1- 0

APSDONE CP PROCESS INTERRUPT HANDLER MAY 29, 1980

T A B L E O F C O N T E N T S

DEFINING NECESSARY SYMBOLS FROM SNAP-II EXECUTIVE

APS0004 AP PROCESS INTERRUPT HANDLER MAY 2R, 1980

(00001) \*  
(00002) \*  
(00003) \*  
(00004) \*  
(00005) \*  
(00006) \*  
(00007) \*  
(00008) \*  
(00009) \*  
(00010) \*  
(00011) \*  
(00012) \*  
(00013) \*  
(00014) \*  
(00015) \*  
(00016) \*

THE AP POPP INTERRUPT ROUTINE HAS BEEN MODIFIED  
FOR GTP SYLVANIA TO SPEED UP PROCESSING.  
IN ADDITION, SPECIAL CODE HAS BEEN ADDED TO CONVERSE W/ TH  
ATC DISPATCH PROGRAM "CSPIER.TXT".

THE LIMITATIONS ARE DESCRIBED IN THE DOCUMENTATION.

000002F6 (00013) BL = S02F6 SFT PC TO CSW FOR AP DONE INTERRUPT

002F6 6F02 DATA APS0004 SFT CSW TO NEW START ADDRESS

DEFINE NECESSARY SYMBOLS FROM SNAP-II EXECUTIVE  
 RELEASE 3.05

```

(00017) *
(00018) *
(00019) *
00001390 (00020)
00000245 (00021)
00000244 (00022)
00000006 (00023)
00000008 (00024)
00000000 (00025)
01000244 (00026)
00000010 (00027)
00000004 (00028)
00011100 (00029)
00000000 (00030)
00011100 (00031)
00011100 (00032)
00011100 (00033)
00000003 (00034)
00000002 (00035)
(00036) *
(00037) *
00000011 (00038)
00000020 (00039)
(00040) *
00000001 (00041)
00000502 (00042)
0000057E (00043)
00000540 (00044)
00000020 (00045)
0000003E (00046)
0000003E (00047)
00000001 (00048)
00000002 (00049)
(00050) *
00005000 (00051)
(00052) *
00000F02 (00053)
00001C12 (00054)
00011100 (00055)
(00056) *
00000002 (00057)
(00058) *
(00059) *
(00060) *
  
```

```

ANOMTS = $1300
AP$ASSS = $245
AP$CSU = $244
AP$CSPU = $A
AP$FCH = $H
AP$GT = $D
AP$PAP = $244
AP$PHF = $A
AP$SHPD = $4
AP$SIA = $1FFC0
AP$SIZE = $H
AP$SRWD = $0
AP$SIA = $1FFCA
AP$SPC = $1FFCH
AP$SIZE = $4
PUSSTRT = $2
  
```

```

FLGRT = $11
FLGSSET = $20
  
```

```

NS = 1
ISVTS=$507
ISAS125=ISVTS+D'125'
ISAS126=ISVTS+D'126'
IDV$32 = $20
IDV$62 = $3F
IDV$63 = $3F
IDV$1 = $1
IDV$2 = $2
  
```

```

OFFSET = $5000
  
```

```

SCHEMTRY = $E02
SFT$SNAH = $1C12
SYS$FLGS = $1FFCF
  
```

```

NS = $2
  
```

```

EJECT
  
```



```

(00061) *
00000R2 (00062) BL = S0F02      SFT PC TO OLD APSDNE + HS (FVEN)
(00063) *
(00064) *
(00065) * THIS MODULE IS ENTERED WHENEVER RA MARKS THE
(00066) * TRANSITION FROM ON TO OFF OF THE EXECUTIVE HAS
(00067) * GENERATED THE INTERRUPT BECAUSE THE AP IS IOLP AND
(00068) * A TASK IS PENDING
(00069) *
(00070) * THE MODULE RELEASES THE CURRENT ACTIVE TASK
(00071) * AND ACTIVATES ANY PENDING TASK.
(00072) *
(00073) * THE CONTENTS OF THE AP-PROCESSOR CONTROL BLOCK
(00074) * ARE USED TO CONTROL THE FUNCTIONS OF THIS MODULE
(00075) *
(00076) * THE MODULE ASSUMES THAT REGISTERS R1-R7 ARE
(00077) * PRESERVED BY THE INTERRUPT.
(00078) *
000R2 C060024A (00079) APSDNE  R6,APSCSL  GET POINTERS TO SUPPORT LISTS
(00080) **      (00081) **      R6, SFFF   MASK ADDRESS FOR SIGN EXTENSION
(00082) *      (00083) **      R7, SFFF   IF ABOVE 16X HALFWORDS
(00084) *      (00085) **      R6       JUMP IF NULL
(00086) *
000R4 0260     (00087) **      APSDNE10,E0Z
000R5 80100FC0 (00088) *
(00089) *
000R7 0800     (00090) *      EVEN
000R8 F01C000R (00091) **      MOVHR  R1, APSFCR(R6)  GET CURRENT AP-FCH POINTER
(00092) *      (00093) **      DFCM   APSASS  IF SPECIAL SUPPORT SEMAPHORE NON-ZERO
(00094) *      (00095) **      JMP    WAPSCSP(R6), GEZ  GOTO DESIGNATED MODULE
(00096) *
000R8 12F003F (00097) *      AINT  IDEV61, ILVLS2  FLAG AP DONE TO WAITING PROGRAM
(00098) **      (00099) **      MOVHR  R4,R1,NOT,MSKSATO  GET POINTER TO FCH JUST PROCESSED
(00099) **      (00100) **      MOVHR  R5, HSCR4)     AND FCH NUMBER WITH CONTROL BITS
(00101) **      (00102) **      MOVHR  R4, APSFCR   SAVE LAST FCH PROCESSED
(00102) **      (00103) **      MOVHR  R3, APSRFFCR6) IF FERM. ROUND FUNCTION
(00103) **      (00104) **      TORHR  R3, FLSGNMAT  OR BUFFER TESTING INHIBITED
(00104) **      (00105) **      JMP    APSDNE05, NEZ  BYPASS BUFFER IN USE CLEARUP
(00106) *
(00107) *      (00108) **      MOVHR  R3, FCHSMMLR-1  ELSE RESET BUFFER IN-USE FLAGS
(00108) **      (00109) **      MOVHR  R5,NOT,MSKSCURH  POINT TO LAST POSSIBLE LOGICAL BUFFER ID
(00109) **      (00110) **      AOUTP  R4, WSCR3)

```

```

(00105) **
(00106) **R22
(00107) **
(00108) **
(00109) **
(00110) **
(00111) *
(00112) APSDNEF05
(00113)
(00114) *
(00115) *
(00116) *
(00117) * PROCESS PENDING TASK
(00118) *
(00119) APSDNEF10
(00120)
(00121)
(00122) *
(00123)
(00124)
(00125)
(00126) **
(00127) **
(00128) **
(00129) **
(00130) **
(00131) **
(00132)
(00133)
(00134)
(00135) **
(00136) **
(00137) **
(00138) **
(00139) **
(00140) **
(00141) **
(00142) *
(00143) APSDNEF40
(00144)
(00145)
(00146) *
(00147) *R2
(00148) *R2
  
```

```

MOVW R2,R4,MSKSLRAT
LMS R2,7
DECR R4,1
ANDR R5,RCTSAT(R2)
JMP R3,R22
  
```

```

IF FCR NOT IN FUNCTION LIST
  BYPASS RELEASE OF THE FCR
  RELEASE THE FCR
  
```

```
IF PENDING FCR NULL.
```

```
GO SET AP TO IDLE STATE
SET PENDING LIST TO NULL
  
```

```
NOW LOAD APU
```

```
SET NEW CURRENT APU MODULE ORIGIN
```

```
UPDATE CURRENT APU MEMORY ORIGIN
SET MODULE START IN AP
  
```

```
ASSUME APU STARTS AT 0
GET MODULE SIZE
  
```

```
DETERMINE PROCESSOR TYPE
  
```

```
MAP 100, 200
```

```
MAP 300
```

```

MOVW R3,APSRMO
MOVW R2,APUSSTRT(R6)
MOVW R2,APUSPC
MOVW R2,APUSIZE(R6)
DECR R2,HS
SRSTL R2,R2,SYSSFLGS
JMP APSDNEF40
  
```

```

DECR R3,HS
EVEN
IPROC
JMP R3,R2,APUSLA
  
```

```

DECR R3,MS
EVEN
IPROC
JMP R3,R2,APUSLA
  
```

```

CCS 2
SCS 3
  
```



```

(001943) 00021 MOVW R2,R4,MSKSLMT
(001944) 00022 R2,7
(001945) 00023 R2,7
(001946) 00024 R1,MSKSPDRU
(001947) 00025 R1,RC1SAT(R2)
(001948) 00026 R1,1
(001949) 00027 R1,1
(002000) 00028 ANDRM R5,RC1SAT(R2)
(002001) 00029 R1,RC1SAT(R2)
(002002) 00030 R2,1
(002003) 00031 R2,1
(002004) 00032 R3,021
(002005) 00033 *
(002006) 00034 APSDUMF15 MOVW R6,APCSI
(002007) 00035 MOVW R5,1
(002008) 00036 MOVW R5,APSPAF
(002009) 00037 WAITC DEFVS32,LEVEL1
(002010) *****
(002011) * ADD INTERFACE CODE FOR "CSPUFK"
(002012) MOVW R5,ISAS126
(002013) INCM ISAS125
(002014) ;
(002015) *****
(002016) 00038 RPT
(002017) 00039 EVEN
(002018) 00040 JMP APSDUMF
(002019) ;
(002020) *
(002021) *
(002022) * PATH FOR MULT. PENDING FCP
(002023) *
(002024) *
(002025) 00041 APSDUMF20 MOVW APSCSI
(002026) 00042 MOVW APSPAF
(002027) 00043 WAITC DEFVS32,LEVEL1
(002028) 00044 AINTC DEFVS62,LEVEL1
(002029) *****
(002030) * ADD INTERFACE CODE FOR "CSPUFK"
(002031) MOVW R5,ISAS126
(002032) 00045 INCM ISAS125
(002033) ;
(002034) 00046 RPT
(002035) 00047 EVEN
(002036) 00048 *
  
```

UPDATE SUPPORT LIST POINTERS  
 FLAG AP BUSY

WAIT TILL FCP RELEASE DONE  
 \*\*\*\*\*

SET "APDUMF" SCALAR  
 FCP CALLING CARD. = 0 \* T  
 OP OF MPWHL,  
 \*\*\*\*\*  
 RETURN,BACK TO CSPUFK?

RESTART ON NEXT INTERRUPT  
 T

SET CURRENT FCP NULL.  
 SET PROCESSOR AVAILABLE  
 WAIT TILL FCP RELEASE DONE  
 TELL WAIT ROUTINE AP IS IDLE  
 \*\*\*\*\*

SET "APDUMF" SCALAR  
 FCP CALLING CARD. = 0 \* T  
 OP OF MPWHL,  
 \*\*\*\*\*  
 RETURN,BACK TO CSPUFK?

4. SUCCESS INTERDUPT HANDLER MAY 28, 1980  
BY SORRIS FROM SWAP-11 EXECUTIVE

111 JMD ABSDONE  
003 ?  
111 FMD

RESTART ON NEXT INTERDUPT  
T

ADHPTS: 01390 (00020)  
 APSASSS: 00245 (00021)  
 APSCSI: 0024A (00022) (00079) (00206) (00225)  
 APSCSPI: 00006 (00023)  
 APSDMFO: 00F4C (00112)  
 APSDMFO: 00FC0 (00083) (00113) (00119)  
 APSDMFN: 00F40 (00206)  
 APSDMF0: 00F40 (00121) (00225)  
 APSDMFO: 00F00 (00149)  
 APSDMFA: 00FC0 (00148)  
 APSDMFA: 00F42 (00015) (00079) (00214) (00247)  
 APSDMFA: 00F4A (00094)  
 APSFC: 00006 (00024) (00086)  
 APSG1: 00000 (00025) (00165)  
 APSGAF: 00244 (00026) (00208) (00226)  
 APSMFA: 00010 (00027)  
 APSMFA: 00004 (00028) (00146)  
 APSLA: 1FEC0 (00029) (00157)  
 APSST7F: 00008 (00030) (00154)  
 APSMFA: 00000 (00031) (00125)  
 APSLA: 1FCA (00032) (00145)  
 APSMFA: 1FEC0 (00033) (00132)  
 APSST7F: 00003 (00034) (00133)  
 APSST7F: 00002 (00035)  
 FICSP1: 00011 (00038) (00173)  
 FICSSPT: 00020 (00039) (00173)  
 MS: 00001 (00041) (00134) (00156) (00212) (00231)  
 IPVS32: 00020 (00045) (00115) (00209) (00227)  
 IPVS62: 00036 (00046) (00228)  
 IPVS68: 0003F (00047) (00094)  
 ILS1: 00001 (00048) (00115) (00209) (00227) (00228)  
 ILS2: 00002 (00049) (00094)  
 ILS125: 0057F (00043) (00213) (00232)  
 ILS126: 00580 (00044) (00212) (00231)  
 ILS1: 00502 (00042) (00043) (00044)  
 ILSFT: 05000 (00051)  
 SCDSMFA: 00F02 (00053)  
 SFTSMFA: 01C12 (00054)  
 SVSFLGS: 1FEC0 (00055) (00166) (00173)  
 MS: 00002 (00057) (00143) (00156)













09074 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09075 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09076 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09077 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09078 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09079 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09080 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09081 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09082 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09083 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09084 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09085 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09086 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09087 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09088 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09089 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09090 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09091 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09092 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09093 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09094 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09095 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09096 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09097 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09098 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09099 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S
09100 00000000	000000	ADD	PHILOS,PHITIS,PHIT4S,PHIT4S,PHIT4S,PHIT4S,PHIT4S

SET TO INDICATE FIRST RC  
VR FRAMF AFTER SYNC

09C06 0007			
09C06 0002			
09C06 0005			
09C06 0008			
09C06 0006			
09C06 0001			
09C06 0002			
09C06 0003			
09C06 0004			
09C06 0005			
09C06 0006			
09C06 0007			
09C06 0008			
09C06 0009			
09C06 0010			
09C06 0011			
09C06 0012			
09C06 0013			
09C06 0014			
09C06 0015			
09C06 0016			
09C06 0017			
09C06 0018			
09C06 0019			
09C06 0020			
09C06 0021			
09C06 0022			
09C06 0023			
09C06 0024			
09C06 0025			
09C06 0026			
09C06 0027			
09C06 0028			
09C06 0029			
09C06 0030			
09C06 0031			
09C06 0032			
09C06 0033			
09C06 0034			
09C06 0035			
09C06 0036			
09C06 0037			
09C06 0038			
09C06 0039			
09C06 0040			
09C06 0041			
09C06 0042			
09C06 0043			
09C06 0044			
09C06 0045			
09C06 0046			
09C06 0047			
09C06 0048			
09C06 0049			
09C06 0050			
09C06 0051			
09C06 0052			
09C06 0053			
09C06 0054			
09C06 0055			
09C06 0056			
09C06 0057			
09C06 0058			
09C06 0059			
09C06 0060			
09C06 0061			
09C06 0062			
09C06 0063			
09C06 0064			
09C06 0065			
09C06 0066			
09C06 0067			
09C06 0068			
09C06 0069			
09C06 0070			
09C06 0071			
09C06 0072			
09C06 0073			
09C06 0074			
09C06 0075			
09C06 0076			
09C06 0077			
09C06 0078			
09C06 0079			
09C06 0080			
09C06 0081			
09C06 0082			
09C06 0083			
09C06 0084			
09C06 0085			
09C06 0086			
09C06 0087			
09C06 0088			
09C06 0089			
09C06 0090			
09C06 0091			
09C06 0092			
09C06 0093			
09C06 0094			
09C06 0095			
09C06 0096			
09C06 0097			
09C06 0098			
09C06 0099			
09C06 0100			

ENCODER TABLE FOR A (63)

ENR CODE  
IA DELETED ORDER

PRICE #2 P800, C50SP, 3A1

090C6 1308	(00243)	DATA 0'52561',0'377
090C7 0007		
090C8 2911	(00244)	DATA 0'110513',0'561
090C9 0008		
090CA 5223	(00245)	DATA 0'21077',0'341
090CB 0001		
090CC 5814	(00246)	DATA 0'21531',0'371
090CD 0007		
090CE 5863	(00247)	DATA 0'22628',0'311
090CF 0001		
090CG 4091	(00248)	DATA 0'16529',0'551
090CH 0005		
090CI 2178	(00249)	DATA 0'29050',0'551
090CJ 0005		
090CK 128C	(00250)	DATA 0'31780',0'551
090CL 0005		
090CM 2559	(00251)	DATA 0'59561',0'321
090CN 0002		
090CO 4402	(00252)	DATA 0'119122',0'341
090CP 0003		
090CQ 653C	(00253)	DATA 0'25916',0'371
090CR 0007		
090CS 3420	(00254)	DATA 0'14880',0'311
090CT 0001		
090CU 1340	(00255)	DATA 0'29760',0'321
090CV 0002		
090CW 1409	(00256)	DATA 0'6361',0'331
090CX 0001		
090CY 3162	(00257)	DATA 0'12722',0'361
090CZ 0006		
090D0 636S	(00258)	DATA 0'25345',0'341
090D1 0004		
090D2 4892	(00259)	DATA 0'13970',0'371
090D3 0007		
090D4 6025	(00260)	DATA 0'27941',0'361
090D5 0006		
090D6 2417	(00261)	DATA 0'31070',0'341
090D7 0004		
090D8 5424	(00262)	DATA 0'21540',0'361
090D9 0006		
090DA 5410	(00263)	DATA 0'22544',0'341
090DB 0001		
090DC 4079	(00264)	DATA 0'16505',0'311
090DD 0001		

09000 0000	(002000)	DATA 012000000,0150
09001 0000	(002001)	DATA 013000000,0150
09002 0000	(002002)	DATA 014000000,0150
09003 0000	(002003)	DATA 015000000,0150
09004 0000	(002004)	DATA 016000000,0150
09005 0000	(002005)	DATA 017000000,0150
09006 0000	(002006)	DATA 018000000,0150
09007 0000	(002007)	DATA 019000000,0150
09008 0000	(002008)	DATA 020000000,0150
09009 0000	(002009)	DATA 021000000,0150
09010 0000	(002010)	DATA 022000000,0150
09011 0000	(002011)	DATA 023000000,0150
09012 0000	(002012)	DATA 024000000,0150
09013 0000	(002013)	DATA 025000000,0150
09014 0000	(002014)	DATA 026000000,0150
09015 0000	(002015)	DATA 027000000,0150
09016 0000	(002016)	DATA 028000000,0150
09017 0000	(002017)	DATA 029000000,0150
09018 0000	(002018)	DATA 030000000,0150
09019 0000	(002019)	DATA 031000000,0150
09020 0000	(002020)	DATA 032000000,0150
09021 0000	(002021)	DATA 033000000,0150
09022 0000	(002022)	DATA 034000000,0150
09023 0000	(002023)	DATA 035000000,0150
09024 0000	(002024)	DATA 036000000,0150
09025 0000	(002025)	DATA 037000000,0150
09026 0000	(002026)	DATA 038000000,0150
09027 0000	(002027)	DATA 039000000,0150
09028 0000	(002028)	DATA 040000000,0150
09029 0000	(002029)	DATA 041000000,0150
09030 0000	(002030)	DATA 042000000,0150
09031 0000	(002031)	DATA 043000000,0150
09032 0000	(002032)	DATA 044000000,0150
09033 0000	(002033)	DATA 045000000,0150
09034 0000	(002034)	DATA 046000000,0150
09035 0000	(002035)	DATA 047000000,0150
09036 0000	(002036)	DATA 048000000,0150
09037 0000	(002037)	DATA 049000000,0150
09038 0000	(002038)	DATA 050000000,0150
09039 0000	(002039)	DATA 051000000,0150
09040 0000	(002040)	DATA 052000000,0150
09041 0000	(002041)	DATA 053000000,0150
09042 0000	(002042)	DATA 054000000,0150
09043 0000	(002043)	DATA 055000000,0150
09044 0000	(002044)	DATA 056000000,0150
09045 0000	(002045)	DATA 057000000,0150
09046 0000	(002046)	DATA 058000000,0150
09047 0000	(002047)	DATA 059000000,0150
09048 0000	(002048)	DATA 060000000,0150
09049 0000	(002049)	DATA 061000000,0150
09050 0000	(002050)	DATA 062000000,0150
09051 0000	(002051)	DATA 063000000,0150
09052 0000	(002052)	DATA 064000000,0150
09053 0000	(002053)	DATA 065000000,0150
09054 0000	(002054)	DATA 066000000,0150
09055 0000	(002055)	DATA 067000000,0150
09056 0000	(002056)	DATA 068000000,0150
09057 0000	(002057)	DATA 069000000,0150
09058 0000	(002058)	DATA 070000000,0150
09059 0000	(002059)	DATA 071000000,0150
09060 0000	(002060)	DATA 072000000,0150
09061 0000	(002061)	DATA 073000000,0150
09062 0000	(002062)	DATA 074000000,0150
09063 0000	(002063)	DATA 075000000,0150
09064 0000	(002064)	DATA 076000000,0150
09065 0000	(002065)	DATA 077000000,0150
09066 0000	(002066)	DATA 078000000,0150
09067 0000	(002067)	DATA 079000000,0150
09068 0000	(002068)	DATA 080000000,0150
09069 0000	(002069)	DATA 081000000,0150
09070 0000	(002070)	DATA 082000000,0150
09071 0000	(002071)	DATA 083000000,0150
09072 0000	(002072)	DATA 084000000,0150
09073 0000	(002073)	DATA 085000000,0150
09074 0000	(002074)	DATA 086000000,0150
09075 0000	(002075)	DATA 087000000,0150
09076 0000	(002076)	DATA 088000000,0150
09077 0000	(002077)	DATA 089000000,0150
09078 0000	(002078)	DATA 090000000,0150
09079 0000	(002079)	DATA 091000000,0150
09080 0000	(002080)	DATA 092000000,0150
09081 0000	(002081)	DATA 093000000,0150
09082 0000	(002082)	DATA 094000000,0150
09083 0000	(002083)	DATA 095000000,0150
09084 0000	(002084)	DATA 096000000,0150
09085 0000	(002085)	DATA 097000000,0150
09086 0000	(002086)	DATA 098000000,0150
09087 0000	(002087)	DATA 099000000,0150
09088 0000	(002088)	DATA 100000000,0150
09089 0000	(002089)	DATA 101000000,0150
09090 0000	(002090)	DATA 102000000,0150
09091 0000	(002091)	DATA 103000000,0150
09092 0000	(002092)	DATA 104000000,0150
09093 0000	(002093)	DATA 105000000,0150
09094 0000	(002094)	DATA 106000000,0150
09095 0000	(002095)	DATA 107000000,0150
09096 0000	(002096)	DATA 108000000,0150
09097 0000	(002097)	DATA 109000000,0150
09098 0000	(002098)	DATA 110000000,0150
09099 0000	(002099)	DATA 111000000,0150
09100 0000	(002100)	DATA 112000000,0150

RESIDUE OUT TO FIRST INF  
 ORAMATION BIT  
 POWER SUM TABLE IN RFFER  
 SF ORDER





09034 0005		
09039 0024		
09044 0007		
09048 0024		
09049 0010		
09050 0008		
09054 0004		
09054 0018		
09055 0010		
09051 0030		
09052 0013		
09053 0034		
09054 0011		
09055 0014		
09056 0033		
09057 0004		
09058 0011		
09059 0025		
0905A 0034		
0905B 0048		
0905C 0009		
0905D 0012		
0905E 0023		
0905F 0012		
09060 0016		
09061 0036		
09062 0024		
09063 0036		
09064 0020		
09065 0008		
09066 0034		
09067 0014		
09068 0016		
09069 0019		
0906A 0017		
0906B 0020		
0906C 0006		
0906D 0030		
0906E 0019		
0906F 0028		
09070 0002		
09071 0036		
09072 0017		
09073 0003		
	DATA 0171,0140,0116	
	DATA 0113,0145,0124	
	DATA 0120,0153,0120	
	DATA 0156,0117,0130	
	DATA 0151,0114,0117	
	DATA 0117,0153,0156	
	DATA 0149,0118,0136	
	DATA 0118,0122,0154	
	DATA 0136,0154,0145	
	DATA 0111,0158,0126	
	DATA 0122,0128,0123	
	DATA 0144,0133,0161	
	DATA 0127,0143,0127	
	DATA 0153,0123,0131	

09073 0024	(00412)	DATA 0'437',0'562',0'450'
09075 0034		
09076 0024	(00413)	DATA 0'529',0'557',0'410'
09077 0010		
09078 0030		
09079 0034	(00414)	DATA 0'500',0'411',0'590'
0907A 003A		
0907B 0001		
0907C 0040	(00415)	DATA 0'555',0'587',0'770'
0907D 0037		
0907E 0006		
0907F 0007		
09080 0020	(00416)	DATA 0'435',0'333',0'337'
09081 0004		
09082 0025	(00417)	DATA 0'525',0'524',0'522'
09083 0019		
09084 0018		
09085 0016	(00418)	DATA 0'500',0'551',0'290'
09086 0042		
09087 0005		
09088 0010	(00419)	DATA 0'439',0'430',0'500'
09089 0027		
0908A 0028		
0908B 0042	(00420)	DATA 0'414',0'415',0'443'
0908C 0000		
0908D 000F		
0908E 0020		
0908F 001A	(00421)	DATA 0'526',0'559',0'441'
09090 0036		
09091 0014	(00422)	DATA 0'521',0'417',0'490'
09093 0011		
09094 0041		
09095 0026	(00423)	DATA 0'543',0'414',0'480'
09096 000F		
09097 0008		
09098 0015	(00424)	DATA 0'521',0'511',0'412'
09099 0034		
0909A 000C		
0909B 002A	(00425)	DATA 0'542',0'418',0'410'
0909C 0032		
0909D 000A		
0909E 0017	(00426)	DATA 0'523',0'522',0'415'
0909F 0016		

AD-A091 663

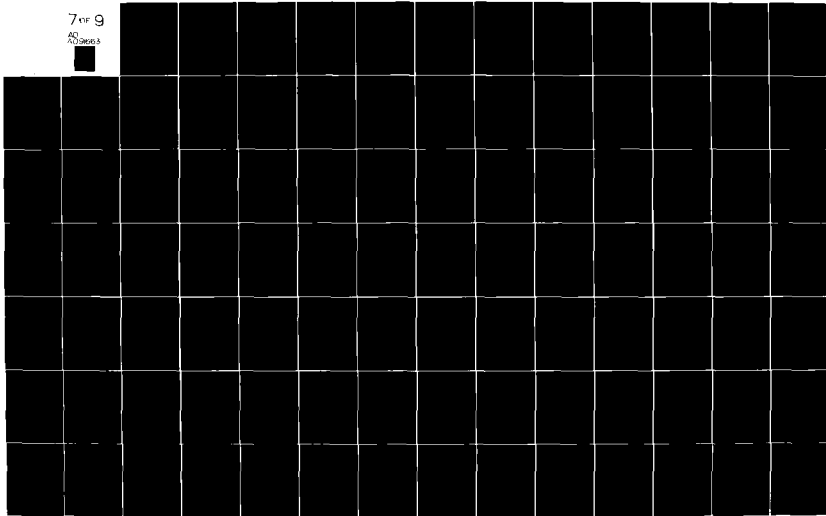
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME S0--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

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AD  
A091663



0002P 0004			
00031 0024	(00327)	DATA 0'36',0'53',0'41'	
00062 0046			
000A3 0029	(00324)	DATA 0'31',0'58',0'28'	
000A8 0014			
000AS 0048	(00329)	DATA 0'62',0'25',0'18'	
000A6 0016			
000A7 0048			
000AW 0019	(00330)	DATA 0'63',0'13',0'27'	
000AA 0034			
000AA 0009	(00331)	DATA 0'61',0'43',0'55'	
000AC 0018			
000AL 0030	(00332)	DATA 0'57',0'23',0'13'	
000AF 0037			
000AG 0039			
000AI 0017	(00333)	DATA 0'49',0'62',0'42'	
000AJ 0031			
000AK 0048			
000AS 002A	(00334)	DATA 0'33',0'57',0'63'	
000AV 0021			
000B7 0039			
000B8 0034	(00335) ;		
	(00336) TAPRCS	DATA 0'11'	
000B9 0001	(00337) ;		
000BA 0002	(00338)	DATA 0'21'	
000BB 0004	(00339)	DATA 0'4'	
000BC 0008	(00340)	DATA 0'8'	
000BD 0010	(00341)	DATA 0'16'	
000BE 0020	(00342)	DATA 0'32'	
000BF 0003	(00343)	DATA 0'3'	
000C0 0006	(00344)	DATA 0'6'	
000C1 000C	(00345)	DATA 0'12'	
000C2 0014	(00346)	DATA 0'24'	
000C3 0030	(00347)	DATA 0'48'	
000C4 0024	(00348)	DATA 0'45'	
000C5 0005	(00349)	DATA 0'5'	
000C6 000A	(00350)	DATA 0'10'	
000C7 0014	(00351)	DATA 0'20'	
000C8 0024	(00352)	DATA 0'40'	
000C9 0014	(00353)	DATA 0'19'	

POWER SUMS 51,53,55 DUF

TO FIRST INFORMATION HIT  
POWER TO RESIDUE CONVERS  
ION TABLE  
FOR A (63,45) RCH CODE.

0900A 0028	(00354)	DATA 0'340
0900B 0008	(00488)	DATA 0'150
0900C 0018	(00386)	DATA 0'340
0900D 0040	(00487)	DATA 0'600
0900E 0046	(00480)	DATA 0'590
0900F 0038	(00359)	DATA 0'530
0900G 0029	(00460)	DATA 0'410
0900H 0011	(00461)	DATA 0'170
0900I 0022	(00462)	DATA 0'330
0900J 0007	(00463)	DATA 0'70
0900K 0004	(00464)	DATA 0'140
0900L 0010	(00465)	DATA 0'280
0900M 0038	(00466)	DATA 0'560
0900N 0033	(00367)	DATA 0'510
0900O 0026	(00468)	DATA 0'470
0900P 0009	(00469)	DATA 0'90
0900Q 0012	(00370)	DATA 0'180
0900R 0024	(00371)	DATA 0'360
0900S 0006	(00472)	DATA 0'110
0900T 0016	(00473)	DATA 0'220
0900U 0020	(00374)	DATA 0'440
0900V 0016	(00475)	DATA 0'270
0900W 0036	(00376)	DATA 0'540
0900X 0028	(00377)	DATA 0'470
0900Y 0010	(00378)	DATA 0'290
0900Z 0038	(00379)	DATA 0'580
09010 0037	(00480)	DATA 0'550
09011 0020	(00381)	DATA 0'480
09012 0019	(00382)	DATA 0'250
09013 0032	(00383)	DATA 0'500
09014 0027	(00384)	DATA 0'490
09015 0000	(00385)	DATA 0'130
09016 0038	(00486)	DATA 0'260
09017 0034	(00387)	DATA 0'520
09018 0020	(00388)	DATA 0'430
09019 0015	(00389)	DATA 0'210
09020 0028	(00490)	DATA 0'420
09021 0016	(00391)	DATA 0'230
09022 0038	(00492)	DATA 0'460
09023 0034	(00393)	DATA 0'310
09024 0048	(00494)	DATA 0'620
09025 0038	(00495)	DATA 0'640
09026 0030	(00496)	DATA 0'610
09027 0039	(00497)	DATA 0'570



09821	0017	(00432)	DATA 0124*
09822	0045	(00433)	DATA 0154*
09823	0043	(00434)	DATA 0151*
09824	0028	(00435)	DATA 0137*
09825	0021	(00436)	DATA 0144*
09826	0037	(00437)	DATA 0155*
09827	0028	(00438)	DATA 0140*
09828	0006	(00439)	DATA 0110*
09829	0030	(00440)	DATA 0161*
09823	0026	(00441)	DATA 0146*
09820	0014	(00442)	DATA 0130*
09820	0032	(00443)	DATA 0150*
09820	0016	(00444)	DATA 0122*
09820	0027	(00445)	DATA 0159*
09826	0020	(00446)	DATA 0144*
09830	0010	(00447)	DATA 0129*
09831	0040	(00448)	DATA 0160*
09832	0028	(00449)	DATA 0132*
09833	0015	(00450)	DATA 0121*
09833	0014	(00451)	DATA 0120*
09835	0030	(00452)	DATA 0150*
09836	0039	(00453)	DATA 0157*
09837	0038	(00454)	DATA 0158*
09838	0000	(00455)	DATA 0101*
09839	0002	(00456)	DATA 012*
09838	0004	(00457)	DATA 014*
09838	0006	(00458)	DATA 016*
09830	0008	(00459)	DATA 018*
09830	0004	(00460)	DATA 0110*
09830	0000	(00461)	DATA 0114*
09840	0010	(00462)	DATA 0116*
09841	0012	(00463)	DATA 0118*
09842	0014	(00464)	DATA 0120*
09833	0016	(00465)	DATA 0122*
09843	0018	(00466)	DATA 0124*
09845	0015	(00467)	DATA 0126*
09846	0010	(00468)	DATA 0128*
09847	0014	(00469)	DATA 0140*
09838	0020	(00470)	DATA 0132*
09848	0022	(00471)	DATA 0134*
09848	0024	(00472)	DATA 0136*
09848	0026	(00473)	DATA 0138*
09848	0028	(00474)	DATA 0140*
09848	0030	(00475)	DATA 0142*
09848	0032	(00476)	DATA 0144*
09848	0034	(00477)	DATA 0146*
09848	0036	(00478)	DATA 0148*
09848	0038	(00479)	DATA 0150*
09848	0040	(00480)	DATA 0152*
09848	0042	(00481)	DATA 0154*
09848	0044	(00482)	DATA 0156*
09848	0046	(00483)	DATA 0158*
09848	0048	(00484)	DATA 0160*
09848	0050	(00485)	DATA 0162*

MULT. TABLE OF ALPHA FOR  
A (63,45)RCH CODE

0044C 0028	(004466)	DATA 0130*
0044D 0029	(004467)	DATA 0131*
0044E 002C	(004468)	DATA 0133*
0044F 002E	(004469)	DATA 0134*
00450 0030	(004470)	DATA 0135*
00451 0032	(004471)	DATA 0136*
00452 0034	(004472)	DATA 0137*
00453 0036	(004473)	DATA 0138*
00454 0038	(004474)	DATA 0139*
00455 003A	(004475)	DATA 0140*
00456 003C	(004476)	DATA 0141*
00457 003E	(004477)	DATA 0142*
00458 0040	(004478)	DATA 0143*
00459 0043	(004479)	DATA 0144*
0045A 0047	(004480)	DATA 0145*
0045B 0049	(004481)	DATA 0146*
0045C 004B	(004482)	DATA 0147*
0045D 004D	(004483)	DATA 0148*
0045E 004F	(004484)	DATA 0149*
0045F 0051	(004485)	DATA 0150*
00460 0053	(004486)	DATA 0151*
00461 0055	(004487)	DATA 0152*
00462 0057	(004488)	DATA 0153*
00463 0059	(004489)	DATA 0154*
00464 005B	(004490)	DATA 0155*
00465 005D	(004491)	DATA 0156*
00466 005F	(004492)	DATA 0157*
00467 0061	(004493)	DATA 0158*
00468 0063	(004494)	DATA 0159*
00469 0067	(004495)	DATA 0160*
0046A 0069	(004496)	DATA 0161*
0046B 006B	(004497)	DATA 0162*
0046C 006E	(004498)	DATA 0163*
0046D 0070	(004499)	DATA 0164*
0046E 0072	(004500)	DATA 0165*
0046F 0074	(004501)	DATA 0166*
00470 0076	(004502)	DATA 0167*
00471 0078	(004503)	DATA 0168*
00472 007A	(004504)	DATA 0169*
00473 007C	(004505)	DATA 0170*
00474 007E	(004506)	DATA 0171*
00475 0080	(004507)	DATA 0172*
00476 0082	(004508)	DATA 0173*
00477 0084	(004509)	DATA 0174*
00478 0086	(004510)	DATA 0175*
00479 0088	(004511)	DATA 0176*
0047A 008A	(004512)	DATA 0177*
0047B 008C	(004513)	DATA 0178*
0047C 008E	(004514)	DATA 0179*
0047D 0090	(004515)	DATA 0180*
0047E 0092	(004516)	DATA 0181*
0047F 0094	(004517)	DATA 0182*
00480 0096	(004518)	DATA 0183*
00481 0098	(004519)	DATA 0184*
00482 009A	(004520)	DATA 0185*
00483 009C	(004521)	DATA 0186*
00484 009E	(004522)	DATA 0187*
00485 00A0	(004523)	DATA 0188*
00486 00A2	(004524)	DATA 0189*
00487 00A4	(004525)	DATA 0190*
00488 00A6	(004526)	DATA 0191*
00489 00A8	(004527)	DATA 0192*
0048A 00AA	(004528)	DATA 0193*
0048B 00AC	(004529)	DATA 0194*



MULT. TABLE OF ALPAA\*\*

DATA	TABLES	DATA
09478 0000	(00540)	DATA 0'01
09479 0004	(00541)	DATA 0'03
0947A 0008	(00542)	DATA 0'06
0947B 000C	(00543)	DATA 0'11
0947C 0030	(00544)	DATA 0'16
0947D 0014	(00545)	DATA 0'20
0947E 0018	(00546)	DATA 0'24
0947F 003C	(00547)	DATA 0'28
09480 0020	(00548)	DATA 0'32
09481 0024	(00549)	DATA 0'36
09482 0028	(0054A)	DATA 0'40
09483 002C	(0054B)	DATA 0'44
09484 0030	(0054C)	DATA 0'48
09485 0034	(0054D)	DATA 0'52
09486 0038	(0054E)	DATA 0'56
09487 003C	(0054F)	DATA 0'60
09488 0043	(00546)	DATA 0'64
09489 0007	(00547)	DATA 0'68
0948A 000B	(00548)	DATA 0'72
0948B 000F	(00549)	DATA 0'76
0948C 0014	(0054A)	DATA 0'80
0948D 0017	(0054B)	DATA 0'84
0948E 001B	(0054C)	DATA 0'88
0948F 0014	(0054D)	DATA 0'92
09490 0023	(0054E)	DATA 0'96
09491 0027	(0054F)	DATA 0'00
09492 002B	(00550)	DATA 0'04
09493 002F	(00551)	DATA 0'08
09494 0033	(00552)	DATA 0'12
09495 0037	(00553)	DATA 0'16
09496 003B	(00554)	DATA 0'20
09497 003F	(00555)	DATA 0'24
09498 0004	(00556)	DATA 0'28
09499 0007	(00557)	DATA 0'32
0949A 0004	(00558)	DATA 0'36
0949B 000A	(00559)	DATA 0'40
0949C 0016	(0055A)	DATA 0'44
0949D 0012	(0055B)	DATA 0'48
0949E 001F	(0055C)	DATA 0'52
0949F 001A	(0055D)	DATA 0'56
094A0 0026	(0055E)	DATA 0'60
094A1 0022	(0055F)	DATA 0'64
094A2 0024	(00560)	DATA 0'68
094A3 002A	(00561)	DATA 0'72
094A4 002A	(00562)	DATA 0'76
094A5 002A	(00563)	DATA 0'80
094A6 002A	(00564)	DATA 0'84
094A7 002A	(00565)	DATA 0'88
094A8 002A	(00566)	DATA 0'92
094A9 002A	(00567)	DATA 0'96
094AA 002A	(00568)	DATA 0'00
094AB 002A	(00569)	DATA 0'04
094AC 002A	(0056A)	DATA 0'08
094AD 002A	(0056B)	DATA 0'12
094AE 002A	(0056C)	DATA 0'16
094AF 002A	(0056D)	DATA 0'20
094B0 002A	(0056E)	DATA 0'24
094B1 002A	(0056F)	DATA 0'28
094B2 002A	(00570)	DATA 0'32
094B3 002A	(00571)	DATA 0'36
094B4 002A	(00572)	DATA 0'40
094B5 002A	(00573)	DATA 0'44

094A4 0036	(005733)	DATA 05541
094A5 0037	(005735)	DATA 05501
094A6 0038	(005736)	DATA 05621
094A7 0039	(005777)	DATA 05581
094A8 0040	(005781)	DATA 0551
094A9 0041	(005791)	DATA 0511
094AA 0042	(005801)	DATA 05131
094AB 0043	(005811)	DATA 0521
094AC 0044	(005821)	DATA 05231
094AD 0045	(005831)	DATA 05171
094AE 0046	(005841)	DATA 05291
094AF 0047	(005851)	DATA 05251
094B0 0025	(005861)	DATA 05371
094B1 0021	(005871)	DATA 05331
094B2 0021	(005881)	DATA 05451
094B3 0029	(005891)	DATA 05411
094B4 0035	(005901)	DATA 05531
094B5 0031	(005911)	DATA 05491
094B6 0030	(005921)	DATA 05611
094B7 0030	(005931)	DATA 05571
094B8 0000	(005941)	DATA 0501
094B9 0008	(005951)	DATA 0501
094BA 0010	(005961)	DATA 05161
094BB 0018	(005971)	DATA 05241
094BC 0020	(005981)	DATA 05321
094BD 0028	(005991)	DATA 05401
094BE 0030	(006001)	DATA 05481
094BF 0038	(006011)	DATA 05561
094C0 0003	(006021)	DATA 0531
094C1 0006	(006031)	DATA 05111
094C2 0013	(006041)	DATA 05191
094C3 0016	(006051)	DATA 05271
094C4 0023	(006061)	DATA 05351
094C5 0028	(006071)	DATA 05431
094C6 0033	(006081)	DATA 05511
094C7 0046	(006091)	DATA 05591
094C8 0006	(006101)	DATA 0561
094C9 0004	(006111)	DATA 05141
094CA 0016	(006121)	DATA 05221
094CB 0014	(006131)	DATA 05301
094CC 0026	(006141)	DATA 05381
094CD 0021	(006151)	DATA 05461
094CE 0036	(006161)	DATA 05541
094CF 0034	(006171)	DATA 05621

MULT. TABLE OF ALPHA\*\*3

TABLES

094100 0000	(006104)	DATA 0151
094101 0000	(006109)	DATA 0133
094102 0015	(006200)	DATA 0121
094103 0010	(006211)	DATA 0129
094104 0025	(006222)	DATA 0133
094105 0020	(006233)	DATA 0135
094106 0035	(006244)	DATA 0133
094107 0030	(006255)	DATA 0161
094108 0030	(006266)	DATA 0112
094109 0003	(006277)	DATA 0141
09410A 0010	(006288)	DATA 0128
09410B 0014	(006299)	DATA 0120
09410C 0020	(006300)	DATA 0144
09410D 0024	(006311)	DATA 0146
09410E 0030	(006322)	DATA 0160
09410F 0034	(006333)	DATA 0152
094110 0004	(006344)	DATA 0115
094111 0007	(006355)	DATA 0117
094112 0014	(006366)	DATA 0131
094113 0017	(006377)	DATA 0123
094114 0024	(006388)	DATA 0147
094115 0027	(006399)	DATA 0139
094116 0034	(006400)	DATA 0163
094117 0037	(006411)	DATA 0155
094118 000A	(006422)	DATA 0130
094119 0002	(006433)	DATA 0127
09411A 001A	(006444)	DATA 0126
09411B 0012	(006455)	DATA 0104
09411C 0026	(006466)	DATA 0142
09411D 003A	(006477)	DATA 0134
09411E 0042	(006488)	DATA 0154
09411F 0009	(006499)	DATA 0150
094120 0001	(006511)	DATA 0111
094121 0019	(006522)	DATA 0125
094122 0011	(006533)	DATA 0117
094123 0029	(006544)	DATA 0141
094124 0021	(006555)	DATA 0131
094125 0039	(006566)	DATA 0157
094126 0031	(006577)	DATA 0139
094127 0000	(006588)	DATA 0130
094128 0000	(006599)	DATA 0100

ACH

MESS-ZEIT. # FÜR BLICK

...

PAGE 21: PROG. CSPOSD.FAI

04900 0000 (00000) ?  
04901 0000 (00001) SENS DATA 000?  
... (00002) RECS DATA 0000?  
(00003) ?  
(00004) \*  
(00005) \* END OF DATA MEMORY  
(00006) \*JECT

1).MESS-ICM. # FOR HL.  
MESSICM. # FOR BLOCK 1)  
DATA MEM. FOR REGISTER W

1-N7





09470 80004624 (00755)  
 09472 04000580 (00756)  
 09473 2005 (00757)  
 09475 0400 (00758)  
 09476 00000580 (00759)  
 09478 0470 (00760)  
 09479 0400 (00761)  
 0947A 80004622 (00762)  
 0947C 0000571 (00763) LOKSPA  
 0947F 00000580 (00764) LOKSPA  
 09480 2005 (00765)  
 09481 0400 (00766)  
 09482 00000580 (00767)  
 09484 0470 (00768)  
 09485 0400 (00769)  
 09486 80004622 (00770)  
 (00771) ?  
 (00772)

YES

SAY AP OUT RUFF EMPTY  
HAS ADDONE HAPPENED?

JWP TEST0  
 SWNSL 0,APDDEL  
 RUP ELK6  
 KVVZM APDDEL  
 KKT  
 KVEE  
 JWP TEST0  
 KVVZM DUTTEL  
 SWNSL 0,APDDEL  
 RUP ELK6  
 KVVZM  
 KVVZM APDDEL  
 ELT  
 KVVZM  
 JWP TEST0  
 KVVZM













0A071 2A3C	(00984) *	IS PARM BUFFER HIT 5 CLE
	(00989) *	AR?
	(00990)	
	(00991) HITS	
	(00992) :	
0A075 0204K0E	(00993) *	OTHERWISE, SET LOCATION
	(00994) :	OF SEN ARRAY TO BE A 1
	(00995)	INCREMENT THE COUNTER R2
0A077 2B21	(00996) *	
	(00997)	
	(00998) HITS	
	(00999) :	
0A079 0204K0E	(00999) *	IS PARM BUFFER HIT 4 CLE
	(00999) :	AR?
	(00999)	
	(00999) HITS	
	(00999) :	
0A076 2B21	(00999) *	OTHERWISE, SET LOCATION
	(00999) :	OF SEN ARRAY TO BE A 1
	(00999)	INCREMENT THE COUNTER R2
0A07C 2A3C	(00999) *	
	(00999)	
	(00999) HITS	
	(00999) :	
0A07B 0204K0E	(00999) *	IS PARM BUFFER HIT 3 CLE
	(00999) :	AR?
	(00999)	
	(00999) HITS	
	(00999) :	
0A07E 2B21	(00999) *	OTHERWISE, SET LOCATION
	(00999) :	OF SEN ARRAY TO BE A 1
	(00999)	INCREMENT THE COUNTER R2
0A080 2A2C	(00999) *	
	(00999)	
	(00999) HITS	
	(00999) :	
0A081 0204K0E	(00999) *	IS PARM BUFFER HIT 2 CLE
	(00999) :	AR?
	(00999)	
	(00999) HITS	
	(00999) :	
0A084 2B21	(00999) *	OTHERWISE, SET LOCATION
	(00999) :	OF SEN ARRAY TO BE A 1
	(00999)	INCREMENT THE COUNTER R2
0A084 2A1C	(00999) *	
	(00999)	
	(00999) HITS	
	(00999) :	
0A085 0204K0E	(00999) *	IS PARM BUFFER HIT 1 CLE
	(00999) :	AR?
	(00999)	
	(00999) HITS	
	(00999) :	
0A087 2B21	(00999) *	OTHERWISE, SET LOCATION
	(00999) :	OF SEN ARRAY TO BE A 1
	(00999)	INCREMENT THE COUNTER R2
0A088 2A0C	(00999) *	
	(00999)	
	(00999) HITS	
	(00999) :	
0A089 0204K0E	(00999) *	IS PARM BUFFER HIT 0 CLE
	(00999) :	AR?
	(00999)	
	(00999) HITS	
	(00999) :	
0A088 2B21	(01000)	OTHERWISE, SET LOCATION
	(01001)	OF SEN ARRAY TO BE A 1
		INCREMENT THE COUNTER R2

```

0A0023 0 (010023) *
0A0024 0 (010024) *
0A0025 0 (010025) *
0A0026 0 (010026) *
0A0027 0 (010027) *
0A0028 0 (010028) *
0A0029 0 (010029) *
0A0030 0 (010030) *
0A0031 0 (010031) *
0A0032 0 (010032) *
0A0033 0 (010033) *
0A0034 0 (010034) *
0A0035 0 (010035) *
0A0036 0 (010036) *
0A0037 0 (010037) *
0A0038 0 (010038) *
0A0039 0 (010039) *
0A0040 0 (010040) *
0A0041 0 (010041) *
0A0042 0 (010042) *
0A0043 0 (010043) *
0A0044 0 (010044) *
0A0045 0 (010045) *
0A0046 0 (010046) *
0A0047 0 (010047) *
0A0048 0 (010048) *
0A0049 0 (010049) *
0A0050 0 (010050) *
0A0051 0 (010051) *
0A0052 0 (010052) *
0A0053 0 (010053) *
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0A0055 0 (010055) *
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0A0064 0 (010064) *
0A0065 0 (010065) *
0A0066 0 (010066) *
0A0067 0 (010067) *
0A0068 0 (010068) *
0A0069 0 (010069) *
0A0070 0 (010070) *
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0A0096 0 (010096) *
0A0097 0 (010097) *
0A0098 0 (010098) *
0A0099 0 (010099) *
0A0100 0 (010100) *
0A0101 0 (010101) *
0A0102 0 (010102) *
0A0103 0 (010103) *
0A0104 0 (010104) *
0A0105 0 (010105) *
0A0106 0 (010106) *
0A0107 0 (010107) *
0A0108 0 (010108) *
0A0109 0 (010109) *
0A0110 0 (010110) *
0A0111 0 (010111) *
0A0112 0 (010112) *
0A0113 0 (010113) *
0A0114 0 (010114) *
0A0115 0 (010115) *
0A0116 0 (010116) *
0A0117 0 (010117) *
0A0118 0 (010118) *
0A0119 0 (010119) *
0A0120 0 (010120) *
0A0121 0 (010121) *
0A0122 0 (010122) *
0A0123 0 (010123) *
0A0124 0 (010124) *
0A0125 0 (010125) *
0A0126 0 (010126) *
0A0127 0 (010127) *
0A0128 0 (010128) *
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0A0130 0 (010130) *
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0A0136 0 (010136) *
0A0137 0 (010137) *
0A0138 0 (010138) *
0A0139 0 (010139) *
0A0140 0 (010140) *
0A0141 0 (010141) *
0A0142 0 (010142) *
0A0143 0 (010143) *
0A0144 0 (010144) *
0A0145 0 (010145) *

```

INCRMENT THE PARAMETER  
COUNTER

DECREMENT R5, JUMP IF R5  
IS +VE

SPECIALIZE THE VERY LAST PARAMETER (I.E. OTG)  
AFTER INITIALIZING IN ZEROES

HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

SKIP IN LOCATIONS IN SFN  
IS BIT 1 OF OTG SET? SKIP  
INCRMENT SFN ARRAY IF 1  
Y IS NOT  
INCRMENT COUNTER N2

CHECK IF BIT 0 OF OTG SET  
T? SKIP IF NOT  
INCRMENT SFN ARRAY IF 1  
T IS NOT  
INCRMENT COUNTER N2

```

00001 0000 (01000)
00002 0000 (01001) *
00003 0000 (01002) *
00004 0000 (01003) *
00005 0000 (01004) *
00006 0000 (01005) *
00007 0000 (01006) *
00008 0000 (01007) *
00009 0000 (01008) *
00010 0000 (01009) *
00011 0000 (01010) *
00012 0000 (01011) *
00013 0000 (01012) *
00014 0000 (01013) *
00015 0000 (01014) *
00016 0000 (01015) *
00017 0000 (01016) *
00018 0000 (01017) *
00019 0000 (01018) *
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00085 0000 (01084) *
00086 0000 (01085) *
00087 0000 (01086) *
00088 0000 (01087) *
00089 0000 (01088) *
00090 0000 (01089) *

```

MOVE FORTY FOUR IN R5  
 SAVE THE FIRST INIT VALUE  
 DECREMENT R7 BY 1  
 SHIFT LEFT BY 1  
 \*\*\*\*\*  
 WHEN INIT=0, R7<0 AND 0 1  
 S NG!  
 SO TEST & JUMP AROUND SE  
 RIALIZATION SINCE  
 INIT=0 IMPLIES NO BITS T  
 O SERIALIZE!!  
 \*\*\*\*\*  
 GOTO THE CURR.SERIALIZED  
 ION ROUTINE  
 \*\*\*\*\*  
 SAVE THE 1ST INIT VALUE  
 COMPARE THE BIT ASSIGNME  
 NT WITH THE MARK  
 GOTO NEXT BIT DECODING R  
 OUTINE IF BIT ASSIGN. CH  
 ANGES  
 HAS ADDONE HAPPENED?  
 NO, R0P TO EVEN LOC APTF  
 R RET





```

0A11A F1109F0F (01153) STORE F1,F1SAV
0A11C F1209F0F (01155) STORE F2,F2SAV
0A11E F1309F10 (01156) STORE F3,F3SAV
0A120 F1409F12 (01157) STORE F4,F4SAV
0A122 F1509F14 (01158) STORE F5,F5SAV
0A124 F1609F16 (01159) STORE F6,F6SAV
0A126 F1709F18 (01160) STORE F7,F7SAV
0A128 0470 (01161) RPT
0A129 0800 (01162) FVE
0A12A A6109F0C (01163) LOAD F1,F1SAV
0A12C A6209F0E (01164) LOAD F2,F2SAV
0A12E A6309F10 (01165) LOAD F3,F3SAV
0A130 A6409F12 (01166) LOAD F4,F4SAV
0A132 A6509F14 (01167) LOAD F5,F5SAV
0A134 A6609F16 (01168) LOAD F6,F6SAV
0A136 A6709F18 (01169) LOAD F7,F7SAV
0A138 F0709F0E (01159) MOVAR F7,BITS(F-3)
0A13A 0040 (01151) CLR R4
0A13C 0800 (01152) FVE
0A13E F2709F0E (01153) BRTR31 CMPE F7,BITS(F-4)
0A140 (01154) ;
0A142 8020A176 (01155) JMP BITS,GT
0A144 0F00056C (01156) SRSR 0,APDPFLS
0A146 2021 (01157) ROP 4,44
0A148 0800 (01158) ;
0A14A C000580 (01160) MOVZ8 APDPFLS
0A14C F1109F0C (01161) STORE F1,F1SAV
0A14E F1209F0E (01162) STORE F2,F2SAV
0A150 F1309F10 (01163) STORE F3,F3SAV
0A152 F1409F12 (01164) STORE F4,F4SAV
0A154 F1509F14 (01165) STORE F5,F5SAV
0A156 F1609F16 (01166) STORE F6,F6SAV
0A158 F1709F18 (01167) STORE F7,F7SAV
0A15A 0470 (01168) RPT
0A15C 0800 (01169) FVE
0A15E A6109F0C (01170) LOAD F1,F1SAV
0A160 A6209F0E (01171) LOAD F2,F2SAV
0A162 A6309F10 (01172) LOAD F3,F3SAV
0A164 A6409F12 (01173) LOAD F4,F4SAV
0A166 A6509F14 (01174) LOAD F5,F5SAV
0A168 A6609F16 (01175) LOAD F6,F6SAV
0A16A A6709F18 (01176) LOAD F7,F7SAV
0A16C (01177) ;

```

SAVE THE NEXT BIT VALUE

COMPARE THE BIT ASSIGNME  
NT WITH THE MARK  
FI NOT EQUAL, GOTO DECODE  
HAS APDPONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RPT



```

0A104 427800F (01222) 0000 R2, R1, R5, R4)
0A100 0040 (01223) 0000 R3
0A101 0000 (01224) 0000
0A102 427800 (01225) 0017251 0017251
0A103 8020010 (01226) ?
0A104 8020010 (01227) 0000
0A105 8000080 (01228) 0000
0A106 2021 (01229) 0000
0A107 0000 (01230) ?
0A108 0000 (01231) 0000
0A109 0000080 (01232) 0000
0A10A 4110000C (01233) 0000
0A10B 4120000C (01234) 0000
0A10C 4130000C (01235) 0000
0A10D 4140000C (01236) 0000
0A10E 4150000C (01237) 0000
0A10F 4160000C (01238) 0000
0A110 4170000C (01239) 0000
0A111 0000 (01240) 0000
0A112 0000 (01241) 0000
0A113 000000C (01242) 0000
0A114 0020000C (01243) 0000
0A115 0030000C (01244) 0000
0A116 0040000C (01245) 0000
0A117 0050000C (01246) 0000
0A118 0060000C (01247) 0000
0A119 0070000C (01248) 0000
0A11A 0080000C (01249) 0000
0A11B 0090000C (01250) 0000
0A11C 0100000C (01251) 0000
0A11D 0110000C (01252) 0000
0A11E 0120000C (01253) 0000
0A11F 0130000C (01254) 0000
0A120 0140000C (01255) ?
0A121 0000 (01256) 0000
0A122 0000000C (01257) 0000
0A123 0000000C (01258) 0000
0A124 0000000C (01259) ?
0A125 0000000C (01260) 0000
0A126 0000000C (01261) ?
0A127 0000000C (01262) 0000
0A128 0000000C (01263) ?
0A129 0000000C (01264) ?
0A12A 0000000C (01265) ?

```

SAVE THE INIT VALUE  
 COMPARE THE HIT ASSIGNME  
 NT WITH THE MARK  
 IF NOT EQUAL, GOTO DECODE  
 HAS ASSIGNMENT HAPPENED?  
 NO, HOP TO EVEN LOC AFTE  
 R RPT

SKIP IF HIT 2 OF DCT(R4)  
 INCREMENT THE BUFFER POS  
 ITION COUNTER  
 CHECK IF R5 +VE  
 RELOAD THE COUNTER WITH  
 44  
 IS R2 GREATER THAN POST  
 3  
 ADD 16 TO LOOP COUNTER I  
 F LESS THAN OR EQUAL  
 GO BACK TO DRIT251





0A274	F1209404	(01354)	STORE R2,R2SAV	COMPARE R2 WITH POST3
0A280	F1409416	(01355)	STORE R3,R3SAV	IF GREATER THAN POST3, N
0A282	F140941D	(01356)	STORE R4,R4SAV	0 NEED TO PUT IN PARITY
0A283	F1509414	(01357)	STORE R5,R5SAV	BITS
0A286	F1609416	(01358)	STORE R6,R6SAV	MOVE THE FRAME LENGTH TO
0A288	F170941B	(01359)	STORE R7,R7SAV	R6=USEN-1-R2
0A28A	0E70	(01360)	EVF0	MOVF ADDRESS OTDCT-1 TO
0A28B	0E80	(01361)	EVF0	R4
0A28C	A610940C	(01362)	LOAD R1,R1SAV	SET R2 TO BE THE ADDRESS
0A28E	A620940E	(01363)	LOAD R2,R2SAV	:SENIS(R2)
0A290	A6309410	(01364)	LOAD R3,R3SAV	SAVE THE MASK IN R3
0A292	A6409412	(01365)	LOAD R4,R4SAV	POP OTDCT(R4)-->R1..R4=
0A294	A6509414	(01366)	LOAD R5,R5SAV	R4+1
0A296	A6609416	(01367)	LOAD R6,R6SAV	SAVE ONLY THE LAST HIT
0A298	A6709418	(01368)	LOAD R7,R7SAV	SAVE THE VALUE IN SENS(R
0A29A	F2209406	(01369)	COMP R2,POST3S	2).. R2=R2+1
0A29C	8020A2B2	(01370)	MOV PARTUS,GT	HAS ABOVE HAPPENED?
		(01371)		NO, HUP TO EVEN LOC AFTE
		(01372)		R RPT
		(01373)		
		(01374)		
0A29E	F0C0940E	(01375)	MOVFP R6,POST4S	
0A270	3E63	(01376)	STORE R6,R2	
0A271	0B00	(01377)	EVF0	
0A272	90A0B20B	(01378)	MOVFP R4,OTDCTS-1	
0A273	9C20B60E	(01379)	ADDR R2,SENS	
0A276	F0309400	(01381)	MOVFP R3,IMFS	
0A278	3042	(01382)	POPRT R4,R1	
		(01383)	ORITGSI	
		(01384)		
		(01385)		
0A279	0B00	(01386)	EVFN	
0A27A	0A16	(01387)	ZCPR R1,R3	
0A27B	0B00	(01388)	EVFN	
0A27C	3322	(01389)	POSTRT R2,R1	
		(01390)		
0A27D	0B00	(01391)	EVFN	
0A27E	0E005800	(01392)	SENIS(0,APPEFELS	
0A280	7021	(01393)	MOV #1,33	
		(01394)		
0A282	CC000058	(01395)	MOV/S APPEFELS	
0A283	F130940E	(01396)	STORE R1,R1SAV	
0A286	F1209404	(01397)	STORE R2,R2SAV	

0A284	11100410	(01404)	STORE R4,R3,SAV	
0A28A	11100412	(01406)	STORE R1,R3,SAV	
0A290	11100414	(01408)	STORE R5,R5,SAV	
0A296	11100416	(0140A)	STORE R6,R6,SAV	
0A29C	11100418	(0140C)	STORE R7,R7,SAV	
0A2A2	047A	(0140E)	R4+	
0A2A8	0400	(01410)	MOVE	
0A2AE	4E10940F	(01412)	LOAD R1,R1,SAV	
0A2B4	4E20940E	(01414)	LOAD R2,R2,SAV	
0A2BA	4E30940D	(01416)	LOAD R3,R3,SAV	
0A2C0	4E40940C	(01418)	LOAD R4,R4,SAV	
0A2C6	4E50940B	(0141A)	LOAD R5,R5,SAV	
0A2CC	4E60940A	(0141C)	LOAD R6,R6,SAV	
0A2D2	4E709409	(0141E)	LOAD R7,R7,SAV	
0A2D8	4E809408	(01420)	POP R5,ENDSO	
0A2DE	4E909407	(01422)	POP R5,ENDFOR	
0A2E4	4EA09406	(01424)	COMPARE R2,POSTEST	
0A2EA	4EB09405	(01426)	JMP ENDSO,GT	
0A2F0	4EC09404	(01428)	ADDRESS R2,FACTORS	
0A2F6	4ED09403	(0142A)	SUBMR R6,FACTORS	
0A304	4EE09402	(0142C)	POP R5,DEPTOUST	
0A30A	4EF09401	(0142E)	JMP ENDSRS	
0A310	4F009400	(01430)		
0A318	4F109400	(01432)		
0A31E	4F209400	(01434)		
0A324	4F309400	(01436)		
0A32A	4F409400	(01438)		
0A330	4F509400	(0143A)		
0A338	4F609400	(0143C)		
0A33E	4F709400	(0143E)		
0A344	4F809400	(01440)		
0A34A	4F909400	(01442)		
0A350	4FA09400	(01444)		
0A358	4FB09400	(01446)		
0A35E	4FC09400	(01448)		
0A364	4FD09400	(0144A)		
0A36A	4FE09400	(0144C)		
0A370	4FF09400	(0144E)		
0A378	50009400	(01450)		
0A37E	50109400	(01452)		
0A384	50209400	(01454)		
0A38A	50309400	(01456)		
0A390	50409400	(01458)		
0A398	50509400	(0145A)		
0A39E	50609400	(0145C)		
0A3A4	50709400	(0145E)		
0A3AA	50809400	(01460)		
0A3B0	50909400	(01462)		
0A3B8	50A09400	(01464)		
0A3BE	50B09400	(01466)		
0A3C4	50C09400	(01468)		
0A3CA	50D09400	(0146A)		
0A3D0	50E09400	(0146C)		
0A3D8	50F09400	(0146E)		
0A3DE	51009400	(01470)		
0A3E4	51109400	(01472)		
0A3EA	51209400	(01474)		
0A3F0	51309400	(01476)		
0A3F8	51409400	(01478)		
0A3FE	51509400	(0147A)		
0A404	51609400	(0147C)		
0A40A	51709400	(0147E)		
0A410	51809400	(01480)		
0A418	51909400	(01482)		
0A41E	51A09400	(01484)		
0A424	51B09400	(01486)		
0A42A	51C09400	(01488)		
0A430	51D09400	(0148A)		
0A438	51E09400	(0148C)		
0A43E	51F09400	(0148E)		
0A444	52009400	(01490)		
0A44A	52109400	(01492)		
0A450	52209400	(01494)		
0A458	52309400	(01496)		
0A45E	52409400	(01498)		
0A464	52509400	(0149A)		
0A46A	52609400	(0149C)		
0A470	52709400	(0149E)		
0A478	52809400	(014A0)		
0A47E	52909400	(014A2)		
0A484	52A09400	(014A4)		
0A48A	52B09400	(014A6)		
0A490	52C09400	(014A8)		
0A498	52D09400	(014AA)		
0A49E	52E09400	(014AC)		
0A4A4	52F09400	(014AE)		
0A4AA	53009400	(014B0)		
0A4B0	53109400	(014B2)		
0A4B8	53209400	(014B4)		
0A4BE	53309400	(014B6)		
0A4C4	53409400	(014B8)		
0A4CA	53509400	(014BA)		
0A4D0	53609400	(014BC)		
0A4D8	53709400	(014BE)		
0A4DE	53809400	(014C0)		
0A4E4	53909400	(014C2)		
0A4EA	53A09400	(014C4)		
0A4F0	53B09400	(014C6)		
0A4F8	53C09400	(014C8)		
0A4FE	53D09400	(014CA)		
0A504	53E09400	(014CC)		
0A50A	53F09400	(014CE)		
0A510	54009400	(014D0)		
0A518	54109400	(014D2)		
0A51E	54209400	(014D4)		
0A524	54309400	(014D6)		
0A52A	54409400	(014D8)		
0A530	54509400	(014DA)		
0A538	54609400	(014DC)		
0A53E	54709400	(014DE)		
0A544	54809400	(014E0)		
0A54A	54909400	(014E2)		
0A550	54A09400	(014E4)		
0A558	54B09400	(014E6)		
0A55E	54C09400	(014E8)		
0A564	54D09400	(014EA)		
0A56A	54E09400	(014EC)		
0A570	54F09400	(014EE)		
0A578	55009400	(014F0)		
0A57E	55109400	(014F2)		
0A584	55209400	(014F4)		
0A58A	55309400	(014F6)		
0A590	55409400	(014F8)		
0A598	55509400	(014FA)		
0A59E	55609400	(014FC)		
0A5A4	55709400	(014FE)		
0A5AA	55809400	(01500)		
0A5B0	55909400	(01502)		
0A5B8	55A09400	(01504)		
0A5BE	55B09400	(01506)		
0A5C4	55C09400	(01508)		
0A5CA	55D09400	(0150A)		
0A5D0	55E09400	(0150C)		
0A5D8	55F09400	(0150E)		
0A5DE	56009400	(01510)		
0A5E4	56109400	(01512)		
0A5EA	56209400	(01514)		
0A5F0	56309400	(01516)		
0A5F8	56409400	(01518)		
0A5FE	56509400	(0151A)		
0A604	56609400	(0151C)		
0A60A	56709400	(0151E)		
0A610	56809400	(01520)		
0A618	56909400	(01522)		
0A61E	56A09400	(01524)		
0A624	56B09400	(01526)		
0A62A	56C09400	(01528)		
0A630	56D09400	(0152A)		
0A638	56E09400	(0152C)		
0A63E	56F09400	(0152E)		
0A644	57009400	(01530)		
0A64A	57109400	(01532)		
0A650	57209400	(01534)		
0A658	57309400	(01536)		
0A65E	57409400	(01538)		
0A664	57509400	(0153A)		
0A66A	57609400	(0153C)		
0A670	57709400	(0153E)		
0A678	57809400	(01540)		
0A67E	57909400	(01542)		
0A684	57A09400	(01544)		
0A68A	57B09400	(01546)		
0A690	57C09400	(01548)		
0A698	57D09400	(0154A)		
0A69E	57E09400	(0154C)		
0A6A4	57F09400	(0154E)		
0A6AA	58009400	(01550)		
0A6B0	58109400	(01552)		
0A6B8	58209400	(01554)		
0A6BE	58309400	(01556)		
0A6C4	58409400	(01558)		
0A6CA	58509400	(0155A)		
0A6D0	58609400	(0155C)		
0A6D8	58709400	(0155E)		
0A6DE	58809400	(01560)		
0A6E4	58909400	(01562)		
0A6EA	58A09400	(01564)		
0A6F0	58B09400	(01566)		
0A6F8	58C09400	(01568)		
0A6FE	58D09400	(0156A)		
0A704	58E09400	(0156C)		
0A70A	58F09400	(0156E)		
0A710	59009400	(01570)		
0A718	59109400	(01572)		
0A71E	59209400	(01574)		
0A724	59309400	(01576)		
0A72A	59409400	(01578)		
0A730	59509400	(0157A)		
0A738	59609400	(0157C)		
0A73E	59709400	(0157E)		
0A744	59809400	(01580)		
0A74A	59909400	(01582)		
0A750	59A09400	(01584)		
0A758	59B09400	(01586)		
0A75E	59C09400	(01588)		
0A764	59D09400	(0158A)		
0A76A	59E09400	(0158C)		
0A770	59F09400	(0158E)		
0A778	5A009400	(01590)		
0A77E	5A109400	(01592)		
0A784	5A209400	(01594)		
0A78A	5A309400	(01596)		
0A790	5A409400	(01598)		
0A798	5A509400	(0159A)		
0A79E	5A609400	(0159C)		
0A7A4	5A709400	(0159E)		
0A7AA	5A809400	(015A0)		
0A7B0	5A909400	(015A2)		
0A7B8	5AA09400	(015A4)		
0A7BE	5AB09400	(015A6)		
0A7C4	5AC09400	(015A8)		
0A7CA	5AD09400	(015AA)		
0A7D0	5AE09400	(015AC)		
0A7D8	5AF09400	(015AE)		
0A7DE	5B009400	(015B0)		
0A7E4	5B109400	(015B2)		
0A7EA	5B209400	(015B4)		
0A7F0	5B309400	(015B6)		
0A7F8	5B409400	(015B8)		
0A7FE	5B509400	(015BA)		
0A804	5B609400	(015BC)		
0A80A	5B709400	(015BE)		
0A810	5B809400	(015C0)		
0A818	5B909400	(015C2)		
0A81E	5BA09400	(015C4)		
0A824	5BB09400	(015C6)		
0A82A	5BC09400	(015C8)		
0A830	5BD09400	(015CA)		
0A838	5BE09400	(015CC)		
0A83E	5BF09400	(015CE)		
0A844	5C009400	(0		

0A2C1 0800	(01342)	KVFN	SAVE ONLY THE LAST HIT
0A2C2 CC000510	(01343)	MOVZP ADDRESS	SAVE THE VALUE IN SENSOR
0A2C3 4110980C	(01344)	STORE R1,R1SAV	?).. R2ER2+1
0A2C6 41209804	(01445)	STORE R2,R2SAV	LOOP BACK UNTIL THE ENT
0A2C8 41309810	(01446)	STORE R3,R3SAV	IRE FRAME IS DONE.
0A2CA 41409812	(01447)	STORE R4,R4SAV	FROM SPR=2...
0A2CC 41509814	(01448)	STORE R5,R5SAV	TO SPR=1
0A2CE 41609816	(01449)	STORE R6,R6SAV	FROM MRH=2...
0A2D0 41709818	(01450)	STORE R7,R7SAV	TO MRH=1
0A2D2 0E70	(01451)	PBT	PROTECT SIDERAND
0A2D3 0800	(01452)	KVFN	
0A2D3 A610980C	(01353)	LOAD R1,R1SAV	
0A2D6 A6209804	(01354)	LOAD R2,R2SAV	
0A2D8 A6309810	(01455)	LOAD R3,R3SAV	
0A2DA A6409812	(01456)	LOAD R4,R4SAV	
0A2DC A6509814	(01457)	LOAD R5,R5SAV	
0A2DE A6609816	(01358)	LOAD R6,R6SAV	
0A2E0 A6709818	(01459)	LOAD R7,R7SAV	
0A2E2 4A16	(01460)	ADDRH R1,R1	
0A2E3 0800	(01461)	KVFN	
0A2E4 3322	(01462)	PUSHX1 R2,R1	
0A2E5 0800	(01463)	KVFN	
0A2E5 4C60A20C	(01465)	JMP R6,FRIT051	
0A2E6 0800	(01466)	KVFN	
0A2E6 4C60A20C	(01467)	JMP R6,FRIT051	
0A2E8 0200	(01468)	CCS 3	
0A2E9 06A0	(01469)	CCS 2	
0A2EA 0200	(01470)	CCS 5	
0A2EB 06C0	(01471)	CCS 4	
0A2EC 40006244	(01472)	JMP LOGATS	NOT ENOUGH TIME TO PROTECT
	(01473)	JMP RCHRF05	
	(01474)	FJFCT	





0A326	2006	(01519)	SRCL 11,R4
0A327	F054C04	(01520)	MOVW R5,SPNS+P2
0A328	2055	(01521)	SRCL 13,R4
0A32A	F054C06	(01522)	MOVW R5,SPNS+1(P2)
0A32C	2036	(01524)	SRCL 17,R4
0A32D	F054C10	(01524)	MOVW R5,SPNS+2(R2)
0A32E	2036	(01525)	SRCL 11,R4
0A330	F054C11	(01526)	MOVW R5,SPNS+3(P2)
0A332	2024	(01527)	SRCL 10,R4
0A333	F054C12	(01528)	MOVW R5,SPNS+3(P2)
0A335	2014	(01529)	SRCL 9,R4
0A336	F054C13	(01530)	MOVW R5,SPNS+5(P2)
0A338	2006	(01531)	SRCL 6,P3
0A339	F054C14	(01532)	MOVW R5,SPNS+6(R2)
0A33B	2A76	(01533)	SRCL 7,R4
0A33C	F054C15	(01534)	MOVW R5,SPNS+7(R2)
0A33E	2A66	(01535)	SRCL 6,R3
0A33F	F054C16	(01536)	MOVW R5,SPNS+8(R2)
0A341	2A56	(01537)	SRCL 5,R3
0A342	F054C17	(01538)	MOVW R5,SPNS+9(R2)
0A344	2A46	(01539)	SRCL 4,R3
0A345	F054C18	(01540)	MOVW R5,SPNS+10(R2)
0A347	2A36	(01541)	SRCL 3,P3
0A349	F054C19	(01542)	MOVW R5,SPNS+11(P2)
0A34A	2A26	(01543)	SRCL 2,R4
0A34B	F054C1A	(01543)	MOVW R5,SPNS+12(R2)
0A34D	2A16	(01545)	SRCL 1,P3
0A34E	F054C1B	(01546)	MOVW R5,SPNS+13(P2)
0A350	2A06	(01547)	SRCL 0,R3
0A351	F054C1C	(01548)	MOVW R5,SPNS+14(R2)
0A353	2A28	(01549)	SRCL 2,P4
0A354	F054C1D	(01550)	MOVW R5,SPNS+15(R2)
0A356	2A18	(01551)	SRCL 1,R4
0A357	F054C1E	(01552)	MOVW R5,SPNS+16(R2)
0A359	2A08	(01553)	SRCL 0,R4
0A35A	F054C1F	(01554)	MOVW R5,SPNS+17(R2)
		(01555)	ADDW R2,R4
		(01556)	CRTP R2,190
		(01557)	JMP R0RCS,FU
		(01558)	CRTP R2,560
		(01559)	JMP R0RCS,FU
		(01560)	*
		(01561)	*
		(01562)	END OF PROGRAM ROUTINE

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(01563) \*  
(01563) \*  
(01565) ENDFCS E'VEW  
0A35C H00DA354 JMP L00A1S  
(01567) \*  
(01568) \*  
(01569) \*  
EJECT



NO, HOP TO EVEN LOC AFTE  
R HFT

0A39A 2021 (0161A) HOP #1+34  
 0A39A 0800 (0161B) FVEF  
 0A39C CC000580 (01617) \*MOVZM AFDNFTLS  
 0A39F F1109F0C (01618) STORF #1,R15AV  
 0A3A0 F1209F0E (01619) STORF #2,R25AV  
 0A3A2 F1309F10 (01620) STORF #3,R35AV  
 0A3A3 F1409F12 (01621) STORF #4,R45AV  
 0A3A6 F1509F14 (01622) STORF #5,R55AV  
 0A3A8 F1609F16 (01623) STORF #6,R65AV  
 0A3AA F1709F18 (01624) STORF #7,R75AV  
 0A3AC 0E70 (01625) RFT  
 0A3AD 0800 (01626) FVEF  
 0A3AF AF109F0C (01627) LOAD #1,R15AV  
 0A3B0 A6209F0E (01628) LOAD #2,R25AV  
 0A3B2 A6309F10 (01629) LOAD #3,R35AV  
 0A3B3 A6409F12 (01630) LOAD #4,R45AV  
 0A3B6 A6509F14 (01631) LOAD #5,R55AV  
 0A3B8 A6609F16 (01632) LOAD #6,R65AV  
 0A3BA A6709F18 (01633) LOAD #7,R75AV  
 0A3BC 50208C48 (01634) MOVIR #2,SEFUS-2+1,SE#12  
 0A3BE 9030003D (01635) MOVIR #4+1,SE#1-1  
 0A3C0 172416CA (01636) RMVFL #2,R3,SE#2+1,SE#12  
 0A3C2 DF000580 (01637) \* MOVLM CLMSG2,SYSFLGS  
 0A3C3 2021 (01638) \*MRS6.0,AFDNFTLS  
 0A3C5 0800 (01641) HOP #1+34  
 0A3C6 CC000580 (01642) FVEF  
 0A3C8 F1109F0C (01643) \*MOVZM AFDNFTLS  
 0A3CA F1209F0E (01644) STORF #1,R15AV  
 0A3CC F1309F10 (01645) STORF #2,R25AV  
 0A3CE F1409F12 (01646) STORF #3,R35AV  
 0A3D0 F1509F14 (01647) STORF #4,R45AV  
 0A3D2 F1609F16 (01648) STORF #5,R55AV  
 0A3D4 F1709F18 (01649) STORF #6,R65AV  
 0A3D6 0E70 (01650) STORF #7,R75AV  
 0A3D7 0800 (01651) FVEF  
 0A3DA A6109F0C (01652) LOAD #1,R15AV  
 0A3DC A6209F0E (01653) LOAD #2,R25AV  
 0A3DE A6309F10 (01654) LOAD #3,R35AV  
 0A3E0 A6409F12 (01655) LOAD #4,R45AV  
 0A3E2 A6509F14 (01656) LOAD #5,R55AV  
 0A3E4 A6609F16 (01657) LOAD #6,R65AV

HAS AFDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R HFT

```

0A313 002009E1R (01658)
0A316 002008E0A (01659)
0A318 00400003C (01660)
0A31A 072001736 (01661)
0A31C 000005000 (01662)
0A31E 2021 (01663)
0A31F 0000 (01664)
0A320 CC000580 (01666)
0A322 F1309E0C (01667)
0A324 F1209E0E (01668)
0A326 F1309E10 (01669)
0A328 F1409E12 (01670)
0A32A F1509E14 (01671)
0A32C F1609E16 (01672)
0A32E F1709E18 (01673)
0A330 0E70 (01674)
0A332 0800 (01675)
0A334 AC109E0C (01676)
0A336 A6209E0E (01677)
0A338 A7309E10 (01678)
0A33A A8409E12 (01679)
0A33C A9509E14 (01680)
0A33E A6709E18 (01682)
0A340 F0200567 (01683)
0A342 8000A490 (01684)
0A344 CC000C00 (01685)
0A346 07200140C (01687)
0A348 004000580 (01688)
0A34A 2021 (01689)
0A34C 0800 (01690)
0A34E CC000580 (01692)
0A350 F1309E0C (01693)
0A352 F1309E10 (01695)
0A354 F1409E12 (01696)
0A356 F1509E14 (01697)
0A358 F1609E16 (01698)
0A35A F1709E18 (01699)
0A35C 0E70 (01700)
0A35E 0800 (01701)

```

HAS APDOME HAPPENED?  
 NO, HOP TO EVEN LOC AFTR  
 R RFT

STORE AFIC TEMP. IN R2  
 FND OF "TRANSMITTER"  
 MAKE SYNC HIT 0 FOR BUFF  
 FR 1

HAS APDOME HAPPENED?  
 NO, HOP TO EVEN LOC AFTR  
 R RFT

0A424 A6109F0C (01702)  
 0A430 A6209F0F (01703)  
 0A437 A6309F10 (01704)  
 0A443 A6409F12 (01705)  
 0A450 A6509F14 (01706)  
 0A456 A6609F16 (01707)  
 0A463 A6709F18 (01708)  
 0A470 A6809F1A (01709)  
 0A476 A6909F1C (01710)  
 0A483 A7009F1E (01711)  
 0A490 A7109F20 (01712)  
 0A497 A7209F22 (01713)  
 0A504 A7309F24 (01714) ;  
 0A511 A7409F26 (01715)  
 0A518 A7509F28 (01716)  
 0A525 A7609F2A (01717)  
 0A532 A7709F2C (01718)  
 0A539 A7809F2E (01719)  
 0A546 A7909F30 (01720)  
 0A553 A8009F32 (01721)  
 0A560 A8109F34 (01722)  
 0A567 A8209F36 (01723)  
 0A574 A8309F38 (01724)  
 0A581 A8409F3A (01725)  
 0A588 A8509F3C (01726)  
 0A595 A8609F3E (01727)  
 0A602 A8709F40 (01728)  
 0A609 A8809F42 (01729)  
 0A616 A8909F44 (01730)  
 0A623 A9009F46 (01731)  
 0A630 A9109F48 (01732)  
 0A637 A9209F4A (01733)  
 0A644 A9309F4C (01734)  
 0A651 A9409F4E (01735)  
 0A658 A9509F50 (01736)  
 0A665 A9609F52 (01737)  
 0A672 A9709F54 (01738) ;  
 0A679 A9809F56 (01739)  
 0A686 A9909F58 (01740)  
 0A693 AA009F5A (01741)  
 0A700 AA109F5C (01742)  
 0A707 AA209F5E (01743)  
 0A714 AA309F60 (01744)  
 0A721 AA409F62 (01745)

LOAD R1,R1SAV  
 LOAD R2,R2SAV  
 LOAD R3,R3SAV  
 LOAD R4,R4SAV  
 LOAD R5,R5SAV  
 LOAD R6,R6SAV  
 LOAD R7,R7SAV  
 MOVIP R2,SENS-2\*1,SEN12  
 MOVIP R3,LESEN1-1  
 MOVIP R2,R4,SEN15+LS+R12  
 SENSI,0,APDNF15  
 HOP #1,+34  
 FVFD  
 MOVZM APDNF15  
 STORE R1,R1SAV  
 STORE R2,R2SAV  
 STORE R3,R3SAV  
 STORE R4,R4SAV  
 STORE R5,R5SAV  
 STORE R6,R6SAV  
 STORE R7,R7SAV  
 FVFN  
 LOAD R1,R1SAV  
 LOAD R2,R2SAV  
 LOAD R3,R3SAV  
 LOAD R4,R4SAV  
 LOAD R5,R5SAV  
 LOAD R6,R6SAV  
 LOAD R7,R7SAV  
 MOVIP R2,SENS-2\*2\*1,SEN12  
 MOVIP R4,LESEN1-2  
 MOVIP R2,R4,SEN15+2\*1,SEN12  
 SENSI,0,APDNF15  
 HOP #1,+34  
 FVFD  
 MOVZM APDNF15  
 STORE R1,R1SAV  
 STORE R2,R2SAV  
 STORE R3,R3SAV  
 STORE R4,R4SAV  
 STORE R5,R5SAV

HAS ADDONE HAPPENED?  
 NO, HOP TO EVEN LOC AFTE  
 R RET

HAS ADDONE HAPPENED?  
 NO, HOP TO EVEN LOC AFTE  
 R RET

```

0A47C 41604F16 (01746)
0A37F 41704F14 (01747)
0A480 0470 (01748)
0A381 0400 (01749)
0A382 A6104F0C (01750)
0A483 A6204F0E (01751)
0A486 A6304F10 (01752)
0A488 A6404F12 (01753)
0A48A A6504F14 (01754)
0A48C A6604F16 (01755)
0A48E A6704F18 (01756)
0A490 2000 (01757) XMITSE
0A491 0200 (01758)
0A492 04C0 (01759)
0A494 40004F30 (01760)
(01761) ?
(01762)

```

```

STORE R6,R6SAV
STORE R7,R7SAV
RFT
EVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
NOP
CC5 5
SC5 4
JMP TRANSHA
EJECT

```

```

*** FOR DEHUG ONLY ***
FROM MPR2...
TO MRB=1
RETURN TO XMIT UPON THE
NEXT G3 INTERRUPT

```



```
0A445 0400 (01763) *
0A446 C000574 (01764) *
0A447 F8020581 (01765) ?
0A448 F8020581 (01766) ?
0A449 F8020568 (01767) ?
0A44A F8020568 (01768) ?
0A44B F8020568 (01769) ?
0A44C F8020568 (01770) ?
0A44D F8020568 (01771) ?
0A44E F8020568 (01772) ?
0A44F F8020568 (01773) ?
0A450 F8020568 (01774) *
0A451 F8020568 (01775) *
0A452 F8020568 (01776) ?
0A453 F8020568 (01777) *
0A454 F8020568 (01778) *
0A455 F8020568 (01779) ?
0A456 F8020568 (01780) ?
```

```
CLEAR RCVR GO FLAG
TRIG. "TIMER" FL THAT CSP
U RCVR CODE IS ON
RESET THE SYNTHESIZER RE
ADY FLAG
```

```

0A3AC F03984CF (01741) MOVEM SPTS(0),SPTS(5)
0A3AD 02A0 (01742) CLS 2
0A3AE 0400 (01743) SCS 4
0A3AF 0200 (01744) CLS 5
0A3B0 0200 (01745) SCS 4
0A3B1 06C0 (01746) MOVEM R2,SPTS
0A3B2 0200589 (01747) CMVEM R2,VDATAS
0A3B3 0220058F (01748) *****
0A3B4 01749 *****
0A3B5 01749 *****
0A3B6 01749 *****
0A3B7 01749 *****
0A3B8 01749 *****
0A3B9 01749 *****
0A3BA 01749 *****
0A3BB 01749 *****
0A3BC 01749 *****
0A3BD 01749 *****
0A3BE 01749 *****
0A3BF 01749 *****
0A3C0 01749 *****
0A3C1 01749 *****
0A3C2 01749 *****
0A3C3 01749 *****
0A3C4 01749 *****
0A3C5 01749 *****
0A3C6 01749 *****
0A3C7 01749 *****
0A3C8 01749 *****
0A3C9 01749 *****
0A3CA 01749 *****
0A3CB 01749 *****
0A3CC 01749 *****
0A3CD 01749 *****
0A3CE 01749 *****
0A3CF 01749 *****
0A3D0 01749 *****
0A3D1 01749 *****
0A3D2 01749 *****
0A3D3 01749 *****
0A3D4 01749 *****
0A3D5 01749 *****
0A3D6 01749 *****
0A3D7 01749 *****
0A3D8 01749 *****
0A3D9 01749 *****
0A3DA 01749 *****
0A3DB 01749 *****
0A3DC 01749 *****
0A3DD 01749 *****
0A3DE 01749 *****
0A3DF 01749 *****
0A3E0 01749 *****
0A3E1 01749 *****
0A3E2 01749 *****
0A3E3 01749 *****
0A3E4 01749 *****
0A3E5 01749 *****
0A3E6 01749 *****
0A3E7 01749 *****
0A3E8 01749 *****
0A3E9 01749 *****
0A3EA 01749 *****
0A3EB 01749 *****
0A3EC 01749 *****
0A3ED 01749 *****
0A3EE 01749 *****
0A3EF 01749 *****
0A3F0 01749 *****
0A3F1 01749 *****
0A3F2 01749 *****
0A3F3 01749 *****
0A3F4 01749 *****
0A3F5 01749 *****
0A3F6 01749 *****
0A3F7 01749 *****
0A3F8 01749 *****
0A3F9 01749 *****
0A3FA 01749 *****
0A3FB 01749 *****
0A3FC 01749 *****
0A3FD 01749 *****
0A3FE 01749 *****
0A3FF 01749 *****

```

```

FROM SRR=1...
TO SRR=2
FROM MRR=7...
TO MRR=1
*STORE TEMP, SFLG IN R2
COMPARE IT WITH RDATA
*****

```

```

STOP R2 IN SFLG

```

```

IS SFLG =0?
UNLOAD RUF1 IF SFLG=0
SET UP TO UNLOAD RCV2
*MOVE 1ST HALF-WORD, ODD
*SET UP FOR RMOVE

```

```

*START EVEN
HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R PFT

```

```

MOVEM R4,ESFN2=-1
CMVEM RDATA5
JMP FL11S,FO
MOVEM R2,RCV2S=-1
JMP R2,RCV2S
JMP FL11S,FO
*MOVE1, R2,R4,RCV2S+1
SRRS1,0,AFDSEFLS
RUF FL11S

```

```

EVEN
MOVEM ADDR(FLS)
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV
STORE R7,R7SAV
RPT
EVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV

```



04526	06309F12	(01869)	LOAD R4,R5SAV		
04528	06509F14	(01870)	LOAD R5,R5SAV		
0452A	06609F16	(01871)	LOAD R6,R5SAV		
0452C	06709F18	(01872)	LOAD R7,R7SAV		
0452E	0000A592	(01874)	JMP FL17S		
		(01875)			
		(01876)			
04530	00201146	(01877)	FL11S		
04532	07284F40	(01878)	MOVIR R2,RECVIS-2		
04534	0E0005F0	(01879)	MOVIR R2,R4,RF1S		
04536	2005	(01880)	SMPSL 0,APDNF1S		
04537	0800	(01881)	POP R1+6		
04538	0C000580	(01882)	EVEN		
0453A	0E70	(01883)	MOVZM APDNF1S		
0453C	0800	(01884)	RET		
0453E	00201270	(01885)	EVEN		
04540	0040004C	(01886)	MOVIR R2,RECVIS-2+1,SEN22		
04542	17284F7A	(01887)	MOVIR R4,ISEN2-1		
04544	04000580	(01888)	MOVIR R2,R4,RF1S+1,SEN22		
04546	2005	(01889)	SMPSL 0,APDNF1S		
04548	0800	(01890)	POP R1+6		
0454A	0C000580	(01891)	EVEN		
0454C	0E70	(01892)	MOVZM APDNF1S		
0454E	0800	(01893)	RET		
04550	0020127A	(01894)	EVEN		
04552	0040003E	(01895)	MOVIR R2,RECVIS-2+2+1,SEN22		
04554	07284F7A	(01896)	MOVIR R4,ISEN2		
04556	0E000580	(01897)	MOVIR R2,R4,RF1S+2+1,SEN22		
04558	2005	(01898)	SMPSL 0,APDNF1S		
0455A	0800	(01899)	POP R1+6		
0455C	0C000580	(01900)	EVEN		
0455E	0E70	(01901)	MOVZM APDNF1S		
04560	0800	(01902)	RET		
04562	00201365	(01903)	EVEN		
04564	08204F70	(01904)	MOVIR R2,RECVIS-2+3+1,SEN22+1		
04566	2721	(01905)	MOVIR R2,RF1S+1,SEN-1		
04568	0800	(01906)	POP R2,1		
0456A	08204F70	(01907)	EVEN		
0456C	0E000580	(01908)	MOVIR R2,RF1S+1,SEN-1		
0456E	2005	(01909)	SMPSL 0,APDNF1S		
04570	0800	(01910)	POP R1+6		
04572	0C000580	(01911)	EVEN		
04574	0E70	(01912)	MOVZM APDNF1S		
04576			RET		

SET UP TO UNLOAD RECVI  
HAS ADDONE. HAPPENED?

HAS ADDONE. HAPPENED?

HAS ADDONE. HAPPENED?

MOVE LAST HALF WORD

TO ROTH BUFFERS  
HAS ADDONE. HAPPENED?

```

0A567 0600 (01913)
0A568 902011F6 (01914)
(01915) ?
0A56A 9040004C (01916)
0A56C 172891F2 (01917)
0A56E 04000580 (01918)
0A570 2005 (01919)
0A571 0800 (01920)
0A572 C000580 (01921)
0A574 0670 (01922)
0A575 0800 (01923)
0A576 90201270 (01924)
0A578 9040004C (01925)
0A57A 172891DC (01926)
0A57C 14000580 (01927)
0A57E 2005 (01928)
0A57F 0800 (01929)
0A580 C000580 (01930)
0A582 0670 (01931)
0A583 0800 (01932)
0A584 902012FA (01933)
0A586 9040004C (01934)
0A588 17289256 (01935)
0A58A 04000580 (01936)
0A58C 2005 (01937)
0A58D 0800 (01938)
0A58E C000580 (01939)
0A590 0670 (01940)
0A591 0800 (01941)
0A592 0280 (01942)
0A593 06A0 (01943)
(01944) *SYNC CHECK AND UPDATE SEARCH IF NECESSARY.
(01945) ? R1 - PTR TO THE PRODMR BUFFER BEING SEARCHED
(01946)
(01947) ? P2 - OFFSET INTO THE PRODMR, RSPFF, RSSSS BUFFERS
(01948) ? P3 - SCRATCH AC
(01949) ? P4 - MAXCNT, THE MAX. OF RSSSS(CI)
(01950) ? P5 - MAX, THE # OF INSTANCES OF MAXCNT IN RSSSS(CI)
(01951) ? P6 - IPAX, THE FRAME OFFSET WHERE MAXCNT WAS FOUND
(01952) ? VARIANTEG
(01953) ? SYNC - OBTAINING OF FRAME OFFSET TO THE SYNC WORD IN THE
(01954) ? PRODMR BUFFER
0A594 90108F80 (01955)
(01956) ?

```

MOVE SAME BLOCKS TO RUF

HAS APDONE HAPPENED?

HAS APDONE HAPPENED?

HAS APDONE HAPPENED?

FROM SHR=2...  
TO SHR=1

PREPARE TO USE RFI AS NEW  
W BUFFER





```

(02095) ?
0A5F4 9050000 (02096)
(02047) ?
0A5FC 3004 (02048)
(02049) ?
0A5FE 2006 (02050)
(02051) ?
(02052)
0A5FF 4110A04 (02053)
0A600 2051 (02054)
(02055) ?
0A601 2002 (02056)
(02057) ?
(02058)
0A602 00049704 (02059)
0A604 00049704 (02060)
0A606 2011 (02062)
0A607 0800 (02063)
0A608 0020A08 (02064)
(02065) ?
0A609 02400000 (02066)
0A60C 0030A02A (02069)
0A60E 02500001 (02070)
0A610 0110A02A (02071)
0A612 00500570 (02072)
(02073) ?
0A614 00000577 (02074)
0A616 00300004 (02075)
0A618 0030057C (02076)
(02077) ?
0A61A 00310000 (02078)
0A61C 00300579 (02079)
(02080) ?
0A61E 00509004 (02081)
(02082) ?
0A620 00000576 (02083)
0A622 00C00001 (02084)
(02085) ?
0A624 70320001 (02086)
(02087) ?
0A626 0030057A (02088)

```

```

F NEW MAXCNT
:RMAX ← Y (UNIQUE SO FA
R)
:AND WF SAVF THE OFFSET
IN IMAX

```

```

:MAXCNT = R5SS5(I)?
:YES, SO THE MAX ISN'T U
NIQUE.

```

```

:CLEAR R5SS5(I)
:SAVE NEW DATA HIT IN R5
SPF(I)
:DECR #MODEM HUFFER PTR
:DECR OFFSET AND TEST IF
IT IS A UNIQUE MAX? IF SO, WE HAVE

```

```

:MAXCNT ≥ AC0TH?
:NO
:YES: IS NMAX=1?
:NO
:YES: WF HAVE SYNC. SET
R5YN TO +1
:SAVE OFFSET IN SYNC5
:INIT FR5YN TO COUNT ERR
ORS

```

```

SET # OF FRAMES TO WAIT
BEFORE PUTTING OUT SPEECH
SET FIRST FRAME AFTER SY
NC FLAG
: FOR SUPDAT NEXT FRAMF
:R1 POINTS TO WORD HOLDI
NG SYNC
: PICK UP SYNC HIT AND SA
VE IT IN
:OL5YN FOR USE IN SUPDAT

```



```

0A62E 00005760 (02080) JMP R4CVSKA
(02090) ?
(02091) FVLA
0A62E 00200576 (02092) SSS00 MOVW R2,R2+SYN
(02093) ?
0A62E 00000576 (02094) JMP R4CVSKA
(02095) ?
(02096) ?
(02097) ? SUPDAT: CHECK MODEM BUFFER TO SEE IF THE DATA HIT AT THE
(02098) ? EXPECTED SYNC POSITION (START OF BUFFER+SYNCS) HAS THE EXPECTED
(02099) ? VALUE. IF THERE ARE LSTRK ERRORS WITHOUT 2 CONSECUTIVE CORRECT
(02100) ? COMPATIBIONS, CLEAR R5YR TO SIGNIFY LOSS OF FRAME SYNCHRONIZATION.
(02101) ? ENTER WITH R1 POINTING TO THE LATEST RMODEM BUFFER. ALSO, THE
(02102) ? WORD SYNCS HOLDS THE OFFSET IN THE FRAME THAT POINTS TO THE
(02103) ? SYNC WORD TO BE VERIFIED.
(02104) ? CALLED FROM FIVE MODEM INTERRUPT SERVICE ROUTINE.
(02105) ? VARIABLES:
(02106) ? SYNC = BEGINNING OF FRAME OFFSET
(02107) ? DLSYN = SYNC HIT FROM PREVIOUS FRAME
(02108) ? LSTEP = %Z IF SYNC ERROR ON PREVIOUS FRAME
(02109) ? FRSYB = COUNTS SYNC ERRORS (FROM LSTRK DOWN TO 0)
(02110) ? EVER
0A62E FC100577 (02111) SUPDAT ADDR R1,SYNCS
(02112) ?
0A630 00A06654 (02113) MOVW R2,R2+R00S2
(02114) ?
0A632 0420057A (02115) XORW R2,R2+DLSYR
0A634 2C04 (02116) SRRS 0,R2
(02117) ?
0A635 2004 (02118) RCP SUSERR
0A636 0200057B (02119) CRRZ LSTRK
0A638 0110063E (02120) JMP SUSERR+R2
0A63A 90300004 (02121) MOVW R3,LSTRK
0A63C 0030057C (02122) MOVW R3,LRSYB
(02123) ?
0A63E 0000057B (02124) SUSLEF MOVZM LSTRK
(02125) ?
0A640 2009 (02126) HOP SUSVAL
(02127) ?
0A641 0800 (02128) FVLA
0A642 90200001 (02129) SUSERR MOVW R2,R1
(02130) ?
0A644 0020057B (02131) MOVW R2,LSTRK
0A646 0A00057C (02132) DRCW R5SYB

```

PRO SYNC YFT: SET TO -1  
(=SEARCH)  
? FOR GOOD MEASURE

R1 NOW POINTS TO NEW SY  
NC WORD  
0(2) INDEXED BY RUFN+SYN  
CS  
?XOP WITH LAST SYNC HIT  
?SKIP IF DATA BIT SET (G  
000 SYNC)  
?WAS THERE AN ERROR LAST  
?YES  
?NO:  
? CORRECT FRAMES, SO RES  
FT ERROR COUNT  
?PREVIOUS NONERROR FOR N  
EXT FRAME  
?COUNT HERE ON SYNC HIT E  
RROR  
?REMEMBER ERROR FOR NEXT  
?HAVE WE COUNTED LSTRK E

PROB?
EYES, THEREFORE WE'VE LO
ST SYNC
; COMPLEMENT OLSYN IN PRE
PARATION
; FOR USE NEXT FRAME

O(2) INDEXED BY R1

FROM SRH=2,...
TO SRH=1
FROM MRH=2,...
TO MRH=1

NEW DATA IN BUFF 2, SO R
EAD BUFF1+SYNC

BUFF1(BUFF3) NEW, SO REA
D BUFF2+SYNC

HAS APDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

00A3E F120A880 (02143) ; JPP SUSLS, R2
(02145) ;
00A3A 0010A001 (02146) SUSVAL MOVIE R1, R1+SCD
(02147) ;
00A3C F510057A (02138) MOVIE R1, OLSYN
00A34 0E70 (02139) R1, R1+R
(02140) ;
00A3F 0800 (02141) EVEN
00A50 C00057R (02142) SUSLS MOVZM PSYD
00A52 0E70 (02143) RETURN
00A53 0800 (02144) EVEN
00A54 00220000 (02145) RUCS? ADDR O(1,2)
(02146) ;
(02147) UNPREFS EVEN
00A55 0200 (02148) CCS 3
00A57 0800 (02139) CCS 2
00A5M 0200 (02150) CCS 5
00A5N 0800 (02151) CCS 4
00A5A F200058F (02152) COMPZ PHATAS
00A5C 00100662 (02153) JPP F1165, PD
00A5E 0020R7E (02154) MOVIE R2, R15-1
(02155) ;
00A60 0000066A (02156) JPP F117E
00A62 0020AEE0 (02157) FL16S MOVIE R2, R125-1
(02158) ;
00A64 F0700577 (02159) FL17S MOVIE R2, SYRCS
00A66 00400030 (02160) MOVIE R4, LSEMT-1
00A68 052R554A (02161) MOVIE R2, F1, R1CVS
00A6A 0E000580 (02162) SREGI, 0, ADDRFLS
00A6C 2021 (02163) MOV #1, R4
(02164) ;
00A6D 0800 (02165) EVEN
00A6E C0000580 (02166) MOVZM ADDRFLS
00A70 F1109E0C (02167) STORE R1, R15AV
00A72 F1200E0E (02168) STORE R2, R25AV
00A74 F1300E10 (02169) STORE R3, R35AV
00A76 F1400E13 (02170) STORE R4, R45AV
00A7M F1500E16 (02171) STORE R5, R55AV
00A7A F1600E1A (02172) STORE R6, R65AV
00A7C F1700E1E (02173) STORE R7, R75AV
00A7E 0E70 (02174) RET
00A7F 0800 (02175) EVEN
00A80 A6109E0C (02176) LOAD R1, R15AV

0A692 A6209F0F (02177) LOAD K2,P2SAV  
 0A693 A6309F10 (02178) LOAD K3,P3SAV  
 0A694 A6409F12 (02179) LOAD K4,P4SAV  
 0A695 A6509F14 (02180) LOAD K5,P5SAV  
 0A696 A6609F16 (02181) LOAD K6,P6SAV  
 0A697 A6709F18 (02182) LOAD K7,P7SAV  
 0A698 A6809F1A (02183) MOVE P4,LSFN1-1  
 0A699 15289F57 (02184) MOVE P2,P4,RECVS+LSFN1  
 0A69A 1F000F50 (02185) SWRSL 0,APDRFLS  
 0A69B 2021 (02186) HOP R1+34  
 0A69C 0800 (02187) ;  
 0A69D 0800 (02188) EVFP  
 0A69E CC000F50 (02189) MOVZM APDRFLS  
 0A69F F1109F0C (02190) STORE R1,R1SAV  
 0A6A0 F1209F0E (02191) STORE K2,P2SAV  
 0A6A1 F1309F10 (02192) STORE K3,P3SAV  
 0A6A2 F1409F12 (02193) STORE K4,P4SAV  
 0A6A3 F1509F14 (02194) STORE K5,P5SAV  
 0A6A4 F1609F16 (02195) STORE K6,P6SAV  
 0A6A5 F1709F18 (02196) STORE K7,P7SAV  
 0A6A6 0F70 (02197) RFT  
 0A6A7 0800 (02198) EVFP  
 0A6A8 A6109F0C (02199) LOAD K1,P1SAV  
 0A6A9 A6209F0E (02200) LOAD K2,P2SAV  
 0A6AA A6309F10 (02201) LOAD K3,P3SAV  
 0A6AB A6409F12 (02202) LOAD K4,P4SAV  
 0A6AC A6509F14 (02203) LOAD K5,P5SAV  
 0A6AD A6609F16 (02204) LOAD K6,P6SAV  
 0A6AE A6709F18 (02205) LOAD K7,P7SAV  
 0A6AF 0010003D (02206) MOVE P4,LSFN1-1  
 0A6B0 05289F50 (02207) MOVE P2,P4,RECVS+2\*LSFN1  
 0A6B1 1F000F50 (02208) SWRSL 0,APDRFLS  
 0A6B2 2021 (02209) HOP R1+34  
 0A6B3 0800 (02210) ;  
 0A6B4 0800 (02211) EVFP  
 0A6B5 CC000F50 (02212) MOVZM APDRFLS  
 0A6B6 F1109F0C (02213) STORE R1,R1SAV  
 0A6B7 F1209F0E (02214) STORE K2,P2SAV  
 0A6B8 F1309F10 (02215) STORE K3,P3SAV  
 0A6B9 F1409F12 (02216) STORE K4,P4SAV  
 0A6BA F1509F14 (02217) STORE K5,P5SAV  
 0A6BB F1609F16 (02218) STORE K6,P6SAV  
 0A6BC F1709F18 (02219) STORE K7,P7SAV  
 0A6BD 0F70 (02220) RFT

HAS APDRNF HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RFT

HAS APDRNF HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RFT

```

0A6C4 0800 (02221)
0A6D0 A6109F0C (02222)
0A6E2 A6209F0F (02223)
0A6E3 A6309F10 (02224)
0A6E6 A6309F12 (02225)
0A6E8 A6509F1A (02226)
0A6EA A6609F16 (02227)
0A6EC A6709F18 (02228)
0A6E5 -0400040 (02229)
0A6E0 D52895FF (02230)
0A6E2 0E000580 (02231)
0A6E4 2021 (02232)
0A6E5 0800 (02233) ?
0A6E6 CC000580 (02234)
0A6E8 F1109F0C (02235)
0A6EA F1209F0F (02236)
0A6EC F1309F10 (02237)
0A6EE F1409F12 (02238)
0A6F0 F1509F14 (02239)
0A6F2 F1609F16 (02240)
0A6F4 F1709F18 (02241)
0A6F6 0F70 (02242)
0A6F7 0800 (02243)
0A6E8 A6109F0C (02244)
0A6EA A6209F0F (02245)
0A6EC A6309F10 (02246)
0A6EE A6409F12 (02247)
0A700 A6509F14 (02248)
0A702 A6609F16 (02249)
0A704 A6709F18 (02250)
0A706 90400030 (02251)
0A708 D528953C (02252)
0A70A 0E000580 (02253)
0A70C 2021 (02254)
0A70E 02255) ?
0A70F 0800 (02256)
0A710 CC000580 (02257)
0A712 F1109F0C (02258)
0A714 F1209F0F (02259)
0A716 F1309F10 (02260)
0A718 F1409F12 (02261)
0A71A F1509F14 (02262)
0A71C F1609F16 (02263)
0A71E F1709F18 (02264)

```

HAS ADDING HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

HAS ADDING HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

```

EVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
MOVW R4,LSF01-1
MOVW R2,R4,RECVS+4,LSFN1
SMRSL 0,APDNFELS
HOP #1,+34
EVEN
MOVW APDNFELS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV
STORE R7,R7SAV
PRT
EVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
MOVW R4,LSF01-1
MOVW R2,R4,RECVS+4,LSFN1
SMRSL 0,APDNFELS
HOP #1,+34
EVEN
MOVW APDNFELS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV

```

0A71C F1709F14 (02265)	STOP R7,R7SAV
0A71E 0E70 (02266)	RET
0A71F 0800 (02267)	EVFN
0A720 A6109F0C (02268)	LOAD R1,R1SAV
0A722 A6209F0E (02269)	LOAD R2,R2SAV
0A724 A6309F10 (02270)	LOAD R3,R3SAV
0A726 A6409F12 (02271)	LOAD R4,R4SAV
0A728 A6509F14 (02272)	LOAD R5,R5SAV
0A72A A6609F16 (02273)	LOAD R6,R6SAV
0A72C A6709F18 (02274)	LOAD R7,R7SAV
0A72E 9040603A (02275)	MOVW R2,R4,RE,CV5+5*1,SPN1
0A730 D528947A (02276)	MOVW R2,R4,RE,CV5+5*1,SPN1
0A732 DE000580 (02277)	SRST. 0,ADDRESS
0A734 7C21 (02278)	HOP #1+14
0A735 0800 (02279)	EVFN
0A736 CC000580 (02280)	MOVW ADDRESS
0A738 F1709F0C (02281)	STOP R1,R1SAV
0A73A F1709F0E (02282)	STOP R2,R2SAV
0A73C F1309F10 (02283)	STOP R3,R3SAV
0A73E F1409F12 (02284)	STOP R4,R4SAV
0A740 F1509F14 (02285)	STOP R5,R5SAV
0A742 F1609F16 (02286)	STOP R6,R6SAV
0A744 F1709F18 (02287)	STOP R7,R7SAV
0A746 0E70 (02288)	RET
0A748 0800 (02289)	EVFN
0A74A A6109F0C (02290)	LOAD R1,R1SAV
0A74C A6209F0E (02291)	LOAD R2,R2SAV
0A74E A6309F10 (02292)	LOAD R3,R3SAV
0A750 A6409F12 (02293)	LOAD R4,R4SAV
0A752 A6509F14 (02294)	LOAD R5,R5SAV
0A754 A6609F16 (02295)	LOAD R6,R6SAV
0A756 A6709F18 (02296)	LOAD R7,R7SAV
0A758 8000A758 (02297)	JMP DSRDPS NOT ENOUGH TIME TO CORRECT
0A75A 8000A758 (02298)	JMP DSRDPS NOT ENOUGH TIME TO CORRECT
0A75C 8000A758 (02299)	JMP DSRDPS NOT ENOUGH TIME TO CORRECT
0A75E 8000A758 (02300)	JMP DSRDPS NOT ENOUGH TIME TO CORRECT

HAS APODNE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

CORRECT SIDERAND

```

(02401) ***** RC0 DECODING OF SIDEMAND & PART OF MAINMAND DATA
(02402) HCHD05  EVFM
0A75M 001000001 (02403)
0A75A 00000000A (02404)
0A75C 000000000 (02405) ?
0A75E 2021 (02406) HCHD15  SRRSL 0,APDPLS
(02407) HCHD15  WDP #1,114
0A75F 0000 (02408) ?
0A760 000000000 (02409)  EVFM
0A762 000000000 (02410)  MOVZM APDPLS
0A764 000000000 (02411)  STORE R1,R1SAV
0A766 000000000 (02412)  STORE R2,R2SAV
0A768 000000000 (02413)  STORE R3,R3SAV
0A76A 000000000 (02414)  STORE R4,R4SAV
0A76C 000000000 (02415)  STORE R5,R5SAV
0A76E 000000000 (02416)  STORE R6,R6SAV
0A770 000000000 (02417)  STORE R7,R7SAV
0A772 000000000 (02418)  EVFM
0A774 000000000 (02419)  LOAD R1,R1SAV
0A776 000000000 (02420)  LOAD R2,R2SAV
0A778 000000000 (02421)  LOAD R3,R3SAV
0A77A 000000000 (02422)  LOAD R4,R4SAV
0A77C 000000000 (02423)  LOAD R5,R5SAV
0A77E 000000000 (02424)  LOAD R6,R6SAV
0A780 000000000 (02425)  LOAD R7,R7SAV
0A782 000000000 (02426)  MOVZM R5,1R6
0A784 000000000 (02427)  CLP R2
0A786 000000000 (02428)  CLP R3
0A788 000000000 (02429)  CLP R4
0A78A 000000000 (02430)  EVFM
0A78C 000000000 (02431)  SRRSL 0,RFCVS(R1)
0A78E 000000000 (02432)  HCHD25  HOP HCHD35
0A790 000000000 (02433)  EVFM
0A792 000000000 (02434)  MOVZM R6,TRFSMS(R5)
0A794 000000000 (02435)  XORRR R2,R6
0A796 000000000 (02436)  EVFM
0A798 000000000 (02437)  MOVZM R6,TRFSMS(R5)
0A79A 000000000 (02438)  XORRR R3,R6
0A79C 000000000 (02439)  XORRR R4,R7
0A79E 000000000 (02440)  SRRSL 0,APDPLS
0A7A0 000000000 (02441)  HCHD35  HOP #1,114
0A7A2 000000000 (02442)  EVFM
0A7A4 000000000 (02443)  ?
0A7A6 000000000 (02444)  ?

```

```

SET INPUT BUFFER POINTER
CLEAR FROM COUNTERS(R1),
(R2)
HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTR
R RPT

```

```

R5=4 OF POWER SUM TABLE
CLEAR POWER SUM S1
CLEAR POWER SUM S3
CLEAR POWER SUM S5

```

```

READ INPUT DATA

```

```

HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTR
R RPT

```

```

0A706 0000580 (02335) MOVZM APDRFELS
0A707 0109400 (02336) STORE R1,R1SAV
0A708 0109400 (02337) STORE R2,R2SAV
0A709 0109410 (02338) STORE R3,R3SAV
0A70A 0109410 (02339) STORE R4,R4SAV
0A70B 0109410 (02340) STORE R5,R5SAV
0A70C 0109410 (02341) STORE R6,R6SAV
0A70D 0109410 (02342) STORE R7,R7SAV
0A70E 0000 (02343) PRT
0A70F 0000 (02344) EVEN
0A710 0000 (02345) LOAD R1,R1SAV
0A711 0000 (02346) LOAD R2,R2SAV
0A712 0000 (02347) LOAD R3,R3SAV
0A713 0000 (02348) LOAD R4,R4SAV
0A714 0000 (02349) LOAD R5,R5SAV
0A715 0000 (02350) LOAD R6,R6SAV
0A716 0000 (02351) LOAD R7,R7SAV
0A717 0000 (02352) BCR R1,1
0A718 0000 (02353) DECF R5,3
0A719 0000 (02354) JRF BCR02S,CF
0A71A 0000 (02355) POWER SUMS(S1,S3,S5) APF CALCULATED AND STORED IN R2,R3,R4
0A71B 0000 (02356) CHECK THE RANGE OF PHURS
0A71C 0000 (02357) SRRSE 0,APDRFELS
0A71D 0000 (02358) HOP R1,R34
0A71E 0000 (02359) EVEN
0A71F 0000 (02360) MOVZM APDRFELS
0A720 0109400 (02361) STORE R1,R1SAV
0A721 0109400 (02362) STORE R2,R2SAV
0A722 0109410 (02363) STORE R3,R3SAV
0A723 0109410 (02364) STORE R4,R4SAV
0A724 0109410 (02365) STORE R5,R5SAV
0A725 0109410 (02366) STORE R6,R6SAV
0A726 0109410 (02367) STORE R7,R7SAV
0A727 0000 (02368) PRT
0A728 0000 (02369) EVEN
0A729 0000 (02370) LOAD R1,R1SAV
0A72A 0000 (02371) LOAD R2,R2SAV
0A72B 0000 (02372) LOAD R3,R3SAV
0A72C 0000 (02373) LOAD R4,R4SAV
0A72D 0000 (02374) LOAD R5,R5SAV
0A72E 0000 (02375) LOAD R6,R6SAV
0A72F 0000 (02376) LOAD R7,R7SAV
0A730 0000 (02377) PRT
0A731 0000 (02378) EVEN
0A732 0000 (02379) LOAD R1,R1SAV
0A733 0000 (02380) LOAD R2,R2SAV
0A734 0000 (02381) LOAD R3,R3SAV
0A735 0000 (02382) LOAD R4,R4SAV
0A736 0000 (02383) LOAD R5,R5SAV
0A737 0000 (02384) LOAD R6,R6SAV
0A738 0000 (02385) LOAD R7,R7SAV
0A739 0000 (02386) PRT
0A73A 0000 (02387) EVEN
0A73B 0000 (02388) LOAD R1,R1SAV
0A73C 0000 (02389) LOAD R2,R2SAV
0A73D 0000 (02390) LOAD R3,R3SAV
0A73E 0000 (02391) LOAD R4,R4SAV
0A73F 0000 (02392) LOAD R5,R5SAV
0A740 0000 (02393) LOAD R6,R6SAV
0A741 0000 (02394) LOAD R7,R7SAV
0A742 0000 (02395) PRT
0A743 0000 (02396) EVEN
0A744 0000 (02397) LOAD R1,R1SAV
0A745 0000 (02398) LOAD R2,R2SAV
0A746 0000 (02399) LOAD R3,R3SAV
0A747 0000 (02400) LOAD R4,R4SAV
0A748 0000 (02401) LOAD R5,R5SAV
0A749 0000 (02402) LOAD R6,R6SAV
0A74A 0000 (02403) LOAD R7,R7SAV
0A74B 0000 (02404) PRT
0A74C 0000 (02405) EVEN
0A74D 0000 (02406) LOAD R1,R1SAV
0A74E 0000 (02407) LOAD R2,R2SAV
0A74F 0000 (02408) LOAD R3,R3SAV
0A750 0000 (02409) LOAD R4,R4SAV
0A751 0000 (02410) LOAD R5,R5SAV
0A752 0000 (02411) LOAD R6,R6SAV
0A753 0000 (02412) LOAD R7,R7SAV
0A754 0000 (02413) PRT
0A755 0000 (02414) EVEN
0A756 0000 (02415) LOAD R1,R1SAV
0A757 0000 (02416) LOAD R2,R2SAV
0A758 0000 (02417) LOAD R3,R3SAV
0A759 0000 (02418) LOAD R4,R4SAV
0A75A 0000 (02419) LOAD R5,R5SAV
0A75B 0000 (02420) LOAD R6,R6SAV
0A75C 0000 (02421) LOAD R7,R7SAV
0A75D 0000 (02422) PRT
0A75E 0000 (02423) EVEN
0A75F 0000 (02424) LOAD R1,R1SAV
0A760 0000 (02425) LOAD R2,R2SAV
0A761 0000 (02426) LOAD R3,R3SAV
0A762 0000 (02427) LOAD R4,R4SAV
0A763 0000 (02428) LOAD R5,R5SAV
0A764 0000 (02429) LOAD R6,R6SAV
0A765 0000 (02430) LOAD R7,R7SAV
0A766 0000 (02431) PRT
0A767 0000 (02432) EVEN
0A768 0000 (02433) LOAD R1,R1SAV
0A769 0000 (02434) LOAD R2,R2SAV
0A76A 0000 (02435) LOAD R3,R3SAV
0A76B 0000 (02436) LOAD R4,R4SAV
0A76C 0000 (02437) LOAD R5,R5SAV
0A76D 0000 (02438) LOAD R6,R6SAV
0A76E 0000 (02439) LOAD R7,R7SAV
0A76F 0000 (02440) PRT
0A770 0000 (02441) EVEN
0A771 0000 (02442) LOAD R1,R1SAV
0A772 0000 (02443) LOAD R2,R2SAV
0A773 0000 (02444) LOAD R3,R3SAV
0A774 0000 (02445) LOAD R4,R4SAV
0A775 0000 (02446) LOAD R5,R5SAV
0A776 0000 (02447) LOAD R6,R6SAV
0A777 0000 (02448) LOAD R7,R7SAV
0A778 0000 (02449) PRT
0A779 0000 (02450) EVEN
0A77A 0000 (02451) LOAD R1,R1SAV
0A77B 0000 (02452) LOAD R2,R2SAV
0A77C 0000 (02453) LOAD R3,R3SAV
0A77D 0000 (02454) LOAD R4,R4SAV
0A77E 0000 (02455) LOAD R5,R5SAV
0A77F 0000 (02456) LOAD R6,R6SAV
0A780 0000 (02457) LOAD R7,R7SAV
0A781 0000 (02458) PRT
0A782 0000 (02459) EVEN
0A783 0000 (02460) LOAD R1,R1SAV
0A784 0000 (02461) LOAD R2,R2SAV
0A785 0000 (02462) LOAD R3,R3SAV
0A786 0000 (02463) LOAD R4,R4SAV
0A787 0000 (02464) LOAD R5,R5SAV
0A788 0000 (02465) LOAD R6,R6SAV
0A789 0000 (02466) LOAD R7,R7SAV
0A78A 0000 (02467) PRT
0A78B 0000 (02468) EVEN
0A78C 0000 (02469) LOAD R1,R1SAV
0A78D 0000 (02470) LOAD R2,R2SAV
0A78E 0000 (02471) LOAD R3,R3SAV
0A78F 0000 (02472) LOAD R4,R4SAV
0A790 0000 (02473) LOAD R5,R5SAV
0A791 0000 (02474) LOAD R6,R6SAV
0A792 0000 (02475) LOAD R7,R7SAV
0A793 0000 (02476) PRT
0A794 0000 (02477) EVEN
0A795 0000 (02478) LOAD R1,R1SAV
0A796 0000 (02479) LOAD R2,R2SAV
0A797 0000 (02480) LOAD R3,R3SAV
0A798 0000 (02481) LOAD R4,R4SAV
0A799 0000 (02482) LOAD R5,R5SAV
0A79A 0000 (02483) LOAD R6,R6SAV
0A79B 0000 (02484) LOAD R7,R7SAV
0A79C 0000 (02485) PRT
0A79D 0000 (02486) EVEN
0A79E 0000 (02487) LOAD R1,R1SAV
0A79F 0000 (02488) LOAD R2,R2SAV
0A7A0 0000 (02489) LOAD R3,R3SAV
0A7A1 0000 (02490) LOAD R4,R4SAV
0A7A2 0000 (02491) LOAD R5,R5SAV
0A7A3 0000 (02492) LOAD R6,R6SAV
0A7A4 0000 (02493) LOAD R7,R7SAV
0A7A5 0000 (02494) PRT
0A7A6 0000 (02495) EVEN
0A7A7 0000 (02496) LOAD R1,R1SAV
0A7A8 0000 (02497) LOAD R2,R2SAV
0A7A9 0000 (02498) LOAD R3,R3SAV
0A7AA 0000 (02499) LOAD R4,R4SAV
0A7AB 0000 (02500) LOAD R5,R5SAV
0A7AC 0000 (02501) LOAD R6,R6SAV
0A7AD 0000 (02502) LOAD R7,R7SAV
0A7AE 0000 (02503) PRT
0A7AF 0000 (02504) EVEN
0A7B0 0000 (02505) LOAD R1,R1SAV
0A7B1 0000 (02506) LOAD R2,R2SAV
0A7B2 0000 (02507) LOAD R3,R3SAV
0A7B3 0000 (02508) LOAD R4,R4SAV
0A7B4 0000 (02509) LOAD R5,R5SAV
0A7B5 0000 (02510) LOAD R6,R6SAV
0A7B6 0000 (02511) LOAD R7,R7SAV
0A7B7 0000 (02512) PRT
0A7B8 0000 (02513) EVEN
0A7B9 0000 (02514) LOAD R1,R1SAV
0A7BA 0000 (02515) LOAD R2,R2SAV
0A7BB 0000 (02516) LOAD R3,R3SAV
0A7BC 0000 (02517) LOAD R4,R4SAV
0A7BD 0000 (02518) LOAD R5,R5SAV
0A7BE 0000 (02519) LOAD R6,R6SAV
0A7BF 0000 (02520) LOAD R7,R7SAV
0A7C0 0000 (02521) PRT
0A7C1 0000 (02522) EVEN
0A7C2 0000 (02523) LOAD R1,R1SAV
0A7C3 0000 (02524) LOAD R2,R2SAV
0A7C4 0000 (02525) LOAD R3,R3SAV
0A7C5 0000 (02526) LOAD R4,R4SAV
0A7C6 0000 (02527) LOAD R5,R5SAV
0A7C7 0000 (02528) LOAD R6,R6SAV
0A7C8 0000 (02529) LOAD R7,R7SAV
0A7C9 0000 (02530) PRT
0A7CA 0000 (02531) EVEN
0A7CB 0000 (02532) LOAD R1,R1SAV
0A7CC 0000 (02533) LOAD R2,R2SAV
0A7CD 0000 (02534) LOAD R3,R3SAV
0A7CE 0000 (02535) LOAD R4,R4SAV
0A7CF 0000 (02536) LOAD R5,R5SAV
0A7D0 0000 (02537) LOAD R6,R6SAV
0A7D1 0000 (02538) LOAD R7,R7SAV
0A7D2 0000 (02539) PRT
0A7D3 0000 (02540) EVEN
0A7D4 0000 (02541) LOAD R1,R1SAV
0A7D5 0000 (02542) LOAD R2,R2SAV
0A7D6 0000 (02543) LOAD R3,R3SAV
0A7D7 0000 (02544) LOAD R4,R4SAV
0A7D8 0000 (02545) LOAD R5,R5SAV
0A7D9 0000 (02546) LOAD R6,R6SAV
0A7DA 0000 (02547) LOAD R7,R7SAV
0A7DB 0000 (02548) PRT
0A7DC 0000 (02549) EVEN
0A7DD 0000 (02550) LOAD R1,R1SAV
0A7DE 0000 (02551) LOAD R2,R2SAV
0A7DF 0000 (02552) LOAD R3,R3SAV
0A7E0 0000 (02553) LOAD R4,R4SAV
0A7E1 0000 (02554) LOAD R5,R5SAV
0A7E2 0000 (02555) LOAD R6,R6SAV
0A7E3 0000 (02556) LOAD R7,R7SAV
0A7E4 0000 (02557) PRT
0A7E5 0000 (02558) EVEN
0A7E6 0000 (02559) LOAD R1,R1SAV
0A7E7 0000 (02560) LOAD R2,R2SAV
0A7E8 0000 (02561) LOAD R3,R3SAV
0A7E9 0000 (02562) LOAD R4,R4SAV
0A7EA 0000 (02563) LOAD R5,R5SAV
0A7EB 0000 (02564) LOAD R6,R6SAV
0A7EC 0000 (02565) LOAD R7,R7SAV
0A7ED 0000 (02566) PRT
0A7EE 0000 (02567) EVEN
0A7EF 0000 (02568) LOAD R1,R1SAV
0A7F0 0000 (02569) LOAD R2,R2SAV
0A7F1 0000 (02570) LOAD R3,R3SAV
0A7F2 0000 (02571) LOAD R4,R4SAV
0A7F3 0000 (02572) LOAD R5,R5SAV
0A7F4 0000 (02573) LOAD R6,R6SAV
0A7F5 0000 (02574) LOAD R7,R7SAV
0A7F6 0000 (02575) PRT
0A7F7 0000 (02576) EVEN
0A7F8 0000 (02577) LOAD R1,R1SAV
0A7F9 0000 (02578) LOAD R2,R2SAV
0A7FA 0000 (02579) LOAD R3,R3SAV
0A7FB 0000 (02580) LOAD R4,R4SAV
0A7FC 0000 (02581) LOAD R5,R5SAV
0A7FD 0000 (02582) LOAD R6,R6SAV
0A7FE 0000 (02583) LOAD R7,R7SAV
0A7FF 0000 (02584) PRT
0A800 0000 (02585) EVEN
0A801 0000 (02586) LOAD R1,R1SAV
0A802 0000 (02587) LOAD R2,R2SAV
0A803 0000 (02588) LOAD R3,R3SAV
0A804 0000 (02589) LOAD R4,R4SAV
0A805 0000 (02590) LOAD R5,R5SAV
0A806 0000 (02591) LOAD R6,R6SAV
0A807 0000 (02592) LOAD R7,R7SAV
0A808 0000 (02593) PRT
0A809 0000 (02594) EVEN
0A80A 0000 (02595) LOAD R1,R1SAV
0A80B 0000 (02596) LOAD R2,R2SAV
0A80C 0000 (02597) LOAD R3,R3SAV
0A80D 0000 (02598) LOAD R4,R4SAV
0A80E 0000 (02599) LOAD R5,R5SAV
0A80F 0000 (02600) LOAD R6,R6SAV
0A810 0000 (02601) LOAD R7,R7SAV
0A811 0000 (02602) PRT
0A812 0000 (02603) EVEN
0A813 0000 (02604) LOAD R1,R1SAV
0A814 0000 (02605) LOAD R2,R2SAV
0A815 0000 (02606) LOAD R3,R3SAV
0A816 0000 (02607) LOAD R4,R4SAV
0A817 0000 (02608) LOAD R5,R5SAV
0A818 0000 (02609) LOAD R6,R6SAV
0A819 0000 (02610) LOAD R7,R7SAV
0A81A 0000 (02611) PRT
0A81B 0000 (02612) EVEN
0A81C 0000 (02613) LOAD R1,R1SAV
0A81D 0000 (02614) LOAD R2,R2SAV
0A81E 0000 (02615) LOAD R3,R3SAV
0A81F 0000 (02616) LOAD R4,R4SAV
0A820 0000 (02617) LOAD R5,R5SAV
0A821 0000 (02618) LOAD R6,R6SAV
0A822 0000 (02619) LOAD R7,R7SAV
0A823 0000 (02620) PRT
0A824 0000 (02621) EVEN
0A825 0000 (02622) LOAD R1,R1SAV
0A826 0000 (02623) LOAD R2,R2SAV
0A827 0000 (02624) LOAD R3,R3SAV
0A828 0000 (02625) LOAD R4,R4SAV
0A829 0000 (02626) LOAD R5,R5SAV
0A82A 0000 (02627) LOAD R6,R6SAV
0A82B 0000 (02628) LOAD R7,R7SAV
0A82C 0000 (02629) PRT
0A82D 0000 (02630) EVEN
0A82E 0000 (02631) LOAD R1,R1SAV
0A82F 0000 (02632) LOAD R2,R2SAV
0A830 0000 (02633) LOAD R3,R3SAV
0A831 0000 (02634) LOAD R4,R4SAV
0A832 0000 (02635) LOAD R5,R5SAV
0A833 0000 (02636) LOAD R6,R6SAV
0A834 0000 (02637) LOAD R7,R7SAV
0A835 0000 (02638) PRT
0A836 0000 (02639) EVEN
0A837 0000 (02640) LOAD R1,R1SAV
0A838 0000 (02641) LOAD R2,R2SAV
0A839 0000 (02642) LOAD R3,R3SAV
0A83A 0000 (02643) LOAD R4,R4SAV
0A83B 0000 (02644) LOAD R5,R5SAV
0A83C 0000 (02645) LOAD R6,R6SAV
0A83D 0000 (02646) LOAD R7,R7SAV
0A83E 0000 (02647) PRT
0A83F 0000 (02648) EVEN
0A840 0000 (02649) LOAD R1,R1SAV
0A841 0000 (02650) LOAD R2,R2SAV
0A842 0000 (02651) LOAD R3,R3SAV
0A843 0000 (02652) LOAD R4,R4SAV
0A844 0000 (02653) LOAD R5,R5SAV
0A845 0000 (02654) LOAD R6,R6SAV
0A846 0000 (02655) LOAD R7,R7SAV
0A847 0000 (02656) PRT
0A848 0000 (02657) EVEN
0A849 0000 (02658) LOAD R1,R1SAV
0A84A 0000 (02659) LOAD R2,R2SAV
0A84B 0000 (02660) LOAD R3,R3SAV
0A84C 0000 (02661) LOAD R4,R4SAV
0A84D 0000 (02662) LOAD R5,R5SAV
0A84E 0000 (02663) LOAD R6,R6SAV
0A84F 0000 (02664) LOAD R7,R7SAV
0A850 0000 (02665) PRT
0A851 0000 (02666) EVEN
0A852 0000 (02667) LOAD R1,R1SAV
0A853 0000 (02668) LOAD R2,R2SAV
0A854 0000 (02669) LOAD R3,R3SAV
0A855 0000 (02670) LOAD R4,R4SAV
0A856 0000 (02671) LOAD R5,R5SAV
0A857 0000 (02672) LOAD R6,R6SAV
0A858 0000 (02673) LOAD R7,R7SAV
0A859 0000 (02674) PRT
0A85A 0000 (02675) EVEN
0A85B 0000 (02676) LOAD R1,R1SAV
0A85C 0000 (02677) LOAD R2,R2SAV
0A85D 0000 (02678) LOAD R3,R3SAV
0A85E 0000 (02679) LOAD R4,R4SAV
0A85F 0000 (02680) LOAD R5,R5SAV
0A860 0000 (02681) LOAD R6,R6SAV
0A861 0000 (02682) LOAD R7,R7SAV
0A862 0000 (02683) PRT
0A863 0000 (02684) EVEN
0A864 0000 (02685) LOAD R1,R1SAV
0A865 0000 (02686) LOAD R2,R2SAV
0A866 0000 (02687) LOAD R3,R3SAV
0A867 0000 (02688) LOAD R4,R4SAV
0A868 0000 (02689) LOAD R5,R5SAV
0A869 0000 (02690) LOAD R6,R6SAV
0A86A 0000 (02691) LOAD R7,R7SAV
0A86B 0000 (02692) PRT
0A86C 0000 (02693) EVEN
0A86D 0000 (02694) LOAD R1,R1SAV
0A86E 0000 (02695) LOAD R2,R2SAV
0A86F 0000 (02696) LOAD R3,R3SAV
0A870 0000 (02697) LOAD R4,R4SAV
0A871 0000 (02698) LOAD R5,R5SAV
0A872 0000 (02699) LOAD R6,R6SAV
0A873 0000 (02700) LOAD R7,R7SAV
0A874 0000 (02701) PRT
0A875 0000 (02702) EVEN
0A876 0000 (02703) LOAD R1,R1SAV
0A877 0000 (02704) LOAD R2,R2SAV
0A878 0000 (02705) LOAD R3,R3SAV
0A879 0000 (02706) LOAD R4,R4SAV
0A87A 0000 (02707) LOAD R5,R5SAV
0A87B 0000 (02708) LOAD R6,R6SAV
0A87C 0000 (02709) LOAD R7,R7SAV
0A87D 0000 (02710) PRT
0A87E 0000 (02711) EVEN
0A87F 0000 (02712) LOAD R1,R1SAV
0A880 0000 (02713) LOAD R2,R2SAV
0A881 0000 (02714) LOAD R3,R3SAV
0A882 0000 (02715) LOAD R4,R4SAV
0A883 0000 (02716) LOAD R5,R5SAV
0A884 0000 (02717) LOAD R6,R6SAV
0A885 0000 (02718) LOAD R7,R7SAV
0A886 0000 (02719) PRT
0A887 0000 (02720) EVEN
0A888 0000 (02721) LOAD R1,R1SAV
0A889 0000 (02722) LOAD R2,R2SAV
0A88A 0000 (02723) LOAD R3,R3SAV
0A88B 0000 (02724) LOAD R4,R4SAV
0A88C 0000 (02725) LOAD R5,R5SAV
0A88D 0000 (02726) LOAD R6,R6SAV
0A88E 0000 (02727) LOAD R7,R7SAV
0A88F 0000 (02728) PRT
0A890 0000 (02729) EVEN
0A891 0000 (02730) LOAD R1,R1SAV
0A892 0000 (02731) LOAD R2,R2SAV
0A893 0000 (02732) LOAD R3,R3SAV
0A894 0000 (02733) LOAD R4,R4SAV
0A895 0000 (02734) LOAD R5,R5SAV
0A896 0000 (02735) LOAD R6,R6SAV
0A897 0000 (02736) LOAD R7,R7SAV
0A898 0000 (02737) PRT
0A899 0000 (02738) EVEN
0A89A 0000 (02739) LOAD R1,R1SAV
0A89B 0000 (02740) LOAD R2,R2SAV
0A89C 0000 (02741) LOAD R3,R3SAV
0A89D 0000 (02742) LOAD R4,R4SAV
0A89E 0000 (02743) LOAD R5,R5SAV
0A89F 0000 (02744) LOAD R6,R6SAV
0A8A0 0000 (02745) LOAD R7,R7SAV
0A8A1 0000 (02746) PRT
0A8A2 0000 (02747) EVEN
0A8A3 0000 (02748) LOAD R1,R1SAV
0A8A4 0000 (02749) LOAD R2,R2SAV
0A8A5 0000 (02750) LOAD R3,R3SAV
0A8A6 0000 (02751) LOAD R4,R4SAV
0A8A7 0000 (02752) LOAD R5,R5SAV
0A8A8 0000 (02753) LOAD R6,R6SAV
0A8A9 0000 (02754) LOAD R7,R7SAV
0A8AA 0000 (02755) PRT
0A8AB 0000 (02756) EVEN
0A8AC 0000 (02757) LOAD R1,R1SAV
0A8AD 0000 (02758) LOAD R2,R2SAV
0A8AE 0000 (02759) LOAD R3,R3SAV
0A8AF 0000 (02760) LOAD R4,R4SAV
0A8B0 0000 (02761) LOAD R5,R5SAV
0A8B1 0000 (02762) LOAD R6,R6SAV
0A8B2 0000 (02763) LOAD R7,R7SAV
0A8B3 0000 (02764) PRT
0A8B4 0000 (02765) EVEN
0A8B5 0000 (02766) LOAD R1,R1SAV
0A8B6 0000 (02767) LOAD R2,R2SAV
0A8B7 0000 (02768) LOAD R3,R3SAV
0A8B8 0000 (02769) LOAD R4,R4SAV
0A8B9 0000 (02770) LOAD R5,R5SAV
0A8BA 0000 (02771) LOAD R6,R6SAV
0A8BB 0000 (02772) LOAD R7,R7SAV
0A8BC 0000 (02773) PRT
0A8BD 0000 (02774) EVEN
0A8BE 0000 (02775) LOAD R1,R1SAV
0A8BF 0000 (02776) LOAD R2,R2SAV
0A8C0 0000 (02777) LOAD R3,R3SAV
0A8C1 0000 (02778) LOAD R4,R4SAV
0A8C2 0000 (02779) LOAD R5,R5SAV
0A8C3 0000 (02780) LOAD R6,R6SAV
0A8C4 0000 (02781) LOAD R7,R7SAV
0A8C5 0000 (02782) PRT
0A8C6 0000 (02783) EVEN
0A8C7 0000 (02784) LOAD R1,R1SAV
0A8C8 0000 (02785) LOAD R2,R2SAV
0A8C9 0000 (02786) LOAD R3,R3SAV
0A8CA 0000 (02787) LOAD R4,R4SAV
0A8CB 0000 (02788) LOAD R5,R5SAV
0A8CC 0000 (02789) LOAD R6,R6SAV
0A8CD 0000 (02790) LOAD R7,R7SAV
0A8CE 0000 (02791) PRT
0A8CF 0000 (02792) EVEN
0A8D0 0000 (02793) LOAD R1,R1SAV
0A8D1 0000 (02794) LOAD R2,R2SAV
0A8D2 0000 (02795) LOAD R3,R3SAV
0A8D3 0000 (02796) LOAD R4,R4SAV
0A8D4 0000 (02797) LOAD R5,R5SAV
0A8D5 0000 (02798) LOAD R6,R6SAV
0A8D6 0000 (02799) LOAD R7,R7SAV
0A8D7 0000 (02800) PRT
0A8D8 0000 (02801) EVEN
0A8D9 0000 (02802) LOAD R1,R1SAV
0A8DA 0000 (02803) LOAD R2,R2SAV
0A8DB 0000 (02804) LOAD R3,R3SAV
0A8DC 0000 (02805) LOAD R4,R4SAV
0A8DD 0000 (02806) LOAD R5,R5SAV
0A8DE 0000 (02807) LOAD R6,R6SAV
0A8DF 0000 (02808) LOAD R7,R7SAV
0A8E0 0000 (02809) PRT
0A8E1 0000 (02810) EVEN
0A8E2 0000 (02811) LOAD R1,R1SAV
0A8E3 0000 (02812) LOAD R2,R2SAV
0A8E4 0000 (02813) LOAD R3,R3SAV
0A8E5 0000 (02814) LOAD R4,R4SAV
0A8E6 0000 (02815) LOAD R5,R5SAV
0A8E7 0000 (02816) LOAD R6,R6SAV
0A8E8 0000 (02817) LOAD R7,R7SAV
0A8E9 0000 (02818) PRT
0A8EA 0000 (02819) EVEN
0A8EB 0000 (02820) LOAD R1,R1SAV
0A8EC 0000 (02821) LOAD R2,R2SAV
0A8ED 0000 (02822) LOAD R3,R3SAV
0A8EE 0000 (02823) LOAD R4,R4SAV
0A8EF 0000 (02824) LOAD R5,R5SAV
0A8F0 0000 (02825) LOAD R6,R6SAV
0A8F1 0000 (02826) LOAD R7,R7SAV
0A8F2 0000 (02827) PRT
0A8F3 0000 (02828) EVEN
0A8F4 0000 (02829) LOAD R1,R1SAV
0A8F5 0000 (02830) LOAD R2,R2SAV
0A8F6 0000 (02831) LOAD R3,R3SAV
0A8F7 0000 (02832) LOAD R4,R4SAV
0A8F8 0000 (02833) LOAD R5,R5SAV
0A8F9 0000 (02834) LOAD R6,R6SAV
0A8FA 0000 (02835) LOAD R7,R7SAV
0A8FB 0000 (02836) PRT
0A8FC 0000 (02837) EVEN
0A8FD 0000 (02838) LOAD R1,R1SAV
0A8FE 0000 (02839) LOAD R2,R2SAV
0A8FF 0000 (02840) LOAD R3,R3SAV
0A900 0000 (02841) LOAD R4,R4SAV
0A901 0000 (02842) LOAD R5,R5SAV
0A902 0000 (02843) LOAD R6,R6SAV
0A903 0000 (02844) LOAD R7,R7SAV
0A904 0000 (02845) PRT
0A905 0000 (02846) EVEN
0A906 0000 (02847) LOAD R1,R1SAV
0A907 0000 (02848) LOAD R2,R2SAV
0A908 0000 (02849) LOAD R3,R3SAV
0A909 0000 (02850) LOAD R4,R4SAV
0A90A 0000 (02851) LOAD R5
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0A719 1054 (02380)  MOVBR R5,R2
0A719 1056 (02390)  ADDR R5,R3
0A719 1058 (02394)  ADDR R5,R4
0A719 1060 (02392)  EVLN
0A719 1010A004 (02393)  JMP RCHDUS,F0
      : CALCULATE 10T3=K7=51**3+53=R2**4+R3
0A713 1009044 (02394)  MOV2M TESTS
0A716 1074 (02395)  MOVBR P1,R2
0A717 0800 (02397)  EVLN
0A718 1010A000 (02394)  JMP RCHDUS,F0
0A71A 10740446 (02399)  MOVBR R7,THRPCS(R2)
0A71C 4054 (02400)  MOVBR R6,R7
0A71D 3000 (02401)  ADDR R6,R6
0A71E 9260003F (02402)  CVTR R6,R3
0A71F 1A30 (02403)  SKPL,LT
0A71F 9F60003F (02404)  SUBTR R6,R3
0A71A 0800 (02405)  EVLN
0A71A 10600400 (02406)  MOVBR R6,STEPS
      :
0A716 4054 (02408)  ADDR R6,R7
0A717 0800 (02409)  EVLN
0A718 9260003F (02410)  CVTR R6,R3
0A71A 1A30 (02411)  SKPL,LT
0A71C 4054 (02412)  SUBTR R6,R3
      : PEAD FPC TABLE FOR S1**3
0A71D 0800 (02414)  EVLN
0A71E 107C9009 (02415)  MOVBR R7,THRPCS(R6)
0A800 4476 (02416)  RCHRS
0A801 0800 (02417)  EVLN
0A802 1010A00A (02418)  JMP RCHDUS,F0
      : MORE THAN TWO ERRORS OCCUR
0A804 0220 (02422)  TEST R2
0A805 1930 (02423)  SFP R6
0A806 0E40 (02424)  CLR R3
0A807 0230 (02425)  TEST R3
0A808 1010A016 (02426)  JMP RCHDUS,F0
0A80A 1058040B (02427)  MOVBR R5,THRPCS(R3)
0A80C 10504000 (02428)  ADDR R5,STEPS
0A80E 9250003F (02429)  CVTR R5,R3
0A810 1A30 (02430)  SKPL,LT
0A811 9F50003F (02431)  SUBTR R5,R3
      : PEAD FPC TABLE

```

NO ERROR IF P5=0

CLPAR ERROR COUNTER

CHECK R6<63

STORE POWER INDEX OF S1\*  
#2

R7=S1\*\*3+S3

GO TO RCHRS IF R7=0

CALCULATE SIGMA2 AND SIGMA3

CALCULATE S1\*\*2+S3+S5 FOR SIGMA2 CALC.



```

00013 0000 (02433)
00014 0000 (02434)
00015 0000 (02435)
00016 0000 (02436)
00017 0000 (02437)
00018 0000 (02438)
00019 0000 (02439)
00020 0000 (02440)
00021 0000 (02441)
00022 0000 (02442)
00023 0000 (02443)
00024 0000 (02444)
00025 0000 (02445)
00026 0000 (02446)
00027 0000 (02447)
00028 0000 (02448)
00029 0000 (02449)
00030 0000 (02450)
00031 0000 (02451)
00032 0000 (02452)
00033 0000 (02453)
00034 0000 (02454)
00035 0000 (02455)
00036 0000 (02456)
00037 0000 (02457)
00038 0000 (02458)
00039 0000 (02459)
00040 0000 (02460)
00041 0000 (02461)
00042 0000 (02462)
00043 0000 (02463)
00044 0000 (02464)
00045 0000 (02465)
00046 0000 (02466)
00047 0000 (02467)
00048 0000 (02468)
00049 0000 (02469)
00050 0000 (02470)
00051 0000 (02471)
00052 0000 (02472)
00053 0000 (02473)
00054 0000 (02474)
00055 0000 (02475)

```

EVEN  
MOVW R3,TRPDC(R3)  
SUBSE 0, R3, PLS  
NOP PLS, R3  
EVEN  
MOVW APONES  
STORE R1, R1, SAV  
STORE R2, R2, SAV  
STORE R3, R3, SAV  
STORE R4, R4, SAV  
STORE R5, R5, SAV  
STORE R6, R6, SAV  
STORE R7, R7, SAV  
EVEN  
EVEN  
LOAD R1, R1, SAV  
LOAD R2, R2, SAV  
LOAD R3, R3, SAV  
LOAD R4, R4, SAV  
LOAD R5, R5, SAV  
LOAD R6, R6, SAV  
LOAD R7, R7, SAV  
XORR R1, R4  
MOVW R4, R3  
JMP RCHDUS, P0  
MOVW R6, TRPDC(R3)  
RDC TAPL, P0, P7  
MOVW R3, TRPDC(R7)  
SUBR R6, R3  
SKPL CF  
ADDR R6, R3  
MOVW R3, TRPDC(R6)  
CALCULATE, SIGMA3  
MOVW R4, P2  
EVEN  
JMP RCHDUS, P0  
MOVW R5, TRPDC(R2)  
ADDR R5, R6  
EVEN  
CMPLE R5, R3  
SKPL LT  
SUBR R5, R3  
EVEN

R3=SI\*\*2+S3+S5  
R4=R3  
R6=R6/R3  
SIGMA2=R3

HAS ANYONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

```

0A850 F03A9F6 (02477)  MOVW P4,TRDPCS(R5)
0A851 443F (02478)  XORPP R4,R7
0A852 0810 (02479)  EVEN
0A853 001A8A6 (02480)  JMP RCHDPS,R0
0A854 F069FEC (02481)  ? 3 OR
0A855 9051F4C (02482)  MOVW P5,PFSS
0A856 4064 (02483)  ? CHECK THE FIRST POSITION
0A857 4466 (02484)  MOVW P6,P2
0A858 4368 (02485)  XORPP R6,P3
0A859 2761 (02486)  XORPP R6,R4
0A860 8110A6A (02487)  DECP R6,1
0A861 F509F6F (02488)  ? STORE ERROR POSITION
0A862 F0104FFD (02489)  INCM R6STS
0A863 F0249F38 (02490)  ? MULTIPLY ALPHA TO R2
0A864 F03B9F78 (02491)  MOVW R3,TRALIS(R2)
0A865 F0489F68 (02492)  ? MULTIPLY ALPHA TO R3
0A866 4064 (02493)  MOVW R4,TRALIS(R4)
0A867 4366 (02494)  MOVW R6,P2
0A868 4368 (02495)  XORPP R6,R4
0A869 2761 (02496)  DECP R6,1
0A870 8110A6A (02497)  ? STORE ERROR POSITION
0A871 F509F6F (02498)  INCM R6STS
0A872 F0104FFD (02499)  ? MULTIPLY ALPHA TO R2
0A873 4064 (02500)  MOVW R3,TRALIS(R2)
0A874 4366 (02501)  MOVW R4,TRALIS(R4)
0A875 4368 (02502)  MOVW R6,P2
0A876 F509F6F (02503)  XORPP R6,R4
0A877 4C72 (02504)  DECP R6,1
0A878 F0609F68 (02505)  ? STORE ERROR POSITION
0A879 F07C0FEC (02506)  INCM R6STS
0A880 805A86A (02507)  MOVW P7,P5
0A881 F0509FEC (02508)  ANDPP R7,P1
0A882 F2509F68 (02509)  MOVW R6,PFSTS
0A883 8110A6A (02510)  ? CHECK ERROR CORRECTION STATUS
0A884 90600001 (02511)  TDR R5,RCHDPS
0A885 F0709F68 (02512)  CORRECT 3 ERRORS
0A886 F0709F68 (02513)  MOVW R6,1
0A887 F56F9533 (02514)  MOVW R7,PFSS+1
0A888 F0709F68 (02515)  XORPP R6,PFSTS-1(R7)
0A889 F56F9533 (02516)  MOVW R7,PFSS+1
0A890 F0709F68 (02517)  XORPP R6,PFSTS-1(R7)
0A891 F56F9533 (02518)  MOVW R7,PFSS+1
0A892 F0709F68 (02519)  XORPP R6,PFSTS-1(R7)
0A893 F56F9533 (02520)  MOVW R7,PFSS+1

```

R4=P7+S1\*SIGMA2

TWO ERRORS IF R4=0

R7 IS THE ERROR POSITION

```

0A017 550450A (02521) MOVW R6,RFCVS-1(R7)
0A018 F0609FC (02522) MOVW R6,RFCSS
0A019 921004C (02523) MOVW R1,R6A
0A020 1010 (02524) SKPL RF
0A021 F0609FA (02525) MOVW R6,RFCSS-2
0A022 0800 (02526) EVEN
0A023 921007E (02527) MOVW R1,127
0A024 1010 (02528) SKPL RF
0A025 F0609FH (02529) MOVW R6,RFCSS-1
0A026 0800 (02530) EVEN
0A027 025411 * MOVW R1,129
0A028 025432 * JMP RCHDAS,11
0A029 025333 * JMP DSRDHS
0A030 025343 *
0A031 025353 *
0A032 025363 * END OF RCH DECODER EXECUTION
0A033 025373 *
0A034 025383 * JMP RCHDAS
0A035 025393 *
0A036 025403 *
0A037 025413 *
0A038 025423 *
0A039 025433 * RCHDAS MOVW 4,RFCSS
0A040 025443 * JMP RCHDAS
0A041 025453 * CORRECT 1 OR 2 PADDRS
0A042 025463 * RCHDAS MOVW 1,RFCSS
0A043 025473 * CLR R3
0A044 025483 * EVEN
0A045 025493 * JMP RCHDAS+2
0A046 025503 * RCHDAS MOVW 2,RFCSS
0A047 025513 * MOVW R5,-62
0A048 025523 * MOVW R6,R3
0A049 025533 * MOVW R6,R1
0A050 025543 * EVEN
0A051 025553 * JMP RCHDAS+4
0A052 025563 * CORRECT ERROR
0A053 025573 * MOVW R6,R1
0A054 025583 * MOVW R6,RFCVS-1(R1)
0A055 025593 * MOVW R2,THALIS(R2)
0A056 025603 * MOVW R3,THALIS(R3)
0A057 025613 * MOVW R6,R2
0A058 025623 * MOVW R6,R1
0A059 025633 *
0A060 025643 *

```

CORRECT ONLY SIDERAND

2 ERRORS

RR=ER2+P3

```

0ARC5 0800 (02565)      EVEN
0ARC6 010A0C0 (02566)      JMP RCH24S,IF
0ARC6 010A0C0 (02567) ?  CURRENT ERRORS
0ARC6 407A (02568)      MOVPR R7,R5
0ARC6 4C72 (02569)      ADDR R7,R1
0ARC6 96800001 (02570)      MOVIR R6,1
0ARC6 F689543 (02571)      XORR R6,RFCVS-1(R7)
0ARC6 8950A00F (02572) RCH24S  LJM R5,RCH25S
0AR10 8000A004 (02573)      JMP RCH24S
0AR10 8000A004 (02574) *
0AR10 8000A004 (02575) *
0AR10 8000A004 (02576) ?  END OF RCH DECODING LIST
0AR10 8000A004 (02577) FND0CS  EVEN
0AR10 8000A004 (02578)      JMP DSRHIFS
0AR10 8000A004 (02579)      EJECT

```



0A90E F1200405 (02621)	STORE R2,R2SAV		
0A910 F1300410 (02625)	STORE R3,R3SAV		
0A912 F1300411 (02626)	STORE R4,R4SAV		
0A913 F1300413 (02627)	STORE R5,R5SAV		
0A916 F1300416 (02629)	STORE R6,R6SAV		
0A918 F1700418 (02630)	STORE R7,R7SAV		
0A91A 0E70 (02630)	EVEN		
0A91A 0E00 (02631)			
0A91C A610040C (02632)	LOAD R1,R1SAV		
0A91E A620040E (02633)	LOAD R2,R2SAV		
0A920 A6300410 (02634)	LOAD R3,R3SAV		
0A922 A6400412 (02635)	LOAD R4,R4SAV		
0A924 A6500414 (02636)	LOAD R5,R5SAV		
0A926 A6600416 (02637)	LOAD R6,R6SAV		
0A928 A6700418 (02638)	LOAD R7,R7SAV		
0A92A 00000062 (02639)	JMP 01000062(PS)		
0A92C FAD004534 (02640)	FVPA		
0A92E 0250040E (02642)	SMOCL 0,RFCVS(R2)		SKIP IF THE REC'D HIT IS
0A930 7621 (02643)	SM 5,R0PH5(P4)		SET HIT 5 OF PARM TO RE
0A931 0800 (02645)			A 1
0A932 00000062 (02646)	INCR R2,1		INCREMENT R2 BY 1
0A934 02400406 (02648)	EVEN		
0A936 0A0004534 (02651)	SMOCL 0,RFCVS(R2)		SKIP IF THE REC'D HIT IS
0A938 02400406 (02653)	SM 4,R0PH4(P4)		SET HIT 4 OF ROPH TO RE
0A939 7621 (02655)			1
0A93A 00000062 (02656)	INCR R2,1		INCREMENT R2 BY 1
0A93B 0800 (02657)	EVEN		
0A93C 0A0004534 (02659)	SMOCL 0,RFCVS(R2)		SKIP IF THE REC'D HIT IS
0A93E 02400406 (02661)	SM 3,R0PH3(P4)		SET HIT 3 OF ROPH TO RE
0A93F 7621 (02663)			1
0A940 0800 (02664)	INCR R2,1		INCREMENT R2 BY 1
0A941 0A0004534 (02666)	SMOCL 0,RFCVS(R2)		SKIP IF THE REC'D HIT IS
0A943 02400406 (02668)	SM 2,R0PH2(P4)		SET HIT 2 OF ROPH TO RE
0A944 7621 (02670)			1
0A945 0800 (02671)	INCR R2,1		INCREMENT R2 BY 1
0A946 0A0004534 (02673)	SMOCL 0,RFCVS(R2)		SKIP IF THE REC'D HIT IS
0A948 02400406 (02675)	SM 1,R0PH1(P4)		SET HIT 2 OF ROPH TO RE
0A949 7621 (02677)			1

0A944 2621	(02684)	INCR R2,1	INCREMENT R2 BY 1
0A949 0200	(02689)	EVFN	
0A94A 0A030544	(02671) *	SMOCT 0,RFCV5(R2)	SKIP IF THE REC'D HIT IS
0A94C 020806FF	(02672)	SMR 0,R0P5(R4)	SET HIT 0 OF R0P5 TO RF
0A94E 2621	(02673) :	INCR R2,1	INCREMENT R2 BY 1
0A94F 0000	(02674)	EVFN	
0A950 2631	(02675) *	INCR R4,1	INCREMENT THE PARM. COUNT
0A951 0000	(02676)	EVFN	TER BY 1
0A952 0E50A903	(02677) :	GO TO R5,100051	AND GO TO THE NEXT ONE
0A954 FC700CFE	(02682) :	ADDER R2,FF10FF5	
0A956 C0896FF	(02683)		
0A958 0A040544	(02684) :	R0V76 R0P5(R4)	SKIP THE NEXT 18 POSITIO
0A95A 021046FF	(02685) :	SMOCT 0,RFCV5(R2)	NS SINCE THESE ARE PARTI
0A95C 2621	(02686)	INCR R2,1	Y BITS FOR ERROR CONTROL
0A95D 0000	(02687)	EVFN	CLEAR THE PARM BUFFER
0A95F 0A040544	(02688)	SMRCL 0,RFCV5(R2)	SKIP IF THE REC'D HIT IS
0A960 020806FF	(02689) :	SMR 1,R0P5(R4)	SET HIT 1 OF R0P5 TO RF
0A962 2621	(02690)	INCR R2,1	INCREMENT THE LOOP COUNT
0A963 0000	(02691)	EVFN	BY ONE
0A965 0A040544	(02692)	SMRCL 0,RFCV5(R2)	SKIP IF THE REC'D HIT IS
0A967 020806FF	(02693)	SMR 0,R0P5(R4)	SET HIT 0 OF R0P5 TO RF
0A969 2621	(02694)	INCR R2,1	INCREMENT R2 BY 1
0A96A 0000	(02695) :	EVFN	
0A96C 0000	(02696)		
0A96E 0000	(02697)		
0A970 0000	(02698) *		
0A972 0000	(02699) *		
0A974 0000	(02700) *		
0A976 0000	(02701) *		
0A978 0000	(02702) :		
0A97A 0000	(02703) :		
0A97C 0000	(02704) :		
0A97E 0000	(02705) *		
0A980 0000	(02706) *		
0A982 0000	(02707) *		
0A984 0000	(02708) *		
0A986 0000	(02709) *		
0A988 0000	(02710) *		
0A98A 0000	(02711) *		

FROM THESE DECODED PARAMETERS  
HIT ASSIGNMENT MADE FOR DESERIALIZATION  
OF BIT CORRS IS COMPUTED

```

(02717) ; DESERIALIZED THE QUANTIZED OCT CODES
(02718) ; ASSUMING HORIZONTAL CODING
(02719) *
0A964 F000FFCF (02715) MOVEM CIRCSD,SYSEFLG
0A966 F000580 (02716) SPSL 0,AFIDNLS
0A968 2021 (02717) HOP B134
0A969 0409 (02718) *
0A96A C000580 (02719) MOVEM ADRESL
0A96C F100F0C (02720) STORE F1,R1SAV
0A96E F12040F (02721) STORE F2,R2SAV
0A970 F130910 (02722) STORE F3,R3SAV
0A972 F140412 (02723) STORE F4,R4SAV
0A974 F150914 (02724) STORE F5,R5SAV
0A976 F160416 (02725) STORE F6,R6SAV
0A978 F170918 (02726) STORE F7,R7SAV
0A97A 0E70 (02727) WFT
0A97C 0E00 (02728) *
0A97E A62040F (02729) LOAD F1,M1SAV
0A980 A630910 (02730) LOAD F2,R2SAV
0A982 A640412 (02731) LOAD F3,R3SAV
0A984 A650914 (02732) LOAD F4,R4SAV
0A986 A660416 (02733) LOAD F5,R5SAV
0A988 A670918 (02734) LOAD F6,R6SAV
0A98A F000576 (02735) SPSL 0,RG07
0A98C 8000A96 (02736) *
0A98E C000576 (02737) JMP DSD05
0A990 F849FFCF (02738) *
(02739) *
(02740) *
(02741) *
(02742) *
(02743) *
(02744) *
(02745) *
(02746) *
(02747) *
(02748) *
(02749) *
(02750) *
(02751) *
(02752) *
(02753) *
(02754) *
(02755) *

```

HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RPT

HAS AP WRITTEN REBIT YET  
?  
NO, WAIT FOR OK  
RESET GO FLAG

MOVEM SETSC0,SYSEFLG  
\* THE "MOVEFL" OP'S CAUSE A MEMORY BUS ERROR!  
REASON IS UNKNOWN, 5/17/80

MOVE 2 BLOCKS OF 0'S TO ROTDCT  
MFD IT FOR RPT  
MOVE LONG BUT DECR BY 2!  
ROTDCT(1)=0 FOR 0.LF.L.L  
F.255

DON'T DESEK IF FIRST FRA



```

(021586) ;
0A99C CC0009C4 (021577) MOVZM R7, R1
0A99E 80000460 (021584) JMP R7, R1
0A9A0 863004F7 (021589) LOAD R4, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15
0A9A2 F05004C4 (021601) MOVZM R5, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15
0A9A3 7257 (021611) DECF R5, 2
0A9A5 0800 (021627) EVEN
0A9A6 F01004A8 (021633) MOVZM R7, R1, R15
(021643) ;
0A9A8 2771 (021655) DECF R7, 1
0A9A9 3A71 (021666) ILS R7, 1
(021677) EVEN
(021688) *****
(021699) * SPECIAL CODE TO CORRECT FOR R1RIT=0 CASE 6/12/80
(021710) TEST R7
(021721) ;
0A9AB 0800 (021732) EVEN
(021743) ;
0A9AC 8030A9F6 (021754) JMP R15, R1
(021765) ;
0A9AE 806F9C7A (021776) *****
(021787) ;
0A9AF 806F9C7A (021787) JMP R15, R15 (R7)
(021798) ;
(021809) *
(021820) ;
(021831) *
(021842) ;
0A9B0 04000580 (021853) GRIT4S SERIALI, ZATION ROUTINE FOR HIT 4
0A9B2 2021 (021864) HOP R1, 14
(021875) ;
0A9B3 0800 (021886) EVEN
0A9B4 CC000500 (021897) MOVZM R1, R15
0A9B6 F110040C (021908) STORE R1, R15
0A9B8 F120040F (021919) STORE R2, R15
0A9BA F1300410 (021930) STORE R3, R15
0A9BC F1400412 (021941) STORE R4, R15
0A9BE F1500414 (021952) STORE R5, R15
0A9C0 F1600416 (021963) STORE R6, R15
0A9C2 F1700418 (021974) STORE R7, R15
0A9C4 0E70 (021985) RET
0A9C5 0800 (021996) EVEN
0A9C6 A610040C (022007) LOAD R1, R15
0A9C8 A620040F (022018) LOAD R2, R15
0A9CA A6300410 (022029) LOAD R3, R15

```

```

ME
RESTORE R4
MOVE FORTY FOUR IN R5

SAVE THE FIRST IBIT VALUE
DECREMENT R7 BY 1
SHIFT LEFT BY 1

WHEN R1RIT=0, R1<0 AND R1
IS RC!
SO TEST & JUMP AROUND SERIALI, ZATION SINCE
IBIT=0 IMPLIES NO BITS TO
SERIALI, ZATION ROUTINE
GOTO THE CORR. DESERIALI, ZATION ROUTINE

HAS AFDONE HAPPENED?
NO, HOP TO EVEN LOC AFTER
R RET

```



IS R2 GREATER THAN POST  
3  
ADD 1R TO R2 IF LESS THA  
N OR EQUAL  
GO BACK TO DAI1S1

HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

SAVE THE NEXT IBIT VALUE

COMPARE THE HIT ASSIGNE  
NT WITH THE MARK  
IF NOT EQUAL, GOTO DECODE  
HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

```

00A0C 4220AC06 (02845) CMPWR R2,POSTAS
(02845) ?
00A0E 4320A604 XCF GR11S,14
(02847) ?
00A10 400A0A04 JMP GR11AS1
(02849) *
(02850) *
(02851) ?
(02852) *
(02853)
00A12 4220AC09 (02854) CMPWR R2,POSTAS
00A14 4010A0F6 (02855) JMP EXITS,F0
00A16 4F000504 (02856) SRRSI 0,AP00F0S
00A18 2021 (02857) HOP 14,14
(02858) ?
(02859)
00A1A C000540 (02860) MOVZM AP00F0S
00A1C 1110F0C (02861) JPP R1,R1SAV
00A1E 4120F0F (02862) STORF R2,R2SAV
00A20 4130F10 (02863) STORF R3,R3SAV
00A22 4140F12 (02864) STORF R4,R4SAV
00A24 4150F14 (02865) STORF R5,R5SAV
00A26 4160F16 (02866) STORF R6,R6SAV
00A28 4170F18 (02867) STORF R7,R7SAV
00A2A 0F70 (02868) PFT
00A2C 0800 (02869) EVEN
00A2E 4610F0C (02870) LOAD R1,R1SAV
00A30 4620F0E (02871) LOAD R2,R2SAV
00A32 4630F10 (02872) LOAD R3,R3SAV
00A34 4640F12 (02873) LOAD R4,R4SAV
00A36 4650F14 (02874) LOAD R5,R5SAV
00A38 4670F16 (02875) LOAD R6,R6SAV
00A3A 403A9AA6 (02876) MOVWR R7,R11ITS,(R4)
00A3C 0140 (02879) CLR R4
00A3E 0800 (02880) EVEN
00A40 427A9AA6 (02881) CMPWR R7,R11ITS,(R4)
(02881) ?
00A42 4020AA78 (02882) JPP GR11S,GT
00A44 0F000504 (02883) SRRSI 0,AP00F0S
00A46 2021 (02884) HOP 14,14
(02885) ?
(02886)
00A48 C000540 (02887)

```

0AA7M F130040C (022484)	STORE R1,R1SAV	
0AA7A F130040E (022488)	STORE R2,R2SAV	
0AA7C F1300410 (022490)	STORE R3,R3SAV	
0AA7E F1300412 (022491)	STORE R4,R4SAV	
0AA80 F1300413 (022492)	STORE R5,R5SAV	
0AA82 F1300416 (022493)	STORE R6,R6SAV	
0AA84 F1300418 (022494)	STORE R7,R7SAV	
0AA86 0000 (022495)	R FT	
0AA8E 0000 (022496)	R FT	
0AA94 A610040C (022497)	LOAD R1,R1SAV	
0AA9A A620040E (022498)	LOAD R2,R2SAV	
0AA9C A6300410 (022499)	LOAD R3,R3SAV	
0AA9E A6400412 (022500)	LOAD R4,R4SAV	
0AAA0 A6500414 (022501)	LOAD R5,R5SAV	
0AAA2 A6600416 (022502)	LOAD R6,R6SAV	
0AAA4 A6700418 (022503)	LOAD R7,R7SAV	
0AAAE A680041A (022504)	SMOVL 0,RFCVS(R2)	
0AA90 0240040A (022506)	SM 3,400FCVS(R4)	
0AA92 7631 (022507)	TRCR R4,1	
0AA94 0000 (022508)	R FT	
0AA9C 7671 (022509)	TRCR R2,1	
0AA9E 0000 (022510)	R FT	
0AA90 0000 (022511)	R FT	
0AA9E 0050040E (022512)	LDI R5,GRIT31	
0AA70 0050040E (022513)	MOVHR R5,FURFORS	
0AA72 0220040C (022514)	CMOHR R2,POST35	
0AA74 0320040E (022517)	XCT CMT315,IE	
0AA76 0000040E (022519)	JM GRIT31	
0AA78 0000040E (022520)	R FT	
0AA7A 0000040E (022521)	R FT	
0AA7C 0000040E (022522)	R FT	
0AA7E 0000040E (022523)	R FT	
0AA78 0220040C (022524)	R FT	
0AA7A 0010040E (022525)	GRIT25	
0AA7C 0000040E (022526)	CMOHR R2,POST45	
0AA7E 0000040E (022527)	JM FRTS,FI	
0AA78 2021 (022528)	SMOVL 0,APDSF15	
0AA7A 0000 (022529)	HOP R1,44	
0AA7C 0000040E (022530)	R FT	
0AA7E 0000040E (022531)	R FT	

SKIP IF HIT 0 OF RECV(R2)  
) IS CLEARED

INCREMENT THE BUFFER POS  
ITION COUNTER

GO BACK TO GRIT31  
RELOAD THE COUNTER WITH  
44  
IS R2 GREATER THAN POST  
3  
ADD 18 TO LOOP COUNTER I  
F LESS THAN OR EQUAL TO  
GO BACK TO GRIT31

DESERIALIZATION FOR HIT 2

HAS APDOME HAPPENED?  
NO, HOP TO EVEN LDC AFTE  
R RFT

```

00002 F1109F0C (02942)
00004 F1209F0C (02943)
00006 F1309F10 (02944)
00008 F1409F12 (02945)
00010 F1509F14 (02946)
00012 F1609F16 (02947)
00014 F1709F18 (02948)
00016 F1809F1A (02949)
00018 F1909F1C (02950)
00020 F2009F1E (02951)
00022 F2109F20 (02952)
00024 F2209F22 (02953)
00026 F2309F24 (02954)
00028 F2409F26 (02955)
00030 F2509F28 (02956)
00032 F2609F2A (02957)
00034 F2709F2C (02958)
00036 F2809F2E (02959)
00038 F2909F30 (02960)
00040 F3009F32 (02961)
00042 F3109F34 (02962)
00044 F3209F36 (02963)
00046 F3309F38 (02964)
00048 F3409F3A (02965)
00050 F3509F3C (02966)
00052 F3609F3E (02967)
00054 F3709F40 (02968)
00056 F3809F42 (02969)
00058 F3909F44 (02970)
00060 F4009F46 (02971)
00062 F4109F48 (02972)
00064 F4209F4A (02973)
00066 F4309F4C (02974)
00068 F4409F4E (02975)
00070 F4509F50 (02976)
00072 F4609F52 (02977)
00074 F4709F54 (02978)
00076 F4809F56 (02979)
00078 F4909F58 (02980)
00080 F5009F5A (02981)
00082 F5109F5C (02982)
00084 F5209F5E (02983)
00086 F5309F60 (02984)
00088 F5409F62 (02985)
00090 F5509F64 (02986)
00092 F5609F66 (02987)
00094 F5709F68 (02988)
00096 F5809F6A (02989)
00098 F5909F6C (02990)
00100 F6009F6E (02991)
00102 F6109F70 (02992)
00104 F6209F72 (02993)
00106 F6309F74 (02994)
00108 F6409F76 (02995)
00110 F6509F78 (02996)
00112 F6609F7A (02997)
00114 F6709F7C (02998)
00116 F6809F7E (02999)
00118 F6909F80 (03000)
00120 F7009F82 (03001)
00122 F7109F84 (03002)
00124 F7209F86 (03003)
00126 F7309F88 (03004)
00128 F7409F8A (03005)
00130 F7509F8C (03006)
00132 F7609F8E (03007)
00134 F7709F90 (03008)
00136 F7809F92 (03009)
00138 F7909F94 (03010)
00140 F8009F96 (03011)
00142 F8109F98 (03012)
00144 F8209F9A (03013)
00146 F8309F9C (03014)
00148 F8409F9E (03015)
00150 F8509FA (03016)
00152 F8609FC (03017)
00154 F8709FE (03018)
00156 F8809F0 (03019)
00158 F8909F2 (03020)
00160 F9009F4 (03021)
00162 F9109F6 (03022)
00164 F9209F8 (03023)
00166 F9309FA (03024)
00168 F9409FC (03025)
00170 F9509FE (03026)
00172 F9609F0 (03027)
00174 F9709F2 (03028)
00176 F9809F4 (03029)
00178 F9909F6 (03030)
00180 FA009F8 (03031)
00182 FA109FA (03032)
00184 FA209FC (03033)
00186 FA309FE (03034)
00188 FA409F0 (03035)
00190 FA509F2 (03036)
00192 FA609F4 (03037)
00194 FA709F6 (03038)
00196 FA809F8 (03039)
00198 FA909FA (03040)
00200 FAA09FC (03041)
00202 FAB09FE (03042)
00204 FAC09F0 (03043)
00206 FAD09F2 (03044)
00208 FAE09F4 (03045)
00210 FAF09F6 (03046)
00212 FB009F8 (03047)
00214 FB109FA (03048)
00216 FB209FC (03049)
00218 FB309FE (03050)
00220 FB409F0 (03051)
00222 FB509F2 (03052)
00224 FB609F4 (03053)
00226 FB709F6 (03054)
00228 FB809F8 (03055)
00230 FB909FA (03056)
00232 FBA09FC (03057)
00234 FBB09FE (03058)
00236 FBC09F0 (03059)
00238 FBD09F2 (03060)
00240 FBE09F4 (03061)
00242 FBF09F6 (03062)
00244 FBG09F8 (03063)
00246 FBH09FA (03064)
00248 FBI09FC (03065)
00250 FBJ09FE (03066)
00252 FBK09F0 (03067)
00254 FBL09F2 (03068)
00256 FBM09F4 (03069)
00258 FBN09F6 (03070)
00260 FBO09F8 (03071)
00262 FBP09FA (03072)
00264 FBQ09FC (03073)
00266 FBR09FE (03074)
00268 FBS09F0 (03075)
00270 FBT09F2 (03076)
00272 FBV09F4 (03077)
00274 FBW09F6 (03078)
00276 FBX09F8 (03079)
00278 FBY09FA (03080)
00280 FBZ09FC (03081)
00282 FC009FE (03082)
00284 FC109F0 (03083)
00286 FC209F2 (03084)
00288 FC309F4 (03085)
00290 FC409F6 (03086)
00292 FC509F8 (03087)
00294 FC609FA (03088)
00296 FC709FC (03089)
00298 FC809FE (03090)
00300 FC909F0 (03091)
00302 FCA09F2 (03092)
00304 FCB09F4 (03093)
00306 FCC09F6 (03094)
00308 FCD09F8 (03095)
00310 FCE09FA (03096)
00312 FCF09FC (03097)
00314 FCG09FE (03098)
00316 FCH09F0 (03099)
00318 FCI09F2 (03100)
00320 FCI09F4 (03101)
00322 FCI09F6 (03102)
00324 FCI09F8 (03103)
00326 FCI09FA (03104)
00328 FCI09FC (03105)
00330 FCI09FE (03106)
00332 FCI09F0 (03107)
00334 FCI09F2 (03108)
00336 FCI09F4 (03109)
00338 FCI09F6 (03110)
00340 FCI09F8 (03111)
00342 FCI09FA (03112)
00344 FCI09FC (03113)
00346 FCI09FE (03114)
00348 FCI09F0 (03115)
00350 FCI09F2 (03116)
00352 FCI09F4 (03117)
00354 FCI09F6 (03118)
00356 FCI09F8 (03119)
00358 FCI09FA (03120)
00360 FCI09FC (03121)
00362 FCI09FE (03122)
00364 FCI09F0 (03123)
00366 FCI09F2 (03124)
00368 FCI09F4 (03125)
00370 FCI09F6 (03126)
00372 FCI09F8 (03127)
00374 FCI09FA (03128)
00376 FCI09FC (03129)
00378 FCI09FE (03130)
00380 FCI09F0 (03131)
00382 FCI09F2 (03132)
00384 FCI09F4 (03133)
00386 FCI09F6 (03134)
00388 FCI09F8 (03135)
00390 FCI09FA (03136)
00392 FCI09FC (03137)
00394 FCI09FE (03138)
00396 FCI09F0 (03139)
00398 FCI09F2 (03140)
00400 FCI09F4 (03141)
00402 FCI09F6 (03142)
00404 FCI09F8 (03143)
00406 FCI09FA (03144)
00408 FCI09FC (03145)
00410 FCI09FE (03146)
00412 FCI09F0 (03147)
00414 FCI09F2 (03148)
00416 FCI09F4 (03149)
00418 FCI09F6 (03150)
00420 FCI09F8 (03151)
00422 FCI09FA (03152)
00424 FCI09FC (03153)
00426 FCI09FE (03154)
00428 FCI09F0 (03155)
00430 FCI09F2 (03156)
00432 FCI09F4 (03157)
00434 FCI09F6 (03158)
00436 FCI09F8 (03159)
00438 FCI09FA (03160)
00440 FCI09FC (03161)
00442 FCI09FE (03162)
00444 FCI09F0 (03163)
00446 FCI09F2 (03164)
00448 FCI09F4 (03165)
00450 FCI09F6 (03166)
00452 FCI09F8 (03167)
00454 FCI09FA (03168)
00456 FCI09FC (03169)
00458 FCI09FE (03170)
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00462 FCI09F2 (03172)
00464 FCI09F4 (03173)
00466 FCI09F6 (03174)
00468 FCI09F8 (03175)
00470 FCI09FA (03176)
00472 FCI09FC (03177)
00474 FCI09FE (03178)
00476 FCI09F0 (03179)
00478 FCI09F2 (03180)
00480 FCI09F4 (03181)
00482 FCI09F6 (03182)
00484 FCI09F8 (03183)
00486 FCI09FA (03184)
00488 FCI09FC (03185)
00490 FCI09FE (03186)
00492 FCI09F0 (03187)
00494 FCI09F2 (03188)
00496 FCI09F4 (03189)
00498 FCI09F6 (03190)
00500 FCI09F8 (03191)
00502 FCI09FA (03192)
00504 FCI09FC (03193)
00506 FCI09FE (03194)
00508 FCI09F0 (03195)
00510 FCI09F2 (03196)
00512 FCI09F4 (03197)
00514 FCI09F6 (03198)
00516 FCI09F8 (03199)
00518 FCI09FA (03200)
00520 FCI09FC (03201)
00522 FCI09FE (03202)
00524 FCI09F0 (03203)
00526 FCI09F2 (03204)
00528 FCI09F4 (03205)
00530 FCI09F6 (03206)
00532 FCI09F8 (03207)
00534 FCI09FA (03208)
00536 FCI09FC (03209)
00538 FCI09FE (03210)
00540 FCI09F0 (03211)
00542 FCI09F2 (03212)
00544 FCI09F4 (03213)
00546 FCI09F6 (03214)
00548 FCI09F8 (03215)
00550 FCI09FA (03216)
00552 FCI09FC (03217)
00554 FCI09FE (03218)
00556 FCI09F0 (03219)
00558 FCI09F2 (03220)
00560 FCI09F4 (03221)
00562 FCI09F6 (03222)
00564 FCI09F8 (03223)
00566 FCI09FA (03224)
00568 FCI09FC (03225)
00570 FCI09FE (03226)
00572 FCI09F0 (03227)
00574 FCI09F2 (03228)
00576 FCI09F4 (03229)
00578 FCI09F6 (03230)
00580 FCI09F8 (03231)
00582 FCI09FA (03232)
00584 FCI09FC (03233)
00586 FCI09FE (03234)
00588 FCI09F0 (03235)
00590 FCI09F2 (03236)
00592 FCI09F4 (03237)
00594 FCI09F6 (03238)
00596 FCI09F8 (03239)
00598 FCI09FA (03240)
00600 FCI09FC (03241)
00602 FCI09FE (03242)
00604 FCI09F0 (03243)
00606 FCI09F2 (03244)
00608 FCI09F4 (03245)
00610 FCI09F6 (03246)
00612 FCI09F8 (03247)
00614 FCI09FA (03248)
00616 FCI09FC (03249)
00618 FCI09FE (03250)
00620 FCI09F0 (03251)
00622 FCI09F2 (03252)
00624 FCI09F4 (03253)
00626 FCI09F6 (03254)
00628 FCI09F8 (03255)
00630 FCI09FA (03256)
00632 FCI09FC (03257)
00634 FCI09FE (03258)
00636 FCI09F0 (03259)
00638 FCI09F2 (03260)
00640 FCI09F4 (03261)
00642 FCI09F6 (03262)
00644 FCI09F8 (03263)
00646 FCI09FA (03264)
00648 FCI09FC (03265)
00650 FCI09FE (03266)
00652 FCI09F0 (03267)
00654 FCI09F2 (03268)
00656 FCI09F4 (03269)
00658 FCI09F6 (03270)
00660 FCI09F8 (03271)
00662 FCI09FA (03272)
00664 FCI09FC (03273)
00666 FCI09FE (03274)
00668 FCI09F0 (03275)
00670 FCI09F2 (03276)
00672 FCI09F4 (03277)
00674 FCI09F6 (03278)
00676 FCI09F8 (03279)
00678 FCI09FA (03280)
00680 FCI09FC (03281)
00682 FCI09FE (03282)
00684 FCI09F0 (03283)
00686 FCI09F2 (03284)
00688 FCI09F4 (03285)
00690 FCI09F6 (03286)
00692 FCI09F8 (03287)
00694 FCI09FA (03288)
00696 FCI09FC (03289)
00698 FCI09FE (03290)
00700 FCI09F0 (03291)
00702 FCI09F2 (03292)
00704 FCI09F4 (03293)
00706 FCI09F6 (03294)
00708 FCI09F8 (03295)
00710 FCI09FA (03296)
00712 FCI09FC (03297)
00714 FCI09FE (03298)
00716 FCI09F0 (03299)
00718 FCI09F2 (03300)
00720 FCI09F4 (03301)
00722 FCI09F6 (03302)
00724 FCI09F8 (03303)
00726 FCI09FA (03304)
00728 FCI09FC (03305)
00730 FCI09FE (03306)
00732 FCI09F0 (03307)
00734 FCI09F2 (03308)
00736 FCI09F4 (03309)
00738 FCI09F6 (03310)
00740 FCI09F8 (03311)
00742 FCI09FA (03312)
00744 FCI09FC (03313)
00746 FCI09FE (03314)
00748 FCI09F0 (03315)
00750 FCI09F2 (03316)
00752 FCI09F4 (03317)
00754 FCI09F6 (03318)
00756 FCI09F8 (03319)
00758 FCI09FA (03320)
00760 FCI09FC (03321)
00762 FCI09FE (03322)
00764 FCI09F0 (03323)
00766 FCI09F2 (03324)
00768 FCI09F4 (03325)
00770 FCI09F6 (03326)
00772 FCI09F8 (03327)
00774 FCI09FA (03328)
00776 FCI09FC (03329)
00778 FCI09FE (03330)
00780 FCI09F0 (03331)
00782 FCI09F2 (03332)
00784 FCI09F4 (03333)
00786 FCI09F6 (03334)
00788 FCI09F8 (03335)
00790 FCI09FA (03336)
00792 FCI09FC (03337)
00794 FCI09FE (03338)
00796 FCI09F0 (03339)
00798 FCI09F2 (03340)
00800 FCI09F4 (03341)
00802 FCI09F6 (03342)
00804 FCI09F8 (03343)
00806 FCI09FA (03344)
00808 FCI09FC (03345)
00810 FCI09FE (03346)
00812 FCI09F0 (03347)
00814 FCI09F2 (03348)
00816 FCI09F4 (03349)
00818 FCI09F6 (03350)
00820 FCI09F8 (03351)
00822 FCI09FA (03352)
00824 FCI09FC (03353)
00826 FCI09FE (03354)
00828 FCI09F0 (03355)
00830 FCI09F2 (03356)
00832 FCI09F4 (03357)
00834 FCI09F6 (03358)
00836 FCI09F8 (03359)
00838 FCI09FA (03360)
00840 FCI09FC (03361)
00842 FCI09FE (03362)
00844 FCI09F0 (03363)
00846 FCI09F2 (03364)
00848 FCI09F4 (03365)
00850 FCI09F6 (03366)
00852 FCI09F8 (03367)
00854 FCI09FA (03368)
00856 FCI09FC (03369)
00858 FCI09FE (03370)
00860 FCI09F0 (03371)
00862 FCI09F2 (03372)
00864 FCI09F4 (03373)
00866 FCI09F6 (03374)
00868 FCI09F8 (03375)
00870 FCI09FA (03376)
00872 FCI09FC (03377)
00874 FCI09FE (03378)
00876 FCI09F0 (03379)
00878 FCI09F2 (03380)
00880 FCI09F4 (03381)
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00884 FCI09F8 (03383)
00886 FCI09FA (03384)
00888 FCI09FC (03385)
00890 FCI09FE (03386)
00892 FCI09F0 (03387)
00894 FCI09F2 (03388)
00896 FCI09F4 (03389)
00898 FCI09F6 (03390)
00900 FCI09F8 (03391)
00902 FCI09FA (03392)
00904 FCI09FC (03393)
00906 FCI09FE (03394)
00908 FCI09F0 (03395)
00910 FCI09F2 (03396)
00912 FCI09F4 (03397)
00914 FCI09F6 (03398)
00916 FCI09F8 (03399)
00918 FCI09FA (03400)
00920 FCI09FC (03401)
00922 FCI09FE (03402)
00924 FCI09F0 (03403)
00926 FCI09F2 (03404)
00928 FCI09F4 (03405)
00930 FCI09F6 (03406)
00932 FCI09F8 (03407)
00934 FCI09FA (03408)
00936 FCI09FC (03409)
00938 FCI09FE (03410)
00940 FCI09F0 (03411)
00942 FCI09F2 (03412)
00944 FCI09F4 (03413)
00946 FCI09F6 (03414)
00948 FCI09F8 (03415)
00950 FCI09FA (03416)
00952 FCI09FC (03417)
00954 FCI09FE (03418)
00956 FCI09F0 (03419)
00958 FCI09F2 (03420)
00960 FCI09F4 (03421)
00962 FCI09F6 (03422)
00964 FCI09F8 (03423)
00966 FCI09FA (03424)
00968 FCI09FC (03425)
00970 FCI09FE (03426)
00972 FCI09F0 (03427)
00974 FCI09F2 (03428)
00976 FCI09F4 (03429)
00978 FCI09F6 (03430)
00980 FCI09F8 (03431)
00982 FCI09FA (03432)
00984 FCI09FC (03433)
00986 FCI09FE (03434)
00988 FCI09F0 (03435)
00990 FCI09F2 (03436)
00992 FCI09F4 (03437)
00994 FCI09F6 (03438)
00996 FCI09F8 (03439)
00998 FCI09FA (03440)
01000 FCI09FC (03441)

```

SAVE THE IBIT VALUE

```

COMPARE THE BIT ASSIGNME
NT WITH THE MARK
IF NOT EQUAL, GOTO DECIDE
HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

```

SKIP IF BIT 0 OF R6VC(R2)



```

0AB08 0130 (03020) CLR R4
0AB09 0200 (03021) *VEJ
(03022) *
(03023) *
0AB0A F2700AA6 (03024) CMPR R7,R2,R15 (R4)
(03025) ?
0AB0B 80200014 (03026) JMP CR15,GT
(03027) ?
0AB0C 04000500 (03028) SMOVL 0,APDRPLS
0AB10 2021 (03029) MOV R1,R34
(03030) ?
0AB11 0800 (03031) EVEN
0AB12 0C000500 (03032) MOVZK APDRPLS
0AB13 F1100F0C (03033) STORE R1,R15AV
0AB16 F1200E0F (03034) STORE R2,R25AV
0AB18 F1300E10 (03035) STORE R3,R35AV
0AB1A F1400E12 (03036) STORE R4,R45AV
0AB1C F1500E14 (03037) STORE R5,R55AV
0AB1E F1600E16 (03038) STORE R6,R65AV
0AB20 F1700E18 (03039) STORE R7,R75AV
0AB22 0470 (03040) PFT
(03041) ?
0AB23 0800 (03042) EVEN
0AB24 A6100F0C (03043) LOAD R1,R15AV
0AB26 A6200E0E (03044) LOAD R2,R25AV
0AB28 A6300E10 (03045) LOAD R3,R35AV
0AB2A A6400E12 (03046) LOAD R4,R45AV
0AB2C A6500E14 (03047) LOAD R5,R55AV
0AB2E A6600E16 (03048) LOAD R6,R65AV
0AB30 A6700E18 (03049) LOAD R7,R75AV
0AB32 0A049544 (03050) SMOVL 0,R4(CV5)(R2)
(03051) ?
0AB33 021806C4 (03052) SMR 1,POT(CFS)(R4)
0AB36 2641 (03053) T6CR R4,1
0AB37 0800 (03054) EVEN
0AB38 2621 (03055) T6CR R2,1
(03056) ?
0AB39 0800 (03057) EVEN
0AB3A 0C500A0A (03058) L6P R5,C6TTIS
0AB3C F0509C0F (03059) MOVMP R5,R0R6R8
(03060) ?
0AB3E F2200C46 (03061) CMPR R2,R0R15
(03062) ?
0AB40 83200E68 (03063) XCT C6TTIS,GF
(03064) ?

```

COMPARE THE BIT ASSIGNME  
NT WITH THE MARK  
GOTO SERIALIZATION ROUTI  
NE IF NOT EQUAL  
HAS APODNE HAPPENED?  
NO, HOP TO EVEN LOC AFTE  
R RET

SKIP IF BIT 0 OF R4(CV82  
) IS CLEARED

INCREMENT THE BUFFER POS  
ITION COUNTER

CHECK IF R5 +VE  
RELOAD THE COUNTER WITH  
44  
IS R2 GREATER THAN POST  
4  
ADD 1R TO R2 IF LESS THA  
N OR EQUAL TO

```

00032 0000A00F (03063) JMP GRIT01
(03065) *
(03066) *
(03067) * DESERIALIZED OF BIT 0
(03068) *
(03069) *
00033 F2200C89 (03069) GRIT05 CMPR R2,P0STAS
(03070) *
00034 0010A0F6 (03071) *
(03072) *
00035 0E000540 (03073) *
(03074) *
00036 2021 (03075) *
(03076) *
00037 0800 (03077)
00038 C0000580 (03077)
00039 F1100E0C (03078)
00040 F1200E0E (03079)
00041 F1300E10 (03080)
00042 F1400E12 (03081)
00043 F1500E14 (03082)
00044 F1600E16 (03083)
00045 F1700E18 (03084)
00046 0E70 (03085)
00047 0800 (03086)
00048 A6100E0C (03087)
00049 A6200E0E (03088)
00050 A6300E10 (03089)
00051 A6400E12 (03090)
00052 A6500E14 (03091)
00053 A6600E16 (03092)
00054 A6700E18 (03093)
00055 F2200C86 (03094)
00056 8D2D0A82 (03095)
(03096) *
(03097) *
(03098) *
(03099) *
00060 F6600C89 (03100)
00061 4F64 (03101)
00062 0400 (03102)
00063 0E30 (03103)
00064 0800 (03104)
00065 9C200543 (03105)
(03106) *
(03107) GRIT01 POPR R2,R1

```

GO BACK TO GRIT01

COMPARE R2 WITH THE FRAME  
LENGTH  
IF GREATER THAN POST3, N  
IF FINISHED  
HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LOC AFTR  
R RET

COMPARE R2 WITH POST3  
IF GREATER THAN POST3, N  
O NEED TO PUT IN PARITY  
BITS

MOVE THE FRAME LENGTH TO  
R6=LSEN-1-R2

CLEAR R4

SET R2 TO HF THE ADDRESS  
:R6CVS(R2)  
POP R6CVS(R2)-->R1...R2=R



0A070 2802	(04100) ;	SPRCL 0,41	2*1	DON'T CHANGE ROTDCT IF 0
0A07A 1200K0C4	(04101)	SPR 0,ROTDCTS(4)		SRT LOW HIT OF THE ROTDC
	(04111) ;			T PARM
0A07C 2841	(04112)	TRCH R4,1		INCREMENT R4 BY 1
0A07D 0800	(04113)	EVFN		HAS ADDONE HAPPENED?
0A07E 0F00SND	(04114)	SPDSL 0,APDDEFLS		NO, HOP TO EVEN LOC AFTE
0A080 2021	(04115)	HOP #1+34		R RFT
	(04116) ;			
0A081 0800	(04117)	EVFN		
0A082 C000S80	(04118)	MOVZM ADDRFLS		
0A083 F109F0C	(04119)	STORE R1,R1SAV		
0A086 F1203E0F	(04120)	STORE R2,R2SAV		
0A088 F1309F10	(04121)	STORE R3,R3SAV		
0A08A F1409F12	(04122)	STORE R4,R4SAV		
0A08C F1509F14	(04123)	STORE R5,R5SAV		
0A08E F1609F16	(04124)	STORE R6,R6SAV		
0A090 F1709F18	(04125)	STORE R7,R7SAV		
0A092 0F70	(04126)	RFT		
0A093 0800	(04127)	EVFN		
0A094 A6109F0C	(04128)	LOAD R1,R1SAV		
0A096 A6209F0E	(04129)	LOAD R2,R2SAV		
0A098 A6309F10	(04130)	LOAD R3,R3SAV		
0A09A A6409F12	(04131)	LOAD R4,R4SAV		
0A09C A6509F14	(04132)	LOAD R5,R5SAV		
0A09E A6609F16	(04133)	LOAD R6,R6SAV		
0A0A0 A6709F18	(04134)	LOAD R7,R7SAV		
0A0A2 8C509FAF	(04135)	DJP R5,GRNDSD		CHECK IF R5 +VE.
0A0A4 F0509C9F	(04136)	MOVVR R5,F0REDRS		RELOAD THE COUNTER WITH
	(04137) ;			44
0A0A6 F2209C9E	(04138)	CYEMR R2,POST3S2		IS R2 GREATER THAN POST
	(04139) ;			3
0A0A8 80209A9F	(04140)	JMP GRNDSD,GT		LEAVE IF GREATER THAN
0A0AA FC209C9E	(04141)	ADDR R2,FICTRNS		ADD 18 TO R2
0A0AC F4609C9E	(04142)	SUBR R6,FICTRNS		SUBTRACT 18 FROM R6
0A0AE 8C609A78	(04143)	DJP R6,GRNDSD		LOOP BACK UNTIL THE PNT
	(04144) ;			IRE FRAMF IS DONE.
0A0B0 80009A9F	(04145)	JMP FICTS		
	(04146) ;			
	(04147) ;			
	(04148) ;			
	(04149) ;			
0A0B2 F0609C9E	(04150)	MOVVR R6,POST4S		MOV THE FRAME LENGTH TO
0A0B3 316A	(04151)	SUBR R6,R2		R6=LSR-1-R2

```

0ABF5 0800 (03152) EVEN
0ABF6 0800 (03153) CLR R4
0ABF7 0800 (03154) EVEN
0ABF8 0C200511 (03155) ADDR R2,PCVS-1
0ABF9 0C200511 (03156) ?
0ABA 1022 (03157) HRTTOSI POPXT R2,R1
0ABAA 2A03 (03158) ?
0ABAB 2A03 (03159) SPECL 0,R1
0ABC 220006C4 (03160) SBR 0,PROTECTS(R4)
0ABC1 1000540 (03161) SBRST 0,AFUNELS
0ABC2 2021 (03162) HOP 81,834
0ABC3 0800 (03163) ?
0ABC4 0C000580 (03164) EVEN
0ABC5 110040C (03165) MOVZK ADDRESS
0ABC6 110040C (03166) STORF R1,R1SAV
0ABC7 110040E (03167) STORF R2,R2SAV
0ABC8 1100410 (03168) STORF R3,R3SAV
0ABC9 1100412 (03169) STORF R4,R4SAV
0ABCA 1100414 (03170) STORF R5,R5SAV
0ABCB 1100416 (03171) STORF R6,R6SAV
0ABCC 1100418 (03172) STORF R7,R7SAV
0ABCD 0E70 (03173) PFT
0ABCE 0800 (03174) EVEN
0ABCF 1610040C (03175) LOAD R1,R1SAV
0ABD0 1620040E (03176) LOAD R2,R2SAV
0ABD1 16300410 (03177) LOAD R3,R3SAV
0ABD2 16400412 (03178) LOAD R4,R4SAV
0ABD3 16500414 (03179) LOAD R5,R5SAV
0ABD4 16600416 (03180) LOAD R6,R6SAV
0ABD5 16700418 (03181) LOAD R7,R7SAV
0ABD6 2641 (03182) INCR R4,1
0ABD7 0800 (03183) EVEN
0ABD8 0C60040A (03184) DJP R6,HRTTOSI
0ABD9 00000400 (03185) ?
0ABDA 00000400 (03186) EXITS
0ABDB 00000400 (03187) JMP RECVGRA
0ABDC 00000400 (03188) EJECT

```

CLEAR R4

SET R2 TO 44 THE ADDRESS  
:PCVS(R2)  
POP PCV(R2)-->R1...R2=H  
?+1

SET LOW BIT IN ROTDCT  
HAS ADDONE HAPPENED?  
NO, HOP TO EVEN LDC AFTE  
R RET

INCREMENT R4 BY 1

LOOP BACK UNTIL THE ENT  
IRE FRAME IS DDONE.

```

0AC0E 04000571 (04190) LORRES
0AC0A 0400047E (04191) SRRSL 0,APDRPDL
                                JPP LORSRAT
0AC0C 0402056A (04192) ?
0AC0E 0402056A (04193) MOVEM L,CUTS
0AC0F 0402056B (04194) MOVEM SPTRCO,SYSPFLGS
0AC10 0402056C (04195) CCS 3
0AC11 0402056D (04196) CCS 2
0AC12 0402056E (04197) CCS 4
0AC13 0402056F (04198) CCS 5
0AC14 04020570 (04199) SRRSL 0,APDRPFLS
0AC15 04020571 (04200) POP R1,4
0AC16 04020572 (04201) FVER
0AC17 04020573 (04202) MOVZM APDRPFLS
0AC18 04020574 (04203) RET
0AC19 04020575 (04204) FVER
0AC1A 04020576 (04205) CRRMZ R5,0
0AC1B 04020577 (04206) JPP LUTZS,4
0AC1C 04020578 (04207) ?
0AC1D 04020579 (04208) CRRMZ R6,5,0
0AC1E 04020580 (04209) JPP LORSR,CF
0AC1F 04020581 (04210) ?
0AC20 04020582 (04211) TRCM R6,5,0
0AC21 04020583 (04212) ?
0AC22 04020584 (04213) MOVIR R4,R1,TH1-1
0AC23 04020585 (04214) MOVIR R2,ZFOS-1
0AC24 04020586 (04215) CRRMZ OFLGS
0AC25 04020587 (04216) JPP LUTZS,FO
0AC26 04020588 (04217) MOVIR R2,R1,NOUITZS
0AC27 04020589 (04218) ?
0AC28 04020590 (04219) SRRSL 0,APDRPFLS
0AC29 04020591 (04220) POP R1,4
0AC2A 04020592 (04221) FVER
0AC2B 04020593 (04222) MOVZM APDRPFLS
0AC2C 04020594 (04223) RET
0AC2D 04020595 (04224) FVER
0AC2E 04020596 (04225) MOVIR R4,R1,TH1-1
0AC2F 04020597 (04226) MOVIR R2,ZFOS-1
0AC30 04020598 (04227) MOVIR R2,R4,NOUITZS+R1,TH12
0AC31 04020599 (04228) JPP GTIOL,5
0AC32 04020600 (04229) MOVIR R2,R4,NOUITZS
0AC33 04020601 (04230) SRRSL 0,APDRPFLS
0AC34 04020602 (04231) POP R1,4
0AC35 04020603 (04232) FVER

```

```

WAIT TIL AP OUT RUFF FUL
L
CLEAR D/A INTERRUPT FLAG
FROM SRR=2...
TO SRR=1
FROM MRR=1...
TO MRR=2
HAS APDRP. HAPPENED?

```

```

OUTPUT SILENCE IF NO SYN
C

```

```

OUTPUT SPEECH IS WE'VE W
AILED LONG ENUFF
COUNT THIS AS ONE MORE F
HAF. WAITED

```

```

LOAD BUFFER #2 IF OFLG S
ET

```

```

LOAD BUFFER #1

```

OAC26	00000580	(04233)	MOVZK	APDNFELS	
OAC28	0476	(04234)	RET		
OAC29	0400	(04235)	EVBN		
OAC2A	90400030	(04236)	MOVTR	R2,R1,TH1-1	
OAC2C	90200202	(04237)	MOVTR	R2,ZR0S-1	
OAC2E	07281088	(04238)	MOVVEL	R2,R4,ROUTTS+RUTH12	
OAC30	8000ACAC	(04239)	JMP	GTIOLSS	
OAC32	90300040	(04240)	MOVTR	R4,RETH1-1	
OAC33	90200342	(04241)	MOVTR	R2,OUTHS-2	
OAC36	02000504	(04242)	CHNZ	Z,DFLS	
OAC38	8030ACAC	(04243)	JMP	FI20S,FO	
OAC3A	07281102	(04244)	MOVVEL	R2,R4,ROUTTS	
OAC3C	05000580	(04245)	SMNSL	0,APDNFELS	
OAC3E	2005	(04246)	ROP	RT+8	
OAC3F	0400	(04247)	EVBN		
OAC40	00000580	(04248)	MOVZM	APDNFELS	
OAC42	0476	(04249)	RET		
OAC43	0400	(04250)	EVBN		
OAC44	90200404	(04251)	MOVTR	R2,OUTHS-2+RUTH12	
OAC46	90300030	(04252)	MOVTR	R4,RETH1-2	
OAC48	0728117E	(04253)	MOVVEL	R2,R4,ROUTTS+RUTH12	
OAC4A	8000ACAC	(04254)	JMP	GTIOLSS	
OAC4C	07281100	(04255) ?			
OAC4E	04000580	(04256) ?			
OAC50	2005	(04257) FI-20S	MOVVEL	R2,R4,ROUTTS	
OAC52	00000580	(04258)	SMNSL	0,APDNFELS	
OAC54	0476	(04259)	ROP	RT+8	
OAC56	0400	(04260)	EVBN		
OAC58	00000580	(04261)	MOVZK	APDNFELS	
OAC5A	0476	(04262)	RET		
OAC5C	0400	(04263)	EVBN		
OAC5E	90200404	(04264)	MOVTR	R2,OUTHS-2+RUTH12	
OAC60	90300030	(04265)	MOVTR	R4,RETH1-2	
OAC62	07281088	(04266)	MOVVEL	R2,R4,ROUTTS+RUTH12	
OAC64	8000ACAC	(04267) ?			
OAC66	0400	(04268) ?			
OAC68	00000580	(04269) GTIOLSS	EVBN		
OAC6A	0200	(04270)	CCS	5	
OAC6C	0400	(04271)	SCS	4	
OAC6E	00000580	(04272)	MOVLM	CRSGD,SYRFLS	
OAC70	0000047C	(04273)	JMP	LORISMA	
OAC72	00001082	(04274) *	DEFDEF	GLOBAL,SYRFLS	
OAC74	00000400	(04275)	D16SS1	=S40F2	
OAC76	00000400	(04276)	D16SS2	=S4A00	

! PREPARE FOR BLOCK MOVE  
 EXAMINE OFLG  
 IF OFLG=0, GOTO FI,20  
 HAS ADDONE HAPPENED?

HAS ADDONE HAPPENED?

FROM MRR=2...  
 TO MRR=1  
 GO BACK TO DISPATCHER

0000AACA (03277)	DZSN1=SAACA		
00001A44 (03278)	FZASN1=SAAPP		
00004B12 (03279)	IOSSINT=SAPI2		
(03280)			
0AC02 CC000566 (03281)	RAZP,DZA,MOVM INTERRUPT SERVICE RTMS		
0AC04 CC000567 (03282)	SPNSIF MOVZM ISAS100		ANA=0
0AC06 R0004B12 (03283)	MOVZM ISAS101		AFIIG=0
0AC08 CC000568 (03284)	JMP IOSSINT		
0AC0A F0500503 (03285)	SPNSZF MOVZM ISAS100		ANA=0
0AC0C F0500504 (03286)	MOVW P5,ISAS01		AFIIG=1
0AC0E R0004B12 (03287)	MOVW P5,ISAS101		
0AC10 CC000569 (03288)	JMP IOSSINT		SYN=0
0AC12 CC000569 (03289)	RCVPSIF MOVZM ISAS102		SFIIG=0
0AC14 F0101314 (03290)	MOVZM ISAS104		
0AC16 F2000574 (03291)	MOVW P1,PF0V15		
0AC18 2000 (03292)	CMF#Z F5YH		
0AC1A R0004B24 (03293)	POP		
0AC1C R0004B24 (03294)	SKPL LF		
0AC1E CC00056A (03295)	CALL R0,SUPDAT		DO SYNC UPDATE
0AC20 F0500503 (03296)	JMP IOSSINT		SYN=0
0AC22 F0500569 (03297)	RCVMSZF MOVZM ISAS102		SFIIG=1
0AC24 F0101369 (03298)	MOVW P5,ISAS01		
0AC26 F2000574 (03299)	MOVW P5,ISAS103		
0AC28 2000 (03300)	MOVW P1,PF0V25		
0AC2A R0004B24 (03301)	CMF#Z F5YN		
0AC2C R0004B12 (03302)	POP		
0AC2E CC00056A (03303)	SKPL LF		
0AC30 F0500503 (03304)	CALL R0,SUPDAT		DO SYNC UPDATE
0AC32 F0500569 (03305)	JMP IOSSINT		OUT=0
0AC34 F0101369 (03306)	MOVZM ISAS104		OFIIG=0
0AC36 F0500503 (03307)	MOVZM ISAS105		
0AC38 F0101369 (03308)	JMP IOSSINT		OUT=0
0AC3A F0500503 (03309)	OUTSZF MOVZM ISAS104		OFIIG=1
0AC3C F0500569 (03310)	MOVW P5,ISAS01		
0AC3E F0500569 (03311)	MOVW P5,ISAS105		
0AC40 R0004B12 (03312)	JMP IOSSINT		INP=0
0AC42 CC00056C (03313)	INSIF MOVZM ISAS106		IFIIG=0
0AC44 F0500560 (03314)	MOVZM ISAS107		
0AC46 R0004B12 (03315)	JMP IOSSINT		INP=0
0AC48 CC00056C (03316)	INSZF MOVZM ISAS106		IFIIG=1
0AC4A F0500503 (03317)	MOVW P5,ISAS01		
0AC4C F0500569 (03318)	MOVW P5,ISAS107		
0AC4E R0004B12 (03319)	JMP IOSSINT		
(03320)			



PAGE 001      PROG. CSMPSP.TXT

04AF8 0E70	(03165)	SET
04AF9 0800	(03166)	EVFN
04AFA 8000A0F4	(03167)	JMP 021811
	(03168) *	
04AFC 8000A012	(03169) *	PATCH EXECUTIVE TO CREATE BINARY VALUED I/O FLAGS
	(03170)	JMP 105510
	(03171)	END







FMS:SS: 0A244 (01068) (01124) (01200) (01243) (01346) (01423) (01467)  
 FMS: 0C01 (00214) (00785) (00789) (00793) (00803)  
 FMSHFM: 0409 (00801) (00804) (00806)  
 FMSHPT: 0407 (00747) (00749) (00743)  
 FMS: 0408 (00745)  
 FMS2S: 0408 (00749)  
 FMS3S: 0406 (00749)  
 FMS4S: 0404 (00803)  
 FMSA: 057C (00047) (02076) (02132) (02145) (03146)  
 FMS: 0406 (02774) (02855) (02926) (02947) (03071) (03145) (03186)  
 FMS: 0A9A (00200) (02671)  
 FMS: 0A94 (00200) (00200) (02665)  
 FMS: 0A94 (00201) (00201) (02659)  
 FMS: 0A94 (00201) (00201) (02653)  
 FMS: 0A94 (00200) (00200) (00201) (00201) (02647)  
 FMS: 0A92 (00200) (02641)  
 FMS: 0A50 (01797) (01877)  
 FMS: 0A52 (01874) (01947)  
 FMS: 0A62 (02153) (02157)  
 FMS: 0A64 (02156) (02159)  
 FMS: 0A4C (03243) (03257)  
 FMS: 040 (00841) (00854)  
 FMS: 0402 (00853) (00865)  
 FMS: 0A14 (01608) (01685)  
 FMS: 0404 (00217) (01053) (01115) (01186) (01258) (01333) (01414) (02760) (02842) (02913)  
 FMS: 0404 (02944) (03054) (03136)  
 FMS: 0404 (00214)  
 FMS: 0404 (00205) (02081) (02754) (02757)  
 FMS: 0A44 (00203) (03026) (03064)  
 FMS: 0A76 (03107) (03143)  
 FMS: 0A6F (00203) (02953) (02996)  
 FMS: 0A0A (03074) (03057) (03064)  
 FMS: 0A76 (00203) (02842) (02925)  
 FMS: 0AAA (02951) (02983) (02990)  
 FMS: 0A12 (00203) (02809) (02854)  
 FMS: 0A34 (02840) (02912) (02919)  
 FMS: 0A90 (00203) (02763)  
 FMS: 0A90 (02407) (02841) (02848)  
 FMS: 0A6F (03135) (03140) (03143)  
 FMS: 0404 (00675)  
 FMS: 0A5C (00675) (03224) (03234) (03254) (03269)  
 FMS: 0404 (00674)  
 FMS: 0001 (00050) (01775)  
 FMS: 0A02 (03095) (03150)

HEATERS:	00000	(00157)	(03184)						
HEATS:	00000	(00164)	(01006)	(01078)	(01001)	(01150)	(01222)	(01294)	(01299)
HEAVY:	00000	(00012)	(00110)	(00122)	(00121)				
HEAVY:	00000	(00066)	(02038)	(02086)	(02136)				
HEAVY:	00000	(00088)	(00880)						
HEAVY:	00000	(00013)	(00110)	(00122)	(00123)				
HEAVY:	00000	(00113)	(03160)						
HEAVY:	00000	(00116)	(03164)						
HEAVY:	00000	(00195)	(00957)						
HEAVY:	00000	(00198)	(01071)						
HEAVY:	00000	(00200)	(02630)						
HEAVY:	00000	(00203)	(02777)						
HEAVY:	00000	(00005)	(06891)	(00722)	(00829)				
HEAVY:	00000	(00005)	(00793)	(00806)					
HEAVY:	00000	(00005)	(00712)	(00833)					
HEAVY:	00000	(00167)	(00843)	(00852)	(00855)	(00864)			
HEAVY:	00000	(00093)							
HEAVY:	00000	(00674)	(00689)	(00690)					
HEAVY:	00000	(00279)	(03288)	(03296)	(03298)	(03305)	(03312)	(03315)	(03319)
HEAVY:	00000	(00023)	(03286)	(03298)	(03310)	(03317)			
HEAVY:	00000	(00025)	(00077)	(03282)	(03285)				
HEAVY:	00000	(00026)	(00079)	(03283)	(03287)				
HEAVY:	00000	(00071)	(00081)	(03289)	(03293)				
HEAVY:	00000	(00028)	(00082)	(03290)	(03299)				
HEAVY:	00000	(00029)	(00083)	(03306)	(03309)				
HEAVY:	00000	(00030)	(00084)	(03307)	(03311)				
HEAVY:	00000	(00031)	(00085)	(03313)	(03316)				
HEAVY:	00000	(00042)	(00086)	(03313)	(03318)				
HEAVY:	00000	(00033)	(00093)						
HEAVY:	00000	(00033)	(00094)						
HEAVY:	00000	(00035)	(00095)						
HEAVY:	00000	(00036)	(00096)						
HEAVY:	00000	(00037)	(00097)						
HEAVY:	00000	(00038)	(00098)						
HEAVY:	00000	(00038)	(00098)						
HEAVY:	00000	(00023)	(00024)	(00025)	(00026)	(00027)	(00028)	(00029)	(00030)
HEAVY:	00000	(00033)	(00034)	(00035)	(00036)	(00037)	(00038)	(00039)	(00040)
HEAVY:	00000	(00043)	(00044)	(00045)	(00046)	(00047)	(00048)		
HEAVY:	00000	(00097)							
HEAVY:	00000	(00209)	(03240)						
HEAVY:	00000	(00164)	(03191)						
HEAVY:	00000	(00763)	(03273)						
HEAVY:	00000	(00755)	(03189)						
HEAVY:	00000	(01566)	(01571)						

L00FS:	0304C (00933) (01000)
L00FS1:	03004 (02616) (02680)
L02S:	03006 (01206) (03213)
L071S:	04070 (03216) (03279)
L0P0M:	00000 (00066) (00214)
L0P0M:	00171 (00065) (00211)
L0P0M:	00034 (00070) (00099)
L0P0M:	00170 (00160) (02183)
L0P0M:	00273 (02716)
L0P0M:	00070 (00071) (01634)
L0P0M:	00070 (00072) (01795)
L0P0M:	00070 (00073) (01825)
L0P0M:	00192 (01929) (01976)
L0P0M:	00570 (00046) (02043)
L0P0M:	00004 (01967) (02075)
L0P0M:	00100 (00063) (00213)
L0P0M:	00040 (00063)
L0P0M:	00000 (00052)
L0P0M:	00000 (00215)
L0P0M:	00171 (01965) (01980)
L0P0M:	0044A (00723) (00830)
L0P0M:	0044A (00714) (00829)
L0P0M:	0044C (01781)
L0P0M:	0044B (00722) (00868)
L0P0M:	0044C (00650) (02304)
L0P0M:	0044C (02575) (02529)
L0P0M:	0044C (00658) (02395)
L0P0M:	0044C (00640) (02079)
L0P0M:	0044C (00131) (00854)
L0P0M:	0044C (00131) (00842)
L0P0M:	0044C (02033) (02053)
L0P0M:	01100 (00135) (03229)
L0P0M:	01100 (00137) (03217)
L0P0M:	00560 (00084) (03215)
L0P0M:	0057A (00045) (02088)
L0P0M:	00000 (00214) (01382)
L0P0M:	0056A (00084) (00753)
L0P0M:	0044C (03306) (03346)
L0P0M:	0044C (04309) (03352)
L0P0M:	0044A (00179) (03241)
L0P0M:	00571 (00096) (00692)
L0P0M:	0044C (00214) (00433)
L0P0M:	0044C (01635) (01660)
L0P0M:	00207 (02229) (02230)
L0P0M:	00904 (00905) (01605)
L0P0M:	00206 (02207) (02252)
L0P0M:	00601 (00902) (01710)
L0P0M:	00218 (02184) (02253)
L0P0M:	00601 (01659) (01661)
L0P0M:	00186 (01709) (01711)
L0P0M:	00186 (01850) (01733)
L0P0M:	00186 (01895) (01425)
L0P0M:	00186 (01851) (01894)
L0P0M:	00186 (01885) (01887)
L0P0M:	00186 (01896) (01903)
L0P0M:	00186 (01933) (01935)
L0P0M:	00186 (02119) (02124)
L0P0M:	00186 (02121) (02121)
L0P0M:	00186 (02751) (02751)
L0P0M:	0044C (00993) (02011)
L0P0M:	0044C (02492) (02508)
L0P0M:	0044C (02543) (02550)
L0P0M:	0044C (02491) (02504)
L0P0M:	0044C (02511) (02516)
L0P0M:	0044C (02518) (02520)
L0P0M:	0044C (02512) (02522)
L0P0M:	0044C (03211) (03211)
L0P0M:	0044C (00862) (00862)
L0P0M:	0044C (00850) (00850)
L0P0M:	0044C (03238) (03257)
L0P0M:	0044C (03227) (03244)
L0P0M:	0044C (03242) (03253)
L0P0M:	0044C (02115) (02138)
L0P0M:	0044C (01435) (03193)
L0P0M:	0044C (00753) (03193)
L0P0M:	0044C (03306) (03346)
L0P0M:	0044C (04309) (03352)
L0P0M:	0044A (00179) (03241)
L0P0M:	00571 (00096) (00692)
L0P0M:	0044C (00214) (00433)

POSTS:	00105 (00207)	(01117)	(01105)	(01200)	(01335)	(01369)	(02844)	(02915)	(02946)	(03060)
POSTS:	00106 (00208)	(01118)	(01106)	(01201)	(01336)	(01370)	(02845)	(02916)	(02947)	(03061)
	(01094)									
POSTS1:	00107 (00209)	(01119)	(01107)	(01202)	(01337)	(01371)	(02846)	(02917)	(02948)	(03062)
POSTS2:	00108 (00210)	(01120)	(01108)	(01203)	(01338)	(01372)	(02847)	(02918)	(02949)	(03063)
POSTS3:	00109 (00211)	(01121)	(01109)	(01204)	(01339)	(01373)	(02848)	(02919)	(02950)	(03064)
	(01095)									
POSTS:	00110 (00212)	(01122)	(01110)	(01205)	(01340)	(01374)	(02849)	(02920)	(02951)	(03065)
POSTS:	00111 (00213)	(01123)	(01111)	(01206)	(01341)	(01375)	(02850)	(02921)	(02952)	(03066)
	(01096)									
POSTS:	00112 (00214)	(01124)	(01112)	(01207)	(01342)	(01376)	(02851)	(02922)	(02953)	(03067)
POSTS:	00113 (00215)	(01125)	(01113)	(01208)	(01343)	(01377)	(02852)	(02923)	(02954)	(03068)
	(01097)									
POSTS:	00114 (00216)	(01126)	(01114)	(01209)	(01344)	(01378)	(02853)	(02924)	(02955)	(03069)
POSTS:	00115 (00217)	(01127)	(01115)	(01210)	(01345)	(01379)	(02854)	(02925)	(02956)	(03070)
	(01098)									
POSTS:	00116 (00218)	(01128)	(01116)	(01211)	(01346)	(01380)	(02855)	(02926)	(02957)	(03071)
POSTS:	00117 (00219)	(01129)	(01117)	(01212)	(01347)	(01381)	(02856)	(02927)	(02958)	(03072)
	(01099)									
POSTS:	00118 (00220)	(01130)	(01118)	(01213)	(01348)	(01382)	(02857)	(02928)	(02959)	(03073)
POSTS:	00119 (00221)	(01131)	(01119)	(01214)	(01349)	(01383)	(02858)	(02929)	(02960)	(03074)
	(01100)									
POSTS:	00120 (00222)	(01132)	(01120)	(01215)	(01350)	(01384)	(02859)	(02930)	(02961)	(03075)
POSTS:	00121 (00223)	(01133)	(01121)	(01216)	(01351)	(01385)	(02860)	(02931)	(02962)	(03076)
	(01101)									
POSTS:	00122 (00224)	(01134)	(01122)	(01217)	(01352)	(01386)	(02861)	(02932)	(02963)	(03077)
POSTS:	00123 (00225)	(01135)	(01123)	(01218)	(01353)	(01387)	(02862)	(02933)	(02964)	(03078)
	(01102)									
POSTS:	00124 (00226)	(01136)	(01124)	(01219)	(01354)	(01388)	(02863)	(02934)	(02965)	(03079)
POSTS:	00125 (00227)	(01137)	(01125)	(01220)	(01355)	(01389)	(02864)	(02935)	(02966)	(03080)
	(01103)									
POSTS:	00126 (00228)	(01138)	(01126)	(01221)	(01356)	(01390)	(02865)	(02936)	(02967)	(03081)
POSTS:	00127 (00229)	(01139)	(01127)	(01222)	(01357)	(01391)	(02866)	(02937)	(02968)	(03082)
	(01104)									
POSTS:	00128 (00230)	(01140)	(01128)	(01223)	(01358)	(01392)	(02867)	(02938)	(02969)	(03083)
POSTS:	00129 (00231)	(01141)	(01129)	(01224)	(01359)	(01393)	(02868)	(02939)	(02970)	(03084)
	(01105)									
POSTS:	00130 (00232)	(01142)	(01130)	(01225)	(01360)	(01394)	(02869)	(02940)	(02971)	(03085)
POSTS:	00131 (00233)	(01143)	(01131)	(01226)	(01361)	(01395)	(02870)	(02941)	(02972)	(03086)
	(01106)									
POSTS:	00132 (00234)	(01144)	(01132)	(01227)	(01362)	(01396)	(02871)	(02942)	(02973)	(03087)
POSTS:	00133 (00235)	(01145)	(01133)	(01228)	(01363)	(01397)	(02872)	(02943)	(02974)	(03088)
	(01107)									
POSTS:	00134 (00236)	(01146)	(01134)	(01229)	(01364)	(01398)	(02873)	(02944)	(02975)	(03089)
POSTS:	00135 (00237)	(01147)	(01135)	(01230)	(01365)	(01399)	(02874)	(02945)	(02976)	(03090)
	(01108)									
POSTS:	00136 (00238)	(01148)	(01136)	(01231)	(01366)	(01400)	(02875)	(02946)	(02977)	(03091)
POSTS:	00137 (00239)	(01149)	(01137)	(01232)	(01367)	(01401)	(02876)	(02947)	(02978)	(03092)
	(01109)									
POSTS:	00138 (00240)	(01150)	(01138)	(01233)	(01368)	(01402)	(02877)	(02948)	(02979)	(03093)
POSTS:	00139 (00241)	(01151)	(01139)	(01234)	(01369)	(01403)	(02878)	(02949)	(02980)	(03094)
	(01110)									
POSTS:	00140 (00242)	(01152)	(01140)	(01235)	(01370)	(01404)	(02879)	(02950)	(02981)	(03095)
POSTS:	00141 (00243)	(01153)	(01141)	(01236)	(01371)	(01405)	(02880)	(02951)	(02982)	(03096)
	(01111)									
POSTS:	00142 (00244)	(01154)	(01142)	(01237)	(01372)	(01406)	(02881)	(02952)	(02983)	(03097)
POSTS:	00143 (00245)	(01155)	(01143)	(01238)	(01373)	(01407)	(02882)	(02953)	(02984)	(03098)
	(01112)									
POSTS:	00144 (00246)	(01156)	(01144)	(01239)	(01374)	(01408)	(02883)	(02954)	(02985)	(03099)
POSTS:	00145 (00247)	(01157)	(01145)	(01240)	(01375)	(01409)	(02884)	(02955)	(02986)	(03100)
	(01113)									
POSTS:	00146 (00248)	(01158)	(01146)	(01241)	(01376)	(01410)	(02885)	(02956)	(02987)	(03101)
POSTS:	00147 (00249)	(01159)	(01147)	(01242)	(01377)	(01411)	(02886)	(02957)	(02988)	(03102)
	(01114)									
POSTS:	00148 (00250)	(01160)	(01148)	(01243)	(01378)	(01412)	(02887)	(02958)	(02989)	(03103)
POSTS:	00149 (00251)	(01161)	(01149)	(01244)	(01379)	(01413)	(02888)	(02959)	(02990)	(03104)
	(01115)									
POSTS:	00150 (00252)	(01162)	(01150)	(01245)	(01380)	(01414)	(02889)	(02960)	(02991)	(03105)
POSTS:	00151 (00253)	(01163)	(01151)	(01246)	(01381)	(01415)	(02890)	(02961)	(02992)	(03106)
	(01116)									
POSTS:	00152 (00254)	(01164)	(01152)	(01247)	(01382)	(01416)	(02891)	(02962)	(02993)	(03107)
POSTS:	00153 (00255)	(01165)	(01153)	(01248)	(01383)	(01417)	(02892)	(02963)	(02994)	(03108)
	(01117)									
POSTS:	00154 (00256)	(01166)	(01154)	(01249)	(01384)	(01418)	(02893)	(02964)	(02995)	(03109)
POSTS:	00155 (00257)	(01167)	(01155)	(01250)	(01385)	(01419)	(02894)	(02965)	(02996)	(03110)
	(01118)									
POSTS:	00156 (00258)	(01168)	(01156)	(01251)	(01386)	(01420)	(02895)	(02966)	(02997)	(03111)
POSTS:	00157 (00259)	(01169)	(01157)	(01252)	(01387)	(01421)	(02896)	(02967)	(02998)	(03112)
	(01119)									
POSTS:	00158 (00260)	(01170)	(01158)	(01253)	(01388)	(01422)	(02897)	(02968)	(02999)	(03113)
POSTS:	00159 (00261)	(01171)	(01159)	(01254)	(01389)	(01423)	(02898)	(02969)	(03000)	(03114)
	(01120)									
POSTS:	00160 (00262)	(01172)	(01160)	(01255)	(01390)	(01424)	(02899)	(02970)	(03001)	(03115)
POSTS:	00161 (00263)	(01173)	(01161)	(01256)	(01391)	(01425)	(02900)	(02971)	(03002)	(03116)
	(01121)									
POSTS:	00162 (00264)	(01174)	(01162)	(01257)	(01392)	(01426)	(02901)	(02972)	(03003)	(03117)
POSTS:	00163 (00265)	(01175)	(01163)	(01258)	(01393)	(01427)	(02902)	(02973)	(03004)	(03118)
	(01122)									
POSTS:	00164 (00266)	(01176)	(01164)	(01259)	(01394)	(01428)	(02903)	(02974)	(03005)	(03119)
POSTS:	00165 (00267)	(01177)	(01165)	(01260)	(01395)	(01429)	(02904)	(02975)	(03006)	(03120)
	(01123)									
POSTS:	00166 (00268)	(01178)	(01166)	(01261)	(01396)	(01430)	(02905)	(02976)	(03007)	(03121)
POSTS:	00167 (00269)	(01179)	(01167)	(01262)	(01397)	(01431)	(02906)	(02977)	(03008)	(03122)
	(01124)									
POSTS:	00168 (00270)	(01180)	(01168)	(01263)	(01398)	(01432)	(02907)	(02978)	(03009)	(03123)
POSTS:	00169 (00271)	(01181)	(01169)	(01264)	(01399)	(01433)	(02908)	(02979)	(03010)	(03124)
	(01125)									
POSTS:	00170 (00272)	(01182)	(01170)	(01265)	(01400)	(01434)	(02909)	(02980)	(03011)	(03125)
POSTS:	00171 (00273)	(01183)	(01171)	(01266)	(01401)	(01435)	(02910)	(02981)	(03012)	(03126)
	(01126)									
POSTS:	00172 (00274)	(01184)	(01172)	(01267)	(01402)	(01436)	(02911)	(02982)	(03013)	(03127)
POSTS:	00173 (00275)	(01185)	(01173)	(01268)	(01403)	(01437)	(02912)	(02983)	(03014)	(03128)
	(01127)									
POSTS:	00174 (00276)	(01186)	(01174)	(01269)	(01404)	(01438)	(02913)	(02984)	(03015)	(03129)
POSTS:	00175 (00277)	(01187)	(01175)	(01270)	(01405)	(01439)	(02914)	(02985)	(03016)	(03130)
	(01128)									
POSTS:	00176 (00278)	(01188)	(01176)	(01271)	(01406)	(01440)	(02915)	(02986)	(03017)	(03131)
POSTS:	00177 (00279)	(01189)	(01177)	(01272)	(01407)	(01441)	(02916)	(02987)	(03018)	(03132)
	(01129)									
POSTS:	00178 (00280)	(01190)	(01178)	(01273)	(01408)	(01442)	(02917)	(02988)	(03019)	(03133)
POSTS:	00179 (00281)	(01191)	(01179)	(01274)	(01409)	(01443)	(02918)	(02989)	(03020)	(03134)
	(01130)									





SPISG3:	00027 (00020) (00823) (00842)
SPISG:	00569 (00062) (01786)
SPISND:	0A56A (01991)
SPISLP:	0A56A (01984) (01988)
SPISPS:	09FF8 (00826) (00871)
SSSALT:	0A562 (02030)
SSSLP:	0A5C4 (02015) (02064)
SSSLPT:	0A604 (02050) (02053) (02056) (02060)
SSSMAT:	0A602 (02049) (02059)
SSSMO:	0A62A (02089) (02071) (02092)
STAKE:	0A600 (00053)
SUSLEP:	0A634 (02120) (02124)
SUSLEP:	0A632 (02118) (02129)
SUSLSE:	0A650 (02134) (02142)
SUSVAL:	0A6AA (02126) (02136)
SUPPAT:	0A628 (02111) (02295) (03304)
SVFS:	00382 (00054) (00055)
SVAS:	00504 (00061) (01778)
SYNLS:	027C4 (00186) (00187)
SYNPTS:	09335 (00189) (00190)
SYNCS:	00577 (00042) (02074) (02111) (02159)
SYNTM:	0A5AF (01973) (01986)
SYNRP:	0A5C4 (01975) (02011)
SYSPICS:	1PFCF (00021) (00704) (00742) (00823) (00832) (00867) (01781) (02715) (02741) (03194)
	(03272)
THALIS:	09F4M (00465) (02494) (02560)
THALZS:	09F7H (00530) (02496) (02561)
THAL3S:	09F8R (00594) (02497)
THPACS:	09CA2 (00224) (01506)
THPACS:	09089 (00336) (02415) (02434) (02465) (02477)
THPMS:	09CF0 (00271) (02335) (02338)
THPPCS:	090FH (00400) (02399) (02427) (02459) (02461) (02470)
THPSIDS:	09C94 (00222)
TRANMA:	09F40 (00704) (00795) (01760)
TST50:	09F22 (00694) (00762) (00770)
TST51:	09F4A (00704) (00712)
TST52:	09F52 (00721) (00729)
TST53:	09FF4 (00739) (00753)
UNLS:	09F04 (00843)
UNPPFLS:	0A656 (01965) (02147)
MS:	00002 (00056) (01991) (01994)
XDATAS:	00564 (00094) (01598) (01604)
XGCI:	00575 (00030) (00694) (00822)
XMITS:	09FA2 (00695) (00822)



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IMTISE: 03300 (01604) (01757)  
IMPCASE: 04306 (00731) (01774)  
ZS: 00003 (00057)  
ZTIS: 04004 (00221)  
ZPUS: 00004 (00212)  
ZBUS: 04203 (00175) (00897) (00900) (00903) (03214) (03226) (03237)

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) P- 0

### 3.6.3 Fortran Programs

The modules contained in this section are:

FCBGN	PITLPC
FF2R	QDPARM
VMOV1	VPBS
VMOV2	BASIS
VMOV3	ASRT
MPFDVM	SDCT
MPDCTM	BITA
MPQDPP	QDCT
MPDQPP	CHANL
MPMWGX	DPARM
MPFSTV	SSRT
MPSSRT	DDCT
MPIDCM	DCTR
MPASRT	VARDC
MPFSTQ	OUTPUT
MPFSTD	SNR
MPVDNM	TAPE2
MPBASP	OPT1
MPSCAN	OPT2
MPCDBA	OPT3
MASTER	OPT4
INIT	OPT5
USER	OPT6
MAPON	OPT7
CREATE	OPT8
DEFINE	OPT8A (OPT8B and OPT8C are similar)
SETUP	PREMAP
LOAD	FILEN
IOSM	
AOM	
ADAM	
SYSTEMA	
INPUT	
DCVAR	
DCTF	

0001 C INTEGER FUNCTION FCRG(FCRNO,Y,A,U,H,V,C,W,D,IS)

C TITLE: FCRGN

C PURPOSE:

C GENERATE FCR AND START EXECUTION OF MAP

C F4 USAGE:

C MAP = FCRG(FCRNO,Y,A,U,H,V,C,W,D,IS)

C DESCRIPTION OF PARAMETERS

C FCRNO INTEGER VARIABLE SPECIFYING FCR NUMBER

C Y,U,V,W - INTEGER VARIABLES SPECIFYING MAP

C HOPPER IDENTIFIERS

C A,H,C,D - INTEGER VARIABLES SPECIFYING MAP

C SCALAR IDENTIFIERS

C IS - POINTER TO LOGICAL ARRAY SUPPLIED BY BLOCK DATA. K-TH

C ELEMENT IS .TRUE. IF K-TH ID ARGUMENT

C IS TO BE USED, OTHERWISE .FALSE.

C IFR - OPTIONAL ERROR INDICATOR

C MAP =0 INDICATES NO ERROR (IDENTIFIERS WITHIN PROPER BOUNDS).

C MAP =K INDICATES K-TH ARGUMENT IN

C THE CALLING LIST IS

C OUTSIDE LEGAL BOUNDS. MAP EXECUTION ABORTED.

C FUNCTIONS AND SUBROUTINES REQUIRED:

C NORM(FCRG,FCRS) - INDICATES MAP EXECUTION.

0002 C INTEGER Y,A,U,H,V,C,W,D,FCRNO,HIDMX,HIDM,N,SIDMX,SIDM,N

0003 C INTEGER FCRG,FCRS,FCRN2,RUNMP

0004 C INTEGER FCRSZ,HAS,FCR,IRI

0005 C LOGICAL ISARG

0006 C DIMENSION ISARG(8,72)

0007 C COMMON/FPZZZ/FCRG(11),FCRSZ(11,7),HWS,FCR(6),MODCH(4),IRI,LEVEL

0008 C DIMENSION FCRS(11)

0009 C DATA HIDM,N1,IRI,MX/637,SIDM,N/07,SIDMX/1917

0010 C DATA FCRS/0,10\*8/

0011 C DATA ISARG(1,1),ISARG(2,1),ISARG(3,1),ISARG(4,1),

1 ISARG(5,1),ISARG(6,1),ISARG(7,1),ISARG(8,1)/

2 .FALSE.,.TRUE.,.FALSE.,.TRUE.,.FALSE.,.TRUE.,.TRUE.,.FALSE.,/

0012 C DATA ISARG(1,2),ISARG(2,2),ISARG(3,2),ISARG(4,2),

1 ISARG(5,2),ISARG(6,2),ISARG(7,2),ISARG(8,2)/

2 4\*.TRUE.,.4\*.FALSE.,/

0013 C DATA ISARG(1,3),ISARG(2,3),ISARG(3,3),ISARG(4,3),

1 ISARG(5,3),ISARG(6,3),ISARG(7,3),ISARG(8,3)/

2 6\*.TRUE.,.2\*.FALSE.,/

0014 C DATA ISARG(1,4),ISARG(2,4),ISARG(3,4),ISARG(4,4),

1 ISARG(5,4),ISARG(6,4),ISARG(7,4),ISARG(8,4)/

2 5\*.TRUE.,.3\*.FALSE.,/

0015 C DATA ISARG(1,5),ISARG(2,5),ISARG(3,5),ISARG(4,5),

1 ISARG(5,5),ISARG(6,5),ISARG(7,5),ISARG(8,5)/

```

2 39.TRUE..59.FALSE./
DATA ISARG(1,9),ISARG(2,6),ISARG(3,6),ISARG(4,6),
1 ISARG(5,6),ISARG(6,6),ISARG(7,6),ISARG(8,6)/
2 .FALSE..39.TRUE..49.FALSE./
DATA ISARG(1,7),ISARG(2,7),ISARG(3,7),ISARG(4,7),
1 ISARG(5,7),ISARG(6,7),ISARG(7,7),ISARG(8,7)/
2 .FALSE..49.TRUE..39.FALSE./
DATA ISARG(1,8),ISARG(2,8),ISARG(3,8),ISARG(4,8),
1 ISARG(5,8),ISARG(6,8),ISARG(7,8),ISARG(8,8)/
2 .TRUE..FALSE..TRUE..FALSE..39.FALSE./
DATA ISARG(1,9),ISARG(2,9),ISARG(3,9),ISARG(4,9),
1 ISARG(5,9),ISARG(6,9),ISARG(7,9),ISARG(8,9)/
2 39.TRUE..FALSE..TRUE..39.FALSE./
DATA ISARG(1,10),ISARG(2,10),ISARG(3,10),ISARG(4,10),
1 ISARG(5,10),ISARG(6,10),ISARG(7,10),ISARG(8,10)/
2 29.FALSE..TRUE..59.FALSE./
DATA ISARG(1,11),ISARG(2,11),ISARG(3,11),ISARG(4,11),
1 ISARG(5,11),ISARG(6,11),ISARG(7,11),ISARG(8,11)/
2 .TRUE..FALSE..TRUE..59.FALSE./
DATA ISARG(1,12),ISARG(2,12),ISARG(3,12),ISARG(4,12),
1 ISARG(5,12),ISARG(6,12),ISARG(7,12),ISARG(8,12)/
2 .TRUE..39.FALSE..TRUE..39.FALSE./
DATA ISARG(1,13),ISARG(2,13),ISARG(3,13),ISARG(4,13),
1 ISARG(5,13),ISARG(6,13),ISARG(7,13),ISARG(8,13)/
2 .TRUE..79.FALSE./
DATA ISARG(1,14),ISARG(2,14),ISARG(3,14),ISARG(4,14),
1 ISARG(5,14),ISARG(6,14),ISARG(7,14),ISARG(8,14)/
2 89.TRUE./
DATA ISARG(1,15),ISARG(2,15),ISARG(3,15),ISARG(4,15),
1 ISARG(5,15),ISARG(6,15),ISARG(7,15),ISARG(8,15)/
2 29.TRUE..FALSE..TRUE..FALSE..TRUE..29.FALSE./
DATA ISARG(1,16),ISARG(2,16),ISARG(3,16),ISARG(4,16),
1 ISARG(5,16),ISARG(6,16),ISARG(7,16),ISARG(8,16)/
2 69.TRUE..FALSE..TRUE./
DATA ISARG(1,17),ISARG(2,17),ISARG(3,17),ISARG(4,17),
1 ISARG(5,17),ISARG(6,17),ISARG(7,17),ISARG(8,17)/
2 39.TRUE..FALSE..TRUE..FALSE..TRUE..FALSE./
DATA ISARG(1,18),ISARG(2,18),ISARG(3,18),ISARG(4,18),
1 ISARG(5,18),ISARG(6,18),ISARG(7,18),ISARG(8,18)/
2 .TRUE..FALSE..TRUE..39.FALSE..TRUE..FALSE./
DATA ISARG(1,19),ISARG(2,19),ISARG(3,19),ISARG(4,19),
1 ISARG(5,19),ISARG(6,19),ISARG(7,19),ISARG(8,19)/
2 .FALSE..FALSE..FALSE..39.FALSE..FALSE..FALSE./
DATA ISARG(1,20),ISARG(2,20),ISARG(3,20),ISARG(4,20),
1 ISARG(5,20),ISARG(6,20),ISARG(7,20),ISARG(8,20)/
2 .TRUE..TRUE..FALSE..TRUE..FALSE..TRUE..29.FALSE./
DATA ISARG(1,21),ISARG(2,21),ISARG(3,21),ISARG(4,21),
1 ISARG(5,21),ISARG(6,21),ISARG(7,21),ISARG(8,21)/
2 .TRUE..TRUE..FALSE..FALSE..FALSE..29.FALSE./
DATA ISARG(1,22),ISARG(2,22),ISARG(3,22),ISARG(4,22),
1 ISARG(5,22),ISARG(6,22),ISARG(7,22),ISARG(8,22)/
2 .TRUE..FALSE..TRUE..FALSE..TRUE..FALSE..TRUE..FALSE./

```

```

0034 IF (FCG00,LT,DTSESH=1
0035 FCGM2=IABS(FCG00)
C
C LOAD FCG VALUES
0036 FCG(1) = 0
0037 FCG(2) = FCG02
0038 FCG(3) = ISESH
0039 FCG(4) = Y
0040 FCG(5) = A
0041 FCG(6) = U
0042 FCG(7) = R
0043 FCG(8) = Y
0044 FCG(9) = C
0045 FCG(10) = W
0046 FCG(11) = 0
C
C VALIDATE ARGUMENT IF USED, SET TO ZERO IF NOT.
0047 K=0
0048 DO 400 I=4,10,2
0049 J=I+1
0050 IF (ISARG(I-1,IS)) GO TO 100
0051 FCG(I)=0
0052 GO TO 200
0053 100 K=I+1
0054 IF (FCG(I),GT,HIDMN,OR,FCG(I),GT,NUMX) GO TO 1000
0055 200 IF (ISARG(J-1,IS)) GO TO 300
0056 FCG(J)=0
0057 GO TO 400
0058 300 K=J+1
0059 IF (FCG(J),GT,SIDMN,OR,FCG(J),GT,SIDMX) GO TO 1000
0060 400 CONTINUE
C
C START MAP AND RETURN
0061 FCG6 = MIN=FCG(1),FCRS(1)
0062 RETURN
C
C MAP EXECUTION ABORTED: SET ERROR CODE AND RETURN
0063 1000 FCG6=K
0064 IF (FCG6,NE,0) CALL MPERR(FCG6)
0065 FFINN
0066 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC0001	00046	144 R*,I,CUN,LCCL
SDATA	00014	6 R*,D,CUN,LCCL
SWAMS	00010	106 R*,D,CUN,LCCL
4PZZZ	00032	101 R*,D,OVN,CRL

TOTAL SPACE ALLOCATED = 001574 446

FUNMAN IV-PLUS V02-514  
FCRGN.VIN /DE/PH

21:13:55 11-SEP-80

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NO PFP INSTRUCTIONS GENERATED

FCRGN.LP/0.1.1.1=FCRGN/DE/MITH

11  
11

FORTRAN IV-PLUS V02-S1F  
FF2R.8TN /WH

13:37:15 09-AUG-80

PAGE 1

0001            INTEG M FUNCTION FF2R (Y, SA, U, V, W)  
0002            INTEG M FCHGN, Y, SA, U, V, W  
0003            FF2R = FCRGN(-214, Y, SA, U, V, O, W, O, 14)  
0004            RETURN  
0005            END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000054	22 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	17 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000120 40

NO PPP INSTRUCTIONS GENERATED

FF2R.LP/LI:1=FF2R/NUTR

FORTRAN IV-PLUS V02-51V 11:35:19 09-AUG-80

```

VMOV1.FTN
      /JOB/WR
0001  INTEGER FUNCTION VMOV1(Y)
0002  INTEGER FCRCM,Y
0003  VMOV1=FCRCM(237,Y,0,0,0,0,0,0,0,0,0,0,13)
0004  RETURN
0005  END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CUN,I,CL
SPDATA	000014	6 RW,D,CUN,I,CL
STDATA	000030	12 RW,D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

VMOV1.LP/LI:1=VMOV1/DF/NOTR



FORTRAN IV-PLUS V02-SIF 13135143 09-AUG-80  
VMOV2.FTM /DE/WR

0001 INTEGER FUNCTION VMOV2(Y)  
0002 INTEGER FCNCRN,Y  
0003 VMOV2=FCNCRN(238,Y,0,0,0,0,0,0,0,1)  
0004 RETURN  
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CI
SPDATA	000014	6 RW,D,CON,I,CI
SIDATA	000030	12 RW,D,CON,I,CI

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

VMOV2.LP/1.1.1=VMOV2/DE/NOTR



```

0001      INTEGER FUNCTION MPFDVM(Y,U,V)
           C
           C      MAP USAGE
           C
           C      IFRN=MPFDVM(Y,U,V)
           C
0002      INTEGER FCRGN,Y,U,V
0003      MPFDVM=FCRGN(240,Y,0,U,0,V,0,0,0,8)
0004      RETURN
0005      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	16 RW,I,CUN,LCL
SPDATA	000014	6 RW,D,CUN,LCL
SDATA	000030	12 RW,D,CUN,LCL

TOTAL SPACE ALLOCATED = 000104 34

NO FPD INSTRUCTIONS GENERATED

MPFDVM,LP/LI:1:MPFDVM/DE/NOTR

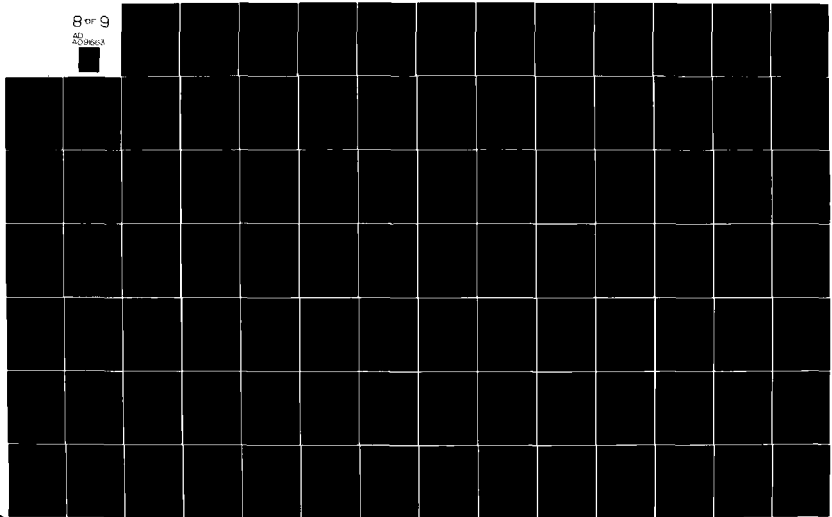
AD-A691 663

GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)  
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8 of 9  
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FORTHAN IV-PLUS V02-51E  
MPDCTM.FTN /DF/WR

0001 C INTEGER FUNCTION MPDCTM(Y,U,V,W)  
C MAP USAGE:  
C IFRH=MPDCTM(Y,U,V,W)  
C  
0002 INTEGER FCRCGN,Y,U,V,W  
0003 MPDCTM=FCRCGN(241.Y,0,U,0,V,0,W,0,22)  
0004 RETURN  
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000046	RM,1,CON,LCL
SPDATA	000014	RM,D,CON,LCL
SIDATA	000030	RM,D,CON,LCL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPDCTM,LP/LI:1=MPDCTM/DE/NOTR



FURTRAM IV-PLUS V02-51F  
MPDOPP.FTN /JF/WH

0001 INTEGER FUNCTION MPDOPP(Y,U,V)

C  
C  
C  
C

MAP USAGE:

IFRM=MPDOPP(Y,U,V)

0002 INTEGER FCRCN,U,Y,V,W  
0003 MPDOPP=FCRCN(243,Y,0,U,0,V,0,0,0,H)  
0004 RETURN  
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	RM,I,CON,LCL
SPDATA	000014	RM,D,CON,LCL
SIDATA	000030	RM,D,CON,LCL
SVARS	000002	RM,D,CON,LCL

TOTAL SPACE ALLOCATED = 000106 35

NO FPP INSTRUCTIONS GENERATED

MPDOPP,LP/II:1=MPDOPP/DE/NOTR

FORTRAN IV-PLUS V02-51E  
MPMWGX.FTN

13:36:10 04-AUG-80

PAGE 1

0001 C INTEGER FUNCTION MPMWGX(Y,U,V,W)

C MAP USAGE:

C IPRR=MPMWGX(Y,U,V,W)

0002 C INTEGER FCBN,Y,U,V,W

0003 MPMWGX=FCBN(244,Y,0,U,0,V,0,W,0,0,22)

0004 RPTURN

0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	19 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPMWGX,LP/LI:1=MPMWGX/DE/NOTR





0001 C INTEGER FUNCTION MPSSRT(Y)

C MAP USAGE:

C IEMR=MPSSRT(Y)

C

0002 INTEGER FCHGN,Y  
0003 MPSSRT=FCHGN(247,Y,0,0,0,0,0,0,0,13)  
0004 RETURN  
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	RW,I,CUN,I,CU
SPDATA	000014	RW,D,CUN,I,CU
SIDATA	000030	RW,D,CUN,I,CU

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

MPSSRT.LP/LI:1=MPSSRT/DE/NOTR



FORTRAN IV-PLUS V02-S1E /DF/WR  
MPASRT.FTN

0001 C INTEGER FUNCTION MPASRT(Y)

C  
C MAP USAGE:

C I=RR=MPASRT(Y)

C

0002 INTEGER FCHGN,Y  
0003 MPASRT=FCHGN(249,Y,0.0,0.0,0.0,0.13)  
0004 RETURN  
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCORF1	000024	RM,I,CON,I,CL
SPDATA	000014	RM,D,CON,I,CL
SIDATA	000030	RM,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000070 28

NO FPP INSTRUCTIONS GENERATED

MPASRT,LP/LI:1=MPASRT/DF/NOTR

```

0001 C INTEGER FUNCTION MPFSTO(Y,U,V,W)
      C MAP USAGE:
      C
      C IFHR=MPFSTO(Y,U,V,W)
      C
0002 C
0003 C INTEGER FCRGN(Y,U,V,W)
0004 C MPFSTO=FCRGN(250,Y,0,U,0,V,0,W,0,22)
0005 C RETURN
      C END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000046	RM,I,COM,LCL
SPDATA	000014	RM,D,COM,LCL
SIDATA	000030	RM,D,COM,LCL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPFSTO,IP/II:1=MPFSTO/DE/NOTR

FORTRAN IV-PLUS V02-51F 13:36:37 09-AUG-80  
MPFSTD.PTN /DF/WR

```

0001      INTEGER FUNCTION MPFSTD(Y,U,V,W)
          C
          C      MAP USAGE:
          C
          C      IFMR=MPFSTD(Y,U,V,W)
          C
0002      INTEGER ECHGN,Y,U,V,W
0003      MPFSTD=FCBGN(251,Y,0,U,0,V,0,W,0,22)
0004      RETURN
0005      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	RW,I,CON,I,CI
SPDATA	000014	RW,D,CON,I,CI
SIDATA	000030	RW,D,CON,I,CI

TOTAL SPACE ALLOCATED = 000112 37

NO PFP INSTRUCTIONS GENERATED

MPFSTD,LP/LI:1=MPFSTD/DE/NOTR

```
0001 C      INTEGK FUNCTION MPVDNM(Y,U,V)
      C      MAP USAGE:
      C
      C      TFR=MPVDNM(Y,U,V)
      C
0002      INTEGK FCRGN,U,V
0003      MPVDNM=FCRGN(252,Y,0,U,0,V,0,0,0,0,8)
0004      RETURN
0005      END
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	RM,I,CON,LCL
SPDATA	000014	RM,D,CON,LCL
SIDATA	000030	RM,D,CON,LCL

TOTAL SPACE ALLOCATED = 000104 34

NO FPP INSTRUCTIONS GENERATED

MPVDNM,LP/LI:1=MPVDNM/DE/NOIR





FORTRAN IV-PLUS V02-S1F  
MPSCAN.FTN /DE/WR

0001 C INTEGER FUNCTION MPSCAN(Y)

C MAP USAGE:

C C

C C IFR=MPSCAN(Y)

C C

0002 INTEGER FCGRN(U,V,W  
0003 MPSCAN=FCGRN(254,Y,0,0,0,0,0,0,0,0,0,0,13)

0004 RETURN

0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SVARS	000030	12 RW,D,CON,I,CL
	000006	3 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000076 31

NO FPP INSTRUCTIONS GENERATED

MPSCAN,IP/II:1=MPSCAN/DE/NOTR

```

0001      C      INTEGFR FUNCTION MPCDRA(Y)
          C      MAP USAGE:
          C
          C      IFRH=MPCDRA(Y)
          C

```

```

0002      INTEGFR FCRCN,Y,U,V
0003      MPCDRA=FCRCN(255,Y,0,0,0,0,0,0,0,13)
0004      RETURN
0005      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL
SVARS	000004	2 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000074 30

NO FPP INSTRUCTIONS GENERATED

MPCDRA.IP/LI:1=MPCDRA/DF/MOTR







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21:16:56

KONTRAK IV-PLUS V02-514  
MASTER.ATC /DE/AN

MASTER.IP/II:1=MASTER.ATC/DE/NOTP



FORTRAN IV-PIUS V02-N11 13:27:26 09-AUG-80

```

INIT,PTN
0023 MSHRT=H
0024 MLDNG=Q
C VERY ACCURATE VALUE FOR PI=3.14159...
0025 PI=4.0*ATAN(1.)
C LOGARITHM BASE 10 OF 2
0026 DLG2=ALOG(2.0)
C TRANSMIT, RECEIVE SIZE, I,SEN
0027 I,SEN=369
C # OF SIDEHAND HITS/FRAME, LPAPH
0028 I,PAHM=13
C FRAME SIZE, LTH
0029 LTH=256
0030 LTH2=2*LTH
0031 LTH3=3*LTH
0032 LTH4=4*LTH
0033 LTHM1=LTH-1
C LPC ORDER, LPCN
0034 LPCN=H
C OUTPUT INTERPOLATING SIZE, NP
0035 NP=10
0036 X=NP
C CULAR CUMULATIVE SNR RATIO, CNS
0037 CNS=0.
C FRAME COUNTER, ICOUNT
0038 ICOUNT=0
C ARGUMENT FOR TRIG FUNCTIONS
0039 ARG=PI*(2.*LTH-1.)/(2.*LTH)
C
C SET UP SPEECH I/O HANDLER
C
0040 IST=1
0041 NSKIP=1
0042 NSKIPS=NSKIP
C INPUT RECORD SIZE, NTOTI
0043 NTOTI=LTH-NP
C INPUT UPDATE SIZE, NTUPS
0044 NTUPS=LTH-NP
C OUTPUT RECORD SIZE, NTOTO
0045 NTOTO=LTH-NP
C RESET ALL ARRAYS
0046 DO 1 I=1,NTOTI
0047 NIM(I)=0
0048 DO 2 I=1,NTOTO
0049 NOUT(I)=0
0050 RETURN
0051 END

```

PROGRAM SECTIONS	NAME	SIZE	ATTRIBUTES
	SCODE1	000366	173
	SVARS	000060	24
	MTAPFO	002260	600
	MTAPF1	000012	5

PW, I, CON, I, C.  
 PW, D, CON, I, C.  
 PW, D, OVR, G, H.  
 PW, D, OVP, G, H.



FORTRAN IV-PLUS V02-51F  
INIT.FTN

13:27:26 09-AUG-80

PAGE 3

MTAPP2	000012	5	RW,D,OVR,GHI.
OVRT	002020	520	RW,D,OVR,GHI.
OVRO	002006	515	RW,D,OVR,GHI.
OVRF	006016	1543	RW,D,OVR,GHI.
OVRA	000152	53	RW,D,OVR,GHI.
OVVP	002022	521	RW,D,OVR,GHI.
OVRR	005004	1282	RW,D,OVR,GHI.
OVW2	001002	257	RW,D,OVR,GHI.
OVRL	000016	7	RW,D,OVR,GHI.
OVRO	000012	5	RW,D,OVR,GHI.
VAPDFF	000200	64	RW,D,OVR,GHI.

TOTAL SPACE ALLOCATED = 025450 5524

INIT.U.P/I:1=INIT/DE/MOTR



```

C      CALL STOU(QUATT,5,1,0,15W,0,DSW)
0023  TYPE 106
0024  FORMAT(1H,10X,'>>>>> GET 9600 RPS ATC SYSTEM <<<<<<')
C      TYPE 104
0025  FORMAT(1HS,'FRAME FIRING(Y/N)? ')
0026  READ(5,104)TIMING
0027  FORMAT(1H)
0028  WRITE(5,104)TIMING
0029  RETURN=VS
0030  LIVE=NO
0031  START=1
0032  IPRC=1
0033  IF((TIMING.EQ.YES)GO TO 2
0034  TIMING=NO
0035  TYPE 107
0036  FORMAT(1HS,'LIVE INPUT(Y/N)? ')
0037  FORMAT(1HS,'START-UP(Y/N)? ')
0038  ACCEPT 104,LIVE
0039  IPRC=NO
0040  IF(LIVE.EQ.NO)ITMP=YES
0041  LIVE=ITMP
0042  IF(LIVE.EQ.YES)GO TO 1
0043  TYPE 102
0044  FORMAT(1HS,'REAL TIME OPERATION(Y/N)? ')
0045  ACCEPT 104,RTIM
0046  RTIME=NO
0047  TYPE 105
0048  FORMAT(1HS,'STARTING FRAME # =')
0049  ACCEPT 101,STAKE
0050  START=1
0051  IPRC=1
0052  IF(TIMING.EQ.YES)GO TO 1
0053  TYPE 100
0054  FORMAT(1HS,'NO. OF FRAMES= ')
0055  ACCEPT 101,IREC
0056  FORMAT(16)
0057  TYPE 110
0058  CONTINUE
0059  FORMAT(//1X,'SYSTEM INITIALIZATION:')
0060  RETURN
0061  END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000270	77
SDATA	000104	34
SVAPS	000046	14
MTAPP0	002260	600
MTAPP1	000017	5
MTAPP2	000017	5
MTAPP3	012260	2648
MTAPP5	000064	26
OVPT	002020	520
OVRO	002006	515

PROGRAM IV-PLUS 002-SIF  
USER,FIN /DB/MP

21:17:14 11-SEP-60

PAGE 3

OVRR	006016	1843	RM,D,OVRR,GML
OVRR	000152	53	RM,D,OVRR,GML
OVRR	002022	521	RM,D,OVRR,GML
OVRR	005004	1282	RM,D,OVRR,GML
OVRR	001002	757	RM,D,OVRR,GML
OVRR	000016	7	RM,D,OVRR,GML
OVRR	000012	5	RM,D,OVRR,GML
OVRR	000210	64	RM,D,OVRR,GML

TOTAL SPACE ALLOCATED = 037740 H176

NO PEP INSTRUCTIONS GENERATED

USER,GP/ELI=USER/DE/MOTR



0027 DATA HYPER,MYPER,MYPER/2,4,8/  
0028 DATA LONG,SUBST,CVVPER,CVMMU/0,1,1,0/  
0029 DATA POST,POST,POST/64,128,192/  
0030 DATA WORK,AL,ALR,X11,INDRMA,PARFEM,DC1\*1/10,11,0,0,12,13,14/  
0031 DATA DC1\*2,RI,AL,AF,PARC,TRP1,TR2/15,16,17,18,3,19,20/  
0032 DATA DIRECTA,YNM/21,0/  
0033 DATA VLONG,CUSZ,VSHRT,YDM,XDM,MAPX/23,24,25,26,27,28/  
0034 DATA MINT,DCTM,OPRM,OUTCTM/29,30,31,32/  
0035 DATA ITEM,RENT,MEZ,ADUTM,ADULT,ADULT/2/44,34,35,36,37,38/  
0036 DATA RECVI,REC2,SENI,SEH2/39,40,41,42/  
0037 DATA AGRM,AUTDCT,ATRIT/43,44,45/  
0038 DATA TAP1,TAP4,X2/46,47,48/  
0039 DATA SEN,INPR/49,50/  
0040 DATA SYRNI,RFI/67,63/  
0041 DATA RECV,AROPR,ARODCT,RIGHT/56,57,58,2/  
0042 DATA UPRM,ZRO,UNITY/59,1,63/  
0043 DATA DCTTT,DCTTT2,INDR1,INDR2,MINTJ/51,52,53,54,55/  
0044 DATA MINT2,MINT3,MINT4,ODCT1/60,5,6,7/  
0045 DATA FAMILY,INIT,RCVDF/110,113,127/  
0046 DATA OPRM,ORDCTM/8,9/

C	C	C	K A P - 3 0 0									
C	C	C	M U L T I P L I C A T I O N S									
C	C	C	MI	BU	MA	MAKS-I	MS	ST	SI	WS		
C	C	C	43	1	35328.	35311.	14	1	E	L		
C	C	C	44	1	35342.	35337.	256	1	F	L		
C	C	C	45	1	35598.	35853.	256	1	E	L		
C	C	C	49	1	35854.	36273.	370	1	E	L		
C	C	C	50	1	36274.	36479.	256	1	E	L		
C	C	C	53	1	36480.	36848.	369	1	E	L		
C	C	C	54	1	36849.	37217.	369	1	E	L		
C	C	C	57	1	37218.	37586.	369	1	E	L		
C	C	C	58	1	37587.	37955.	369	1	E	L		
C	C	C	59	1	37956.	38211.	256	1	E	L		
C	C	C	56	1	38212.	38581.	370	1	E	L		
C	C	C	57	1	38582.	38950.	14	1	E	L		
C	C	C	58	1	38951.	39319.	256	1	E	L		
C	C	C	62	1	39320.	39688.	369	1	E	L		
C	C	C	61	1	39689.	40057.	369	1	E	L		
C	C	C	2	1	39590.	40245.	256	1	E	L		
C	C	C	63	1	10060.	14499.	4500	1	E	L		
C	C	C	1	1	8800.	9999.	1200	1	E	L		

C	C0057	74	2	1024.0	1535.0	256	R	E	L
C	C0058	75	2	2048.0	2067.0	10	R	E	L
C	C0059	76	2	2068.0	2087.0	10	R	E	L
C	C0060	77	2	2088.0	2579.0	256	R	E	L
C	C0061	78	2	2088.0	2343.0	256	I	E	L
C	C0062	79	2	2344.0	2855.0	256	R	E	L
C	C0063	30	2	2856.0	2867.0	12	I	E	L
C	C0064	31	2	2868.0	3123.0	256	I	E	L
C	C0065	32	2	3124.0	3369.0	246	I	E	L
C	C0066	33	2	3370.0	3615.0	246	I	E	L
C	C0067	34	2	3616.0	3861.0	246	I	E	L
C	C0068	35	2	3862.0	4107.0	246	I	E	L
C	C0069	36	2	4108.0	4353.0	246	I	E	L
C	C0070	37	2	4354.0	4599.0	246	I	E	L
C	C0071	38	2	4600.0	4968.0	369	I	E	L
C	C0072	39	2	4969.0	5337.0	369	I	E	L
C	C0073	40	2	5340.0	5709.0	370	I	E	L
C	C0074	41	2	5710.0	6079.0	370	I	E	L
C	C0075	42	2	6080.0	6449.0	370	I	E	L
C	C0076	51	2	6450.0	6820.0	370	I	E	L
C	C0077	52	2	6830.0	7200.0	370	I	E	L
C	C0078	53	2	7210.0	7580.0	370	I	E	L
C	C0079	54	2	7600.0	7960.0	370	I	E	L
C	C0080	55	2	8000.0	8340.0	370	I	E	L
C	C0081	56	2	8350.0	8720.0	370	I	E	L
C	C0082	57	2	8700.0	9100.0	370	I	E	L
C	C0083	58	2	9050.0	9480.0	370	I	E	L
C	C0084	59	2	9400.0	9860.0	370	I	E	L
C	C0085	60	2	9810.0	10240.0	370	I	E	L
C	C0086	61	2	10220.0	10620.0	370	I	E	L
C	C0087	62	2	10630.0	11000.0	370	I	E	L
C	C0088	63	2	11040.0	11380.0	370	I	E	L
C	C0089	64	2	11450.0	11760.0	370	I	E	L
C	C0090	65	2	11860.0	12140.0	370	I	E	L
C	C0091	66	2	12270.0	12520.0	370	I	E	L
C	C0092	67	2	12680.0	12900.0	370	I	E	L
C	C0093	68	2	13090.0	13280.0	370	I	E	L
C	C0094	69	2	13500.0	13660.0	370	I	E	L
C	C0095	70	2	13910.0	14040.0	370	I	E	L
C	C0096	71	2	14320.0	14420.0	370	I	E	L
C	C0097	72	2	14730.0	14800.0	370	I	E	L
C	C0098	73	2	15140.0	15180.0	370	I	E	L
C	C0099	74	2	15550.0	15560.0	370	I	E	L
C	C0100	75	2	15960.0	15960.0	370	I	E	L



C OPRA	31	2	11816,0	11829,0	14	I	E	L
C OPRA	8	2	11830,0	11843,0	14	I	E	L
C OPRA	9	2	11844,0	12099,0	256	I	E	L



```

C          INITIAL MAP=300 SETUP
C
C          INSTN=0
C          STATUS=MDPM(3)
C          IF (STATUS.NE.0) GO TO 10
C          I5IN=-1
C          STATUS=MPHC(0)
C          IF (STATUS.NE.0) GO TO 10
C
C          OFFLINE PROGRAM CONSTANTS
C
C          LARGEST FFT SIZE,LMAX
C          SMALLEST FFT SIZE,LMIN
C          L4AT=20000000
C          L4I=20000000
C          LMAX=FLOAT(LMAX)
C          LMIN=FLOAT(LMIN)
C          FRAME PROCESSING SIZE,XBTH
C          XLTH=FLOAT(LTH)
C          IPARS=ITZRECEIVE BUFFER SIZE,I5FN
C          I5SP=FLOAT(I5SN)
C          # OF SIDEHAND BITS/FRAME,I5PARM
C          I5PARM=FLOAT(I5PARM)
C          DARG=1.0/FLOAT(LTH4)
C          XLGCLB=ALOG10(16.0)
C
C          MAP SCALAR TABLE SETUP
C
C          DO 1 I=50,127
C          SCALAR(I)=0.0
C          SCALAR(50)=1.0/FLMAX
C          SCALAR(51)=1.0/FLMIN
C          SCALAR(54)=1.0/FLAT(LTH)
C          SCALAR(56)=DARG
C          SCALAR(57)=-DARG
C          SCALAR(74)=FLOAT(I5PCN+1)
C          SCALAR(75)=FLOAT(I5PCN)
C          SCALAR(76)=4.0
C          SCALAR(77)=10.0E-10
C          SCALAR(79)=FLOAT(HTLTH)
C          I5PP 9101
C          FURPAT(I5S,I5IT MAX,= ' )
C          I5AD(5,9002,END)=9100,I5RR=9100)HTLTH
C          HTLTH=3.
C          SCALAR(84)=HTLTH
C          I5PP 9001
C          FURPAT(I5S,I5PC THRESHOLD,= ' )
C          I5AD(5,9002,END)=9000,I5RR=9000)THRSH
C          THRSH=.995
C          FURPAT(I5,8)
C          SCALAR(85)=THRSH
C          SCALAR(87)=FLOAT(LTH)
C          SCALAR(89)=0.62293E-01
C          SCALAR(90)=0.44600E+01

```















C DIAGNOSTIC TRAP AREA

0386 IO TYPE 100, INSTR, STATUS  
 0387 CALL EXIT  
 0388 IOU FORWATTIX, 100\*MAP FRONCH\* \*MAPUN\* INSTR=, I4, HAS STATUS=, I6/  
 0389 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCUD01	040564	234 RW, I, CUB, ECL
SPDATA	000374	126 RW, D, CUB, ECL
SHDATA	002344	626 RW, D, CUB, ECL
SVARS	000134	46 RW, D, CUB, ECL
STMFES	000004	2 RW, D, CUB, ECL
WEAPF0	002260	600 RW, D, OVR, GHL
WEAPF1	000012	5 RW, D, OVR, GHL
WEAPE2	000012	5 RW, D, OVR, GHL
WVRI	002020	570 RW, D, OVR, GHL
WVRD	002006	515 RW, D, OVR, GHL
WVRF	006016	1543 RW, D, OVR, GHL
WVRA	000152	53 RW, D, OVR, GHL
WVVP	002022	521 RW, D, OVR, GHL
WVRR	005004	1282 RW, D, OVR, GHL
WVR2	001002	257 RW, D, OVR, GHL
WVRC	000016	7 RW, D, OVR, GHL
WVRD	000012	5 RW, D, OVR, GHL
WVRDF	000200	64 RW, D, OVR, GHL

TOTAL SPACE ALLOCATED = 040666 H411

\*MAPUN.FIN:1=MAPUN/DF/MOTH



DATA RIDRF2,BASE2,SIZE2/63,MR00,0,1200,0/  
DATA RIDRF3,BASE3,SIZE3/63,14000,0,2380,0/

CONFIGURE 5 BIND FIRST BUFFER REGION

```
0028 C
0029 C
0030 C
0031 D INSTR=100
0032 D STATUS=MPCH(RUSI+RIDRF1,BASE1,SIZE1,INTEGR,EVERY,LONG)
0033 D IF(STATUS,NE,0)GO TO 10
0034 D INSTR=1
0035 D STATUS=MPCHF(RIDRF1,153)
0036 D IF(STATUS,NE,0)GO TO 10
0037 D INSTR=153
0038 D STATUS=MPFDVM(MAPX,NIMM,X2)
0039 D IF(STATUS,NE,0)GO TO 10
0040 D INSTR=154
0041 D STATUS=MPDCTM(DCTM,X1,X2,COSZ)
0042 D IF(STATUS,NE,0)GO TO 10
0043 D INSTR=155
0044 D STATUS=MPMWGX(RI,NIMM,KE,A1)
0045 D IF(STATUS,NE,0)GO TO 10
0046 D INSTR=156
0047 D STATUS=MPDPP(OPRM,PARC,A1)
0048 D IF(STATUS,NE,0)GO TO 10
0049 D INSTR=157
0050 D STATUS=MPFSTV(A1)
0051 D IF(STATUS,NE,0)GO TO 10
0052 D INSTR=158
0053 D STATUS=MPPRASP(DCTM1,PWEITM,TMP2,X1)
0054 D IF(STATUS,NE,0)GO TO 10
0055 D INSTR=159
0056 D STATUS=MPASRT(IORDRM)
0057 D IF(STATUS,NE,0)GO TO 10
0058 D INSTR=160
0059 D STATUS=MPSCAN(DCTM2)
0060 D IF(STATUS,NE,0)GO TO 10
0061 D INSTR=161
0062 D STATUS=MPCDH(MIRIT3)
0063 D IF(STATUS,NE,0)GO TO 10
0064 D INSTR=162
0065 D STATUS=MPFSTG(OTDCTM,MIRIT3,TMP1,TMP2)
0066 D IF(STATUS,NE,0)GO TO 10
0067 D INSTR=163
0068 D STATUS=MPDQPP(PARC,OPPRM,A1)
0069 D IF(STATUS,NE,0)GO TO 10
0070 D INSTR=164
0071 D STATUS=MPFSTD(ORDCTM,ORDCTM,TMP1,IORDR2)
0072 D IF(STATUS,NE,0)GO TO 10
0073 D INSTR=165
0074 D STATUS=MPIDCM(X2,DRDCTM,COSZ)
0075 D IF(STATUS,NE,0)GO TO 10
0076 D INSTR=166
0077 D STATUS=MPVDNM(NOUTM,YNM,X2)
0078 D IF(STATUS,NE,0)GO TO 10
0079 D INSTR=167
0080 D STATUS=MPSSPT(IORDPM)
0081 D IF(STATUS,NE,0)GO TO 10
```

```

0001 D INSTR=7
0002 STATUS=MPTRF(0)
0003 IF (STATUS.NE.0)GO TO 10
C
C CONFIGURE & BIND SECOND BUFFER REGION
C
0004 C INSTR=101
0005 STATUS=MPCR(BUS1+RIDRF2,BASE2,SIZE2,INTGR,EVERY,LONG)
0006 IF (STATUS.NE.0)GO TO 10
0007 C INSTR=1
0008 STATUS=MPCR(BRIDRF2,168)
0009 IF (STATUS.NE.0)GO TO 10
0010 INSTR=168
0011 STATUS=FFTN(X2,1,X2,VSHRT,WORK)
0012 IF (STATUS.NE.0)GO TO 10
0013 C INSTR=169
0014 STATUS=FFTN(X1,1,X1,VLONG,WORK)
0015 IF (STATUS.NE.0)GO TO 10
0016 C INSTR=170
0017 STATUS=FFR(X1,4,X1,VLONG,WORK)
0018 IF (STATUS.NE.0)GO TO 10
0019 C INSTR=171
0020 STATUS=FFTN(X2,1,X2,VSHRT,WORK)
0021 IF (STATUS.NE.0)GO TO 10
0022 C INSTR=4
0023 STATUS=MPTRF(0)
0024 IF (STATUS.NE.0)GO TO 10
C
C CONFIGURE & BIND THIRD BUFFER REGION
C
0105 C INSTR=102
0106 STATUS=MPCR(BUS1+RIDRF3,BASE3,SIZE3,INTGR,EVERY,LONG)
0107 IF (STATUS.NE.0)GO TO 10
0108 C INSTR=5
0109 STATUS=MPCR(BRIDRF3,172)
0110 IF (STATUS.NE.0)GO TO 10
0111 C INSTR=172
0112 STATUS=VMQV1(AOPR)
0113 IF (STATUS.NE.0)GO TO 10
0114 C INSTR=173
0115 STATUS=VMQV2(ORPPM)
0116 IF (STATUS.NE.0)GO TO 10
0117 C INSTR=174
0118 STATUS=VMQV3(RIAT)
0119 IF (STATUS.NE.0)GO TO 10
0120 C INSTR=175
0121 STATUS=MPCDHA(MIRIT1)
0122 IF (STATUS.NE.0)GO TO 10
C>>>> SPECIAL "ADV" S WHICH FUNCTION AS NULL. MODFM W/ 1 FRAME DELAY
C CHANGED NEXT PRE-HD-FCN FOR NO DELAY FOR DEMO *****
C
0123 C INSTR=176
0124 STATUS=VMQV(AOPR,AOPR)
0125 IF (STATUS.NE.0)GO TO 10
0126 C INSTR=177
0127 STATUS=VMQV(SEMI,AOPR)
0128 IF (STATUS.NE.0)GO TO 10

```

```

0129 D INSTR=178
      C CHANGED NEXT LINE FOR DFMD -- TOOK OUT DELAY*****
0130 STATUS=VMOV(ARODCT,AOTDCT)
0131 IF (STATUS.NE.0)GOTO 10
0132 D INSTR=179
0133 STATUS=VMOV(SEN2,AOTDCT)
0134 IF (STATUS.NE.0)GOTO 10
0135 D INSTR=6
0136 STATUS=MPHF(0)
0137 IF (STATUS.NE.0)GO TO 10
      C
0138 TYPE 101
0139 FORMAT(1X,' (2) PRE-ROUND FUNCTIONS CREATED!')
0140 RETURN
      C
      C DIAGNOSTIC TRAP AREA
      C
0141 10 TYPE 100,INSTR,STATUS
0142 CALL EXIT
0143 100 FORMAT(1X,'****MAP ERROR*** "CREATE" INSTR=',I4,' HAS STATUS=',I6/)
0144 FND
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	002026	523 RW,I,CON,LCL
SPDATA	000010	12 RW,D,CON,LCL
SIDATA	000524	170 RW,D,CON,LCL
SVARS	000110	36 RW,D,CON,LCL
MTAPF0	002260	600 RW,D,OVR,GRL
MTAPF1	000012	5 RW,D,OVR,GRL
MTAPF2	000012	5 RW,D,OVR,GRL
NVR1	002020	520 RW,D,OVR,GRL
NVRO	002006	515 RW,D,OVR,GRL
NVRF	006016	1543 RW,D,OVR,GRL
NVRA	000152	53 RW,D,OVR,GRL
NVRP	002022	571 RW,D,OVR,GRL
NVRR	005004	1282 RW,D,OVR,GRL
NVP2	001002	257 RW,D,OVR,GRL
NVRL	000016	7 RW,D,OVR,GRL
NVRD	000012	5 RW,D,OVR,GRL
MAPDEF	000200	64 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 027714 6118

CREATE.PTN/1:1=CYPDATE/DE/NOTH







```

0070 C HF(157)=MPFSTV(A1)
0071 D STATUS=MPXHF(157)
0072 D IF(STATUS.NE.0)GO TO 10
0073 C INSTR=41
0074 D HF(170)=FF2R(X1,4,X1,VLONG,WORK)
0075 D STATUS=MPXHF(170)
0076 D IF(STATUS.NE.0)GO TO 10
0077 D INSTR=42
0078 C HF(158)=MPHASP(DCTM1,MPFITM,IMP2,X1)
0079 D STATUS=MPXHF(158)
0080 D IF(STATUS.NE.0)GO TO 10
0081 D INSTR=43
0082 C HF(159)=MPASRT(IONDRM)
0083 D STATUS=MPXHF(159)
0084 D IF(STATUS.NE.0)GO TO 10
0085 D INSTR=44
0086 C HF(160)=MPSCAN(DCTM2)
0087 D STATUS=MPXHF(160)
0088 D IF(STATUS.NE.0)GO TO 10
0089 D INSTR=45
0090 C HF(161)=MPCDRA(MIRIT3)
0091 D STATUS=MPXHF(161)
0092 D IF(STATUS.NE.0)GO TO 10
0093 D INSTR=46
0094 C HF(162)=MPFSTO(OTDCTM,MIRIT,IMP1,IMP2)
0095 D STATUS=MPXHF(162)
0096 D IF(STATUS.NE.0)GO TO 10
0097 D INSTR=48
0098 C IF(TIMING.EQ.YES)STATUS=MPXFL(ATCSYN)
0099 D IF(STATUS.NE.0)GO TO 10
0100 D INSTR=1050
0101 C STATUS=MPFEL(FL1)
0102 D IF(STATUS.NE.0)GO TO 10
0103 C INSTR=1055
0104 C LIST "ATCSYN": ATC SYNTHESIZER W/PROR DECODER & DESERIALIZATION
0105 D INSTR=1055
0106 C STATUS=MPFEL(ATCSYN)
0107 D IF(STATUS.NE.0)GO TO 10
0108 C *****
0109 C PATCHED OUT FOLLOWING LINE TO EXECUTE IN "DKMO" MODE IF NOT
0110 C IN TIMING MODE.
0111 C IF(LIVE.EQ.YES)GO TO 200
0112 C IF(TIMING.EQ.YES)GO TO 200
0113 C >>>> INTRODUCE "MULTI" MODEM
0114 D INSTR=580
0115 C HF(176)=VMOV(ARQPH,AOPH)
0116 C STATUS=MPXHF(176)
0117 D IF(STATUS.NE.0)GO TO 10
0118 C *****DON'T NEED DELAY FOR DEMO***
0119 CD INSTR=581
0120 CC HF(177)=VMOV(RECV1,AOPH)
0121 C STATUS=MPXHF(177)
0122 CD IF(STATUS.NE.0)GO TO 10
0123 C *****
0124 D INSTR=582
0125 C HF(178)=VMOV(ARQDCT,AOTDCT)
  
```

```

0101 C STATUS=MPXHF(178)
      D IF(STATUS.NE.0)GO TO 10
      C***** REMOVED FOR DEMO *****
      CD INSTR=583
      CC HF(179)=VM0V(MPCV2,AOTDCT)
      C STATUS=MPXHF(179)
      CD IF(STATUS.NE.0)GO TO 10
      C*****
      C*****
0102 200 CONTINUE
0103 D INSTR=60
      C HF(173)=VM0V2(ORPRM)
      STATUS=MPXHF(173)
0104 D IF(STATUS.NE.0)GO TO 10
      INSTR=61
      C HF(163)=MPDOPP(PARC,ORPRM,A1)
      STATUS=MPXHF(163)
0108 D IF(STATUS.NE.0)GO TO 10
      INSTR=62
      C HF(157)=MPFSTV(A1)
      STATUS=MPXHF(157)
0110 D IF(STATUS.NE.0)GO TO 10
      INSTR=63
      C HF(170)=FF2R(X1,4,X1,VLONG,WORK)
      STATUS=MPXRF(170)
0114 D IF(STATUS.NE.0)GO TO 10
      INSTR=64
      C HF(158)=MPRASP(DCTM1,PWFITM,IMP2,X1)
      STATUS=MPXHF(158)
0116 D IF(STATUS.NE.0)GO TO 10
      INSTR=65
      C HF(167)=MPSSRT(IORDRM)
      STATUS=MPXRF(167)
0120 D IF(STATUS.NE.0)GO TO 10
      INSTR=66
      C HF(160)=MPSCAN(DCTM2)
      STATUS=MPXHF(160)
0122 D IF(STATUS.NE.0)GO TO 10
      INSTR=67
      C HF(175)=MPCDRA(MIRIT1)
      STATUS=MPXHF(175)
0126 D IF(STATUS.NE.0)GO TO 10
      INSTR=670
      C HF(174)=VM0V3(CRIRIT)
      STATUS=MPXHF(174)
0129 D IF(STATUS.NE.0)GO TO 10
      INSTR=68
      C HF(164)=MPFSTD(DRDCTM,ORDCTM,TMP1,IORDR2)
      STATUS=MPXHF(164)
0131 D IF(STATUS.NE.0)GO TO 10
      INSTR=69
      C HF(165)=MPIDCM(X2,DRDCTM,COSZ)
      STATUS=MPXHF(165)
0134 D IF(STATUS.NE.0)GO TO 10
      INSTR=70
      C HF(171)=FFTIN(X2,1,X2,VSHRT,WORK)
      STATUS=MPXRF(171)

```

```

0138 D IF(STATUS.NE.0)GO TO 10
0139 D INSTR=71
0140 C NF(146)=MPVDM(NMUTM,YDM,X2)
0141 D STATUS=MPXNF(146)
0142 D IF(STATUS.NE.0)GO TO 10
0143 D INSTR=1070
0144 C STATUS=MPFFL(FL2)
0145 C IF(STATUS.NE.0)GOTO 10
0146 C LIST "WAIT": THE WAIT LOOP
0147 D INSTR=1080
0148 D STATUS=MPHFL(WATT)
0149 D IF(STATUS.NE.0)GOTO 10
0150 D INSTR=1085
0151 D STATUS=MPHFL(INIT,FO,0,FL4,FL0)
0152 D IF(STATUS.NE.0)GOTO 10
0153 D INSTR=1090
0154 D STATUS=MPIIF(SYN,FO,0,ATCSYN,FL0)
0155 D IF(STATUS.NE.0)GOTO 10
0156 D INSTR=1095
0157 D STATUS=MPIIF(CANA,FO,0,ATCANA,FL0)
0158 D IF(STATUS.NE.0)GOTO 10
0159 C INSTR=1100
0160 C STATUS=MPHFL(FL3)
0161 C LIST "STARTUP":INITIALIZATION LOOP
0162 D INSTR=1105
0163 D STATUS=MPHFL(STARTUP)
0164 D IF(STATUS.NE.0)GOTO 10
0165 D INSTR=1106
0166 D STATUS=MPHFL(INIT,1)
0167 D IF(STATUS.NE.0)GOTO 10
0168 D INSTR=1111
0169 D STATUS=MPHFL(OUT,1)
0170 D IF(STATUS.NE.0)GOTO 10
0171 D INSTR=1112
0172 D STATUS=MPHFL(INP,1)
0173 D IF(STATUS.NE.0)GOTO 10
0174 D INSTR=1113
0175 D STATUS=MPHFL(AFLG,0)
0176 D IF(STATUS.NE.0)GOTO 10
0177 D INSTR=1114
0178 D STATUS=MPHFL(SFLG,0)
0179 D IF(STATUS.NE.0)GOTO 10
0180 D INSTR=1115
0181 D STATUS=MPHFL(IFLG,0)
0182 D IF(STATUS.NE.0)GOTO 10
0183 D INSTR=1116
0184 D STATUS=MPHFL(OFLG,0)
0185 D IF(STATUS.NE.0)GOTO 10
0186 D INSTR=1117
0187 D STATUS=MPHFL(SPIN,-1)
0188 D IF(STATUS.NE.0)GOTO 10
0189 D INSTR=1118
0190 D STATUS=MPHFL(SPOUT,-1)

```

FORTRAN IV-PLUS V07-51F  
 DEFINP.FTM /DE/WR

```

0189 D IF (STATUS.NE.0)GOTO 10
0190 D INSTR=1127
0191 STATUS=MP1ST(KOUNT,SYMCNT)
0192 D IF (STATUS.NE.0)GOTO 10
0193 D INSTR=1123
0194 STATUS=MP1ST(RDATA,-1)
0195 D IF (STATUS.NE.0)GOTO 10
0196 D INSTR=1124
0197 STATUS=MP1ST(XDATA,-1)
0198 D IF (STATUS.NE.0)GOTO 10
0199 D INSTR=1125
0200 STATUS=MP1ST(APDNEF,0)
0201 D IF (STATUS.NE.0)GOTO 10
0202 D INSTR=1126
0203 STATUS=MP1ST(RGD,0)
0204 D IF (STATUS.NE.0)GOTO 10
0205 D INSTR=1127
0206 STATUS=MP1ST(XGD,0)
0207 D IF (STATUS.NE.0)GOTO 10
0208 D INSTR=1128
0209 STATUS=MP1ST(RGD2,0)
0210 D IF (STATUS.NE.0)GOTO 10
0211 D INSTR=1160
0212 STATUS=MP1ST(SYMC,0)
0213 D IF (STATUS.NE.0)GOTO 10
0214 D INSTR=1161
0215 STATUS=MP1ST(RSYN,0)
0216 D IF (STATUS.NE.0)GOTO 10
0217 D INSTR=1162
0218 STATUS=MP1ST(NFWSYN,0)
0219 D IF (STATUS.NE.0)GOTO 10
0220 D INSTR=1163
0221 STATUS=MP1ST(OLSYN,0)
0222 D IF (STATUS.NE.0)GOTO 10
0223 D INSTR=1164
0224 STATUS=MP1ST(LSTEP,0)
0225 D IF (STATUS.NE.0)GOTO 10
0226 D INSTR=1165
0227 STATUS=MP1ST(FRSYN,0)
0228 D IF (STATUS.NE.0)GOTO 10
0229 D INSTR=1137
0230 STATUS=MP1DS(AOM,IOS,ADMPM)
0231 D IF (STATUS.NE.0)GOTO 10
0232 D INSTR=1138
0233 STATUS=MPRNS(AOM,IOS,ADMSA)
0234 D IF (STATUS.NE.0)GOTO 10
0235 D INSTR=1139
0236 STATUS=MP1DS(ADAM,IOS,ADAMPM)
0237 D IF (STATUS.NE.0)GOTO 10
0238 D INSTR=1140
0239 STATUS=MPRNS(ADAM,IOS,ADMSA)
0240 D IF (STATUS.NE.0)GOTO 10
0241 D INSTR=1135
0242 STATUS=MP1DS(IOS10,IOS,IOSPM)
0243 D IF (STATUS.NE.0)GOTO 10
0244 D INSTR=1136

```



FORTRAN IV-PLUS V02-51F  
DEFINE.FTN /DF/WR

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NO FPP INSTRUCTIONS GENERATED  
DEFINE.LP/IT:1=DEFINE/DE/NOTR



```

0026 EQUIVALENCE (IOSPMR(1),XR(1)),(PROGRAM(1),DCT2(1)),(MSGERR,ITOT)
0027 DATA REAL,CMPLEX,INTGR/0,1,2/
0028 DATA E,VERY,SINGLE,DOUBLE/1,2,3/
0029 DATA BYTE,2,BYTE4,BYTE8/2,4,8/
0030 DATA LONG,SHORT,CNVYS,CNVN/0,1,1,0/
0031 DATA HUS1,HUS2,HUS3/64,128,192/
0032 DATA MONU,DUAL,INT,FXT,DAFLT,P,DAFIXP/0,1,0,2,0,4/
0033 DATA CCOFSR,CCOSSR,CCIFSR,CCISSR/0,8,0,16/
0034 DATA CCZFSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035 DATA DDFLT,DDFFIX/0,128/
0036 DATA OFFSET,INTT,FKTT/7,0,1/
0037 DATA PZDIS,PZEN/0,4/
0038 DATA CHANSI/1,-1/
0039 DATA IOSPMR,OVHRD/600*0,12./
0040 DATA IOSPM,IOS,IOSRA,IOSSA/61,2,22100,0,0./
0041 DATA ADMPM,ADMHA,ADMSA,ADMSZ/62,22700,0,0,512./
0042 DATA ADAMPM,ADAMRA,ADAMSA,ADAMSZ/63,23300,0,0,512./
0043 DATA HEXC/0,1,1,2,1,3,4,5,6,7,8,9,
1'A',1'B',1'C',1'D',1'E',1'F'/
0044 DATA CWORD/1'E',1'C',1'C',1'4'/
C
C
C
SFT UP IOS-2(MODEM SCROLL)
C
C
CALL ASSIGN(3,'SY:LINE.PRJ')
CALL LOAD
IF(MSGERR.EQ.0)GO TO 2000
TYPE 2001
2001 FORMAT(IX,'***FATAL: READ ERROR ON "LINE.PRJ" ***'//)
STOP
2000 CONTINUE
C
IOSM(IOS-2 SCROLL)
CALL IOSM
C
ADM(D/A SCROLL)
CALL ADM
C
ADM(A/D SCROLL)
CALL ADM
RETURN
C
DIAGNOSTIC TRAP AREA
C
C
TYPE 100,INSTR,STATUS
CALL EXIT
100 FORMAT(IX,'**MAP ERROR**' "SETUP" INSTR='13,' HAS STATUS='16//)
END

```



PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDF1	000106	35 RW,I,COM,LCL
SPDATA	000020	8 RW,D,COM,LCL
SIDATA	000152	53 RW,D,COM,LCL
SVARS	000232	77 RW,D,COM,LCL
MTAPE0	002260	600 RW,D,OVR,GRL
MTAPE1	000012	5 RW,D,OVR,GRL
MTAPE2	000012	5 RW,D,OVR,GRL
OVR0	002020	520 RW,D,OVR,GRL
OVR1	002006	515 RW,D,OVR,GRL
OVR2	006016	1543 RW,D,OVR,GRL
OVR3	000152	53 RW,D,OVR,GRL
OVR4	002022	521 RW,D,OVR,GRL
OVR5	005004	1282 RW,D,OVR,GRL
OVR6	001002	257 RW,D,OVR,GRL
OVR7	000016	7 RW,D,OVR,GRL
OVR8	000012	5 RW,D,OVR,GRL
MAPDEF	000200	64 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 025534 5550

NO FPP INSTRUCTIONS GENERATED

FORTRAN IV-PLUS V02-51E  
SETUP.FTN /DE/WR

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SETUP.LP/LE:1=SETUP/DE/NOTR



```

C00000
C      DD=00, NULL BYTE
C00000
C      LEFT=78
0034  READ(3,116)DT,(REM(I),I=1,LEFT)
0035  PRINT 113,LINE,DT
0036  GO TO 5
C00000
C      DD=01, SFT AUS BITS
C00000
C      LEFT=76
0037  READ(3,116)DT,ZERO,M,(REM(I),I=1,LEFT)
0038  PRINT 113,LINE,DT,ZERO,M
0039  GO TO 5
C00000
C      DD=02, SET ADDRESS ON AUS
C00000
C      LEFT=72
0040  READ(3,116)DT,ZERO,(H(I),I=1,5),(REM(I),I=1,LEFT)
0041  PRINT 113,LINE,DT,ZERO,(H(I),I=1,5)
0042  LADDR=0
0043  DO 46 J=1,5
0044  IF(H(J).EQ.HEXC(I))GO TO 46
0045  CONTINUE
0046  TYPE 112,DT,H(J),LINE
0047  IERR=1
0048  GO TO 3000
0049  LADDR=LADDR+(16,**(5-J))*(I-1)
0050  GO TO 5
C00000
C      DD=10, PROGRAM HEADER
C00000
C      LEFT=0
0051  READ(3,116)DT,(K(I),I=1,78)
0052  PRINT 113,LINE,DT,(K(I),I=1,78)
0053  TYPE 104,(K(I),I=1,78)
0054  GO TO 5
C00000
C      DD=17, MODULE HEADER
C00000
C      LEFT=0
0054  READ(3,103)ONE,H(1),(I(1),I=1,6),(K(I),I=1,72)
0055  PRINT 114,LINE,ONE,H(1),(I(1),I=1,6),(K(I),I=1,72)
0056  IF(H(1).EQ.ASC(11))PROC=APU
0057  IF(H(1).EQ.ASC(12))PROC=APS
0058  IF(H(1).EQ.ASC(13))PROC=HJM
0059  IF(H(1).EQ.ASC(14))PROC=IOS2
0060  IF(H(1).GT.ASC(14))PROC=UNKN
0061  TYPE 106,(S(I,PROC),I=1,4)
0062  GO TO 5
C00000
C      DD=1F, MODULE FWD
C00000
C      LEFT=0

```

```
0063      READ(3,117)DT,(H(I),I=1,4)
          PRINT 113,LINE,DT,(H(I),I=1,4)
          TYPE 10R
          GO TO 5
0064      C#####
          C
          C#####
          36      LEFT=0
          DD=20,DATA OR INSTRUCTION
          READ(3,105)DT,(N(I),I=1,2)
          PRINT 119,LINE,DT,(N(I),I=1,2)
          NUMWRD=0
          DO 41 J=1,2
          DO 40 I=1,16
          IF(N(J).EQ.HEXC(I))GO TO 41
          CONTINUE
          TYPE 112,DT,(N(J),LINE)
          GO TO 3000
          41      NUMWRD=NUMWRD+(16.*(2-J))*(I-1)
          NUMHEX=NUMWRD*4
          IF (NUMWRD.EQ.0)GO TO 5
          BACKSPACE 3
          READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,NUMHEX)
          DO 43 I=1,NUMWRD
          XVALUE=0.0
          IBASE=(I-1)*4
          DO 44 J=1,4
          JJ=IBASE+J
          DO 42 I=1,16
          IF(H(JJ).EQ.HEXC(I))GO TO 44
          CONTINUE
          TYPE 112,DT,(H(JJ),LINE)
          IPR=1
          GO TO 3000
          44      XVALUE=XVALUE+(16.*(4-J))*(I-1)
          IF(XVALUE.LE.32767.0)GO TO 440
          XVALUE=XVALUE-65536.0
          440     VALUE=IFIX(XVALUE)
          PRINT 120,(S(I,PROC),I=1,4),ADDR,(H(IBASE+I),I=1,4),VALUE
          PM(ADDR)=VALUE
          ADDR=ADDR+1
          GO TO 5
          C#####
          C
          C#####
          37      DD=21,REPEATED 16 HIT DATA
          LEFT=72
          READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,4),(REK(I),I=1,LEFT)
          PRINT 113,LINE,DT,(N(I),I=1,2)
          GO TO 5
          C#####
          C
          C#####
          38      LEFT=68
          READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,8),(REK(I),I=1,LEFT)
          PRINT 113,LINE,DT,(N(I),I=1,2)
          GO TO 5
          C#####
          C
          C#####
          100     C#####
```



FORTRAN IV-PLUS V02-51F  
LOAD.FTN /HR

OVR1 000016 7  
OVRD 000016 7

RW.D,OVR,GRI.  
RW.D,OVR,GRI.

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TOTAL SPACE ALLOCATED = 011124 2346

LOAD.I.F./I.I.=LOAD/NOTH





```

0026 EQUIVALENCE (IUSPMR(1),XR(1)),(PROGPM(1),DCT2(1)),(MSGERR,ITUT)
0027 DATA REAL,CMPEX,INTGR/0,1,2/
0028 DATA FVHLF,SLMGLF,DDHMLF/1,2,3/
0029 DATA NYTF2,XYTF4,RYTF4/2,4,8/
0030 DATA LORG,SHORT,CNVYFS,CNVWD/0,1,1,0/
0031 DATA RUS1,RUS2,RUS3/64,128,142/
0032 DATA MOND,DUAL,INT,EXT,DAFLTP,DAFLXP/0,1,0,2,0,0,4/
0033 DATA CC0FSR,CC0SSR,CC1FSR,CC1SSR/0,8,0,16/
0034 DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035 DATA OFFFLC,DDPRIX/0,128/
0036 DATA OFFSET,INTE,EXTT/2,0,1/
0037 DATA PZOTS,PZFA/0,4/
0038 DATA CHARSI/1,-1/
0039 DATA IUSPMB,DVPHH/6000,12,3/
0040 DATA IUSPM,IUS,IUSRA,IUSSA/61,2,22100,0,0/
0041 DATA AUPM,ADMHA,ADMSA,ADMSZ/62,22700,0,512,0/
0042 DATA ADAMPM,ADAMHA,ADAMSA,ADAMSZ/63,23300,0,512,0/
0043 DATA HEXC/0,1,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,28,29,30,31,32,33,34,35,36,37,38,39,40,41,42,43,44,45,46,47,48,49,50,51,52,53,54,55,56,57,58,59,60,61,62,63,64,65,66,67,68,69,70,71,72,73,74,75,76,77,78,79,80,81,82,83,84,85,86,87,88,89,90,91,92,93,94,95,96,97,98,99,100,101,102,103,104,105,106,107,108,109,110,111,112,113,114,115,116,117,118,119,120,121,122,123,124,125,126,127,128,129,130,131,132,133,134,135,136,137,138,139,140,141,142,143,144,145,146,147,148,149,150,151,152,153,154,155,156,157,158,159,160,161,162,163,164,165,166,167,168,169,170,171,172,173,174,175,176,177,178,179,180,181,182,183,184,185,186,187,188,189,190,191,192,193,194,195,196,197,198,199,200,201,202,203,204,205,206,207,208,209,210,211,212,213,214,215,216,217,218,219,220,221,222,223,224,225,226,227,228,229,230,231,232,233,234,235,236,237,238,239,240,241,242,243,244,245,246,247,248,249,250,251,252,253,254,255,256,257,258,259,260,261,262,263,264,265,266,267,268,269,270,271,272,273,274,275,276,277,278,279,280,281,282,283,284,285,286,287,288,289,290,291,292,293,294,295,296,297,298,299,300,301,302,303,304,305,306,307,308,309,310,311,312,313,314,315,316,317,318,319,320,321,322,323,324,325,326,327,328,329,330,331,332,333,334,335,336,337,338,339,340,341,342,343,344,345,346,347,348,349,350,351,352,353,354,355,356,357,358,359,360,361,362,363,364,365,366,367,368,369,370,371,372,373,374,375,376,377,378,379,380,381,382,383,384,385,386,387,388,389,390,391,392,393,394,395,396,397,398,399,400,401,402,403,404,405,406,407,408,409,410,411,412,413,414,415,416,417,418,419,420,421,422,423,424,425,426,427,428,429,430,431,432,433,434,435,436,437,438,439,440,441,442,443,444,445,446,447,448,449,450,451,452,453,454,455,456,457,458,459,460,461,462,463,464,465,466,467,468,469,470,471,472,473,474,475,476,477,478,479,480,481,482,483,484,485,486,487,488,489,490,491,492,493,494,495,496,497,498,499,500,501,502,503,504,505,506,507,508,509,510,511,512,513,514,515,516,517,518,519,520,521,522,523,524,525,526,527,528,529,530,531,532,533,534,535,536,537,538,539,540,541,542,543,544,545,546,547,548,549,550,551,552,553,554,555,556,557,558,559,560,561,562,563,564,565,566,567,568,569,570,571,572,573,574,575,576,577,578,579,580,581,582,583,584,585,586,587,588,589,590,591,592,593,594,595,596,597,598,599,600,601,602,603,604,605,606,607,608,609,610,611,612,613,614,615,616,617,618,619,620,621,622,623,624,625,626,627,628,629,630,631,632,633,634,635,636,637,638,639,640,641,642,643,644,645,646,647,648,649,650,651,652,653,654,655,656,657,658,659,660,661,662,663,664,665,666,667,668,669,670,671,672,673,674,675,676,677,678,679,680,681,682,683,684,685,686,687,688,689,690,691,692,693,694,695,696,697,698,699,700,701,702,703,704,705,706,707,708,709,710,711,712,713,714,715,716,717,718,719,720,721,722,723,724,725,726,727,728,729,730,731,732,733,734,735,736,737,738,739,740,741,742,743,744,745,746,747,748,749,750,751,752,753,754,755,756,757,758,759,760,761,762,763,764,765,766,767,768,769,770,771,772,773,774,775,776,777,778,779,780,781,782,783,784,785,786,787,788,789,790,791,792,793,794,795,796,797,798,799,800,801,802,803,804,805,806,807,808,809,810,811,812,813,814,815,816,817,818,819,820,821,822,823,824,825,826,827,828,829,830,831,832,833,834,835,836,837,838,839,840,841,842,843,844,845,846,847,848,849,850,851,852,853,854,855,856,857,858,859,860,861,862,863,864,865,866,867,868,869,870,871,872,873,874,875,876,877,878,879,880,881,882,883,884,885,886,887,888,889,890,891,892,893,894,895,896,897,898,899,900,901,902,903,904,905,906,907,908,909,910,911,912,913,914,915,916,917,918,919,920,921,922,923,924,925,926,927,928,929,930,931,932,933,934,935,936,937,938,939,940,941,942,943,944,945,946,947,948,949,950,951,952,953,954,955,956,957,958,959,960,961,962,963,964,965,966,967,968,969,970,971,972,973,974,975,976,977,978,979,980,981,982,983,984,985,986,987,988,989,990,991,992,993,994,995,996,997,998,999,1000,1001,1002,1003,1004,1005,1006,1007,1008,1009,1010,1011,1012,1013,1014,1015,1016,1017,1018,1019,1020,1021,1022,1023,1024,1025,1026,1027,1028,1029,1030,1031,1032,1033,1034,1035,1036,1037,1038,1039,1040,1041,1042,1043,1044,1045,1046,1047,1048,1049,1050,1051,1052,1053,1054,1055,1056,1057,1058,1059,1060,1061,1062,1063,1064,1065,1066,1067,1068,1069,1070,1071,1072,1073,1074,1075,1076,1077,1078,1079,1080,1081,1082,1083,1084,1085,1086,1087,1088,1089,1090,1091,1092,1093,1094,1095,1096,1097,1098,1099,1100,1101,1102,1103,1104,1105,1106,1107,1108,1109,1110,1111,1112,1113,1114,1115,1116,1117,1118,1119,1120,1121,1122,1123,1124,1125,1126,1127,1128,1129,1130,1131,1132,1133,1134,1135,1136,1137,1138,1139,1140,1141,1142,1143,1144,1145,1146,1147,1148,1149,1150,1151,1152,1153,1154,1155,1156,1157,1158,1159,1160,1161,1162,1163,1164,1165,1166,1167,1168,1169,1170,1171,1172,1173,1174,1175,1176,1177,1178,1179,1180,1181,1182,1183,1184,1185,1186,1187,1188,1189,1190,1191,1192,1193,1194,1195,1196,1197,1198,1199,1200,1201,1202,1203,1204,1205,1206,1207,1208,1209,1210,1211,1212,1213,1214,1215,1216,1217,1218,1219,122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C2005 CONTINUE
C     TYPE 2003
C2004 FORMAT(IX,'***FATAL: ILLEGAL CHARACTER IN ICM ***'//)
C     STOP
C2006 DATE=DATE#(16,0##(4-J))*(1-1)
C2004 CONTINUE
C     IF (DATUM.GT.32767.)DATE#=DATE#-65536.
C     IOSPM(4)=FIX(DATUM)
C     NFX FCC4 TO CIOCKS
C     IOSPM(4)=-4924
0061 C
C     DEFINE IOS PROGRAM MEMORY BUFFFF
C
C     IOS=2(400PM SCROLL)
C     IASTR=1000
C     STATUS=MPCLR(HUS1+IOSPM,IOSHA,PAISIZ*OVRHD,ITGR,EVERY,LONG)
C     IF (STATUS.NE.0)GOTO 10
C
C     *HIP IOS PROGRAM INTO HUS1 AREAS
C
C     IOS=2(400PM SCROLL)
C     IASTR=2000
C     IOVRHD=FIX(OVRHD)
C     STATUS=MPDR(IOSPM,IOSPMH(1),HYTE2,CNVHD,IOSPMH(SIZE+IOVRHD))
C     IF (STATUS.NE.0)GOTO 10
C     TYPE 101
C     FORMAT(IX,' (4) IOS-2 SETUP COMPLETE!')
C     RETURN
C
C     DIAGNOSTIC TRAP AREA
C
0072 10 TYPE 100,INSTR,STATUS
0073 CALL EXIT
0074 100 FORMAT(IX,'***MAP ERROR*** "IOSM" IASTR=',I4,' HAS STATUS=',I6//)
0075 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDPT	000627	201
STDATA	000156	55
SVARS	000240	40
STEMPS	000002	1
*TAP10	002260	600
*TAP11	000017	5
*TAP12	000017	5
OVR0	002200	520
OVR0	002006	515
OVR1	006016	1543
OVR4	000152	54
OVRP	002022	521
OVRH	005004	1282
OVR2	001002	257
OVR1	000016	7

FORTRAN IV-PLUS V07-514  
IOSM,LP10 /DE/4R

21:14:21 11-SEP-60

PAGE 4

UVRD 000012 N M,D,OVH,GRU  
MADDF 000200 64 M,D,OVH,GRU

TOTAL SPACE ALLOCATED = 026244 5714

IOSM,LP/1:1=IOSM/10/NOTM



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0026 FOUJVALNCF (IUSPMR(1),XK(1)),(PROGRAM(1),DCT2(1))
0027 DATA KFAL,CMPX,INTGR/0,1,2/
0028 DATA EVERY,SINGLE,DOUBLE/1,2,3/
0029 DATA RYF2,RYTF4,RYTFR/2,4,8/
0030 DATA LONG,SHORT,CNVYFS,CNVMD/0,1,1,0/
0031 DATA HUS1,HUS2,HUS3/64,128,192/
0032 DATA MOND,DUAL,INT,EXT,DAFLTP,DAFIXP/0,1,0,2,0,4/
0033 DATA CCFESR,CCOSSR,CC1FSR,CC1SSR/0,8,0,16/
0034 DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035 DATA DFFLT,DFFIX/0,12R/
0036 DATA OFFSET,INT,FXTT/2,0,1/
0037 DATA P2DIS,P2EM/0,4/
0038 DATA CHANS/1,-1/
0039 DATA IUSPAR,OVERHD/600*0,12./
0040 DATA IUSPM,IOS,IOSRA,IOSSA/61,2,22100,0,0./
0041 DATA A0PM,ADHRA,ADMSA,A0MSZ/62,22700,0,0,512./
0042 DATA ADAPM,ADARRA,ADAMS,ADAMSZ/63,23300,0,0,512./
0043 DATA HEXC/0,1,1,2,3,4,5,6,7,8,9,
  'A','B','C','D','E','F'/
C ADM(D/A SCROLL)
C INSTR=1001
D STATUS=MPCUB(BUSI+ADMPM,ADHRA,ADMSZ,INTGR,EVERY,LONG)
  IF(STATUS.NE.0)GOTO 10
C ADM(D/A SCROLL)
C KNTRL=MONU+DAFIXP+EXT
  FREQ=1.0
D INSTR=2010
D STATUS=ADMID(A0PM,FREQ,KNTRL,NOUT1,NOUT2,OFFSET)
  IF(STATUS.NE.0)GOTO 10
  TYPE 101
  FORMAT(1X,' (5) ADM SETUP COMPLETE!')
  RETURN
C DIAGNOSTIC TRAP AREA
C
C TYPE 100,INSTR,STATUS
CALL EXIT
100 FURMAT(1X,'***MAP ERROR*** ADM INSTR='I3,' HAS STATUS='I6/)'
  FMD
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES	
SCDF1	000136	47	RM,I,COM,I,CL
SIDATA	000156	55	RM,D,COM,I,CL
SVARS	000240	40	RM,D,COM,I,CL
WTAPF0	002260	600	RM,D,OVR,GRI
WTAPF1	000012	5	RM,D,OVR,GRI
WTAPF2	000012	5	RM,D,OVR,GRI
OVR1	002020	520	RM,D,OVR,GRI
OVR0	002006	515	RM,D,OVR,GRI
OVRP	006016	1543	RM,D,OVR,GRI
OVR4	000152	53	RM,D,OVR,GRI
OVRP	002022	521	RM,D,OVR,GRI
OVR8	005004	1282	RM,D,OVR,GRI
OVR2	001002	257	RM,D,OVR,GRI
OVR1	000016	7	RM,D,OVR,GRI
OVRD	000012	5	RM,D,OVR,GRI
MAPDEF	000200	64	RM,D,OVR,GRI

TOTAL SPACE ALLOCATED = 025556 5559

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ADM.FTN

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ADM.LP/1.13=ADM/MOTR





```

0026 EQUIVALENCE (IOSPMH(1),XR(1)),(PROGRAM(1),DCT2(1))
0027 DATA REAL,CMPLX,INTGR/0,1,2./
0028 DATA EVERY,SINGL,DOUBLE/1,2,3/
0029 DATA BYTE,RYTFA,RYTFR/2,4,8/
0030 DATA LONG,SHORT,CNVYFS,CNVNO/0,1,1,0/
0031 DATA RUS1,RUS2,RUS3/64,128,192/
0032 DATA MONO,DUAL,INT,EXT,DAFLTP,DAFIXP/0,1,0,2,0,4./
0033 DATA CC0FSR,CC0SSR,CC1FSR,CC1SSR/0,8,0,16/
0034 DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035 DATA DDFEUT,DDFIX/0,12R/
0036 DATA OFFSET,INTT,EXTT/2,0,1/
0037 DATA P2DIS,P2FN/0,4/
0038 DATA C,MS1/1,-1/
0039 DATA IOSPMH,OVRRD/600*0,12./
0040 DATA IOSPM,IOS,IOSRA,IOSSA/61,2,22100,,0./
0041 DATA ADMPM,ADHRA,ADMSA,ADMSZ/62,22700,,0,,512./
0042 DATA ADAMPM,ADAMRA,ADAMSA,ADAMSZ/63,23300,,0,,512./
0043 DATA HEXC/0,1,1,2,3,4,5,6,7,8,9,
  1,A,1,B,1,C,1,D,1,E,1,F/
C
C ADAMCA/D SCROLL)
C INSTH=1002
D STATUS=MPCLEH(RUS1+ADAMPM,ADAMRA,ADAMSA,ADAMSZ,INTGR,EVERY,1,ONG)
D IF (STATUS.NE.0)GOTO 10
C
C ADAMCA/D SCROLL)
C KNTRI=DDFFIX+CC0SSR+CC1FSR+CC2SSR+CC3SSR
C KNTRL=KNTRI+INTT+P2DIS
C FREQ0=1.
C KNTRI=KNTRI+EXT
C FREQ1=FREQ
C FREQ2=FREQ
D INSTH=2005
D STATUS=ADMSD(ADAMPM,FREQ1,FREQ2,KNTRI,CHANM1,MIN1,MIN2)
D IF (STATUS.NE.0)GOTO 10
101 FORMAT(1X,' (6) ADAM SETUP COMPLETE!')
C RETURN
C
C DIAGNOSTIC TRAP AREA
C
10 TYPE 100,INSTR,STATUS
0056 CALL EXIT
0057 FORMAT(1X,'***MAP ERROR*** ADAM* INSTR=1,I3,' HAS STATUS=1,I6/)
0058 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDEF1	000176	63
STDATA	000167	57
SVARS	000250	84
MTAPF0	002260	600
MTAPF1	000012	5
MTAPF2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVRR	002072	521
OVRL	005004	1282
OVRO	001002	257
OVRF	000016	7
OVRO	000012	5
HAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025632 5581

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ADAM.FTN

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ADAM.IP/LE:1=ADAM/NOTR



```
0027 DATA RYF7,RYF74,RYF77,RYF77,4,8/  
0028 DATA LQMG,SHUMF,CMVFS,CKVM0,0,1,1,0/  
0029 DATA RUS1,RUS2,RUS3/64,178,192/  
0030 DATA ATCANA,ATCSYN,WAIT,STPTOP,TIMER/1,2,3,4,7/  
0031 DATA IUSP1,ADM,ADAM/16,22,23/  
0032 DATA FQ,NE,GF,GT,DF,LT/0,1,2,3,4,5/  
0033 DATA ENTP,PARITY/113,114/  
0034 DATA CM,HELL/040,5807/  
  
C EXECUTE PRE-ROUND FUNCTION LIST  
C  
C IF (CTRL.FQ.ND.AND.LIVE.FQ.NO)RETURN  
C IF (TIMING.FQ.NO)GO TO 1  
C >>>>>> FOR TIMING PURPOSES ONLY!  
C INSTR=0  
C STATUS=MPHL(PARITY,FQ,TIMER)  
C IF (STATUS.NE.0)GO TO 10  
C CALL DATE (ARRAY1)  
C CALL TIME (ARRAY2)  
C TYPE 20,HELL,ARRAY1,ARRAY2  
C FORMAT(/1X,'PDP <=> MAP LINKAGE HAS BEEN SUSPENDED',5A1,/  
C 1,AX,9A1,' AT ',HAL/  
C 2,11,'TYPE "RES ATCA" TO TERMINATE'//)  
C CALL SUSPND  
C GO TO 1000  
  
C >>>>>> ANALYZER  
C INSTR=0  
C IF (LIVE.FQ.YES)GO TO 2  
C INSTR=10  
C STATUS=MPXFL(AICANA)  
C IF (STATUS.NE.0)GO TO 10  
C >>>>>> SYNTHESIZER  
C INSTR=11  
C STATUS=MPXFL(ATCSYN)  
C IF (STATUS.NE.0)GO TO 10  
C RETURN  
C >>>>>> REAL-TIME LIVE SYSTEM  
C CONTINUE  
C INSTR=2090  
C STATUS=MPHL(PARITY,FQ,WAIT)  
C IF (STATUS.NE.0)GO TO 10  
C CALL DATE (ARRAY1)  
C CALL TIME (ARRAY2)  
C TYPE 20,HELL,ARRAY1,ARRAY2  
C CALL SUSPND  
C INSTR=8002  
C STATUS=MPSA(AADM)  
C IF (STATUS.NE.0)GO TO 10  
C INSTR=8003  
C STATUS=MPSA(LADAM)  
C IF (STATUS.NE.0)GO TO 10  
C CONTINUE  
C INSTR=9004  
C STATUS=MPDEL(ATCANA)  
C IF (STATUS.NE.0)GO TO 10  
C INSTR=8005  
C STATUS=MPDEL(ATCSYN)
```

```

0074 D IF(STATUS.NP.0)GO TO 10
0075 D INSTER006
0076 D STATUS=MPDFL(STAT)
0077 D IF(STATUS.NP.0)GO TO 10
0078 D INSTER007
0079 D STATUS=MPDFL(STARTUP)
0080 D IF(STATUS.NP.0)GO TO 10
0081 D INSTER008
0082 D STATUS=MPCLS(0)
0083 D IF(STATUS.NP.0)GO TO 10
0084 C STOP
  
```

DIAGNOSTIC TRAP AREA

```

0085 10 TYPE 100,INSER,STATUS
0086 CALL EXIT
0087 100 FORMAT(1X,'***MAP ERROR*** "SYSTEM" INSTR=',I4,' HAS STATUS=',I6/)
0088 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	000670	220
SPDATA	000004	2
SI0ATA	000346	115
SVAMS	000170	60
WTAPP0	002260	600
WTAPP1	000012	5
WTAPP2	000012	5
OVRI	002020	520
OVRO	002008	515
OVRF	006016	1543
OVRA	000152	53
OVVP	002022	521
OVRR	005004	1282
OVRE	001002	257
OVRL	000016	7
OVRD	000012	5
MAPDF	000174	62

TOTAL SPACE ALL. CAPED = 020430 5772

NO FOR INSTRUCTIONS GENERATED

SYSTMA,LP/1:1=SYSTMA/DE/MUTR



```

0026 C
0027 5010 IF(ICOUNT)5012,5010,5012
0028 NTOTI=NP
0029 NTUPS=NP
0030 CALL TAPE2(8)
0031 CALL TAPE2(1)
0032 INSTR=1
0033 STATUS=MPDR(NINM,NIN(1),HYTE2,CNVYES,NIN(NP))
0034 IF(STATUS.NE.0)GO TO 10
0035 INSTR=2
0036 STATUS=VFILT(XOM,39,NINM,0)
0037 IF(STATUS.NE.0)GO TO 10
0038 NTOTI=LTH-NP
0039 NTUPS=LTH-NP
0040 CALL TAPE2(1)
0041 IF(NEND.NE.0)GO TO 5000
0042 IF(NERR.NE.0)GO TO 5000
0043 ICOUNT=ICOUNT+1
0044 IF(ICOUNT-LT-START)GO TO 5012
0045 IF(ICOUNT-START+1).GT.JHAC)GO TO 5000
0046 WRITE(6,B99)ICOUNT
0047 FORMAT(1H,40(1H>),' TRANSMIT FRAME # ',14,40(1H<))
0048 INSTR=1
0049 STATUS=MPDR(NINM,NIN(1),HYTE2,CNVYES,NIN(NTOTI))
0050 IF(STATUS.NE.0)GO TO 10
0051 INSTR=1
0052 STATUS=VMIV(IMP8,NINM)
0053 IF(STATUS.NE.0)GO TO 10
0054 INSTR=12
0055 STATUS=MPDR(NINM,NIN(1),HYTE2,CNVYES,NIN(NTOTI))
0056 IF(STATUS.NE.0)GO TO 10
0057 WRITE(6,91)(I,NIN(I),I=1,NTOTI)
0058 FORMAT(1X,4(1X,'NIN(',13,')=' ,16,2X))
0059 RETURN
0059 C
0060 FOF ON INPUT,WRITE FOF
0061 C
0062 MEND=1
0063 RETURN
0064 C
0065 DIAGNOSTIC TRAP AREA
0066 C
0067 TYPE 101,INSTR,STATUS
0068 CALL EXIT
0069 FORMAT(1X,'***MAP ERROR*** 'INPUT' INSTR=',13,' HAS STATUS=',16/)
0070 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000672	201 RW,I,CUN,I,C1
SPDATA	000020	H RW,D,CUN,I,C1
SIDATA	000230	76 RW,D,CUN,I,C1
SVARS	000054	27 RW,D,CUN,I,C1



FORTHAN IV-PIHS V02-51E  
INPUT.FTN /UK/WR

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MTAPE0	002260	600	RW,D,OVR,GRI.
MTAPE1	000012	5	RW,D,OVR,GRI.
MTAPE2	000012	5	RW,D,OVR,GRI.
OVR1	002020	520	RW,D,OVR,GRI.
OVR0	002006	515	RW,D,OVR,GRI.
OVRF	006016	1543	RW,D,OVR,GRI.
OVR4	000152	53	RW,D,OVR,GRI.
OVRP	002022	521	RW,D,OVR,GRI.
OVRR	005004	1282	RW,D,OVR,GRI.
OVR2	001002	257	RW,D,OVR,GRI.
OVR1	000016	7	RW,D,OVR,GRI.
OVRD	000012	5	RW,D,OVR,GRI.
MAPDEF	000200	64	RW,D,OVR,GRI.

TOTAL SPACE ALLOCATED = 026150 56R4

INPUT.LP/LI:1=INPUT/DF/NOTR



```

C
C SERIALIZE SIDERAND W/ 1 FRAME DELAY ...
C AND MAINRAND W/ 2 FRAMES DELAY
C
D INSTR=0
D STATUS=VM0VI(AOPR)
D IF(STATUS.NE.0)GOTO 10
C
C CALCULATE DC & VAR THEN NORMALIZE & COMPRESS
C
D INSTR=1
D STATUS=MPFDVM(MAPX,NIMM,X2)
D IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
D CALL MPRDR(MINM,NIN(1),HYTE2,CNVYES,NIN(NTOTI))
D WRITE(6,898)(1,MIN(1),I=1,NTOTI)
D FORMAT(1X,4(1X,'MIN(',I3,')')=',16,2X))
D CALL MPRST(52,DCRIAS,1,CNVYES)
D WRITE(6,900)DCRIAS
D FORMAT(1X,'DCRIAS=',F12.5)
D CALL MPRST(55,VAR,1,CNVYES)
D WRITE(6,901)VAR
D FORMAT(1X,'VAR='F12.5)
D CALL MPRDR(X2,XR(1),HYTF4,CNVYES,XR(LTH))
D WRITE(6,920)(I,XR(I),I=1,LTH)
D FORMAT(1X,4(1X,'XR(',I3,')')=',F15.8,2X))
D CALL MPRDH(COSZ,XR(1),HYTF4,CNVYES,XR(LTH))
D WRITE(6,922)(I,XR(I),I=1,LTH)
D FORMAT(1X,4(1X,'COS(',I3,')')=',F15.8,2X))
D CALL MPRDR(AOPR,NOUT(1),HYTE2,CNVYES,NOUT(LPARM))
D WRITE(6,899)(I,NOUT(I),I=1,LPARM)
D FORMAT(1X,4(1X,'AOPR(',I3,')')=',16,2X))
D CALL MPRDH(AOTDCT,OTDCT(1),HYTE2,CNVYES,OTDCT(LTH))
D WRITE(6,910)(I,OTDCT(I),I=1,LTH)
D FORMAT(1X,4(1X,'AOTDCT(',I3,')')=',16,2X))
D CALL MPRDH(AIHIT,IHIT(1),HYTE2,CNVYES,IHIT(LTH))
D WRITE(6,911)(I,IHIT(I),I=1,LTH)
D FORMAT(1X,4(1X,'AIHIT(',I3,')')=',16,2X))
D CALL MPRDR(MIHIT4,IHIT(1),HYTE2,CNVYES,IHIT(LTH))
D WRITE(6,902)(I,IHIT(I),I=1,LTH)
D FORMAT(1X,4(1X,'MIHIT4(',I3,')')=',16,2X))
D RETURN
C
C DIAGNOSTIC TRAP AREA
C
D TYPE 101, INSTR, STATUS
D CALL EXIT
D FORMAT(1X, ****MAP FRROR*** 'DCVAR' INSTR=' ,I4, ' HAS STATUS=' ,I6 /)
D FND
    
```

PROGRAM SECTIONS	NAME	SIZE	ATTRIBUTES
101	TYPE 101, INSTR, STATUS		
101	CALL EXIT		
101	FORMAT(1X, ****MAP FRROR*** 'DCVAR' INSTR=' ,I4, ' HAS STATUS=' ,I6 /)		
101	FND		

SCONE1	001334	366	RM,I,CON,I,CL
SPDATA	000014	6	RM,D,CON,I,CL
SIATA	000576	191	RM,D,CON,I,CL
SVARS	000054	27	RM,D,CON,I,CL
MTAPE0	002260	600	RM,D,OVR,GRI
MTAPE1	000012	5	RM,D,OVR,GRI
MTAPE2	000012	5	RM,D,OVR,GRI
OVRT	002020	520	RM,D,OVR,GRI
OVRD	002006	515	RM,D,OVR,GRI
OVRF	006016	1543	RM,D,OVR,GRI
OVRG	000152	53	RM,D,OVR,GRI
OVRP	002022	521	RM,D,OVR,GRI
OVRQ	005004	1282	RM,D,OVR,GRI
OVR2	001002	257	RM,D,OVR,GRI
OVR4	000016	7	RM,D,OVR,GRI
OVRD	000012	5	RM,D,OVR,GRI
OVRPOT	001032	269	RM,D,OVR,GRI
MAPDEF	000200	64	RM,D,OVR,GRI

TOTAL SPACE ALLOCATED = 030256 6231

DCVAR.I.P/I.I:1=DCVAR/DF/NOTR



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DCTF.FTN /DP/WH APPLY PUST WEIGHTING W/ HALF SAMPLE DELAY

```

0026 C INSTR=1
0027 D STATUS=FFTM(X2,I,X2,VSHRT,WORK)
0028 D IF(STATUS.NE.0)GO TO 10
0029 D INSTR=2
0030 D STATUS=NPIDCTM(DCTM,X1,X2,COSZ)
0031 D IF(STATUS.NE.0)GO TO 10
0032 D INSTR=3
0033 D STATUS=FFTM(X1,I,X1,VLONG,WORK)
0034 D IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
0035 D CALL MPRDA(DCTM,DCT(1),HYTF4,CNVEES,DCT(I,TH))
0036 D WRITE(6,907)(I,DCT(I),I=1,I,ITH)
0037 D902 FORMAT(1X,4(1X,'DCT(',I3,')='),E15.8,2X))
0038 D RETURN
C
C DIAGNOSTIC TRAP AREA
C
0039 D TYPE 100, INSTR, STATUS
0040 D CALL EXIT
0041 D903 FORMAT(1X, '***MAP ERROR*** DCTF# INSTR=',I3, ' HAS STATUS=',I6/)
0042 D END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000320	104
SPDATA	000004	2
SIDATA	000176	63
SVARS	000054	22
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVR1	002070	520
OVR0	002006	515
OVRF	000016	1543
OVRH	000152	53
OVRP	002022	521
OVRB	005004	1282
OVR2	001002	257
OVR1	000016	7
OVRD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025600 5568

DCTF.LP/I.I:1=DCTF/HP/WH



```

C
D INSTR=1
D STATUS=MPHWGX(RI,NIMW,KF,A1)
D IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
D CALL MPRDR(C1,R(1),HYTF4,CNVYES,R(LPCN+1))
D WRITE(6,903)(1,R(1),1=1,LPCN+1)
D FORMAT(/1X,5(1X,'R(',12,')=',E15.8,2X))
D CALL MPRST(61,G,1,CNVYES)
D CALL MPRST(62,XM,1,CNVYES)
D CALL MPRST(63,VU,1,CNVYES)
D WRITE(XM)
D IVUV=IFIX(VU)
D WRITE(6,904)M,G,IVUV
D FORMAT(/1X,'M=',16,' G=',E15.8,' AND VUV=',11)
D CALL MPRDR(A1,A(1),HYTF4,CNVYES,A(LPCN))
D WRITE(6,906)(1,A(1),1=1,LPCN)
D FORMAT(/1X,4(1X,'A(',12,')=',E15.8,2X))
D CALL MPRDR(AE,PARCOR(1),HYTF4,CNVYES,PARCOR(LPCN))
D WRITE(6,907)(1,PARCOR(1),1=1,LPCN)
D FORMAT(/1X,4(1X,'K(',12,')=',E15.8,2X))
D DO 1 I=2,LPCN
D IF(PARCOR(I).NE.0.)GO TO 1
D WRITE(4,908)ICOUNT,PARCOR(I)
D TYPE 908,ICOUNT,PARCOR(I)
D FORMAT(/1X,'***WARNING*** BAD PARCORS IN FRAME ',I4
D 1,' W/ K(1)=',E15.8/)
D RETURN
D CUN,INUF
D RETURN
C
C DIAGNOSTIC TRAP AREA
C
D TYPE 100,INSTR,STATUS
D CALL EXIT
D FORMAT(1X,'**MAP ERROR** "PITLPC" INSTR=',I3,' HAS STATUS=',I6/)
D END
    
```

NAME	SIZE	ATTRIBUTES
SCDPF1	001000	256 RW,E,CON,I,CL
SPDATA	000020	4 RW,D,CON,I,CL
SIDATA	000446	147 RW,D,CON,I,CL
SVARS	000062	25 RW,D,CON,I,CL
STMP5	000004	2 RW,D,CON,I,CL
WTAPF0	002260	600 RW,D,OVR,GRU
WTAPF1	000012	5 RW,D,OVR,GRU
WTAPF2	000012	5 RW,D,OVR,GRU
OVRI	002020	520 RW,D,OVR,GRU
OVRO	002006	515 RW,D,OVR,GRU
OVRF	006016	1543 RW,D,OVR,GRU

PROGRAM SECTIONS



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PITLPC.FTN /DF/WH

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OVRA	000152	51	RM.D.OVR.GRI.
OVRA	002027	521	RM.D.OVR.GRI.
OVRA	005004	1282	RM.D.OVR.GRI.
OVRA	001002	257	RM.D.OVR.GRI.
OVRA	000016	7	RM.D.OVR.GRI.
OVRA	000012	5	RM.D.OVR.GRI.
MAPDF	000200	64	RM.D.OVR.GRI.

TOTAL SPACE ALLOCATED = 026556 5815

PITLPC.LP/LI:1=PITLPC/DE/MOTR



```

0028      DATA HUS1,HUS2,HUS3/64,128,192/
C
C QUANTIZE PARCOR(J),J=1,...,LPCN,DCHIAS,VAR,G,M
DEQUANTIZE PARCOR(J),J=1,...,LPCN,G,M
C
C INSTR=1
0029      STATUS=MOPDP(UPRM,PARC,A1)
0030      IF(STATUS,NE.0)GO TO 10
0031
C
C RESULTS
C
C CALL MPRDR(OPRM,NOUT(1),RYTF2,CNVYES,NOUT(LPARM+1))
0032      DO 651 I=1,LPCN
0033      OUPAR(I)=NOUT(I)
0034      WRITE(6,924)(J,OTPAR(J),J=1,LPCN)
0035      FORMAT(1X,4(1X,OTK('12,')=',16,2X))
0036      OVAR=NOUT(LPCN+1)
0037      WRITE(6,926)OTVAR
0038      FORMAT(1X,OTVAR=',16)
0039      O1DC=NOUT(LPCN+2)
0040      WRITE(6,925)O1DC
0041      FORMAT(1X,OTDC=',16)
0042      O1M=NOUT(LPCN+3)
0043      OTM=NOUT(LPCN+4)
0044      OTV=NOUT(LPCN+5)
0045      OTG=NOUT(LPCN+5)
0046      WRITE(6,927)OTM,OTG,OTV
0047      FORMAT(1X,OTM=',16', OTG=',16', AND OTV=',16)
0048      CALL MPRDR(PARC,OTPAR(1),HYTF4,CNVYES,OTPAR(LPCN))
0049      WRITE(6,928)(J,OTPAR(J),J=1,LPCN)
0050      FORMAT(1X,4(1X,OTK('12,')=',15,8,2X))
0051      CALL MPRST(RR,OTDC,1,CNVYES)
0052      CALL MPRST(R9,OTVAR,1,CNVYES)
0053      CALL MPRST(90,DTG,1,CNVYES)
0054      CALL MPRST(91,DTM,1,CNVYES)
0055      CALL MPRST(104,DTV,1,CNVYES)
0056      WRITE(6,929)DTM,DTG,DTV
0057      FORMAT(1X,DTM=',F15.8', DTG=',F15.8', AND DTV=',F15.8)
0058      CALL MPRDR(A1,DTA(1),RYTF4,CNVYES,DTA(LPCN))
0059      WRITE(6,931)(J,DTA(J),J=1,LPCN)
0060      FORMAT(1X,4(1X,DTA('12,')=',F15,8,2X))
0061      CALL MPRST(60,ENG,1,CNVYES)
0062      WRITE(6,932)ENG
0063      FORMAT(1X,ENG=',F15.8)
0064      RETURN
C
C DIAGNOSTIC TRAP AREA
C
C TYPE 100, INSTR, STATUS
0065      CALL EXIT
0066      FORMAT(1X, '***MAP ERROR*** "QDPARM" INSTR=',13, ' HAS STATUS=',16/)
0067
0068      END
  
```

PROGRAM SECTIONS  
 NAME SIZE ATTRIBUTES

SCDPF1	001102	289	RW,I,CON,I,CL
SPDATA	000034	14	RW,D,CON,I,CL
SIDATA	000530	172	RW,D,CON,I,CL
SVARS	000056	23	RW,D,CON,I,CL
MTAPE0	002260	600	RW,D,OVR,GHL
MTAPE1	000012	5	RW,D,OVR,GHL
MTAPE2	000012	5	RW,D,OVR,GHL
OVR1	002020	520	RW,D,OVR,GHL
OVR0	002006	515	RW,D,OVR,GHL
OVRF	006016	1543	RW,D,OVR,GHL
OVR4	000152	53	RW,D,OVR,GHL
OVRP	002022	521	RW,D,OVR,GHL
OVR8	005004	1282	RW,D,OVR,GHL
OVR2	001002	257	RW,D,OVR,GHL
OVR1	000016	7	RW,D,OVR,GHL
OVR0	000012	5	RW,D,OVR,GHL
OVR0T	001032	269	RW,D,OVR,GHL
OVR0T	002124	554	RW,D,OVR,GHL
MAPDF	000200	64	RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 032124 669H

NO FPP INSTRUCTIONS GENERATED

ODPARM,LP/LI:1=ODPARM/DF/NOTR



```

C
D 0026 INSTN=1
D 0027 STATUS=MPFSTV(A1)
D 0028 IF(STATUS.NE.0)GO TO 10
D 0029 INSTN=2
D 0030 STATUS=FF2R(X1.4,X1.VIDMG,WDRK)
D 0031 IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
D 0032 CALL MPRST(7),FGR,I,CNVYES)
D 0033 WITH(6.906)K,FGR
D 0034 FORMAT(1X,'K=',I3,' AND FGR=',E15.8)
D 0035 RETURN
C
C DIAGNOSTIC TRAP AREA
C
D 0036 TYPE 100, INSTR, STATUS
D 0037 CALL EXIT
D 0038 FORMAT(1X, '***MAP ERROR*** VPHS' INSTR=',I3,' HAS STATUS=',I6/)
D 0039 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	000208	67 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000150	52 RW,D,CON,I,CL
SVARS	000060	24 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
OVRI	002020	520 RW,D,OVR,GHL
OVR0	002006	515 RW,D,OVR,GHL
OVRF	006016	1543 RW,D,OVR,GHL
OVFA	000152	53 RW,D,OVR,GHL
OVRP	002022	521 RW,D,OVR,GHL
OVRH	005004	1282 RW,D,OVR,GHL
OVR2	001002	257 RW,D,OVR,GHL
OVR1	000016	7 RW,D,OVR,GHL
OVRD	000012	5 RW,D,OVR,GHL
MAPDFF	000200	64 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 025454 5526

VPRS.LP/I.I:1=VPHS/DE/MOTR



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```

BASIS.FTN
C
D INSTRE1
D STATUS=MPHASP(DCTM1,MPF1TM,IMP2,X1)
D IP(STATUS.NF.0)GO TO 10
C
C RESULTS
C
D CALL MWRDR(DCTM1,DCT1(1),RYTF4,CMVYFS,DCT1(LTH))
D WRITE(6,908)(1,DCT1(1),1=L1,LTH)
D FORMAT(1X,4(1X,'DCT1(',13,')=',E15.8,2X))
D CALL MPRDR(IMP2,DCT2(1),RYTF4,CMVYFS,DCT2(LTH))
D WRITE(6,909)(1,DCT2(1),1=L1,LTH)
D FORMAT(1X,4(1X,'DCT2(',13,')=',E15.8,2X))
D RETURN
C
C DIAGNOSTIC TRAP AREA
C
D TYPE 100,INSTR,STATUS
D CALL EXIT
D FORMAT(1X,100MAP FRDR*** BASIS* INSTR=',13,' HAS STATUS=',16/)
D END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDEF1	000350	116 RW,I,CON,I,CL
SIDATA	000216	71 RW,D,CON,I,CL
SVARS	000054	22 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,DVR,GHL
MTAPE1	000012	5 RW,D,DVR,GHL
MTAPE2	000012	5 RW,D,DVR,GHL
OVRI	002020	520 RW,D,DVR,GHL
UVRU	002006	515 RW,D,DVR,GHL
UVRF	006016	1543 RW,D,DVR,GHL
UVRA	000152	54 RW,D,DVR,GHL
UVRP	002022	521 RW,D,DVR,GHL
UVRR	005004	1282 RW,D,DVR,GHL
UVR2	001002	257 RW,D,DVR,GHL
UVRI	000016	7 RW,D,DVR,GHL
UVRD	000012	5 RW,D,DVR,GHL
MAPDFF	000200	64 RW,D,DVR,GHL

TOTAL SPACE ALLOCATED = 025644 5586

NO FPP INSTRUCTIONS GENERATED

BASIS,IP/LI:1-BASIS/DE/NOTH





```

C      SORT TMP2 TO DCTM2
C      SORT DCTM TO TMP3
C      SORT DCTM1 TO TMP4
D      INSTR=1
D      STATUS=MPASRT(IORDRM)
D      IF (STATUS.NE.0) GO TO 10
C      RESULTS
C
D      CALL MPRDH(DCTM2,DCT2(1),NYTF4,CNVVFS,DCT2(LTH))
D      CALL MPRDH(IORDRM,IORDR(1),NYTF2,CNVVFS,IORDR(LTH))
D      WRITE(6,910)(I,IORDR(I),I=1,LTH)
D      FORMAT(/IX,6(IX,'IORDR(',I3,')',I=1,LTH))
D      WRITE(6,909)(I,DCT2(I),I=1,LTH)
D      FORMAT(/IX,4(IX,'DCT2(',I3,')',I=1,LTH))
D      CALL MPRDH(TMP4,DCT1(1),NYTF4,CNVVFS,DCT1(LTH))
D      WRITE(6,911)(I,DCT1(I),I=1,LTH)
D      FORMAT(/IX,4(IX,'DCT1(',I3,')',I=1,LTH))
D      CALL MPRDH(TMP3,DCT(1),NYTF4,CNVVFS,DCT(LTH))
D      WRITE(6,912)(I,DCT(I),I=1,LTH)
D      FORMAT(/IX,4(IX,'DCT(',I3,')',I=1,LTH))
D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
C      TYPE 100, INSTR, STATUS
C      CALL EXIT
D      FORMAT(IX,'***MAP ERROR*** ASRT' INSTR=',I3,' HAS STATUS=',I6/)
D      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDFF1	000620	RW,I,CUN,LCL
SIDATA	000324	RW,D,CUN,LCL
SVANS	000054	RW,D,CUN,LCL
MTAPF0	002260	RW,D,OVR,GHI
MTAPF1	000012	RW,D,OVR,GHI
MTAPF2	000012	RW,D,OVR,GHI
OVRT	002020	RW,D,OVR,GHI
OVRD	002006	RW,D,OVR,GHI
OVRF	006016	RW,D,OVR,GHI
OVRG	000152	RW,D,OVR,GHI
OVRH	002022	RW,D,OVR,GHI
OVRJ	005004	RW,D,OVR,GHI
OVRK	001002	RW,D,OVR,GHI
OVRM	000016	RW,D,OVR,GHI
OVRN	000012	RW,D,OVR,GHI
MAPDF	000200	RW,D,OVR,GHI

TOTAL SPACE ALLOCATED = 026222 5705

FORTRAN IV-PLUS V02-51F  
ASRT.FIN /DF/WH

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NO FPP INSTRUCTIONS GENERATED

ASNT.LP/L1:1=ASRT/DF/MOTH



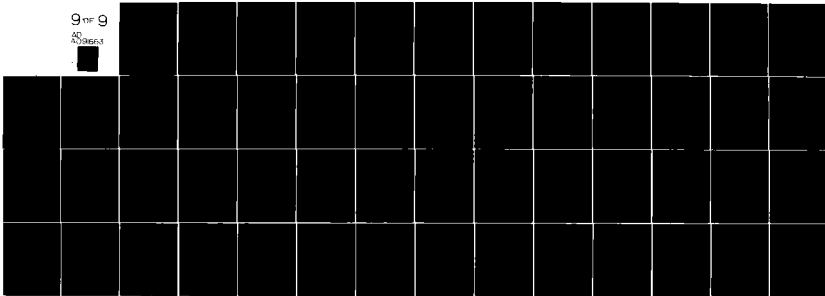
AD-A091 663

GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8  
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME S0--ETC(U)  
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

9 of 9



END  
DATE  
FILMED  
12-80  
DTIC





```

C C ASSIGN BITS TO DCT ELEMENTS
C C ANALYSIS:MODUL=1,SYNTHESIS:MODUL=2
C C INSTR=1
D D IF(MODUL.EQ.1)STATUS=MPCDRA(MIRIT3)
D D IF(MODUL.EQ.2)STATUS=MPCDRA(MIRIT1)
D D IF(STATUS.NE.0)GO TO 10
C C RESULTS
C C
D D IF(MODUL.EQ.1)CALL MPRDR(MIRIT3,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
D D IF(MODUL.EQ.2)CALL MPRDR(MIRIT1,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
D D IF(STATUS.NE.0)GO TO 10
D D911 WHITE(6,911)(I,IRIT(I),I=1,LTH)
D D911 FORMAT('IX,6(IX,'IRIT('I3,')=',I3,2X))
D D911 RETURN
C C DIAGNOSTIC TRAP AREA
C C
D D TYPE 100, INSTR, STATUS
D D CALL EXIT
D D911 FORMAT('IX, '***MAP ERROR*** RITA INSTR='I3, ' HAS STATUS=',I6/)
D D911 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000334	110
SIDATA	000160	56
SVARS	000054	22
SECTID	000002	1
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRT	002020	520
OVRO	002006	515
OVRF	006016	1543
OVPA	000152	53
OVRP	002022	521
OVPR	005004	1282
OVRE	001002	257
OVRL	000016	7
OVRO	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 0255574 5566

NO FPP INSTRUCTIONS GENERATED

RITA,IP/LI:RITA/DF/MOTR





```

0029      DATA RUS1,RUS2,RUS3/64,124,192/
          C
          C QUANTIZE OCT(.) W/ IRIT(.) ACCORDING TO IURDR(.)
          C
          C INSTR=1
          C STATUS=MPFSTO(OTDCTM,MIRIT3,TAPE1,IMP2)
          C IF(STATUS.NE.0)GO TO 10
          C
          C RESULTS
          C
          C DO 21 I=1,LTH
          C IURDR(I)=IURDR(I)+1
          C CALL MPRDH(OTDCTM,NOUIT(1),HYTE2,CNVYES,NOUIT(LTH))
          C DO 22 I=1,LTH
          C J=IURDR(I)
          C OTDCT(J)=NOUIT(I)
          C NOUIT(I)=0
          C WRITE(6,912)(I,OTDCT(I),I=1,LTH)
          C FORMAT(/IX,4(IX,'OTDCT(',I3,')=' ,I3,2X))
          C CALL MPRDH(MIRIT3,IRIT(1),RYTE2,CNVYES,IRIT(LTH))
          C WRITE(6,913)(I,IRIT(I),I=1,LTH)
          C FORMAT(/IX,4(IX,'MIRIT3(',I3,')=' ,I3,2X))
          C RETURN
          C
          C DIAGNOSTIC TRAP AREA
          C
          C TYPE 100, INSTR, STATUS
          C CALL EXIT
          C FORMAT(IX, '***MAP ERROR*** 'ODCT' INSTR=' ,I3, ' HAS STATUS=' ,I6/)
          C FND
  
```

PROGRAM SECTIONS		NAME	SIZE	ATTRIBUTES
SCODE:1	000446	147		RW,I,CON,I,CL
\$IDATA	000216	71		RW,D,CON,I,CL
SVARS	000056	73		RW,D,CON,I,CL
MTAPE:0	002260	600		RW,D,OVR,GHL
MTAPE1	000012	5		RW,D,OVR,GHL
MTAPE2	000012	5		RW,D,OVR,GHL
NVRI	002020	520		RW,D,OVR,GHL
NVRO	002006	515		RW,D,OVR,GHL
NVRF	006016	1543		RW,D,OVR,GHL
NVRA	000152	53		RW,D,OVR,GHL
NVRP	002022	521		RW,D,OVR,GHL
NVRB	005004	1287		RW,D,OVR,GHL
NVR2	001002	257		RW,D,OVR,GHL
NVRL	000016	7		RW,D,OVR,GHL
NVRD	000012	5		RW,D,OVR,GHL
NVHT	001032	269		RW,D,OVR,GHL
NVHT	002124	554		RW,D,OVR,GHL
MAPDEF	000200	64		RW,D,OVR,GHL

FORTHAN IV-PLUS V02-51F  
QDCT.LTN /DE/WR

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TOTAL SPACE ALLOCATED = 031122 6441

NO PFP INSTRUCTIONS GENERATED

QDCT.LP/LI:1=QDCT/DE/NOTR



```

0027 D WRITE(6,699)ICOUNT
0028 D699 FORMAT(1H1,40(1H>),'RECEIVE FRAME 0 ',14,40(1HC))
C
C PASS DATA W/ NO ERRORS
C
0029 D INSTR=1
0030 D STATUS=MPMDR(ARODCT,IAODCT(1),BYTE2,CNVYES,IAODCT(LTH))
0031 D IF(STATUS.NE.0)GO TO 10
0032 D INSTR=2
0033 D STATUS=MPMDR(AOPR,IAOPR(1),BYTE2,CNVYES,IAOPR(LPARM+1))
0034 D IF(STATUS.NE.0)GO TO 10
0035 D WRITE(6,R99)
0036 D R99 FORMAT(/IX,'*** MODEM OUTPUT ***')
0037 D WRITE(6,900)(IAOPR(I),I=1,LPARM)
0038 D900 FORMAT(/IX,6(IX,'AOPR(',I2,')')=',16,2X))
0039 D WRITE(6,901)(IAODCT(I),I=1,LTH)
0040 D901 FORMAT(/IX,6(IX,'AODCT(',I3,')')=',16,2X))
C
C ADD TRANSMISSION DELAY
C
0041 D INSTR=3
0042 D STATUS=MPMDR(AODCT,IAODCT(1),BYTE2,CNVYES,IAODCT(LTH))
0043 D IF(STATUS.NE.0)GO TO 10
0044 D INSTR=4
0045 D STATUS=MPMDR(AOPR,IAOPR(1),BYTE2,CNVYES,IAOPR(LPARM+1))
0046 D IF(STATUS.NE.0)GO TO 10
0047 D WRITE(6,799)
0048 D799 FORMAT(/IX,'*** MODEM INPUT ***')
0049 D WRITE(6,800)(IAOPR(I),I=1,LPARM)
0050 D900 FORMAT(/IX,6(IX,'AOPR(',I2,')')=',16,2X))
0051 D WRITE(6,801)(IAODCT(I),I=1,LTH)
0052 D901 FORMAT(/IX,6(IX,'AODCT(',I3,')')=',16,2X))
0053 D RETURN
C
C DIAGNOSTIC TRAP AREA
C
0054 I0 TYPE 100,INSTR,STATUS
0055 CALL EXIT
0056 I00 FORMAT(IX,'***MAP ERROR*** "CHANI" INSTR=',I3,' HAS STATUS=',I6/)
0057 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODEF1	000644	RM,J,CON,ICL
SIDATA	000452	RM,D,CON,ICL
SVANS	000054	RM,D,CON,ICL
MTAPE0	002260	RM,D,OVR,GRI
MTAPE1	000012	RM,D,OVR,GRI
MTAPE2	000012	RM,D,OVR,GRI
CHANFL	001034	RM,D,OVR,GRI
OVR1	002020	RM,D,OVR,GRI
OVR0	002006	RM,D,OVR,GRI
OVRF	006016	RM,D,OVR,GRI

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CHANL.FTN

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OVR	000152	53	RM,D,OVR,CRI.
OVRP	002022	521	RM,D,OVR,CRI.
OVRB	005004	1282	RM,D,OVR,CRI.
OVR2	001002	257	RM,D,OVR,CRI.
OVR1	000016	7	RM,D,OVR,CRI.
OVRD	000012	5	RM,D,OVR,CRI.
MAPDEF	000200	64	RM,D,OVR,CRI.

TOTAL SPACE ALLOCATED = 027430 602R

NO FPP INSTRUCTIONS GENERATED

CHANL.FP/LI:=CHANL/DE/NOTR



```

0028 C DATA RUS1,RUS2,RUS3/64,128,192/
C C SIDEHAND DEQUANTIZATION LACS SIDEHAND DESERIALIZED
C C HY 3 FRAME ONE TO CSPI IN BACKGROUND
C C
0029 D INSTR=1
0030 D STATUS=VMUVZ(ORPRM)
0031 D IF (STATUS.NF.0)GO TO 10
C C
C C DEQUANTIZE PARCOR(J),J=1,...,LPCN,G,M,VUV
C C
0032 D INSTR=2
0033 D STATUS=MPDPP(PARC,ORPRM,A1)
0034 D IF (STATUS.NF.0)GO TO 10
C C
C C RESULTS
C C
0035 D CALL MPRDH(ORPRM,IRIT(1),HYTE2,CNVYES,IRIT(LPARM+1))
0036 D WHITE(6,927)(J,IRIT(J),J=1,LPARM)
0037 D FORMAT(1X,4(1X,'ORPRM',I2,' '),I6,2X))
0038 D CALL MPRDH(PARC,DRPAR(1),HYTE4,CNVYES,ORPAR(LPCN))
0039 D WHITE(6,928)(J,DRPAR(J),J=1,LPCN)
0040 D FORMAT(1X,4(1X,'DRK',I2,' '),F15.8,2X))
0041 D CALL MPRST(88,DRDC,1,CNVYES)
0042 D CALL MPRST(89,DRVAR,1,CNVYES)
0043 D CALL MPRST(90,DRG,1,CNVYES)
0044 D CALL MPRST(91,DRM,1,CNVYES)
0045 D CALL MPRST(104,DRV,1,CNVYES)
0046 D WHITE(6,925)DRDC,DRVAR
0047 D FORMAT(1X,'DRDC(88)=' ,F15.8,' AND DRVAR(89)=' ,F15.8)
0048 D WHITE(6,929)DRM,DRG,DRV
0049 D FORMAT(1X,'DRM=' ,F15.8,' , DRG=' ,F15.8,' AND DRV=' ,F15.8)
0050 D CALL MPRDR(A1,DRR(1),HYTE4,CNVYES,DRR(LPCN))
0051 D WHITE(6,931)(J,DRR(J),J=1,LPCN)
0052 D FORMAT(1X,4(1X,'DRA',I2,' '),F15.8,2X))
0053 D CALL MPRST(60,ENG,1,CNVYES)
0054 D WHITE(6,932)ENG
0055 D FORMAT(1X,'ENG=' ,F15.8)
0056 D CALL MPRDH(ORDCTM,ORDCT(1),HYTE2,CNVYES,ORDCT(LTH))
0057 D WHITE(6,940)(I,ORDCT(I),I=1,LTH)
0058 D FORMAT(1X,4(1X,'ORDCTM',I3,' '),I6,2X))
0059 D CALL MPRDH(IRIT2,IRIT(1),HYTE2,CNVYES,IRIT(LTH))
0060 D WHITE(6,941)(I,IRIT(I),I=1,LTH)
0061 D FORMAT(1X,4(1X,'IRIT2',I3,' '),I6,2X))
0062 D CALL MPRDH(IORDR2,IORDR(1),HYTE2,CNVYES,IORDR(LTH))
0063 D WHITE(6,942)(I,IORDR(I),I=1,LTH)
0064 D FORMAT(1X,4(1X,'IORDR2',I3,' '),I6,2X))
0065 D CALL MPRDH(DCTI2,DCTI(1),HYTE4,CNVYES,DCTI(LTH))
0066 D WHITE(6,943)(I,DCTI(I),I=1,LTH)
0067 D FORMAT(1X,4(1X,'DCTI',I3,' '),F15.8,2X))
0068 D CALL MPRST(100,DRDC,1,CNVYES)
0069 D CALL MPRST(101,DRVAR,1,CNVYES)
0070 D WHITE(6,944)DRDC,DRVAR
0071 D FORMAT(1X,'DRDC(100)=' ,F15.8,' AND VAR(101)=' ,F15.8)
0072 D RETURN
C C

```











C DEQUANTIZATION OF MAINRAND LAGS MAINRAND DESERIALIZED  
 C BY 1 FRAME DUE TO CSPI IN THE BACKGROUND

0027 INSTR=1  
 C GIVE CSPI MIT ASSIGNMENT FOR UNPACKING OF MAINRAND  
 0028 STATUS=VMV3(IRIT)  
 0029 IF (STATUS.NE.0)GO TO 10

C DEQUANTIZE DCT(I)  
 C  
 0030 INSTR=2  
 0031 STATUS=MPFST(DRDCTM,ORDCTM,TEMP1,IORDR2)  
 0032 IF (STATUS.NE.0)GO TO 10

C RESULTS  
 C  
 0033 CALL MPHDR(DRDCTM,DTDCT(1),BYTE4,CNBYTES,DTDCT(LTH))  
 0034 WRITE(6,934)(I,DTDCT(I),I=1,LTH)  
 0035 FORMAT(/IX,4(IX,'DRDCTM','13,')=' ',F15.8,2X))  
 0036 CALL MPHDR(IRIT,IRIT(1),BYTE2,CNBYTES,IRIT(LTH))  
 0037 WRITE(6,935)(I,IRIT(I),I=1,LTH)  
 0038 FORMAT(/IX,4(IX,'IRIT','13,')=' ',I6,2X))  
 0039 CALL MPHDR(DCT1,DCT1(1),BYTE4,CNBYTES,DCT1(LTH))  
 0040 WRITE(6,936)(I,DCT1(I),I=1,LTH)  
 0041 FORMAT(/IX,4(IX,'DCT1','13,')=' ',F15.8,2X))  
 0042 CALL MPHDR(IORDR1,IORDR(1),BYTE2,CNBYTES,IORDR(LTH))  
 0043 WRITE(6,937)(I,IORDR(I),I=1,LTH)  
 0044 FORMAT(/IX,4(IX,'IORDR','13,')=' ',I6,2X))  
 0045 CALL MPHDR(MIRIT1,IRIT(1),BYTE2,CNBYTES,IRIT(LTH))  
 0046 WRITE(6,938)(I,IRIT(I),I=1,LTH)  
 0047 FORMAT(/IX,4(IX,'MIRIT1','13,')=' ',I6,2X))  
 0048 CALL MPHST(102,DRDC,1,CNBYTES)  
 0049 CALL MPHST(103,DRVAR,1,CNBYTES)  
 0050 WRITE(6,940)DRDC,DRVAR  
 0051 FORMAT(IX,'DRDC(102)=' ,F15.8,2X,' AND DRVAR(103)=' ,F15.8)  
 0052 RETURN

C DIAGNOSTIC TRAP AREA  
 C  
 0053 TYPE 100,INSTR,STATUS  
 0054 CALL EXIT  
 0055 FORMAT(IX,'\*\*\*MAP ERROR\*\*\* "DDCT" INSTR=',I4,' HAS STATUS=',I6/)  
 0056 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	001056	279 RW,I,COM,ICL
SPDATA	000014	6 RW,D,COM,ICL
SIDATA	000506	163 RW,D,COM,ICL
SVARS	000064	26 RW,D,COM,ICL
MTAPE0	002260	600 RW,D,OVR,GMT
MTAPE1	000012	5 RW,D,OVR,GMT
MTAPE2	000012	5 RW,D,OVR,GMT

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DDCT.FTN /PF/WH

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OVRI	002020	520	RW,D,OVR,GRL
OVRO	002006	515	RW,D,OVR,GRL
OVRF	006016	1543	RW,D,OVR,GRL
OVRA	000152	53	RW,D,OVR,GRL
OVRP	002022	521	RW,D,OVR,GRL
OVRQ	005004	1282	RW,D,OVR,GRL
OVR2	001002	257	RW,D,OVR,GRL
OVR3	000016	7	RW,D,OVR,GRL
OVR4	000012	5	RW,D,OVR,GRL
OVR5	002124	554	RW,D,OVR,GRL
MAPDF	000200	64	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 031012 6405

DDCT.LP/LI:1=DDCT/DE/MOTR



```

C
D 0026 INSTR=1
D 0027 STATUS=MPIDCM(X2,PRDCTM,CUSZ)
D 0028 IF(STATUS.NE.0)GO TO 10
D 0029 CALL MPRDH(X2,XR(1),RYTF8,CNVYFS,XR(LTH2))
D 0030 WRITE(6,914)(I,XR(I),I=1,LTH2)
D 0031 INSTR=2
D 0032 STATUS=FFTM(X2,I,X2,VSHRT,WORK)
D 0033 IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
D 0034 CALL MPRDH(X2,XR(1),RYTF4,CNVYFS,XR(LTH))
D 0035 WRITE(6,914)(I,XR(I),I=1,LTH)
D 0036 FORMAT(1X,4(1X,' ',13,' '),E15.8,2X))
D 0037 RETURN
C
C DIAGNOSTIC TRAP AFPA
C
D 0038 TYPE 100, INSTR, STATUS
D 0039 CALL EXIT
D 0040 FORMAT(1X, '***MAP ERROR*** DCTR' INSTR=',', I3, ' HAS STATUS=', I6/)
D 0041 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000406	131 RW,I,CON,I,CL
SPDATA	000004	2 RW,D,CON,I,CL
SIDATA	000172	61 RW,D,CON,I,CL
SVARS	000054	22 RW,D,CON,I,CL
MTAPFD	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000012	5 RW,D,OVR,GRI
OVRT	002020	520 RW,D,OVR,GRI
OVR0	002006	515 RW,D,OVR,GRI
OVRF	006016	1543 RW,D,OVR,GRI
OVRV	000152	53 RW,D,OVR,GRI
OVRP	002022	521 RW,D,OVR,GRI
OVRB	005004	1282 RW,D,OVR,GRI
OVR2	001002	257 RW,D,OVR,GRI
OVR1	000016	7 RW,D,OVR,GRI
OVRD	000012	5 RW,D,OVR,GRI
MAPDEF	000200	64 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 025062 5593

DCTR.LP/LI:1=DCTR/DF/NOTR





```

0027 STATUS=MPVDNM(NOUTM,YNM,X2)
0028 IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
0029 CALL MPRST(100,DCRIAS,1,CNVVFS)
0030 WRITE(6,901)DCRIAS
0031 FORMAT(/IX,'DCRIAS=',F15.8)
0032 CALL MPRST(101,VAR,1,CNVVFS)
0033 WRITE(6,901)VAR
0034 FORMAT(/IX,'VAR=',F15.8)
0035 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0036 IO TYPE 101,INSTR,STATUS
0037 CALL EXIT
0038 FORMAT(IX,'***MAP ERROR*** VARDC INSTR=',I3,' HAS STATUS=',I6/)
0039 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000200	64
SPDATA	000014	6
STDATA	000160	56
SVARS	000052	21
MTAPF0	002260	600
MTAPE1	000012	5
MTAPF2	000012	5
OVRT	002020	520
OVR0	002006	515
OVRP	006016	1543
OVR1	000152	53
OVR2	002022	521
OVR3	005004	1292
OVR4	001002	257
OVR5	000016	7
OVR6	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025450 5524

NO FPP INSTRUCTIONS GENERATED

VARDC.LP/LI:1=VARDC/DF/NOTR



FORTRAN IV=PLUS

OUTPUT.FTM

```

C
C CHECK FOR REQUEST FROM "INPUT"
IF (MPCLS.NE.0) GO TO 5000
INSTR=1
STATUS=MPCLS(1,1)
STATUS=MPDIR(MOUTH, MOUT(1), BYT2, CNVYFS, MOUT(NTOTO))
IF (STATUS.NE.0) GO TO 10
*ITP(6,9)3(1),MOUT(1),I=1,NTOTO)
FORMAT(1X,4(1X,MOUT(1,13,15),16,2X))
C
C DATA OUTPUT
CALL TAPE2(2)
RETURN
C
C FOR ON INPUT, WRITE FOR
C
C COUNT=ICOUNT-1
DO 5001 I=1,NTUPS
MOUT(I)=0
DO 5002 I=1,32
CALL TAPE2(2)
NSKIP=NSKIPS
NEND=0
IST=1
CALL TAPE2(6)
DO 5005 I=1,ICOUNT
CALL TAPE2(1)
IF (MEND) 5077,5076,5077
DO 5006 J=1,NTUPS
MOUT(J)=MIN(J)
CALL TAPE2(2)
DO 5007 I=1,NTUPS
MOUT(I)=0
DO 5008 I=1,4
CALL TAPE2(2)
CALL TAPE2(5)
CALL MPCLS(0)
C
C TYPE 100,ICOUNT
FORMAT(1X,'ATC' F0NE AFTER ',16,' FRAMES!')
TYPE 100
FORMAT(1X,'ATC SYSTEM READY')
CALL EXIT
RETURN
C
C DIAGNOSTIC TRAP AREA
C
C
C TYPE 101,INSTR,STATUS
CALL EXIT
FORMAT(1X,'***MAP ERROR*** "OUTPUT" INSTR=',13,' HAS STATUS=',16/)
END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
0026		
0027		
0028		
0029		
0030		
0031		
0032		
0033		
0034		
0035		
0036		
0037		
0038		
0039		
0040		
0041		
0042		
0043		
0044		
0045		
0046		
0047		
0048		
0049		
0050		
0051		
0052		
0053		
0054		
0055		
0056		
0057		

SC00F1	0002468	155	RW, I, CON, LCL
SPDATA	000020	4	RW, D, CON, LCL
SIDATA	000132	45	RW, D, CON, LCL
SWARS	000056	23	RW, D, CON, LCL
STPMS	000002	1	RW, D, CON, LCL
ATAPF0	002260	600	RW, D, OVR, GHL
ATAPF1	000012	5	RW, D, OVR, GHL
ATAPF2	000012	5	RW, D, OVR, GHL
OVPT	002020	520	RW, D, OVR, GHL
OVRO	002006	515	RW, D, OVR, GHL
OVRF	006016	1543	RW, D, OVR, GHL
OVRA	000152	53	RW, D, OVR, GHL
OVRP	002016	514	RW, D, OVR, GHL
OVRS	005003	1282	RW, D, OVR, GHL
OVW2	001002	257	RW, D, OVR, GHL
OVW1	000016	7	RW, D, OVR, GHL
OVRO	000012	5	RW, D, OVR, GHL
4APDFF	0002100	64	RW, D, OVR, GHL

TOTAL SPACE ALLOCATED = 025716 5607

NO PFP INSTRUCTIONS GENERATED

OUTPUT,LP/LL:1=OUTPUT/DE/NOTR



DATA LUMG,ORR1,CNVFS,CVWD/0,1,1,0/  
DATA PUS1,POS2,POS3/64,128,192/

ADD 3 FRAMES DELAY IN INPUT FOR SMP CALCULATION

INST=0  
STATUS=APPROXSEN,NIWD(1),NYF2,CNVFS,NIND3(NTUPS)  
STATUS=APPROXIN2,IORR(1),NYF2,CNVFS,IORR(NTUPS)  
STATUS=APPROXSEN,IORR(1),NYF2,CNVFS,IORR(NTUPS)  
STATUS=APPROXINI,IORR(1),NYF2,CNVFS,IORR(NTUPS)  
STATUS=APPROXIN2,IORR(1),NYF2,CNVFS,IORR(NTUPS)  
STATUS=APPROXINI,NIND(1),NYF2,CNVFS,NIND(NTUPS)  
IF(STATUS.NE.0)GO TO 1

Now DELAY M & VUV FOR CONSISTENCY

M2=4  
M3=2  
M2=1  
M1=2  
IV3=IV3  
IV3=IV2  
IV2=IV1  
IV1=IFIX(VU)

COMPUTE S/N RATIO

SUM1=0.0  
SUM2=0.0  
NIN(1) MATCHES NOUT(NP+1), ERGD NTUPS=EP MATCHES!

DO 3605 I=1,NTUPS-1  
SOURCE=FLOAT(NIND(I))  
SIGNAL=FLOAT(NOUT(NP+1))  
NINSE=SIGNAL-SOURCE  
SUM1=SUM1+SOURCE\*\*2  
SUM2=SUM2+NINSE\*\*2

SA=0.0  
FRDM=ICOUNT-START+1  
IF(FRDM.E1.0)GO TO 200  
SN=10.0\*ALOG10(SUM1/SUM2)  
IF(FRDM.E0.4)CSN=SN

CSN=((FRDM-1)/FRDM)\*(CSN+1./FRDM)\*SN

\*RTE(3,255)ICOUNT,SN,CSN,M4,IV4

\*RTE(5,255)ICOUNT,SN,CSN,M4,IV4

FORMAT(IX,'FRAM#=',I3,IX,'SN#=',F6.2,IX,'CSN#=',F6.2,IX,/,

1,'PITCH#=',I3,/' K VUV=',I,12)

CONTINUE

RETURN

C DIAGNOSTIC TRAP AREA

C

TYPE 100,INST#,STATUS

CALL EXIT

FORMAT(IX,'\*\*\*MAP ERROR\*\*\* "SN" INST#=',I3,/' HAS STATUS=',I6,/)

END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDP1	000720	232
SDATA	000162	57
SVARS	000110	36
STEMPS	000002	1
ATAPP0	002260	600
ATAPP1	000012	5
ATAPP2	000012	5
DELAY	000020	4
OVR1	002020	520
OVR0	002006	515
OVRP	006016	1543
OVR4	000152	53
OVRP	002022	521
OVRN	005004	1282
OVR2	001002	257
OVRPL	000016	7
OVRD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 026236 5711

SNR.FP/1:1:1=SNR/DF/MOIR



```

0001 SUBROUTINE TAPE2(IU)
0002 IMPLICIT INTEGER(-2)
0003 LOGICAL*4 APPEND
0004 COMMON/MTAPE0/NIN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NIUTI,NTUPS,NTOTO
0006 COMMON/MTAPE2/NEPD,NEFP,NEFLF,NENS,NAUTS
0007 COMMON/MTAPE3/NAF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IUSUC,IFALN,IORWD
0010 I,LOWLR,IEVER,IOSPF,IFEOF,IOEOP,IORLB,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DATA IOATT,IUSUC,IFALN/0001400,1,-34/
0013 DATA IORWD,LOWLR,IEVER,IOSPF,IORLB/02400,0400,-4,02440,01000
0014 DATA IOSPF,IEEOF,IOEOP/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/
0018 GO TO (700,800,900,1100,1000,1200,1300,1400),IU
0019 CALL OPT1
0020 RETURN
0021 CALL OPT2
0022 RETURN
0023 CALL OPT3
0024 RETURN
0025 CALL OPT4
0026 RETURN
0027 CALL OPT5
0028 RETURN
0029 CALL OPT6
0030 RETURN
0031 CALL OPT7
0032 RETURN
0033 CALL OPT8
0034 CALL OPT3
0035 IFC(APPEND)CALL OPT4
0036 RETURN
0037 END
    
```

PROGRAM SECTIONS	NAME	SIZE	ATTRIBUTES
	SCDPF1	000226	RW,I,CON,LCI
	SPDATA	000072	RW,D,CON,LCI
	SIDATA	000002	RW,D,CON,LCI
	MTAPE0	002260	RW,D,OVR,GMI
	MTAPE1	000012	RW,D,OVR,GMI
	MTAPE2	000012	RW,D,OVR,GMI
	MTAPE3	012260	RW,D,OVR,GMI
	MTAPE4	000004	RW,D,OVR,GMI
	MTAPE5	000064	RW,D,OVR,GMI
	MTAPE6	000002	RW,D,OVR,GMI

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TAPE2.PTM /MP

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NO FPP INSTRUCTIONS GENERATED

TAPE2.MP/LI:1=TAPE2/MOTH

```
0001 SUBROUTINE OPT1
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*4 APPEND
0004 COMMON/MTAPE0/IN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,MTOT1,NTUPS,MTOT0
0006 COMMON/MTAPE2/END,NFR,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NAUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPES/MASK,ISW(2),IOATT,IOSUC,IEALN,IORWD
0010 1, IOWH, IEVER, IOSPE, IFFOF, IORGR, MT0(6), MT1(6), DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT, IOSUC, IEALN, IOWH, IEVER, IOSPE, IORGR/02400, 0400, -4, 02440, 01000
0014 DATA IOSPE, IFFOF, IORGR/02440, -10, 03000/
0015 DATA MASK/0377/
0016 DATA MT0/0, 204R, 0, 0, 0, 0/
0017 DATA MT1/0, 204R, 0, 0, 0, 0/

C
C INPUT DATA(I=1)
C
0018 CONTINUE
0019 I=IST+NTUPS
0020 IF(1324.GE.IST) GO TO 200
0021 I=IST-1024
0022 NSKIP=NSKIP+1
0023 KOVER=IST+NTOT1-1325
0024 IF(KOVER.GT.0) GO TO 305
0025 DO 5000 I=1,MTOT1
0026 NIN(I)=NRF(I,IST+I-1)
0027 I=IST+NTUPS
0028 GO TO 6002
0029 IPEP=IST-1024
0030 DO 5001 I=1,300
0031 NRF(I,PEP+I-1)=NRF(IST+I-1)
0032 I=IPEP
0033 IF(NINS.EQ.0) GO TO 2100
C>>>>>>DISK INPUT
0034 DO 5010 I=1,16
0035 READ(7,END=2001,FRR=6000)(ICARD(J),J=1,64)
0036 K=64*(I-1)+300
0037 DO 5010 J=1,64
0038 NRF(K+J)=ICARD(J)
0039 IF(NFR.NE.0) GO TO 6000
0040 GO TO 200
0041 NEND=1
0042 GO TO 200
C>>>>>>TAPE INPUT
0043 NEND=0
0044 NFR=0
0045 CALL GETADR(MTO,NRF(301))
0046 CALL MTO(IORL,2,1,0,ISW,MT0,DSW)
0047 IF(10SUC.EQ.1)AND(MASK,ISW(1))GO TO 200
0048 IF(IAND(IEFOR,MASK).EQ.IAND(MASK,ISW(1)))NEND=1
0049 IF(IAND(IEVER,MASK).EQ.IAND(MASK,ISW(1)))NFR=1
0050 GO TO 200
```

```

0051 6000 TYPE 6001
0052 6001 FORMAT(IX,10*INPUT FILE FRRR*****//)
0053 MFRM=1
0054 6002 RETURN
0055 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000646	211 RW,1,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000062	25 RW,D,CON,I,CL
SVARS	000212	69 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000012	5 RW,D,OVR,GRI
MTAPE3	012260	2648 RW,D,OVR,GRI
MTAPE4	000004	2 RW,D,OVR,GRI
MTAPE5	000064	26 RW,D,OVR,GRI
MTAPE6	000002	1 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 016034 3598

NO FPP INSTRUCTIONS GENERATED

OPT1.LP/1:1=OPT1/NOTR



SCDDF1	000432	141	RW,I,CON,I,CL
SPDATA	000014	6	RW,D,CON,I,CL
SIDATA	000062	25	RW,D,CON,I,CL
SVARS	000217	69	RW,D,CON,I,CL
MTAPF0	002260	600	RW,D,OVR,GRU
MTAPF1	000012	5	RW,D,OVR,GRU
MTAPF2	000012	5	RW,D,OVR,GRU
MTAPF3	012260	2648	RW,D,OVR,GRU
MTAPF4	000004	2	RW,D,OVR,GRU
MTAPF5	000064	26	RW,D,OVR,GRU
MTAPF6	000002	1	RW,D,OVR,GRU

TOTAL SPACE ALLOCATED = 015620 352R

NO FPP INSTRUCTIONS GENERATED

OPT2.LP/LI:1=OPT2/NOTR

```
0001 SUBROUTINE UPT3  
0002 IMPLICIT INTEGER(A-Z)  
0003 LOGICAL A1 APPEND  
0004 COMMON/MTAPE0/NTIN(300),NOUT(300)  
0005 COMMON/MTAPE1/NSKIP,IST,NTOT,NTIPS,NTOTD  
0006 COMMON/MTAPE2/NFND,NFR,NFILE,NINS,NOUTS  
0007 COMMON/MTAPE3/NRF(1324),NHUF(1324)  
0008 COMMON/MTAPE4/IST,TRC  
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IFALW,IORWD  
0010 I,IWLK,IEVER,IOSPF,IFEOF,IOKOF,IORLH,MT0(6),MTI(6),DSW  
0011 DIMENSION ICAPD(64)  
0012 DATA IOATT,IOSUC,IFALW/0001400,1,-34/  
0013 DATA IORWD,IWLK,IEVER,IOSPF,IORLH/02400,0400,-4,02440,01000  
0014 DATA IOSPF,IFEOF,IOKOF/02440,-10,03000/  
0015 DATA MASK/0377/  
0016 DATA MT0/0,2048,0,0,0,0/  
0017 DATA MTI/0,2048,0,0,0,0/  
  
C  
C INITIALIZE(=3)  
C  
0018 CONTINUE  
0019 NEND=0  
0020 NERR=0  
0021 IF((NINS+NOUTS).GT.1)GO TO 901  
0022 UNIT=0  
0023 DO 902 LUN=2,3  
0024 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 902  
0025 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 902  
C>>>>>>MT: ATTACH  
CALL ASLUN(LUN,'MT',UNIT,DSW)  
0026 IF(DSW.NE.1)GO TO 6000  
0027 CALL WTOJ(IOATT,LUN,1,0,ISW(1),0,DSW)  
0028 IF(IOSUC.FO.IAND(MASK,ISW(1)))GO TO 902  
0029 IF(IAND(IEALW,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000  
0030 UNIT=UNIT+1  
0031 DO 903 LUN=2,3  
0032 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 903  
0033 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 903  
C>>>>>>MT: REWIND  
CALL WTOJ(IORWD,LUN,1,0,ISW(1),0,DSW)  
0034 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6001  
0035 CONTINUE  
0036 IF(NFILE.FO.0)GO TO 905  
0037 IF(NINS.NE.0)GO TO 913  
C>>>>>>MT: FILE SKIP  
DO 907 I=1,NFILE-1  
0040 CALL GETADR(MT0,NHF(301))  
0041 CALL WTOJ(IORLH,2,1,0,ISW(1),MT0,DSW)  
0042 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6003  
0043 MT0(I)=1  
0044 CALL WTOJ(IOSPF,2,1,0,ISW(1),MT0,DSW)  
0045 DO 912 J=1,NSKIP  
0046 IF(NINS.FO.0)GO TO 910  
0047 C>>>>>>DISK INPUT  
DO 914 I=1,16
```

```

0049 READ(7,END=905,KRP=6007)((CARD(IJ),JJ=1,64)
0050 K=64*(I-1)+300
0051 DO 914 JJ=1,64
0052 914 NHF(I+JJ)=ICARD(IJ)
0053 GO TO 912
C>>>>>>>TAPF INPUT
0054 910 CALL GETADR(MTO,NRF(301))
0055 CALL WTIO(FORIN,2,1.0,ISW(1),MTO,DSW)
0056 IF(IOSUC.FO.IAND(MASK,ISW(1)))GO TO 912
0057 IF(IAND(TVER,MASK).FO.IAND(MASK,ISW(1)))GO TO 6002
0058 IF(IAND(IEOF,MASK).NF.IAND(MASK,ISW(1)))GO TO 912
0059 NEND=1
0060 912 CONTINUE
0061 995 IREC=1
0062 I=I+1
0063 I=I+1
0064 I=I+1
0065 6000 TYPE 100
0066 GO TO 6010
0067 6001 TYPE 101
0068 GO TO 6010
0069 6002 TYPE 102
0070 GO TO 6010
0071 6003 TYPE 103
0072 6010 NFRK=1
0073 RETURN
0074 100 FORMAT(IX,'*** MT: ATTACH FAILURE! ***')
0075 101 FORMAT(IX,'*** MT: REWIND FAILURE! ***')
0076 102 FORMAT(IX,'*** INPUT FILE "NSKIP" FAILURE! ***')
0077 103 FORMAT(IX,'*** MT: "NFIL" FAILURE! ***')
0078 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODR1	001217	325 RW,1,CON,I,CL
SPDATA	000070	R RW,D,CON,I,CL
SIDATA	000332	109 RW,D,CON,I,CL
SVARS	000214	70 RW,D,CON,I,CL
STFAPS	000004	2 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000017	5 RW,D,OVR,GHL
MTAPE2	000017	5 RW,D,OVR,GHL
MTAPE3	012260	2648 RW,D,OVR,GHL
MTAPE4	000004	2 RW,D,OVR,GHL
MTAPE5	000064	26 RW,D,OVR,GHL
MTAPE6	000007	1 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 016662 3801  
 NO PPP INSTRUCTIONS GENERATED  
 OPT3-LP/LI:1=OPT3/NUIN



```

0001 SUBROUTINE OPT4
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/TAPE0/NINC(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTOT1,NTUPS,NTOTO
0006 COMMON/TAPE2/NEND,NERR,NFILE,NINS,NOUTS
0007 COMMON/TAPE3/NHF(1324),NRUF(1324)
0008 COMMON/TAPE4/IST,IFC
0009 COMMON/TAPES/MASK,ISW(2),IOWT,IOSUC,IFAIN,IORWD
0010 I,IOWH,IFVER,IOSPE,IFOP,IOFOP,IORH,MT0(6),MTI(6),DSW
0011 DIMENSION ICARD(64)
0012 DATA IOWT,IOSUC,IFAIN/0001400,1,-34/
0013 DATA IORWD,IOWH,IFVER,IOSPE,IORH/02400,0400,-4,02440,01000
0014 DATA IOSPE,IFOP,IOFOP/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MTI/0,2048,0,0,0,0/
C
C OUTPUT FILE SKIP(=4)
C
0018 CONTINUE
0019 NERR=0
0020 NEND=0
0021 IREG=1
0022 CALL GETADR(MTI(1),NRUF(1))
0023 CALL WT00(IORH,3,1,0,ISW(1),MTI,DSW)
0024 IF(IOSUC.EQ.IAND(MASK,ISW(1)))GO TO 1
0025 IF(IAND(IFOP,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0026 CALL GETADR(MTI(1),NRUF(1))
0027 CALL WT00(IORH,3,1,0,ISW(1),MTI,DSW)
0028 IF(IOSUC.EQ.IAND(MASK,ISW(1)))GO TO 1
0029 IF(IAND(IFOP,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0030 MTI(1)=1
0031 CALL WT00(IOSPE,3,1,0,ISW(1),MTI,DSW)
0032 RETURN
0033 TYPE 100
0034 RETURN
0035 FORMAT(1X, '*** OUTPUT "APPEND" FAILURE! ***'/)
0036 END
  
```

PROGRAM SECTIONS	NAME	SIZE	ATTRIBUTES
	SCDP1	000274	74 RW,I,CUN,ICL
	SPDATA	000014	6 RW,D,CUN,ICL
	SIDATA	000114	34 RW,D,CUN,ICL
	SVARS	000200	64 RW,D,CUN,ICL
	MTAPE0	002260	600 RW,D,OVR,GHI
	MTAPE1	000012	5 RW,D,OVR,GHI
	MTAPE2	000017	5 RW,D,OVR,GHI
	MTAPE3	012260	2648 RW,D,OVR,GHI
	MTAPE4	000004	2 RW,D,OVR,GHI
	MTAPES	000064	26 RW,D,OVR,GHI

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OPT4.PTN /PR

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NTAPPA 000002 1 HW,D,CIVP,GRI

TOTAL SPACE ALLOCATED = 015432 3469

NO FPP INSTRUCTIONS GENERATED

OPT4.IP/1.11=OPT4/MOTR

```

0001  SUMMOUTINP  IPTS
0002  IMPLICIT INTEGER(A-Z)
0003  LOGICAL A1 APPEND
0004  COMMON/MTAPE0/NIEN(300),NOUT(300)
0005  COMMON/MTAPE1/MSKIP,IST,NTOT1,MTUPS,MTOT0
0006  COMMON/MTAPE2/NEND,NFPP,NFILF,NIMS,NINITS
0007  COMMON/MTAPE3/NHF(1324),NHUF(1324)
0008  COMMON/MTAPE4/IST,INFC
0009  COMMON/MTAPE5/MASK,ISW(7),IOATT,IOSUC,IFALN,IOHWD
0010  I,IOWH,IEVER,IOSPF,IFEOF,IOENF,IOREN,MTD(6),MTI(6),DSW
0011  DIMENSION ICARD(64)
0012  DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0013  DATA IOHWD,IOWH,IEVER,IOSPF,IOREN/02400,0400,-4,02440,01000
0014  DATA IOSPF,IFEOF,IOENF/02440,-10,03000/
0015  DATA MASK/0377/
0016  DATA MTO/0,2048,0,0,0,0/
0017  DATA MTI/0,2048,0,0,0,0/

C
C  END-OF-FILE(=5)
C
0018  CONTINUE
0019  NFR=0
0020  NFD=0
0021  IF(NOUTS.EQ.0)GO TO 1001
C>>>>>>DISK EOF
0022  CALL CLOSE(3)
0023  GO TO 1003
C>>>>>>TAPE EOF
0024  1001  DO 1002 I=1,2
0025  1002  CALL MTOIOIOENF(3,1,0,ISW(1))
0026  IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0027  MT(I)=-1
0028  CALL MTOIOIOSPF(3,1,0,ISW(1),MTI,DSW)
0029  IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0030  1003  IIRG=1
0031  WTRPN
0032  TYPF 100
0033  NFR=1
0034  WTRPN
0035  100  FORMAT(IX,1*** MT: EOF FAILURE! ***/)
0036  END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	000204	66 RW,I,CUN,I,C,L
SPDATA	000014	6 RW,D,CUN,I,C,L
STDATA	000076	31 RW,D,CUN,I,C,L
SVARS	000202	65 RW,D,CUN,I,C,L
MTAPE0	022260	600 RW,D,OVR,C,H,L
MTAPE1	000012	5 RW,D,OVR,C,H,L
MTAPE2	000017	5 RW,D,OVR,C,H,L
MTAPE3	012260	7648 RW,D,OVR,C,H,L

FORTRAN IV-PLUS V02-514  
OPTS.FTN /MH

11:42:01

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MTAPE4	000004	2	RW,D,OVR,CHL
MTAPE5	000064	26	RW,D,OVR,CHL
MTAPE6	000002	1	RW,D,OVR,CHL

TOTAL SPACE ALLOCATED = 015376 3455

NO FPP INSTRUCTIONS GENERATED

OPTS.IP/LI:1=OPTS/MOTR

```

0001 SUBROUTINE OPT6
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A91 APPEND
0004 COMMON/TAPE0/IN(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTOT,NTOPS,NTOTD
0006 COMMON/TAPE2/NEFD,NEFK,NEFK,NTINS,MOUFS
0007 COMMON/TAPE3/NRE(1324),NHR(1324)
0008 COMMON/TAPE4/IST,IFEC
0009 COMMON/TAPES/MASK,ISW(7),IOATT,IUSUC,IFAIN,IORWD
0010 1,IOVER,IFVPR,IUSPF,IUSPF,IFEOF,IOFRH,MT0(6),MT1(6),DSW
0011 DIMENSION ICARD(64)
0012 DATA IOATT,IUSUC,IFAIN/0001400,1,-34/
0013 DATA IOVER,IOVER,IFVPR,IUSPF,IOGRH/02400,0400,-4,02440,01000/
0014 DATA IUSPF,IFEOF,IOFRH/02440,-10,03000/
0015 DATA MASK/3377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/
C
C REWIND/SEARCH INPUT ONLY(=6)
C
0018 CONTINUE
0019 NEND=0
0020 NHR=0
0021 IF(CINLS.FO.O) GO TO 1222
C>>>>>>>DISK REWIND
0022 REWIND 2
0023 GO TO 1204
C>>>>>>>MT: REWIND
0024 GO TO 1204
0025 CALL WTOIO(IORWD,2,1,0,ISW(1),0,DSW)
0026 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6002
0027 IF(NEIFL.FE.1)GO TO 1204
0028 DO 1205 I=1,NFTLE-1
0029 CALL GETADR(MT0,NRE(301))
0030 CALL WTOIO(IUPLR,2,1,0,ISW(1),MT0,DSW)
0031 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0032 MT0(1)=
0033 CALL WTOIO(IUSPF,2,1,0,ISW(1),MT0,DSW)
0034 DO 1210 J=1,NSKIP
0035 IF(CINLS.FO.O)GO TO 1204
C>>>>>>>DISK SEARCH
0036 DO 1207 I=1,16
0037 READ(2,FND=1202,ERR=6001)(ICARD(JJ),JJ=1,64)
0038 K=64*(I-1)+300
0039 DO 1207 JJ=1,64
0040 NRE(K+JJ)=ICARD(JJ)
0041 GO TO 1210
C>>>>>>>TAPE SEARCH
0042 CALL GETADR(MT0,NRE(301))
0043 CALL WTOIO(IORH,2,1,0,ISW(1),MT0,DSW)
0044 IF(IUSUC.FO.IAND(MASK,ISW(1)))GO TO 1210
0045 IF(IAND(IFVPR,MASK).FO.IAND(MASK,ISW(1)))GO TO 6000
0046 IF(IAND(IFEOF,MASK).NE.IAND(MASK,ISW(1)))GO TO 1210
0047 NEND=1
0048 GO TO 6000
0049 CONTINUE
004H 1210

```

```

0049  I:ST=IST*100
0050  I:ST=IST-NTHPS
0051  RETURN
0052  TYPE 100
0053  GO TO 6010
0054  TYPE 101
0055  GO TO 6010
0056  TYPE 102
0057  NFORM=1
0058  RETURN
0059  100  FORMAT(IX, '*** INPUT FILE "NSKIP" FAILURE! ***')
0060  101  FORMAT(IX, '*** INPUT FILE "NFILF" FAILURE! ***')
0061  102  FORMAT(IX, '*** MT: REWIND FAILURE! ***')
0062  END)
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000712	229
SPDATA	000014	6
STDATA	000246	R3
SVARS	000210	6R
STEMPS	000004	2
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	264R
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016266 3675

NO FPP INSTRUCTIONS GENERATED

OPT6.LP/1:1=OPT6/MUTR

```

0001 SUBROUTINE OPT7
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),ROUT(300)
0005 COMMON/MTAPE1/MSKIP,IST,NTOT1,NTOPS,NTOTO
0006 COMMON/MTAPE2/WEND,NEERR,NEILY,NIHS,NOUITS
0007 COMMON/MTAPE3/NHF(1374),NHUF(1324)
0008 COMMON/MTAPE4/LST,INFG
0009 COMMON/MTAPE5/MASK,LSW(2),IOATT,IOSUC,IFAIN,IOHWD
0010 COMMON/MTAPE6/APPEND
0011 DIMENSION ICARD(64)
0012 DATA IOATT,IOSUC,IFAIN/0001400,1,-34/
0013 DATA IOHWD,IOHWR,IEVER,IOSPF,IOHLR/02400,0400,-4,02440,01000
0014 DATA IOSPF,IEDEF,IOEUF/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/
  
```

```

C CLOSE INPUT FILE
C
0018 CONTINUE
0019 NFNED=0
0020 NFNED=0
0021 IF(NINS.EQ.1)CALL CLOSE(2)
0022 RETURN
0023 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODP1	000032	13 RW,I,CON,I,CL
SPDATA	000004	2 RW,D,CON,I,CL
SDATA	000004	2 RW,D,CON,I,CL
SVARS	000200	64 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000012	5 RW,D,OVR,GRI
MTAPE3	012260	2648 RW,D,OVR,GRI
MTAPE4	000004	2 RW,D,OVR,GRI
MTAPE5	000064	26 RW,D,OVR,GRI
MTAPE6	000002	1 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 015120 3368

NO FPP INSTRUCTIONS GENERATED

OPT7.LP/L:1=OPT7/NOTR





0049 101 FORMAT(13)  
0050 GO TO 14  
0051 155 TYPE 107  
0052 107 FORMAT(1HS,'INPUT FILE NAME= ' )  
0053 CALL FILEN(2,EXTN)  
0054 NFILE=1  
0055 14 RETURN  
0056 999 CALL EXIT  
0057 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000524	170 RW,I,CON,I,CL
SPDATA	000010	4 RW,D,CON,I,CL
SIDATA	000300	96 RW,D,CON,I,CL
SVARS	000214	70 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GRL
MTAPE1	000012	5 RW,D,OVR,GRL
MTAPE2	000012	5 RW,D,OVR,GRL
MTAPE3	012260	2648 RW,D,OVR,GRL
MTAPE4	000004	2 RW,D,OVR,GRL
MTAPE5	000064	26 RW,D,OVR,GRL
MTAPE6	000002	1 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 016126 3627

NO FPP INSTRUCTIONS GENERATED

```

0001      C      SUBROUTINE FILE(UNIT,EXT)
          C      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
          C      FROM THE TTY DEVICE 5
          C      DEFAULT DEVICE
          C      UNLESS SPECIFIED IN INPUT STRING
          C      UNIT=UNIT NUMBER
          C      EXT = LOGICAL*1 BUFFER OF EXTENSION
          C
0002      C      IMPLICIT INTEGER(A-Z)
0003      C      LOGICAL*1 INSTR,UNIT,RLNK,EXT
0004      C      DIMENSION INSTR(40)
0005      C      DIMENSION EXT(3)
0006      C      DATA RLNK,UNIT/ ' ', ' '
          C
          C      INPUT FILE
0007      C      AFAD (5,99,FND=999,FAP=152)(INSTR(I),I=1,40)
0008      C      FORMAT(40A1)
          C      CHECK FOR END OF LINE
0009      C      DO 1600 I=40,1,-1
0010      C      J=I
0011      C      IF(INSTR(I).NE.RLNK)GO TO 1601
0012      C      TYPE 151
0013      C      FORMAT(1HS,'>')
0014      C      GO TO 152
0015      C      DO 1602 I=1,J
0016      C      IF(INSTR(I).NE.RLNK) GO TO 1602
          C
          C
          C      BLANK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
          C
0017      C      DO 1603 K=1,J-1
0018      C      INSTR(K)=INSTR(K+1)
0019      C      INSTR(J)=RLNK
0020      C      J=J-1
0021      C      GO TO 1601
0022      C      CONTINUE
0023      C      DO 103 I=1,J
0024      C      IF(INSTR(I).EQ.DOT) GO TO 25
0025      C      INSTR(J+1)=DOT
0026      C      INSTR(J+2)=EXT(1)
0027      C      INSTR(J+3)=EXT(2)
0028      C      INSTR(J+4)=EXT(3)
0029      C      J=J+4
0030      C      CALL SCAN(INSTR,J)
0031      C      CALL ASSIGN(UNIT,INSTR,J)
0032      C      RETURN
0033      C      CALL EXIT
0034      C      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000472	157 RW,I,CUM,I,C,L
SIDATA	000044	14 RW,D,CUM,I,C,L

FURTRAN IV-PLUS V02-SIE  
OPTR.PFN /MM

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SVARS 000060 24 RM.D.COM,I,CL  
STEMPS 000004 2 RM.D.COM,I,CL

TOTAL SPACE ALLOCATED = 000622 201

NO FPP INSTRUCTIONS GENERATED

```

0001      SUBROUTINE SCAN(RUF,I,TH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL(4) RUF,DEVICE
0004      DIMENSION RUF(1),DEVICE(4)
0005      DATA DEVICE/'S','V','O','.'/
0006      DO 1 I=1,I,TH
0007      1   IF(RUF(I).EQ.DEVICE(4))RETURN
0008      LTR=LTR+4
0009      DO 2 I=LTR,5,-1
0010      2   RUF(I)=RUF(I-4)
0011      DO 3 I=1,4
0012      3   RUF(I)=DEVICE(I)
0013      RETURN
0014      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61 R, I, CON, I, CL
SIDATA	000012	5 R, D, CON, I, CL
SVARS	000006	3 R, D, CON, I, CL
STEMP'S	000002	1 R, D, CON, I, CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

OPTB.LP/LI:1=OPTB/MOTR

```

0001 SUBROUTINE OPIN
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL Q1,FATIN,FXTOT,APPEND
0004 COMMON/TAPE0/NIN(300),MOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NT0F1,NT0FS,NDUTD
0006 COMMON/TAPE2/NERD,NERF,NFILE,NIMS,NDUTS
0007 COMMON/TAPE3/NHR(1324),NRUF(1324)
0008 COMMON/TAPE4/IST,IREG
0009 COMMON/TAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IURWD
0010 I,IURB,IFVER,IUSPF,IURCH,*TO(6),MT1(6),DSW
0011 COMMON/TAPE6/APPEND
0012 DIMENSION FATIN(3),EXTOUT(3)
0013 DATA YES,NO/YES,'N'/
0014 DATA FATIN/1,'N','P'/
0015 DATA EXTOUT/0,'U','T'/
0016 DATA NERD,NERF,NFILE/0,0,0/
0017 DATA NSKIP,IST/1,1/
0018 DATA IOATT,IOSUC,IFALN/001400,1,-34/
0019 DATA IURWD,IURB,IFVER,IUSPF,IURCH/02400,0400,-4,02440,U1000
0020 DATA IOSPF,IFEDE,IURUF/02440,-10,03000/
0021 DATA MASK/0377/
0022 DATA MT1/0,2048,0,0,0,0/
0023 DATA MT1/0,2048,0,0,0,0/
C
C GET FILE INFO(=R)
C
0024 APPEND=.FALSE.
C900 TYPE 100
C100 FORMAT(/,1HS,' IS THE INPUT ON MAG. TAPE.(Y/N)? ')
C READ(5,102,FMD=999,FRR=90)ANSER
ANSER=NO
0025 FORMAT(A1)
NINSE1
IF(ANSER.EQ.NO)NIMS=1
NDUTS=1
C901 TYPE 103
C103 FORMAT(1HS,' IS THE OUTPUT GOING TO MAG TAPE.(Y/N)? ')
C READ(5,102,FMD=999,FRR=90)ANSER
ANSER=NO
0030 IF(ANSER.EQ.NO)NDUTS=1
0031 IF(NDUTS.EQ.1)GO TO 5
0032 TYPE 104
0033 FORMAT(1HS,' APPEND DATA? ')
0034 READ (5,102,FMD=999,FRR=90)ANSER
0035 IF(ANSER.EQ.YES)APPEND=.TRUE.
0036 IF(NDUTS.EQ.0)GO TO 151
0037 C
C KSK11 SUPPORTED FILE
C903 TYPE 105
C105 FORMAT(1HS,' OUTPUT FILE NAME= ')
C CALL FILEM(3,FXTOT)
CALL ASSIGN(3,'ML: ')
C
C BEGINNING OF INPUT
C
0039 IF(NIMS.EQ.1)GO TO 155

```

```

0040 004  IFF 106
0041 106  FORMAT(1HS,'MI FILE GO.=(13) ')
0042 0042 READ(5,10),FMS=000,FFF=004)NFILF
0043 101  FORMAT(13)
0044 0044 GO TO 14
0045 155  CONTINUE
      C155  IFF 107
      C107  FORMAT(1HS,'INPUT FILE NAME= ')
      C      CALL FILEN(2,EXTIN)
0046 0046 CALL ASSIGN(2,'15,100)WHICFA-MIN',17)
0047 0047 NFILF=1
0048 0048 RETURN
0049 0049 CALL EXIT
0050 0050 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC001	00030	108 R, I, COM, ICL
SPDATA	00042	17 P, D, COM, ICL
SIDATA	00100	32 P, D, COM, ICL
SVARS	00214	70 R, D, COM, ICL
MTAPE0	00280	600 R, D, OVR, GRU
MTAPE1	00012	5 R, D, OVR, GRU
MTAPE2	00012	5 R, D, OVR, GRU
MTAPE3	01280	2648 R, D, OVR, GRU
MTAPE4	00004	2 R, D, OVR, GRU
MTAPE5	00004	2 R, D, OVR, GRU
MTAPE6	00002	1 R, D, OVR, GRU

TOTAL SPACE ALLOCATED = 015564 3514

NO FPP INSTRUCTIONS GENERATED

```

0001 C SUBROUTINE FILE(UNIT,FXT)
    C THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
    C FROM THE TTY DEVICE 5
    C DEFAULT DEVICE
    C UNLESS SPECIFIED IN INPUT STRING
    C UNIT=UNIT NUMBER
    C FXT = LOGICAL+1 BUFFER OF EXTENSION
  
```

```

0002 C IMPLICIT INTEGER(A-Z)
0003 C LOGICAL+1 INSTR,DOT,MLNK,FXT
0004 C DIMENSION INSTR(40)
0005 C DIMENSION EXT(3)
0006 C DATA MLNK,DOT/ ' ','.'/
  
```

```

0007 C INPUT FILE
0008 152 READ (5,99,FND=999,FNF=152)(INSTR(I),I=1,40)
0009 99 FORMAT(40A1)
0010 C CHECK FOR END OF LINE
0011 DO 1600 I=40,1,-1
0012 J=1
0013 IF(INSTR(I).NE.MLNK)GO TO 1601
0014 FVF=151
0015 FORMAT(1HS,'>')
0016 GO TO 152
0017 DO 1602 I=1,J
0018 IF(INSTR(I).NE.MLNK) GO TO 1602
  
```

MARK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT

```

0019 C
0020 C
0021 C
0022 1600 1603 K=I,J-1
0023 INSTR(K)=INSTR(K+1)
0024 INSTR(J)=MLNK
0025 J=J-1
0026 CONTINUE
0027 DO 103 I=1,J
0028 IF(INSTR(I).EQ.DOT) GO TO 25
0029 INSTR(J+1)=DOT
0030 INSTR(J+3)=EXT(2)
0031 INSTR(J+4)=EXT(3)
0032 J=J+4
0033 CALL SCAN(INSTR,J)
0034 CALL ASSIGN(UNIT,INSTR,J)
0035 PFTDIN
0036 CALL EXIT
0037 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDP1	000477	157 P,1,COR,LOC
SIDATA	000044	18 R,0,COR,LOC

POMMAN IV-PLUS V02-51P  
DPTBA.FTK /06/MX

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SVAMS 000060 24 M,D,CUN,I,CL  
STEPS 000004 2 M,D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000622 201

NO PFP INSTRUCTIONS GENERATED



```
C
0001      SUPPORTIVE SCANCPUF,LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*8 HOF,DEVICE
0004      DIMENSION HOF(1),DEVICE(4)
0005      DATA DEVICE/5,5,5,5/
0006      DO 1 I=1,LTH
0007      IF (HOF(I).EQ.0)DEVICE(4)=HOF(I)
0008      LTH=LTH+4
0009
0010      DO 2 I=LTH,5,-1
0011      HOF(I)=HOF(I-4)
0012      DO 3 I=1,4
0013      HOF(I)=DEVICE(I)
0014      RETURN
      END
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61
SLDATA	000012	5
SWARS	000006	3
STEMPS	000002	1

TOTAL SPACE ALLOCATED = 000214 70

NO FOR INSTRUCTIONS GENERATED

UPTRA.LP/LI:=OPTIM/DEF/NOTR

```

C PRMAP.FTN ORIGINATED:29-MAY-79
C UPDATED:04-JUN-79
C THIS PROGRAM INTERPRETS ASCII TEXT FILES FOR THE MAP
C ASSEMBLER AND ALIGNS ALL FIELDS W/ SPACES AND REMOVES
C ALL TABS. ALL FILES ARE ASSUMED TO BE 120 COLUMNS WIDE.
C OUTPUT LINE FORMAT:
C COLUMNS 1,2...7 LABEL
C COLUMN 8 BLANK
C COLUMNS 9,10...55 SOURCE CODE
C COLUMN 56 BLANK
C COLUMNS 57,58...120 COMMENTS
C
C HYFF BLANK,TAB,HYPHEN,STAR,CR,DUMMY,SCOLON
C HYFF A(LAST
C HYFF A(200),L(200)
C LOGICAL*1 IN(3),OUT(3),OUTPUT
C DATA IN/'T','X','Y',/OUT/'M','A','P',/
C DATA BLANK,TAB,HYPHEN,STAR,CR,SCOLON/040,011,047,052,015,073/
C
C TYPE 100
C CALL FIFEM(2,IN)
C TYPE 101
C CALL FIFEM(3,OUT)
C LINF=0
C
C HEAD EACH SOURCE "LINE"
C
C LINF=LINF+1
C OUTPUT=.TRUE.
C HEAD(2,102,END=200,ERR=300)(A(1),I=1,120)
C
C "BLANK" OUT OUTPUT LINE
C
C DO 10 I=1,120
C I(I)=BLANK
C
C SPECIAL LINE OPERATORS
C
C IF(A(1).EQ.HYPHEN)GO TO 70
C IF(A(1).EQ.STAR)GO TO 70
C IF(A(1).EQ.SCOLON)GO TO 70
C LABEL FIELD
C
C I=1
C IF(A(1).EQ.TAB)GO TO 12
C I(I)=A(I)
C I=I+1
C IF(I.GT.120)GO TO 80
C GO TO 11
C NEXT=I+1
C
C COMMAND FIELD
C
C J=0
C I=I+1
  
```

FORTHAN IV-PLUS V02-51F /TRENDCONS/WH  
PREMAP.FTN

```

0029 21 IF(A(I),FO,TAH)GO TO 22
0030 L(9+J)=A(I)
0031 J=J+1
0032 IF(I).GT.111)GO TO 40
0033 I=I+1
0034 IF(I).GT.120)GO TO 80
0035 GO TO 21
0036 NEXT=I+1
22 COMMENT FIELD
C
C
30 K=0
0037 I=NEXT
0038 IF(A(I),FO,TAH)GO TO 80
0039 IF(A(LAST,FO,HLANK,AND,A(I),FO,HLANK)GO TO 80
0040 IF(A(I),FO,CR)GO TO 80
0041 ALAST=A(I)
0042 OUTPUT=.TRUE.
0043 L(57+K)=A(I)
0044 K=K+1
0045 IF(K).E.23)GO TO 32
0046 WRITE(3,106)(L(I),I=1,120)
0047 DO 33 I=1,120
0048 L(I)=HLANK
0049 L(I)=SCOLON
0050 K=0
0051 OUTPUT=.FALSE.
0052 ALAST=HLANK
0053 I=I+1
0054 IF(I).GT.120)GO TO 80
0055 GO TO 31
0056
C
C COPY LINE "EN TOTD"
C
70 DO 71 I=1,120
0057 L(I)=A(I)
0058
C
C GENERATE CORRECTED OUTPUT LINE
C
80 IF(OUTPUT,FO,.TRUE.)WRITE(3,106)(L(I),I=1,120)
0059 GO TO 1
0060
C
C NORMAL EXIT
C
200 CALL CLOSE(2)
0061 CALL CLOSE(3)
0062 TYPE 103,LINE
0063 STOP
0064
C
C ABNORMAL EXIT
C
300 CALL CLOSE(2)
0065 CALL CLOSE(3)
0066 TYPE 104,LINE
0067 GO TO 2
0068
C

```

```

C LINE STRUCTURE ERROR
C
0069 CALL CLOSE(2)
0070 CALL CLOSE(3)
0071 TYPE 105,LINE
0072 STOP
C
C FORMAT DECLARATIONS
C
0073 FORMAT(1HS,'INPUT FILENAME= ')
0074 FORMAT(1HS,'OUTPUT FILENAME= ')
0075 FORMAT(120A1)
0076 FORMAT(1X,'NORMAL EXIT AFTER ',13,' LINES!')
0077 FORMAT(1X,'***WARNING*** FILE ERROR IN LINE ',13/)
0078 FORMAT(1X,'***WARNING*** LINE STRUCTURE ERROR IN LINE ',13/)
0079 FORMAT(120A1)
0080 FORMAT(1X,1005)
0081 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001374	342 P,I,CON,I,CL
SPPATA	000010	4 P,I,CON,I,CL
SIDATA	000304	98 P,I,CON,I,CL
SVARS	000654	214 P,I,CON,I,CL

TOTAL SPACE ALLOCATED = 002564 698

NO FPP INSTRUCTIONS GENERATED

PREMAP.IP/I:1=PREMAP

```

0001 C SUPROUTINE FILEM(UNIT,EXT)
0002 C THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
0003 C FROM THE TTY DEVICE 5
0004 C DEFAULT DEVICE
0005 C UNLESS SPECIFIED IN INPUT STRING
0006 C UNIT=UNIT NUMBER
0007 C EXT = LOGICAL*1 BUFFER OF EXTENSION
0008 C
0009 C IMPLICIT INTEGER(A-Z)
0010 C LOGICAL*1 INSTR,DOT,BLANK,EXT
0011 C DIMENSION INSTR(40)
0012 C DIMENSION EXT(3)
0013 C DATA BLANK,DOT/ ' ','.'/
0014 C
0015 C INPUT FILE
0016 C READ (5,99)(INSTR(I),I=1,40)
0017 C FORMAT(40A1)
0018 C CHECK FOR END OF LINE
0019 C DO 1600 I=40,1,-1
0020 C J=I
0021 C IF(INSTR(I).NE.BLANK)GO TO 1601
0022 C TYPE 151
0023 C FORMAT(IHS,'>')
0024 C GO TO 152
0025 C DO 1602 I=1,J
0026 C IF(INSTR(I).NE.BLANK) GO TO 1602
0027 C
0028 C BLANK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
0029 C
0030 C DO 1603 K=I,J-1
0031 C INSTR(K)=INSTR(K+1)
0032 C INSTR(J)=BLANK
0033 C J=J-1
0034 C CONTINUE
0035 C GO TO 1601
0036 C DO 103 I=1,J
0037 C IF(INSTR(I).EQ.DOT) GO TO 25
0038 C INSTR(J+1)=DOT
0039 C INSTR(J+2)=EXT(1)
0040 C INSTR(J+3)=EXT(2)
0041 C INSTR(J+4)=EXT(3)
0042 C J=J+4
0043 C CALL SCAM(INSTR,J)
0044 C CALL ASSIGN(UNIT,INSTR,J)
0045 C RETURN
0046 C END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	000444	146 PW,I,CON,I,CL
SIDATA	000042	17 PW,D,CON,I,CL

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FORTRAN IV-PLUS V02-S1E  
FILEN.FTN /MP

SVARS 000060 24 RW,D,CON,I,CI,  
STMP'S 000004 2 HW,D,CON,I,CI,

TOTAL SPACE ALLOCATED = 000572 149

NO FPP INSTRUCTIONS GENERATED

```

C
0001 SUBROUTINE SCAN(RUF,LTH)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*4 RUF,DEVICE
0004 DIMENSION RUF(1),DEVICE(4)
0005 DATA DEVICE/'S','Y','O','.'/
0006 DO 1 I=1,LTH
0007 1 IF(RUF(I).EQ.DEVICE(4))RETURN
0008 LTH=LTH+4
0009 DO 2 I=LTH,5,-1
0010 2 RUF(I)=RUF(I-4)
0011 DO 3 I=1,4
0012 3 RUF(I)=DEVICE(I)
0013 RETURN
0014 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000172	61 RW,I,CON,LCL
SIDATA	000012	5 RW,D,CON,LCL
SVARS	000006	3 RW,D,CON,LCL
STEMPS	000002	1 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

FILEN.LP/II:1=FILEN/MOTR

END

DATE  
FILMED

12-80

DTIC





C SCAN DCT LIST FOR ALL SIGNIFICANT POINTS  
PRELIMINARY HIT ASSIGNMENT TO TAPI

0027 INSTR=1  
0028 STATUS=MPSCAN(DCFM2)  
0029 IF (STATUS.NE.0) GO TO 10

C RESULTS

0030 CALL MPRST(R0,CTFMP,1,CNVYES)  
0031 CALL MPRST(R2,RITSMH,1,CNVYES)  
0032 CALL MPRST(R3,XITOT,1,CNVYES)  
0033 ITOT=FIX(XITOT)  
0034 WRITE(6,930)RITSMH,CTFMP,ITOT  
0035 FORMAT(1X,'RITSE ',F15.8,' W/ CTFMP=',F15.8,' @ ITOT= ',I3)  
0036 CALL MPRDR(TMPI,XR(1),RYTES,CNVYES,XR(LTH))  
0037 WRITE(6,911)(I,IFIX(XR(I)),I=1,LTH)  
0038 FORMAT(1X,6(1X,'TRIT(',I3,')=',I3,2X))  
0039 RETURN

C DIAGNOSTIC TRAP AREA

0040 TYPE 100, INSTR, STATUS  
0041 CALL EXIT  
0042 FORMAT(1X, '\*\*\*MAP ERROR\*\*\* "SDCT" INSTR=',I3, ' HAS STATUS=',I6/  
0043 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	000344	114 RW, I, CON, LCL
SPDATA	000020	R RW, D, CON, LCL
SIDATA	000250	H4 RW, D, CON, LCL
SVARS	000064	26 RW, D, CON, LCL
MTAPE0	002260	600 RW, D, OVR, GHL
MTAPE1	000012	5 RW, D, OVR, GHL
MTAPE2	000012	5 RW, D, OVR, GHL
OVRT	002020	520 RW, D, OVR, GHL
OVR0	002006	515 RW, D, OVR, GHL
OVR6	006016	1543 RW, D, OVR, GHL
OVR4	000152	53 RW, D, OVR, GHL
OVRP	002022	521 RW, D, OVR, GHL
OVRP	005004	1282 RW, D, OVR, GHL
OVR2	001002	257 RW, D, OVR, GHL
OVR0	000016	7 RW, D, OVR, GHL
OVR0	000017	5 RW, D, OVR, GHL
MAPDEF	000200	64 RW, D, OVR, GHL

TOTAL SPACE ALLOCATED = 025722 5609

SDCT.I/P/LI:1:1=SDCT/DF/NOTR



```

C      ASSIGN BITS TO OCT ELEMENTS
C
C      ANALYSIS:MODUL=1,SYNTHESIS:MODUL=2
C      INSTR=1
D      IF(MODUL.EQ.1)STATUS=MPCDRA(MIRIT3)
D      IF(MODUL.EQ.2)STATUS=MPCDRA(MIRITI)
D      IF(STATUS.NE.0)GO TO 10
C
C      RESULTS
C
D      IF(MODUL.EQ.1)CALL MPRDR(MIRIT3,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
D      IF(MODUL.EQ.2)CALL MPRDR(MIRITI,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
D      IF(STATUS.NE.0)GO TO 10
D      WRITE(6,911)(I,IRIT(I),I=1,LTH)
D      FORMAT(1X,6(IX,'IRIT(',I3,')='',I3,2X))
D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
C      TYPE 100, INSTR, STATUS
D      CALL EXIT
D      FORMAT(IX, '***MAP ERROR*** "RITA" INSTR=',I3, ' HAS STATUS=',I6/)
D      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000334	110 RW,I,CON,LCU
STDATA	000160	56 RW,D,CON,LCU
SVARS	000054	72 RW,D,CON,LCU
SECTID	000002	1 RW,D,OVR,GRI
MTAPE0	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000012	5 RW,D,OVR,GRI
OVRT	002020	570 RW,D,OVR,GRI
OVR0	002006	515 RW,D,OVR,GRI
OVR1	006016	1543 RW,D,OVR,GRI
OVR2	000152	53 RW,D,OVR,GRI
OVR3	002022	521 RW,D,OVR,GRI
OVR4	005004	1282 RW,D,OVR,GRI
OVR5	001002	257 RW,D,OVR,GRI
OVR6	000016	7 RW,D,OVR,GRI
OVR7	000012	5 RW,D,OVR,GRI
MAPDEF	000200	64 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 025574 5566

NO FPP INSTRUCTIONS GENERATED

RITA,IP/LI:RITA/DE/NOTR





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ODCT.PTN /DK/MR

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TOTAL SPACE ALLOCATED = 031122 6491

NO PFP INSTRUCTIONS GENERATED

ODCT.LP/LI:1=ODCT/DK/NOTR





```

0027 D WRITE(6,699)ICOUNT
0028 D699 FORMAT(1H,40(1H>),'RECEIVE FRAME # ',I4,40(1H<))
C
C PASS DATA W/ NO ERRORS
C
0029 D INSTR=1
0030 D STATUS=MPWR(AQDCT,IAODCT(1),BYTE2,CNVYES,IAODCT(LTH))
0031 D IF(STATUS.NE.0)GO TO 10
0032 D INSTR=2
0033 D STATUS=MPWR(AQPR,IAOPR(1),BYTE2,CNVYES,IAOPR(LPARM+1))
0034 D IF(STATUS.NE.0)GO TO 10
0035 D WRITE(6,699)
0036 D699 FORMAT(/IX,*** MODEM OUTPUT ***)
0037 D WRITE(6,900)(IAOPR(I),I=1,LPARM)
0038 D FORMAT(/IX,6(1X,'AQPR',I2,')=' ,I6,2X))
0039 D WRITE(6,901)(IAODCT(I),I=1,LTH)
0040 D901 FORMAT(/IX,6(1X,'AQDCT',I3,')=' ,I6,2X))
C
C ADD TRANSMISSION DELAY
C
0041 C
0042 C INSTR=3
0043 D STATUS=MPWR(AODCT,IAODCT(1),BYTE2,CNVYES,IAODCT(LTH))
0044 D IF(STATUS.NE.0)GO TO 10
0045 D INSTR=4
0046 D STATUS=MPWR(AOPR,IAOPR(1),BYTE2,CNVYES,IAOPR(LPARM+1))
0047 D IF(STATUS.NE.0)GO TO 10
0048 D WRITE(6,799)
0049 D799 FORMAT(/IX,*** MODEM INPUT ***)
0050 D WRITE(6,800)(IAOPR(I),I=1,LPARM)
0051 D FORMAT(/IX,6(1X,'AOPR',I2,')=' ,I6,2X))
0052 D WRITE(6,801)(IAODCT(I),I=1,LTH)
0053 D801 FORMAT(/IX,6(1X,'AODCT',I3,')=' ,I6,2X))
C
C DIAGNOSTIC TRAP AREA
C
0054 I0 TYPE 100,INSTR,STATUS
0055 CALL EXIT
0056 I00 FORMAT(1X,****MAP ERRORS*** "CHANL" INSTR=' ,I3, ' HAS STATUS=' ,I6/)
0057 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000644	710 RW,I,CON,LCU
\$IDATA	000452	149 RW,D,CON,LCU
\$VARS	000054	22 RW,D,CON,LCU
MTAPF0	002260	600 RW,D,OVR,GRU
MTAPF1	000012	5 RW,D,OVR,GRU
MTAPF2	000012	5 RW,D,OVR,GRU
CHANFL	001034	270 RW,D,OVR,GRU
OVRT	002020	520 RW,D,OVR,GRU
OVRD	002006	515 RW,D,OVR,GRU
OVRF	006016	1543 RW,D,OVR,GRU

FORTRAN IV-PLUS V02-S1E  
CHANL.PTN /DF/WR

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OVRA	000152	53	RM,D,OVR,CRI
OVRP	002022	521	RM,D,OVR,CRI
OVRA	005004	1282	RM,D,OVR,CRI
OVR2	001002	257	RM,D,OVR,CRI
OVR1	000016	7	RM,D,OVR,CRI
OVRD	000012	5	RM,D,OVR,CRI
HAPDEF	000200	64	RM,D,OVR,CRI

TOTAL SPACE ALLOCATED = 027430 6028

NO FPP INSTRUCTIONS GENERATED

CHANL.I.P/LI:1=CHANL/DF/NOTR



```

0028      DATA RUS1,RUS2,RUS3/64,128,192/
C
C      SIDHAND DEQUANTIZATION LAGS SIDHAND DESERIALIZED
C      BY 1 FRAME DUE TO CSPU IN BACKGROUND
C
0029      INSTR=1
0030      STATUS=MMOV2(ORPRM)
0031      IF(STATUS.NF.0)GO TO 10
C
C      DEQUANTIZE PARCOR(J),J=1,...,LPCN,G,M,VUV
C
0032      INSTR=2
0033      STATUS=MPDRP(PARC,ORPRM,A1)
0034      IF(STATUS.NF.0)GO TO 10
C
C      RESULTS
C
0035      CALL MPRDH(ORPRM,IRIT(1),BYTE2,CNVYES,IRIT(LPARM+1))
0036      WRITE(6,927)(J,IRIT(J),J=1,LPARM)
0037      FORMAT(/IX,4(IX,'ORPRM',I2,' '),I6,2X))
0038      CALL MPRDH(PARC,DRPAR(1),BYTE4,CNVYES,DRPAR(LPCN))
0039      WRITE(6,928)(J,DRPAR(J),J=1,LPCN)
0040      FORMAT(/IX,4(IX,'DRK',I2,' '),E15.8,2X))
0041      CALL MPRST(RR,DRDC,1,CNVYES)
0042      CALL MPRST(R9,DRVAR,1,CNVYES)
0043      CALL MPRST(90,DRG,1,CNVYES)
0044      CALL MPRST(91,DRM,1,CNVYES)
0045      CALL MPRST(104,DRV,1,CNVYES)
0046      WRITE(6,925)DRDC,DRVAR
0047      FORMAT(IX,'DRDC(RR)=' ,F15.8,' AND DRVAR(89)=' ,E15.8)
0048      WRITE(6,929)DRM,DRG,DRV
0049      FORMAT(/IX,'DRM=' ,F15.8,' , DRG=' ,F15.8,' AND DRV=' ,E15.8)
0050      CALL MPRDH(A1,DRA(1),BYTE4,CNVYES,DRA(LPCN))
0051      WRITE(6,931)(J,DRA(J),J=1,LPCN)
0052      FORMAT(/IX,4(IX,'DRA',I2,' '),E15.8,2X))
0053      CALL MPRST(60,ENG,1,CNVYES)
0054      WRITE(6,932)ENG
0055      FORMAT(IX,'ENG=' ,F15.8)
0056      CALL MPRDH(ORDCTM,ORDCT(1),BYTE2,CNVYES,ORDCT(LTH))
0057      WRITE(6,940)(I,ORDCT(I),I=1,LTH)
0058      FORMAT(/IX,4(IX,'ORDCTM',I3,' '),I6,2X))
0059      CALL MPRDH(MRIT2,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0060      WRITE(6,941)(I,IRIT(I),I=1,LTH)
0061      FORMAT(/IX,4(IX,'MRIT2',I3,' '),I6,2X))
0062      CALL MPRDH(IORDR2,IORDR(1),BYTE2,CNVYES,IORDR(LTH))
0063      WRITE(6,942)(I,IORDR(I),I=1,LTH)
0064      FORMAT(/IX,4(IX,'IORDR2',I3,' '),I6,2X))
0065      CALL MPRDH(DCTI2,DCTI(1),BYTE4,CNVYES,DCTI(LTH))
0066      WRITE(6,943)(I,DCTI(I),I=1,LTH)
0067      FORMAT(/IX,4(IX,'DCTI',I3,' '),E15.8,2X))
0068      CALL MPRST(100,DRDC,1,CNVYES)
0069      CALL MPRST(101,DRVAR,1,CNVYES)
0070      WRITE(6,944)DRDC,DRVAR
0071      FORMAT(IX,'DRDC(100)=' ,F15.8,' AND VAR(101)=' ,E15.8)
0072      RETURN
C

```

C DIAGNOSTIC TRAP AREA  
 C  
 0073 10 TYPE 100, INSTR, STATUS  
 0074 CALL, EXIT  
 0075 100 FORMAT(IX, '\*\*\*MAP ERROR\*\*\* 'DPARM' INSTR='14, ' HAS STATUS=', 16//)  
 0076 FMD

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001542	433 RW, I, CON, I, CL
SPDATA	000044	14 RW, D, CON, I, CL
SIDATA	001036	271 RW, D, CON, I, CL
SVARS	000056	23 RW, D, CON, I, CL
MTAPE0	002260	600 RW, D, OVR, GHL
MTAPE1	000012	5 RW, D, OVR, GHL
MTAPE2	000012	5 RW, D, OVR, GHL
OVRT	002020	520 RW, D, OVR, GHL
OVR0	002006	515 RW, D, OVR, GHL
OVR6	006016	1543 RW, D, OVR, GHL
OVR4	000152	53 RW, D, OVR, GHL
OVRP	002022	521 RW, D, OVR, GHL
OVR8	005004	1282 RW, D, OVR, GHL
OVR2	001002	257 RW, D, OVR, GHL
OVRU	000016	7 RW, D, OVR, GHL
OVRD	000012	5 RW, D, OVR, GHL
OVRDT	002174	554 RW, D, OVR, GHL
OVR0T	001032	260 RW, D, OVR, GHL
MAPDEF	000200	64 RW, D, OVR, GHL

TOTAL SPACE ALLOCATED = 033102 6945  
 DPARM, LP/LI:1=DPARM/DE/WOIR



```

C      SORT TMP2 TO DCTM2
C      SORT DCTM1 TO TMP4
C
D      INSTR=1
D      STATUS=MPSSRT(IORDRM)
D      IF(STATUS.NE.0)GO TO 10
C
C      RESULTS
C
D      CALL MPRDR(DCTM2,DCT2(1),HYTE4,CNVYES,DCT2(LTH))
D      CALL MPRDR(IORDRM,IORDR(1),HYTE2,CNVYES,IORDR(LTH))
D      WRITE(6,910)(I,IORDR(I),I=1,LTH)
D      FORMAT(/1X,6(1X,'IORDR(',I3,')=',I3,2X))
D      WRITE(6,909)(I,DCT2(I),I=1,LTH)
D      FORMAT(/1X,4(1X,'DCT2(',I3,')=',E15.8,2X))
D      CALL MPRDR(TMP4,DCT1(1),HYTE4,CNVYES,DCT1(LTH))
D      WRITE(6,911)(I,DCT1(I),I=1,LTH)
D      FORMAT(/1X,4(1X,'DCT1(',I3,')=',E15.8,2X))
D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
I0     TYPE 100,INSTR,STATUS
I0     CALL EXIT
I00    FORMAT(1X,'***MAP ERROR*** "SSRT" INSTR=',I3,' HAS STATUS=',I6/)
I000   END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCOFF1	000470	156 RW,I,CON,I,CL
SDATA	000256	87 RW,D,CON,I,CL
SVARS	000054	72 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
OVRT	002020	570 RW,D,OVR,GHL
OVR0	002006	515 RW,D,OVR,GHL
OVRF	006016	1543 RW,D,OVR,GHL
OVRG	000152	51 RW,D,OVR,GHL
OVRP	002022	521 RW,D,OVR,GHL
OVRP	005004	1282 RW,D,OVR,GHL
OVR2	001007	757 RW,D,OVR,GHL
OVRU	000016	7 RW,D,OVR,GHL
OVRD	000012	5 RW,D,OVR,GHL
MAPDEF	000200	64 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 026024 5642

NO PDP INSTRUCTIONS GENERATED

SSRT,LP/LI:1=SSRT/DE/NUH





DEQUANTIZATION OF MAINRAND LAGS MAINRAND DESERIALIZED  
BY 1 FRAME DIFF TO CPU IN THE BACKGROUND

INSTR=1  
GIVE CPU HIT ASSIGNMENT FOR UNPACKING OF MAINRAND  
STATUS=MM(VVCHIT)  
IF (STATUS.NE.0) GO TO 10

DEQUANTIZE DCT(I)

INSTR=2

STATUS=MPFST(DRDCTM,ORDCTM,TMP1,IODR2)  
IF (STATUS.NE.0) GO TO 10

RESULTS

CALL MPHDR(DRDCTM,DTDCT(I),BYTE4,CNVYES,DTDCT(LTH))  
WRITE(6,934)(I,DTDCT(I),I=1,LTH)  
FORMAT(1X,4(1X,'DRDCTM(',I3,')='),F15.8,2X))  
CALL MPHDR(WRIT,IRIT(I),BYTE2,CNVYES,IRIT(LTH))  
WRITE(6,935)(I,IRIT(I),I=1,LTH)  
FORMAT(1X,4(1X,'RIRIT(',I3,')='),I6,2X))  
CALL MPHDR(DCTI,DCTI(I),BYTE4,CNVYES,DCTI(LTH))  
WRITE(6,936)(I,DCTI(I),I=1,LTH)  
FORMAT(1X,4(1X,'DCTI(',I3,')='),F15.8,2X))  
CALL MPHDR(IODR1,IODR(I),BYTE2,CNVYES,IODR(LTH))  
WRITE(6,937)(I,IODR(I),I=1,LTH)  
FORMAT(1X,4(1X,'IODR1(',I3,')='),I6,2X))  
CALL MPHDR(WRIT,IRIT(I),BYTE2,CNVYES,IRIT(LTH))  
WRITE(6,938)(I,IRIT(I),I=1,LTH)  
FORMAT(1X,4(1X,'WIRIT(',I3,')='),I6,2X))  
CALL MPKST(102,DRDC,I,CNVYES)  
CALL MPKST(103,DRVAR,I,CNVYES)  
WRITE(6,940)DRDC,DRVAR  
FORMAT(1X,'DRDC(102)='),F15.8,2X,' AND DRVAR(103)='),F15.8)  
RETURN

DIAGNOSTIC TRAP AREA

TYPE 100, INSTR, STATUS  
CALL EXIT  
FORMAT(1X,'\*\*\*MAP ERROR\*\*\* "DDCT" INSTR=',I4,' HAS STATUS=',I6/)  
END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	001056	270
SPDATA	000014	6
SIDATA	000506	163
SVARS	000064	26
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5

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DDCT.FTN /DE/WH

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OVRI	002020	520	RW,D,OVR,GRL
OVRO	002006	515	RW,D,OVR,GRL
OVRF	006016	1543	RW,D,OVR,GRL
OVRA	000152	53	RW,D,OVR,GRL
OVRR	002022	521	RW,D,OVR,GRL
OVRR	005004	1242	RW,D,OVR,GRL
OVRR	001002	257	RW,D,OVR,GRL
OVRL	000016	7	RW,D,OVR,GRL
OVRO	000012	5	RW,D,OVR,GRL
OVRO	002124	554	RW,D,OVR,GRL
MAPDF	000200	64	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 031012 6405

DDCT.LP/L1:1=DDCT/DE/NOTR



```

0026 C INSTR=1
0027 D STATUS=MPIDCM(X2,DNDCTM,CUSZ)
0028 D IF(STATUS.NE.0)GO TO 10
0029 D CALL MPRDH(X2,XR(1),RYTR,CNVVFS,XR(LTH2))
0030 D WRITE(6,914)(1,XR(1),I=1,LTH2)
0031 D INSTR=2
0032 D STATUS=EFTIN(X2,1,X7,VSHKT,WORK)
0033 D IF(STATUS.NE.0)GO TO 10
C
C RESULTS
C
0034 D CALL MPRDH(X2,XR(1),RYTR4,CNVVFS,XR(LTH))
0035 D WRITE(6,914)(1,XR(1),I=1,LTH)
0036 D914 FORMAT(1X,A(1X,'X',13,1)=' ',E15.8,2X))
0037 D RETURN
C
C DIAGNOSTIC TRAP AREA
C
0038 I0 TYPE 100,INSTR,STATUS
0039 CALL EXIT
0040 I00 FORMAT(1X,'***MAP ERROR*** "UCTR" INSTR=',13,' HAS STATUS=',16/)
0041 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCORE1	000406	131
SPDATA	000004	2
SINATA	000172	61
SVAPS	000054	22
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRT	002020	520
OVR0	002006	515
OVRF	006016	1543
OVR4	000152	53
OVRP	002022	521
OVR2	005004	1282
OVR2	001002	257
OVR4	000016	7
OVRD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025662 5593  
 UCTR.I.P/I.1:1=DCTP/DF/NOTR



```

0027 STATUS=MPVDNM(NOUTM,YOM,X2)
0028 IF (STATUS.NE.0)GO TO 10
C
C RESULTS
C
0029 CALL MPRST(100,DCRIAS,1,CNVYFS)
0030 WRITE(6,90)DCRIAS
0031 FORMAT(1X,'DCRIAS=',F15.8)
0032 CALL MPRST(101,VAR,1,CNVYFS)
0033 WRITE(6,90)VAR
0034 FORMAT(1X,'VAR=',F15.8)
0035 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0036 IO TYPE 101, INSTR, STATUS
0037 CALL EXIT
0038 FORMAT(1X,'***MAP ERROR*** VARDC INSTR=',I3,' HAS STATUS=',I6/)
0039 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000200	64 RW,I,CON,LCB
SPDATA	000014	4 RW,D,CON,LCB
SIDATA	000160	56 RW,D,CON,LCB
SVARS	000052	21 RW,D,CON,LCB
MTAPF0	002260	600 RW,D,OVR,GAL
MTAPE1	000012	5 RW,D,OVR,GAL
MTAPE2	000012	5 RW,D,OVR,GAL
OVRT	002024	520 RW,D,OVR,GAL
OVR0	002006	515 RW,D,OVR,GAL
OVR1	006016	1543 RW,D,OVR,GAL
OVR2	000152	51 RW,D,OVR,GAL
OVR3	002022	521 RW,D,OVR,GAL
OVR4	005004	1282 RW,D,OVR,GAL
OVR5	001002	257 RW,D,OVR,GAL
OVR6	000016	7 RW,D,OVR,GAL
OVR7	000012	5 RW,D,OVR,GAL
MAPDEF	000200	64 RW,D,OVR,GAL

TOTAL SPACE ALLOCATED = 025450 5524

NO FPP INSTRUCTIONS GENERATED

VARDC.LP/LI:1=VARDC/DE/MOTR



```

C
C CHECK FOR REQUEST FROM "INPUT"
0026 IF (NEND.NE.0)GO TO 5000
0027 INSTR=1
0028 STATUS=MP&E(1,1)
0029 STATUS=MP&R(MOUTH,MOU(1),RYE2,CNVYFS,MOU(NTUTO))
0030 IF (STATUS.NE.0)GO TO 10
0031 *ITP&G(1)(1,MOU(1),I=1,NTUTO)
0032 *FORMAT(1X,4(1X,'MOU(1,13,')=',16,2X))
C
C DATA OUTPUT
0033 CALL TAPE2(2)
0034 RETURN
C
C FOR ON INPUT,WRITE FDP
C
0035 ICOUNT=ICOUNT-1
0036 DO 5001 I=1,NTUPS
0037 MOU(I)=0
0038 DO 5002 I=1,32
0039 CALL TAPE2(2)
0040 *SKIP=NSKIPS
0041 *NEND=0
0042 *IST=1
0043 CALL TAPE2(6)
0044 DO 5005 I=1,ICOUNT
0045 CALL TAPE2(1)
0046 IF (NEND)5077,5076,5077
0047 DO 5006 J=1,NTUPS
0048 MOU(J)=NIR(J)
0049 CALL TAPE2(2)
0050 DO 5007 I=1,NTUPS
0051 *MOU(I)=0
0052 DO 5008 I=1,N
0053 CALL TAPE2(2)
0054 CALL TAPE2(5)
0055 CALL MPCLS(0)
C
C TYPE 100,ICOUNT
0056 *FORMAT(1X,'ATC' DONE AFTER ',16,' FRAMES!')
C
C TYPE 100
0057 *FORMAT(1X,'ATC SYSTEM READY')
C
C CALL EXIT
C
C RETURN
C
C DIAGNOSTIC TRAP AREA
C
0058 DO
0059 CALL EXIT
0060 *FORMAT(1X,'***HAP ERROR*** "OUTPUT" INSTEP=',13,' HAS STATUS=',16/)
0061 END

```

PROGRAM SECTIONS

NAME SIZE ATTRIBUTES



SCDDP1	000466	155	RW,I,CON,LCL
SPDATA	000020	8	RW,D,CON,LCL
SIDATA	000132	45	RW,D,CON,LCL
SWARS	000056	23	RW,D,CON,LCL
STEPMS	000002	1	RW,D,CON,LCL
TEAPF0	002260	600	RW,D,CON,LCL
TEAPF1	000012	5	RW,D,CON,LCL
TEAPF2	000012	5	RW,D,CON,LCL
OVRO	002020	520	RW,D,CON,LCL
OVRF	002006	515	RW,D,CON,LCL
OVRA	000152	1543	RW,D,CON,LCL
OVRR	002016	519	RW,D,CON,LCL
OVRS	005004	1282	RW,D,CON,LCL
OVRT	001002	257	RW,D,CON,LCL
OVRO	000016	7	RW,D,CON,LCL
OVRO	000012	5	RW,D,CON,LCL
MAPDFF	000200	64	RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 025716 5607

NO FPP INSTRUCTIONS GENERATED

OUTPUT,LP/1:1=OUTPUT/DE/NOTR



FORTRAN IV-PLUS 002-SIF 21:16:10 11-SEP-80

DATA LONG,SHORT,CNVYFS,CNVAVZ(0,1,1,0/  
DATA POS1,POS2,POS3/64,128,192

ADD 3 FRAMES DELAY IN INPUT FOR SMP CALCULATION

STATUS=PPDR(CSN,NIND(1),WTF2,CVYFS,NIND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
STATUS=PPDR(CSN,IND(1),WTF2,CVYFS,IND(NTUPS))  
IF(STATUS.NE.0)GO TO 10  
DO W=1,3  
W1=W-1  
W2=W  
W3=W  
IV4=IV3  
IV5=IV2  
IV2=IV1  
IV3=IVX(IV)

COMPUTE S/N RATIO

SUM1=0.0  
SUM2=0.0  
DO 3605 I=1,NIUTO-NP  
SOURCE=FOAT(NTR3(I))  
NOISE=SIGNAL-SOURCE  
SUM1=SUM1+SOURCE\*\*2  
SUM2=SUM2+NOISE\*\*2  
SN=0.0  
FRNUM=ICOUNT-START+1  
IF(FRNUM.LT.4)GO TO 200  
SN=10.0\*ALOG10(SUM1/SUM2)  
IF(FRNUM.EQ.4)CSN=SN  
CSN=(FRNUM-1)/FRNUM)\*CSN(1./FRNUM)\*\*SN  
\*RTF(3,255)ICOUNT,SN,CSN,M4,IV4  
\*RTF(5,255)ICOUNT,SN,CSN,M4,IV4  
FORMAT(IX,'FRAME=',I3,XX,'SN=',F6.2,XX,'CSN=',F6.2,3A  
'P',PITCH='I3,'X' VUV='I12)  
CONTINUE  
RETURN

DIAGNOSTIC TRAP AREA

TYPE 100,INSTR,STATUS

CALL EXIT

FORMAT(IX,'\*\*\*MAP ERROR\*\*\* \*SNR\* INSTR=',I3,' HAS STATUS=',I6,/  
END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000720	732
SDATA	000162	57
SVARS	000110	36
STEMPS	000002	1
*TAPE0	002260	600
*TAPF1	000012	5
*TAPF2	000012	5
DELAY	000020	H
OVR1	002020	520
OVR0	002006	515
OVRF	006016	1543
OVR4	000152	53
OVRP	002022	521
OVRB	005004	1282
OVR2	001002	257
OVRT	000016	7
OVRD	000012	5
*APDEF	000200	64

TOTAL SPACE ALLOCATED = 026236 5711

SNR,LP/UT:1=SNR/DF/NOTR

```

0001 SUBROUTINE TAPE2(IID)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 APPEND
0004 COMMON/MTAPE0/NIN(300),MOUT(300)
0005 COMMON/MTAPE1/MSKIP,IST,NIOTI,NTUPS,MTOTO
0006 COMMON/MTAPE2/NEPD,NFPP,NFILE,NINS,MOUTS
0007 COMMON/MTAPE3/NRF(1324),NRUF(1324)
0008 COMMON/MTAPE4/LST,IRFC
0009 COMMON/MTAPE5/MASK,ISM(2),IDATT,IUSUC,IFALN,IORWD
0010 I,IOWLR,IEVER,IOSPF,IFFOF,IOFLA,MT0(6),MT1(6),OSW
0011 COMMON/MTAPE6/APPEND
0012 DATA IORWD,IOWLR,IEVER,IOSPF,IOWLR/02400,0400,-4,02440,01000
0013 DATA IOSPF,IEFOF,IOFLA,-10,03000/
0014 DATA MASK/0377/
0015 DATA MT0/0,2048,0,0,0,0/
0016 DATA MT1/0,2048,0,0,0,0/
0017 GO TO (700,800,900,1100,1000,1200,1300,1400),IQ
0018 CALL OPT1
0019 RETURN
0020 CALL OPT2
0021 RETURN
0022 CALL OPT3
0023 RETURN
0024 CALL OPT4
0025 RETURN
0026 CALL OPT5
0027 RETURN
0028 CALL OPT6
0029 RETURN
0030 CALL OPT7
0031 RETURN
0032 CALL OPT8
0033 CALL OPT3
0034 IF (APPEND) CALL OPT4
0035 RETURN
0036 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000226	75 RW,I,CON,I,CI.
SPDATA	000072	9 RW,D,CON,I,CI.
SIDATA	000002	1 RW,D,CON,I,CI.
MTAPE0	002260	600 RW,D,OVR,GMI.
MTAPE1	000012	5 RW,D,OVR,GMI.
MTAPE2	000012	5 RW,D,OVR,GMI.
MTAPE3	017260	2648 RW,D,OVR,GMI.
MTAPE4	000004	7 RW,D,OVR,GMI.
MTAPE5	000064	26 RW,D,OVR,GMI.
MTAPE6	000002	1 RW,D,OVR,GMI.

FURTHAN IV-PLUS V02-51F  
TAPE2.FTN /MP

11:41:24

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NO PFP INSTRUCTIONS GENERATED

TAPE2.I/P/LI:1=TAPE2/N0TH

```
0001 SUBROUTINE OPTI
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,MTOTI,NTUPS,MTOTU
0006 COMMON/MTAPE2/MEND,NFR,NFILE,MINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NRF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISW(2),IDATT,IOSUC,IEAIN,IORWD
0010 I,IUMH,IEVFR,IOSPE,IEFDF,IOREL,MT0(6),MTI(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IDATT,IOSUC,IEAIN/0001400,1,-34/
0014 DATA IORWD,IUMH,IEVFR,IOSPE,IOREL/02400,0400,-4,02440,01000
0015 DATA IOSPE,IEFDF,IOFDF/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MTI/0,2048,0,0,0,0/
C
C INPUT DATA(=1)
C
0018 CONTINUE
0019 ISTE=IST+NTUPS
0020 IF(1324.GE.IST) GO TO 200
0021 ISTE=IST-1024
0022 NSKIP=NSKIP+1
0023 KOVER=IST+NTOTI-1325
0024 IF(KOVER.GT.0) GO TO 305
0025 DO 5000 I=1,NTOTY
0026 WIN(I)=NRF(LST+I-1)
0027 ISTE=IST+NTUPS
0028 GO TO 6002
0029 IPEP=IST-1024
0030 DO 5001 I=1,300
0031 NRF(IPEP+I-1)=NRF(LST+I-1)
0032 ISTE=IPEP
0033 IF(MINS.EQ.0) GO TO 2100
C>>>>>>>DISK INPUT
0034 DO 5010 I=1,16
0035 READ(7,END=2001,FRR=6000)(ICARD(J),J=1,64)
0036 K=64*(I-1)+300
0037 DO 5010 J=1,64
0038 NRF(K+J)=ICARD(J)
0039 IF(NFR.NE.0) GO TO 6000
0040 GO TO 200
0041 MEND=1
0042 GO TO 200
C>>>>>>>TAPE INPUT
0043 MEND=0
0044 NFR=0
0045 CALL GETARR(MT0,NRF(301))
0046 CALL MTOU(IORWD,2,1,0,ISW,MT0,DSW)
0047 IF(IOSUC.EQ.1)AND(MASK,ISW(1))GO TO 200
0048 IF(IAND(IEFDF,MASK).EQ.IAND(MASK,ISW(1)))MEND=1
0049 IF(IAND(IEVFR,MASK).EQ.IAND(MASK,ISW(1)))NFR=1
0050 GO TO 200
```

```

0051 6000 TYPE 6001
0052 6001 FORMAT(IX,1000*INPUT FILE ERROR***)/)
0053 MPRM=1
0054 RETURN
0055 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000646	211 RW,I,CON,I,CL
SPOATA	000014	6 RW,D,CON,I,CL
SIDATA	000062	25 RW,D,CON,I,CL
SVARS	000212	69 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHI
MTAPE1	000012	5 RW,D,OVR,GHI
MTAPE2	000012	5 RW,D,OVR,GHI
MTAPE3	012260	2648 RW,D,OVR,GHI
MTAPE4	000004	2 RW,D,OVR,GHI
MTAPE5	000064	26 RW,D,OVR,GHI
MTAPE6	000002	1 RW,D,OVR,GHI

TOTAL SPACE ALLOCATED = 016034 3598

NO PDP INSTRUCTIONS GENERATED

OPT1.6P/61:1=OPT1/NOTR





SCODE1	000432	141	RW,I,COM,I,CL
SPDATA	000014	6	RW,D,COM,I,CL
SIIDATA	000062	25	RW,D,COM,I,CL
SVANS	000212	69	RW,D,COM,I,CL
MTAPE0	002260	600	RW,D,OVR,GRU
MTAPE1	000012	5	RW,D,OVR,GRU
MTAPE2	000012	5	RW,D,OVR,GRU
MTAPE3	012260	2648	RW,D,OVR,GRU
MTAPE4	000004	2	RW,D,OVR,GRU
MTAPE5	000064	26	RW,D,OVR,GRU
MTAPE6	000002	1	RW,D,OVR,GRU

TOTAL SPACE ALLOCATED = 015620 352R

NO FPP INSTRUCTIONS GENERATED

OPT2.LP/I:1=OPT7/NOTR

```

0001 SUBROUTINE UPT3
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/TAPE0/NTM(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTOT1,NTUPS,NTOTO
0006 COMMON/TAPE2/NEND,NFRF,NFILE,NINS,NOUTS
0007 COMMON/TAPE3/NRF(1324),NHUF(1324)
0008 COMMON/TAPE4/IST,IRFC
0009 COMMON/TAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IORWD
0010 I,IUWR,IEVER,IOSPF,IEKOF,IORLH,MTO(6),MTI(6),DSW
0011 COMMON/TAPE6/APPEND
0012 DIMENSION ICAPD(64)
0013 DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0014 DATA IORWD,IUWR,IEVER,IOSPF,IORLH/02400,0400,-4,02440,01000
0015 DATA IOSPF,IEKOF,IEKOF/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MTO/0,2048,0,0,0,0/
0018 DATA MTI/0,2048,0,0,0,0/
C
C INITIALIZE(=3)
C
0018 CONTINUE
0019 NEND=0
0020 NFRF=0
0021 IF((NINS*NOUTS).GT.1)GO TO 901
0022 UNIT=0
0023 DO 902 LUN=2,3
0024 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 902
0025 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 902
C>>>>>>>MT: ATTACH
0026 CALL ASMLUN(LUN,MT',UNIT,DSW)
0027 IF(DSW.NE.1)GO TO 6000
0028 CALL MTOIOI(0ATT,LUN,1,0,ISW(1),0,DSW)
0029 IF(IOSUC.EQ.IAND(MASK,ISW(1)))GO TO 902
0030 IF(IAND(IEALN,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0031 UNIT=UNIT+1
0032 DO 903 LUN=2,3
0033 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 903
0034 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 903
C>>>>>>>MT: REWIND
0035 CALL MTOIOIORWD(LUN,1,0,ISW(1),0,DSW)
0036 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6001
0037 CONTINUE
0038 IF(NFILE.FO.0)GO TO 945
0039 IF(NINS.NE.0)GO TO 913
C>>>>>>>MT: FILE SKIP
0040 DO 907 I=1,NFILE-1
0041 CALL GETADR(MTO,NHF(301))
0042 CALL MTOIOIORLH(2,1,0,ISW(1),MTO,DSW)
0043 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6003
0044 MTO(I)=1
0045 CALL MTOIOIOSPF(2,1,0,ISW(1),MTO,DSW)
0046 DO 912 J=1,NSKIP
0047 IF(NINS.EQ.0)GO TO 910
C>>>>>>>DISK INPUT
0048 DO 914 I=1,16

```

```

0049 READ(2,FMD=905,ERR=6002)(ICARD(JJ),JU=1,64)
0050 K=64*(J-1)+300
0051 DO 914 JJ=1,64
0052 NMF(K*JJ)=ICARD(JJ)
0053 GO TO 912
C>>>>>TAPF INPUT
910 CALL GETADR(MTO,NMF(301))
911 CALL WTGIN(IORLH,2,1,0,ISW(1),MTO,USW)
912 IF(UUSUC.FQ.IAND(MASK,ISW(1)))GO TO 912
913 IF(IAND(TEVER,MASK).EQ.IAND(MASK,ISW(1)))GO TO 6002
914 IF(IAND(TEOFF,MASK).NE.IAND(MASK,ISW(1)))GO TO 912
905 NFNDE=1
906 CONTINUE
907 IREG=1
908 ISTE=IST+300
909 ISTE=IST-NTIOPS
910 RETURN
911 TYPE 100
912 GO TO 6010
913 TYPE 101
914 GO TO 6010
915 TYPE 102
916 GO TO 6010
917 TYPE 103
918 NFNPR=1
919 RETURN
904 100 FORMAT(1X,'*** MT: ATTACH FAILURE! ****')
905 101 FORMAT(1X,'*** MT: RWIND FAILURE! ****')
906 102 FORMAT(1X,'*** INPUT FILE "NSKIP" FAILURE! ****')
907 103 FORMAT(1X,'*** MT: "NFIL" FAILURE! ****')
908 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDPF1	001212	325 RW,1,CON,I,CL
SPDATA	000020	R RW,D,CON,I,CL
SIDATA	000332	109 RW,D,CON,I,CL
SVARS	000214	70 RW,D,CON,I,CL
STEMPS	000004	2 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000017	5 RW,D,OVR,GRI
MTAPE3	012260	2648 RW,D,OVR,GRI
MTAPE4	000004	2 RW,D,OVR,GRI
MTAPE5	000064	26 RW,D,OVR,GRI
MTAPE6	000007	1 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 016662 1801

NO FPP INSTRUCTIONS GENERATED

OPT3.GP/1:1=OPT3/NOTR

```

0001 SUBROUTINE OPT4
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL:1 APPEND
0004 COMMON/TAPE0/NINC(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTUT1,NTUPS,NTUTD
0006 COMMON/TAPE2/NEND,NERR,NFILE,NINS,NOUTS
0007 COMMON/TAPE3/NFC(1324),NRUF(1324)
0008 COMMON/TAPE4/IST,INFC
0009 COMMON/TAPES/MASK,ISM(2),I0ATT,I0SUC,IFATN,IORWD
0010 I,I0LH,I0VER,I0SPF,I0F0F,I0R1R,MT0(6),MT1(6),DSW
0011 COMMON/TAPF6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA I0ATT,I0SUC,IFATN/0001800,1,-34/
0014 DATA I0RWD,I0LH,I0VER,I0SPF,I0R1R/02400,0400,-4,02440,01000
0015 DATA I0SPF,I0F0F,I0F0F/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MT1/0,2048,0,0,0,0/
C
C OUTPUT FILE SKIP(=4)
C
0019 CONTINUE
0020 NERR=0
0021 NEND=0
0022 IREG=1
0023 CALL GETADR(MT1(1),NRUF(1))
0024 CALL MTOI(IOR1R,3,1,0,ISM(1),MT1,DSW)
0025 IF(I0SUC.EQ.IAND(MASK,ISM(1)))GO TO 1
0026 IF(I0RWD.IE0F,MASK).NF.IAND(MASK,ISM(1))GO TO 6000
0027 CALL GETADR(MT1(1),NRUF(1))
0028 CALL MTOI(IOR1R,3,1,0,ISM(1),MT1,DSW)
0029 IF(I0SUC.EQ.IAND(MASK,ISM(1)))GO TO 1
0030 IF(I0RWD.IE0F,MASK).NF.IAND(MASK,ISM(1))GO TO 6000
0031 MT1(1)=-1
0032 CALL MTOI(I0SPF,3,1,0,ISM(1),MT1,DSW)
0033 RETURN
0034 TYPE 100
0035 FORMAT(1X,1000 OUTPUT "APPEND" FAILURE! ***/)
0036 END
  
```

NAME	SIZE	ATTRIBUTES
SCOFF1	000274	RW,I,CUN,I,CL
SDATA	000114	RW,D,CUN,I,CL
SIDATA	000114	RW,D,CUN,I,CL
SVARS	000200	RW,D,CUN,I,CL
MTAPE0	002260	RW,D,OVR,CHI
MTAPE1	000012	RW,D,OVR,CHI
MTAPE2	000012	RW,D,OVR,CHI
MTAPE3	032260	RW,D,OVR,CHI
MTAPE4	000004	RW,D,OVR,CHI
MTAPE5	000004	RW,D,OVR,CHI

PROGRAM SECTIONS

FORTRAN IV-PLUS V02-S1F  
OPT4.PTN /WR

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NTAPE6 000002 1 RW.D.OVP.GMI.

TOTAL SPACE ALLOCATED = 015432 1469

NO FPP INSTRUCTIONS GENERATED

OPT4.IP/1.1:1=OPT4/NOTR

```

0001 SUBROUTINE DPT5
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*81 APPEND
0004 COMMON/MTAPE0/IN(300),NOUT(300)
0005 COMMON/MTAPE1/MSKIP,IST,NTOTI,NTUPS,MTOTO
0006 COMMON/MTAPE2/NEHD,NEFP,NEIF,NINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,IRFG
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IORWD
0010 I,IOVH,IVER,IOSPF,IFFDF,IOEUF,IOHLM,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IFALN/001400,1,-34/
0014 DATA IORWD,IOVH,IVER,IOSPF,IOHLM/02400,0400,-4,02440,01000
0015 DATA IOSPF,IFFDF,IOEUF,IOHLM,-10,03000/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/
C
C END-OF-FILE(=5)
C
0018 CONTINUE
0019 NFR=0
0020 NFD=0
0021 IF(NOUTS.EQ.0)GO TO 1001
C>>>>>>>DISK EOF
0022 CALL CLOSE(3)
0023 GO TO 1003
C>>>>>>>TAPE EOF
0024 DO 1002 I=1,2
0025 CALL MTOID(IDEUF,3,1,0,ISW(1))
0026 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0027 MT(I)=-1
0028 CALL MTOID(IOSPF,3,1,0,ISW(1),MT1,DSW)
0029 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0030 IRFG=1
0031 MFTDPN
0032 TYPE 100
0033 NFR=1
0034 MFTURN
0035 100 FORMAT(1X,'*** MT: EOF FAILURE! ***'/)
0036 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCORE1	000204	66 RW,I,CON,NCL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000076	31 RW,D,CON,NCL
SVARS	000202	65 RW,D,CON,NCL
MTAPE0	002760	600 RW,D,OVR,GEN
MTAPE1	000012	5 RW,D,OVR,GEN
MTAPE2	000012	5 RW,D,OVR,GEN
MTAPE3	012760	2648 RW,D,OVR,GEN

FORTRAN IV-PLUS V02-51P  
OPTS.FTN /MH

MTAPE4 000004 2 RW,D,OVR,GRI  
MTAPE5 000064 26 RW,D,OVR,GRI  
MTAPE6 000002 1 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 015376 3455

NO FPP INSTRUCTIONS GENERATED

OPTS.IP/LI:1=OPTS/NOTR





```

0049 I,ST=IST*100
0050 IST=IST-NTHPS
0051 RETURN
0052 TYPE 100
0053 GO TO 6010
0054 TYPE 101
0055 GO TO 6010
0056 TYPE 102
0057 NNN=1
0058 RETURN
0059 100 FORMAT(IX,'*** INPUT FILE "MSKIP" FAILURE! ***'/)
0060 101 FORMAT(IX,'*** INPUT FILE "NFILE" FAILURE! ***'/)
0061 102 FORMAT(IX,'*** MT: REWIND FAILURE! ***'/)
0062 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000712	229
SPDATA	000014	6
SIDATA	000246	R3
SVARS	000210	6R
STMPSP	000004	2
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	264R
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016266 3675

NO FPP INSTRUCTIONS GENERATED

OPT6.LP/LI:1=OPT6/MOTR

```

0001 SUBROUTINE OPT7
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),MOUT(300)
0005 COMMON/MTAPE1/MASKIP,LIST,NTOT1,NTUPS,NTOTO
0006 COMMON/MTAPE2/NEFD,NEFH,NEFLY,MINS,NDUTS
0007 COMMON/MTAPE3/NHF(1324),MHUF(1324)
0008 COMMON/MTAPE4/LST,INFG
0009 COMMON/MTAPE5/MASK,LSN(2),IDATT,IOSUC,IFAIN,IOHWD
0010 1,IOHFR,IEVER,IOSPF,IEFDF,IOEOP,IOHFR,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IFAIN/0001400,1,-34/
0014 DATA IOHWD,IOHFR,IEVER,IOSPF,IOHFR/02400,0400,-4,02440,01000 /
0015 DATA IOSPF,IEFDF,IOEOP/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
      DATA MT1/0,2048,0,0,0,0/
C
C CLOSE INPUT FILE
C
0018 1300 CONTINUE
0019 NEND=0
0020 NFRP=0
0021 IF(MINS.EQ.1)CALL CLOSE(2)
0022 RETURN
0023 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000012	13 RW,I,CON,LCL
SPDATA	000004	2 RW,D,CON,LCL
SIDATA	000004	2 RW,D,CON,LCL
SVARS	000200	64 RW,D,CON,LCL
MTAPE0	022260	600 RW,D,OVR,GHI
MTAPE1	000012	5 RW,D,OVR,GHI
MTAPE2	000012	5 RW,D,OVR,GHI
MTAPE3	012260	2648 RW,D,OVR,GHI
MTAPE4	000004	2 RW,D,OVR,GHI
MTAPE5	000064	26 RW,D,OVR,GHI
MTAPE6	000002	1 RW,D,OVR,GHI

TOTAL SPACE ALLOCATED = 015120 3368

NO FPP INSTRUCTIONS GENERATED

OPT7.LP/LI:1=OPT7/MOFR



FORTRAN IV-PLUS V02-SIF  
OPTR.FTN /MR

```

0049 101  FORMAT(13)
0050      GO TO 14
0051 155  TYPE 107
0052 107  FORMAT(1HS,'INPUT FILE NAME= ')
0053      CALL FILEN(2,EXTN)
0054      NFILE=1
0055 14   RETURN
0056 999  CALL EXIT
0057      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES	
SCODE1	000524	170	RW,I,CUN,I,CU
SPDATA	000010	4	RW,D,CUN,I,CU
SIDATA	000300	96	RW,D,CUN,I,CU
SVARS	000214	70	RW,D,CUN,I,CU
MTAPE0	002260	600	RW,D,OVR,GRL
MTAPE1	000012	5	RW,D,OVR,GRL
MTAPE2	000012	5	RW,D,OVR,GRL
MTAPE3	012260	2648	RW,D,OVR,GRL
MTAPE4	000004	2	RW,D,OVR,GRL
MTAPE5	000064	26	RW,D,OVR,GRL
MTAPE6	000002	1	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 016126 3627

NO PFP INSTRUCTIONS GENERATED

```

0001 SUBROUTINE FILN(UNIT,EXT)
0002 THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
0003 FROM THE TTY DEVICE
0004 DEFAULT DEVICE
0005 UNLESS SPECIFIED IN INPUT STRING
0006 UNIT=UNIT NUMBER
0007 EXT = LOGICAL*1 BUFFER OF EXTENSION
0008
0009 IMPLICIT INTEGER(A-Z)
0010 LOGICAL*1 INSTR,NOT,HLNK,EXT
0011 DIMENSION INSTR(40)
0012 DIMENSION EXT(3)
0013 DATA HLNK,NOT,' ','.'/
0014
0015 INPUT FILE
0016 READ (5,99,FND=999,FRR=152)(INSTR(I),I=1,40)
0017 FORMAT(40A1)
0018 CHECK FOR END OF LINE
0019 DO 1600 I=40,1,-1
0020 J=I
0021 IF((INSTR(I).NE.HLNK)GO TO 1601
0022 TYPE 151
0023 FORMAT(1HS,'>')
0024 GO TO 152
0025 DO 1602 I=1,J
0026 IF(INSTR(I).NE.HLNK) GO TO 1602
0027
0028 HLNK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
0029
0030 DO 1603 K=I,J-1
0031 INSTR(K)=INSTR(K+1)
0032 INSTR(J)=HLNK
0033 J=J-1
0034 GO TO 1601
0035 CONTINUE
0036 DO 103 I=1,J
0037 IF(INSTR(I).EQ.DOT) GO TO 25
0038 INSTR(J+1)=DOT
0039 INSTR(J+2)=EXT(1)
0040 INSTR(J+3)=EXT(2)
0041 INSTR(J+4)=EXT(3)
0042 J=J+4
0043 CALL SCAN(INSTR,J)
0044 CALL ASSIGN(UNIT,INSTR,J)
0045 RETURN
0046 CALL EXIT
0047 FND

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000472	157 RW,I,COM,LCU
SEDATA	000044	14 RW,D,COM,LCU

FURTHAM IV-PLUS V02-51F  
OPTR.FTN /MN

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SVARS 000060 24  
STEMPS 000004 2

RM.D.COM,LCI.  
RM.D.COM,LCI.

TOTAL SPACE ALLOCATED = 000622 201

NO PFP INSTRUCTIONS GENERATED

```

C
0001 SUBROUTINE SCAN(RUF,LTH)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*8 RUF,DEVICE
0004 DIMENSION RUF(1),DEVICE(4)
0005 DATA DEVICE/'S','Y','O','.'/
0006 DO 1 I=1,LTH
0007 1 IF(RUF(I).EQ.DEVICE(4))RETURN
0008 LTH=LTH-1
0009 DO 2 I=LTH,5,-1
0010 2 RUF(I)=RUF(I-1)
0011 DO 3 I=1,4
0012 3 RUF(I)=DEVICE(I)
0013 RETURN
0014 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDEF1	000172	61 RW,I,CON,I,CL
SIDATA	000012	5 RW,D,CON,I,CL
SVARS	000006	3 RW,D,CON,I,CL
STEPS	000002	1 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

OPTR,LP/LI:1=OPTN/NOTR



```

0001 SUBROUTINE IPIN
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL I, IATIN, IXTOUT, APPEND
0004 COMMON/TAP0/IN(300),NDUT(300)
0005 COMMON/TAP1/NSKIP,IST,MTOTI,NFUPS,MTOTO
0006 COMMON/TAP2/NEPD,NEPR,NFILE,NINS,NDUTS
0007 COMMON/TAP3/NAF(1324),NRUF(1324)
0008 COMMON/TAP4/IST,IREC
0009 COMMON/TAP5/MASK,ISM(2),IUAATT,IUSUC,IEALN,IURWD
0010 I,IUWR,IEVER,IUSPE,IEFUF,IEURH,*TO(6),MTI(6),USW
0011 COMMON/TAP6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA YES,NO/'Y','N' /
0014 DATA IXTOUT/'0','10','11' /
0015 DATA NEPD,NEPR,NFILE/0,0,0 /
0016 DATA NSKIP,IST/1,1 /
0017 DATA IUAATT,IUSUC,IEALN/001400,1,-34 /
0018 DATA IURWD,IUWR,IEVER,IUSPE,IEURH/02400,0400,-4,02440,01000 /
0019 DATA IUSPE,IEFUF,IEFUF/02440,-10,03000 /
0020 DATA MASK/0337 /
0021 DATA MT0/0,2048,0,0,0,0 /
0022 DATA MT1/0,2048,0,0,0,0 /
0023
C
C GET FILE INFO(=R)
C
C APPEND=.FALSE.
C900 TYPE 100
C100 FORMAT(/,IHS,' IS THE INPUT ON MAG. TAPE(Y/N)? ')
C ANSR=NO
102 FORMAT(A1)
MINSE1
IF(ANSR.EQ.NO)NTMS=1
NDUTS=1
C901 TYPE 103
C103 FORMAT(IHS,' IS THE OUTPUT GOING TO MAG TAPE(Y/N)? ')
C HEAD(5,102,FND=999,ERR=901)ANSR
ANSR=NO
IF(ANSR.EQ.NO)NDUTS=1
IF(NDUTS.EQ.1)GO TO 5
TYPE 104
104 FORMAT(IHS,' APPEND DATA? ')
HEAD(5,102,FND=999,ERR=902)ANSR
IF(ANSR.EQ.YES)APPEND=.TRUE.
5 IF(NDUTS.EQ.0)GO TO 151
C KX11 SUPPORTED FILE
C903 TYPE 105
C105 FORMAT(IHS,' OUTPUT FILE NAME= ')
C CALL FILENC(3,EXTOUT)
C CALL ASSIGN(3,'NDUTS')
C
C BEGINNING OF INPUT
C
C109 IF(NINS.EQ.1)GO TO 155

```

PROGRAM IV-PLUS V02-51F 21:16:21 11-SEP-80

```

0040 904      TYPE 106
0041 106     FORMATTIMS,M1 FILE NO.=(13) ' )
0042        HEAD(S,101),FND=999,FEE=904)NFIF
0043 101     FORMAT(13)
0044        GO TO 14
0045        CONTINUE
0046        C155 TYPE 107
0047        C107 FORMATTIMS,'INPUT FILE NAME= ' )
0048        CALL FILEMC2,EXTIN)
0049        CALL ASSIGMC(2,'15,1001VUICFA.WIN',17)
0050        MFILE=1
0051        RETURN
0052        CALL EXIT
0053        END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC0041	000130	108 R#,I,CON,I,CL
SPDATA	000042	17 F#,D,CON,I,CL
SIDATA	000100	32 R#,D,CON,I,CL
SVARS	000214	70 R#,D,CON,I,CL
MTAPE0	002260	600 R#,D,OVR,GRL
MTAPE1	000012	5 R#,D,OVR,GRL
MTAPE2	000012	5 R#,D,OVR,GRL
MTAPE3	012260	2648 R#,D,OVR,GRL
MTAPE4	000004	2 R#,D,OVR,GRL
MTAPE5	000064	26 R#,D,OVR,GRL
MTAPE6	000002	1 R#,D,OVR,GRL

TOTAL SPACE ALLOCATED = 015564 3514

NO FPP INSTRUCTIONS GENERATED

```

0001 C      SUMMOTIME FLEN(UNIT,EXT)
      C      THIS SUMMOTIME ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
      C      FROM THE TTY DEVICE 5
      C      DEFAULT DEVICE
      C      UNLESS SPECIFIED IN INPUT STRING
      C      UNIT=UNIT NUMBER
      C      EXT = LOGICAL/1 BUFFER OF EXTENSION
      C
0002 C      IMPLICIT INTEGER(A-Z)
0003 C      LOGICAL/1 INSTR,DUT,HLNK,EXT
0004 C      DIMENSION INSTR(40)
0005 C      DIMENSION EXT(3)
0006 C      DATA HLNK,DUT/ ' ',' '
      C
0007 C      INPUT FILE
0008 C      READ (5,99,FND=999,FRR=152)(INSTR(I),I=1,40)
0009 C      FORMAT(40A1)
      C      CHECK FOR END OF LINE
      C      DO 1600 I=40,1,-1
0010 C      J=1
0011 C      IF (INSTR(I).NE.HLNK)GO TO 1601
0012 C      TYPE 151
0013 C      FORMAT(1HS,' ')
0014 C      GO TO 152
0015 C      DO 1602 I=1,J
0016 C      IF (INSTR(I).NE.HLNK) GO TO 1602
      C
      C
      C      MAKE DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
      C
0017 C      DO 1603 K=1,J-1
0018 C      INSTR(K)=INSTR(K+1)
0019 C      INSTR(J)=HLNK
0020 C      J=J-1
0021 C      GO TO 1601
0022 C      CONTINUE
0023 C      DO 103 I=1,J
0024 C      IF (INSTR(I).EQ.DOT) GO TO 25
0025 C      INSTR(J+1)=DOT
0026 C      INSTR(J+2)=EXT(1)
0027 C      INSTR(J+3)=EXT(2)
0028 C      INSTR(J+4)=EXT(3)
0029 C      J=J+4
0030 C      CALL SCAN(INSTR,J)
0031 C      CALL ASSIGN(UNIT,INSTR,J)
0032 C      RETURN
0033 C      CALL FILL
0034 C      FND
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCOMP1	000377	157 P*,I,COR,BCL
SIDATA	000044	18 R*,O,COR,BCL

POMTAN IV-0018 V02-514  
OPT0A.PIN /04/88

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SVAPS 000060 24 M,D,CUN,I,CL  
STFAPS 000004 2 M,D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000622 201

NO PFP INSTRUCTIONS GENERATED

```

C
0001      SUBROUTINE SCAMPDF(LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*4 RUF,DEVICE
0004      DIMENSION RUF(1),DEVICE(4)
0005      DATA DEVICE/'S','Y','O','?'/
0006      DO 1 I=1,LTH
0007          IF RUF(I).EQ.DEVICE(4) THEN RETURN
0008          LFM=LTH+4
0009      DO 2 I=LTH,S,-1
0010          RUF(I)=RUF(I-4)
0011      DO 3 I=1,4
0012          RUF(I)=DEVICE(I)
0013      RETURN
0014      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDPF1	000172	61
SIDATA	000012	5
SVARS	000006	3
STEMPS	000007	1
		RW,1,CON,1,CL
		RW,D,CON,1,CL
		RW,D,CON,1,CL
		RW,D,CON,1,CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

OPTRA.LP/LC11=OPTRA/DE/MOIR

PREMAP.FTN ORIGINATED:29-MAY-79  
 UPDATED:04-JUN-79  
 THIS PROGRAM INTERPRETS ASCII TEXT FILES FOR THE MAP  
 ASSEMBLER AND ALIGNS ALL FIELDS W/ SPACES AND REMOVES  
 ALL TABS. ALL FILES ARE ASSUMED TO BE 120 COLUMNS WIDE.  
 OUTPUT LINE FORMAT:

C COLUMNS 1,2,...7 LABEL  
 C COLUMN 8 BLANK  
 C COLUMNS 9,10,...55 SOURCE CODE  
 C COLUMN 56 BLANK  
 C COLUMNS 57,58,...120 COMMENTS

0001 HYFF BLANK,TAB,HYPHEN,STAR,CR,DUMMY,SCOLON  
 0002 HYFF ALIST  
 0003 HYFF A(200),I(200)  
 0004 LOGICAL%1 IN(3),OUT(3),OUTPUT  
 0005 DATA IN/'T','X','I','/','OUT/'M','A','P'/'  
 0006 DATA BLANK,TAB,HYPHEN,STAR,CR,SCOLON/040,011,047,052,015,073/

0007 TYPE 100  
 0008 CALL FILEN(2,IN)  
 0009 TYPE 101  
 0010 CALL FILEN(3,OUT)  
 0011 LINE=0

0012 HEAD EACH SOURCE "LINE"  
 0013 LINE=LINE+1  
 0014 OUTPUT=.TRUE.  
 HEAD(2,102,END=200,ERR=300)(A(1),I=1,120)

0015 "BLANK" OUT OUTPUT LINE  
 0016 DO 10 I=1,120  
 I(I)=BLANK

0017 SPECIAL LINE OPERATORS  
 0018 IF(A(1).EQ.HYPHEN)GO TO 70  
 0019 IF(A(1).EQ.STAR)GO TO 70  
 IF(A(1).EQ.SCOLON)GO TO 70

0020 LABEL FIELD  
 0021 I=1  
 0022 IF(A(1).EQ.TAB)GO TO 12  
 I(I)=A(I)  
 I=I+1

0023 IF(I.GT.120)GO TO 80  
 0024 GO TO 11  
 0025 NEXT=I+1  
 0026

0027 COMMAND FIELD  
 0028 J=0  
 I=I+1

```

0029 21 IF(A(I),FO,TAH)GO TO 22
0030 L(999)=A(I)
0031 JE=991
0032 IF(I).GT.111)GO TO 40
0033 I=I+1
0034 IF(I).GT.120)GO TO 40
0035 GO TO 21
0036 NEXT=I+1
      C
      C COMMENT FIELD
      C
0037 30 K=0
0038 I=NEXT
0039 IF(A(I),FO,TAH)GO TO 40
0040 IF(ALAST,FO,BLANK,AND,A(I),FO,BLANK)GO TO 40
0041 IF(A(I),FO,CR)GO TO 40
0042 ALAST=A(I)
0043 OUTPUT=.TRUE.
0044 L(57+K)=A(I)
0045 K=K+1
0046 IF(K.L.23)GO TO 42
0047 WRITE(3,106)(L(I),I=1,120)
0048 DO 33 I=1,120
0049 L(I)=BLANK
0050 L(I)=SCOLON
0051 K=0
0052 OUTPUT=.FALSE.
0053 ALAST=BLANK
0054 I=I+1
0055 IF(I).GT.120)GO TO 40
0056 GO TO 31
      C
      C COPY LINE "EN TOTO"
      C
0057 70 DO 71 I=1,120
0058 L(I)=A(I)
      C
      C GENERATE CORRECTED OUTPUT LINE
      C
0059 40 IF(OUTPUT,FO,.TRUE.)WRITE(3,106)(L(I),I=1,120)
0060 GO TO 1
      C
      C NORMAL EXIT
      C
0061 200 CALL CLOSE(2)
0062 CALL CLOSE(3)
0063 TYPE 103,LINE
0064 STOP
      C
      C ABNORMAL EXIT
      C
0065 300 CALL CLOSE(2)
0066 CALL CLOSE(3)
0067 TYPE 104,LINE
0068 GO TO 2
      C

```

```

C LINE STRUCTURE ERROR
C
0069 CALL CLOSE(2)
0070 CALL CLOSE(3)
0071 TYPE 105,LINE
0072 STOP
C
C FORMAT DECLARATIONS
C
0073 FORMAT(1H,'INPUT FILENAME= ')
0074 FORMAT(1H,'OUTPUT FILENAME= ')
0075 FORMAT(120A1)
0076 FORMAT(1X,'NORMAL EXIT AFTER ',13,' LINES!')
0077 FORMAT(1X,'***WARNING*** FILE ERROR IN LINE ',13/)
0078 FORMAT(1X,'***WARNING*** LINE STRUCTURE ERROR IN LINE ',13/)
0079 FORMAT(120A1)
0080 FORMAT(1X,1005)
0081 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	001374	3R2 RW,I,CON,I,CL
SPDATA	000030	4 RW,D,CON,I,CL
SIDATA	000304	9R RW,D,CON,I,CL
SVARS	000654	214 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 002564 69R

NO FPP INSTRUCTIONS GENERATED

PREMAP,LP/I:1=PREMAP



```

0001      SUBROUTINE FILEN(UNIT,EXT)
C
C      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
C      FROM THE TTY DEVICE &
C      DEFAULT DEVICE
C      UNLESS SPECIFIED IN INPUT STRING
C      UNIT=UNIT NUMBER
C      EXT = LOGICAL:01 BUFFER OF EXTENSION
C
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL:01 INSTR,DOT,BLNK,EXT
0004      DIMENSION INSTR(40)
0005      DIMENSION EXT(3)
0006      DATA BLNK,DOT/ ' ', '.' /
C
C      INPUT FILE
0007      READ (5,99)(INSTR(I),I=1,40)
0008      FORMAT(40A1)
C      CHECK FOR END OF LINE
0009      DO 1600 I=40,1,-1
0010      J=1
0011      IF(INSTR(I).NE.BLNK)GO TO 1601
0012      TYPE 151
0013      FORMAT(IHS,'>')
0014      GO TO 152
0015      DO 1602 I=1,J
0016      IF(INSTR(I).NE.BLNK) GO TO 1602
C
C      BLANK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
C
0017      DO 1603 K=I,J-1
0018      INSTR(K)=INSTR(K+1)
0019      INSTR(J)=BLNK
0020      J=J-1
0021      GO TO 1601
0022      CONTINUE
0023      DO 103 I=1,J
0024      IF(INSTR(I).EQ.DOT) GO TO 25
0025      INSTR(J+1)=DOT
0026      INSTR(J+2)=EXT(1)
0027      INSTR(J+3)=EXT(2)
0028      INSTR(J+4)=EXT(3)
0029      J=J+4
0030      CALL SCAN(INSTR,J)
0031      CALL ASSIGN(UNIT,INSTR,J)
0032      RETURN
0033      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	000444	146 PW,I,COM,ICL
SIDATA	000042	17 PW,D,COM,ICL

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13:28:11

FORTRAN JV-PLUS V02-51E  
FILEN.FTN /#P

SVARS 000060 24 PW.D,CUN,I.CI.  
STEPS 000004 2 HW.D,CUN,I.CI.

TOTAL SPACE ALLOCATED = 000572 149

NO FPP INSTRUCTIONS GENERATED

FORTRAN IV-PLUS V02-51E  
FILEN.FTN /MH

```

C
0001 SUBROUTINE SCAN(HUF,LTH)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*4 HUF,DEVICE
0004 DIMENSION HUF(1),DEVICE(4)
0005 DATA DEVICE/'S','Y','0','.'/
0006 DO 1 I=1,LTH
0007 IF (HUF(I).EQ.DEVICE(4))RETURN
0008 LTH=LTH+4
0009 DO 2 I=LTH,5,-1
0010 HUF(I)=HUF(I-4)
0011 DO 3 I=1,4
0012 HUF(I)=DEVICE(I)
0013 RETURN
0014 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61 RW,I,CON,I,CL
SIDATA	000012	5 RW,D,CON,I,CL
SVARS	000006	3 RW,D,CON,I,CL
STEMPS	000002	1 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

FILEN,LP/LI:1=FILEN/NOFR

END

DATE  
FILMED

12-80

DTIC