



NINTH QUARTERLY PROGRESS REPORT

1 JULY 1978 TO 30 SEPTEMBER 1978

CONTRACT DAABO7 - 76 - C - 0041

MANUFACTURING METHODS AND TECHNIQUES FOR MINIATURE

HIGH VOLTAGE HYBRID MULTIPLIER MODULES

PLACED BY:

NIGHT VISION AND ELECTRO - OPTICAL LABORATORIES
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The results of further life-testing of rectangular and curved multipliers is discussed.

The commencement of the Confirmatory Sample phase is described including improvements in the manufacturing methods.

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MANUFACTURING METHODS AND TECHNIQUES FOR MINIATURE HIGH VOLTAGE HYBRID MULTIPLIER MODULES.

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ABSTRACT

The progress made during the ninth quarter of work on the Manufacturing and Technology Programme for Miniature High Voltage Multiplier Modules is described in this report.

The results of further life-testing of rectangular and curved multipliers is discussed.

The commencement of the Confirmatory Sample phase is described including improvements in the manufacturing methods.

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PURPOSE

This Contract covers component designs, mounting and interconnection techniques, tooling and test methods and other
manufacturing methods and techniques required for production
of rectangular and curved miniature high voltage multiplier
modules. These units are to be used in low cost power
supplies for image intensifier tubes. The full scope and
details of the specification are given in Appendix A to the
Eighth Quarterly Report.

Major milestones in this program consist of delivery of the following items:

- (1) First and second engineering samples and test data.
- (2) Production line layout and schedule.
- (3) Confirmatory samples and test data.
- (4) Production line set-up.
- (5) Pilot production run.
- (6) Production rate demonstration.
- (7) Preparation and publication of a final report.

The general approach is to design and set-up a cost-effective production capability, utilizing already established device technologies and materials, and to demonstrate the production line capability to fabricate at the rate of 125 acceptable units per 40 hour week.

GLOSSARY OF SPECIAL TERMS

Capacitor bank: - Ceramic wafer with metallizations which

perform the function of a number of

capacitors connected in parallel (parallel

bank) or in series (series capacitor bank).

Cure: - To change the physical properties of a material by chemical reaction or by the action of heat and catalyst.

Flash test: - Test consisting of instantaneous application of voltage at its specified value to the part.

Hybrid: - Technology combining thick-films (capacitor banks) with discrete devices (rectifiers).

Multiplier - Device consisting of capacitor banks and Modules:

rectifiers connected and packaged to perform voltage multiplication and rectification.

Pad: - The metallized area on the ceramic bank acting as a plate of a capacitor and used to make an electrical connection to it.

Rectifier: - Semiconductor device with one or more p-n junctions connected in series.

Rectifiersubstrate Assembly: A substrate with rectifiers placed and secured within it. Substrate:

 Part of a multiplier module consisting of a piece of insulating material machined to accommodate the rectifiers and support the capacitor banks.

LIST OF SYMBOLS AND ABBREVIATIONS

- ic charging current (µA)
- C_{x} measured capacitance (pF)
- D.F. dissipation factor (%)
- f frequency (KHz)
- C_i input capacitance (pF)
- I_L load current (nA)
- v_r ripple voltage (V)
- VB breakdown voltage (V)
- V_i input voltage (Vp-p)
- Vo output voltage (V d.c.)
- n efficiency (%)

1. INTRODUCTION

This report describes briefly the progress in the Manufacturing Methods and Techniques for Miniature High Voltage Hybrid Multiplier Modules Program, made during the latest calendar quarter.

In the First Quarterly Report the design and the manufacturing process for rectangular and curved multiplier modules were described. Prototype rectifier-substrate assemblies were fabricated and then redesigned to simplify the assembly operation. The specification covering the requirements for the multiplier modules forms Appendix A of the Report.

In the Second Quarterly Report results of the electrical evaluation of the first sample batch of rectangular capacitor banks TSK 25-250 and TSK 25-251 were given, the choice of the rectifier was made and electrical test results were presented on non-modular multipliers fabricated with TSK 25-250 and TSK 25-251 capacitor banks and standard HV20PD four-junction rectifiers, to evaluate these components.

In the Third Quarterly Report results of electrical tests on rectangular multiplier modules were presented.

For an input voltage of 1 KV, efficiencies above 96% under no-load conditions and above 95% with 500 nA load currents were achieved for all multipliers assembled with TSK 25-250 and TSK 25-251 and three-chip rectifiers. Low ripple voltages, input capacitances and charging currents were also measured on these multipliers.

Results of the mechanical and electrical evaluation of TSK 25-249 curved capacitor banks were also presented in the Third Quarterly Report.

In the Fourth Quarterly Report work on impregnation and coating of the multipliers was discussed as well as some problems associated with the fabrication of the rectifier-substrate assemblies. The fabrication of rectangular and curved multipliers for the First Engineering Sample was discussed.

In the Fifth Quarterly Report were presented the results of electrical performance testing at the room, high $(+52^{\circ}\text{C})$ and low (-54°C) temperatures, as well as effects of thermal shock, and high and low temperature storage.

In the Sixth and Seventh Quarterly Reports were presented the results of testing of rectangular and curved multipliers to the Second Engineering Sample requirements,

steps to improve the frequency performance of the multipliers and optimization of the rectifiers for these devices, as well as results of life testing of multipliers.

In the Eighth Quarterly Report the results of the reliability testing of rectangular and curved multipliers to the Second Engineering Sample requirements were analyzed and further steps to improve the performance of the multipliers and optimize the rectifiers for these devices were discussed.

2. FABRICATION AND EVALUATION OF MULTIPLIERS

2.1 General

The Programme Manager has received formal authorization to proceed to the Confirmatory Sample phase from the Administrative Contracting Officer, Major S.L. Thacher, U.S. Army CERCOM.

2.2 Reliability Testing of Voltage Multipliers

The four (4) encapsulated multiplier modules successfully completed the life test with 2592 hours on each unit. This brings the total times to:

5208 hours for unit #57

3862 hours for unit #76 and 8A

2592 hours for unit #82

for a combined figure of 15,524 unit hours. Therefore, overall, our life test produced results of:

8004 hours on 6 pcs (1000 Vp-p, unencapsulated) 8926 hours on 9 pcs (1150 Vp-p, unencapsulated) 10,416 hours on 5 pcs (1150 Vp-p, encapsulated)

for a total of 27,346 unit hours on 12 pieces.

Further life tests will be conducted on overrun multipliers from the confirmatory sample manufacture and all such units will be encapsulated to eliminate any corona problems.

2.3 Multiplier Design

The evaluation lot of 10,000 rectifier leads (HVRO4M-13) were received on 23 August. This enabled us to proceed with the manufacture of HSC3 devices (part number RD0058) using the thin nailhead leads. By the end of September we had received 480 rectifiers with another 1423 in various stages of manufacture.

Our model shop fabricated and delivered to us 100 pieces each of the rectangular and curved substrate plates made from the polyimide Vespel SP-1 material. The inspection data is presented in Tables 1 and 2 for the rectangular and curved substrates, respectively.

These materials enabled us to commence manufacture of 38 substrate assemblies; 20 rectangular and 18 curved. Since these substrates are polyimide, we

were able to cure the potting epoxy at a temperature of 240° C (compared to the 200° C previously used for the G-10 glass epoxy board).

As the loading of the devices into the substrates is difficult and time consuming - especially when using clips as we had in the past - we designed fixtures to improve the operation. These fixtures (illustrated in Figures 3 and 4) provide stability to the assembly and allow faster loading of the rectifiers as well as superior potting. This is a vast improvement over the previous method although one problem has manifested itself. The loading of the rectifiers into the substrates is simple provided one is careful, however, it is easy to jar a substrate out of its location and upset it. Therefore, some method of holding the substrate down onto the fixture during loading and encapsulation is required. We are presently examining this problem and will modify the fixtures accordingly.

3. CONCLUSIONS

The life testing indicates that the multipliers, as fabricated, will meet the reliability requirements if encapsulated for the test.

The fixtures built for substrate loading substantially improve the assembly technique.

Further changes will be incorporated to ease the manufacture of the multiplier modules and to improve their reliability. The capacitor and rectifier designs are finished and the substrates will need only minor modifications at most.

4. PROGRAMME FOR NEXT QUARTER

- 4.1 Continue manufacture of the confirmatory sample lot.
- 4.2 Commence testing of the confirmatory sample lot.

5. PUBLICATIONS AND REPORTS

No reports or publications were made on the work associated with this program during the current quarter.

6. IDENTIFICATION OF PERSONNEL

Brief descriptions of the background of technical personnel involved were included in the preceding Quarterly Progress Reports.

On 1 September, Dr. M. Korwin-Pawlowski left the employ of Erie Technological Products of Canada and was replaced by Mr. B. Grant Gordon, P.Eng., as Programme Manager on this contract. During the Ninth quarter of the program the following persons worked in their area of responsibility:

INDIVIDUAL	RESPONSIBILITY	HRS. SPENT
Dr. M. Korwin-Pawlowski	Programme Manager (to August 8, 1978)	77
B.G. Gordon	Programme Manager (from August 8, 1978)	91
D. Platt	Manager, Quality Assurance and Control, High Voltage Products	6
D. Archard	Senior Test Technician	10
M. Black	Production Supervisor, Rectifiers	2
K. Cram	Draughtsman	16
C. Grills	Senior Engineering Technician	95
L. Macklin	Draughtsman	5
D. Regan	Senior Engineering Technician	16
	Manufacturing Personnel	22.4
TOTAL HOURS -	in quarter	340.4
TOTAL HOURS -	to date	4379.4

TABLE 1

	N	.5180					.5170					
ı	X	.0700					.0710					
	L	.2510					.2510					
terial)	K		.0397	.0387				.0377		.0371		
(Polyimide Material)	J		.0918	.0867				.0848		.0877		
	Н		.0319	.0337				.0296		.0291		
'A' Substrate Plates	9		.0757	.0775				.0730		.0630		
	F		.0043	.0100	.0050	.0074		.0067		.0112		.0105
	E		.0869	.0869 .0860				.0885	.0877	.0835		
-312-10	D		.0208	.0221 .0298 .0322	.0300	.0329		.0233	.0247	.0255	.0232	.0263
Mechanical Inspection Data for TSK-312-104'A'	၁		.0192 .0205 .0193	.0193 .0193 .0183	.0184	.0199		.0209	.0220	.0218 .0221 .0217	.0227	.0226
	В		.0508	.0501	.0556	.0503		.0509	.0574	.0528	.0542	.0502
	A		.0348	.0394	.0256 .0386 .0374	.0422		.0363	.0397	.0393	.0374	.0366
Mechanical	Hole #	Unit A	33	4 5 9	V 88 6	10 11 12	Unit B	3 2 1) 4 W	9 / 8	10	11 12

Notes: (i) All measurements are in inches.

1

⁽ii) See Figure 1 for dimensioning.

TSK-312-104'A'

Dimensioning of Rectangular Substrate Plate

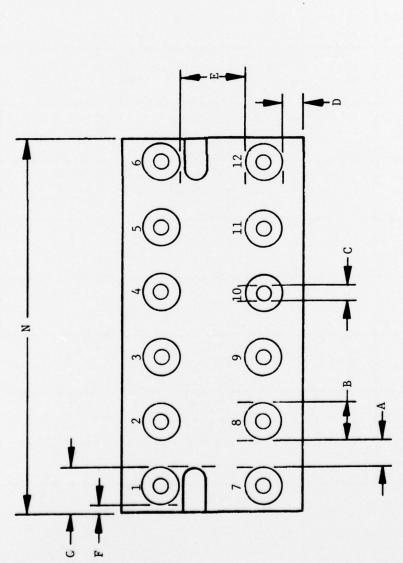


FIGURE 1

TABLE 2a

Mechanical Inspection Data for TSK-313-104'A' Substrate Plates (Polyimide Material)

×	.0337	.0303
J	0690.	.0680
Н	.0340	.0339
9	.1017	.0988
F	.0878	.0850
ы		.0209 .0211 .0202 .0194 .0201 .0202 .0205 .0209 .0209 .0209 .0209 .0203 .0204 .0203 .0203 .0203 .0203
D		.0516 .0520 .0520 .0524 .0523 .0548 .0523 .0517 .0517 .0519 .0520 .0520 .0520 .0518 .0521 .0518 .0521 .0510 .0520
C	.1473	.1515
В	.1051	.1053
А	.0325	.0346
Hole #	Unit A	1 2 3 4 4 6 6 11 12 3 3 3 4 4 6 6 6 7 7 10 11

Notes: (i) All measurements are in inches.

(ii) See Figure 2a for dimensioning.

TSK-313-104'A'

Dimensioning of Curved Substrate Plate

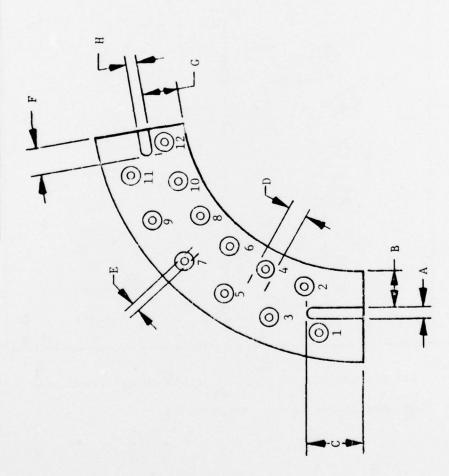


FIGURE 2a

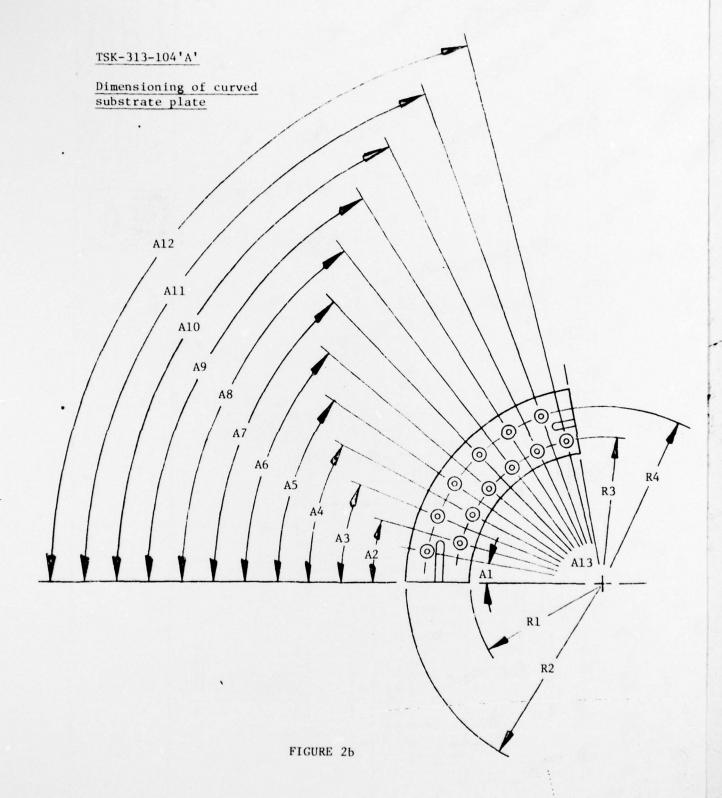
TABLE 2b

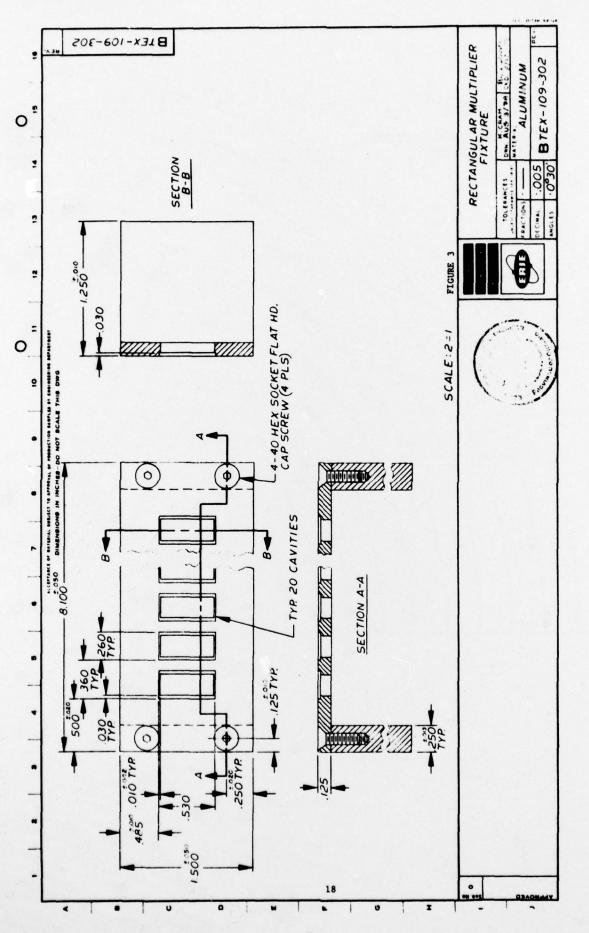
Additional Mechanical Data for TSK-313-104'A' Substrates

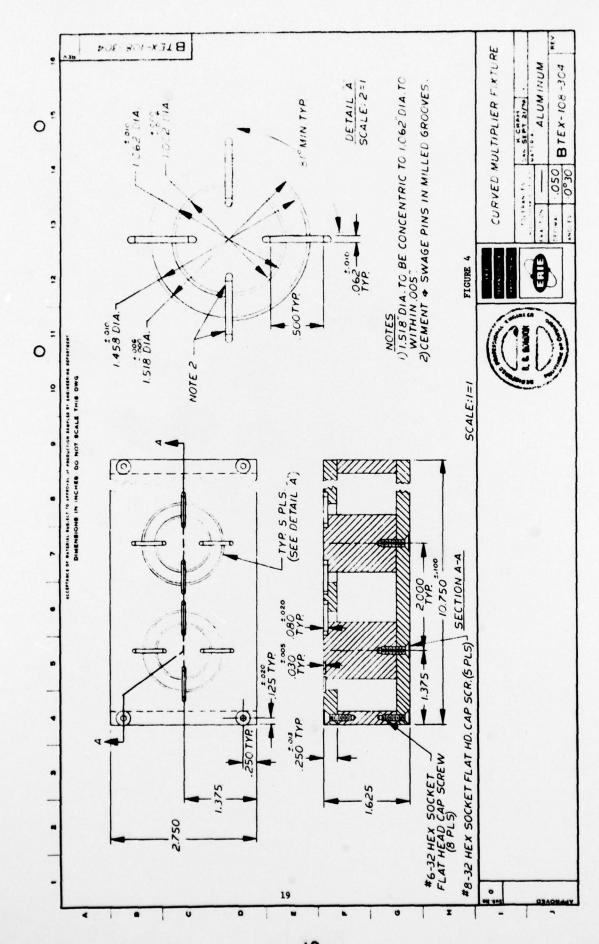
	Unit A	Unit B
R1	.506	.505
R2	.755	.753
R3	.568	.565
R4	.684	.670
A1	10°	10°
A2	15°40'	16 ⁰
А3	21°40'	22 ^o
A4	27 ⁰ 25 '	27 ⁰ 45'
A5	33 ⁰ 25'	33 ⁰ 40'
A6	39 ⁰ 15'	39 ⁰ 45'
A7	45 [°] 25'	46 ⁰
A8	51 [°] 20'	51 ⁰ 45'
A9	57 ⁰ 25'	57 ⁰ 50'
A10	63 ⁰ 05'	63 ⁰ 30'
A11	69 ⁰ 05'	69 ⁰ 40'
A12	75 [°]	75 ⁰ 35 '
A13	79 ⁰	79 [°] 50'

Notes: (i) All measurements are in inches unless specified.

(ii) See Figure 2b for dimensioning.







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