

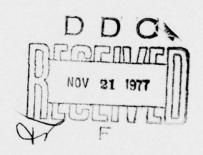
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RALPH W. FISHER

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As the operator rotates his head to observe off axis display detail, the camera is commanded to rotate and the projector follows. Thus, high acuity detail is retained on the foveal axis of the observer's eyes. This system allows wide field-of-view (160%) remote viewing of scenes, with resolution comparable to human vision, using conventional TV system bandwidths.

The gimballed camera and projector mechanical and optical designs are presented along with the method of relaying the optics thru the gimbals. The digital servo system is described along with the associated computer programs. The head tracking system includes sections on the tracker, illuminator, optics and electronics.

Considering that the system is the first of this type, the results were very encouraging. Equipment developed to perform conventional functions worked perfectly including the servo control, TV camera, TV projector, and Head Tracker. The most challenging problem encountered in the development were associated with the state-of-the-art advancement required in non-linear optics. Problems were also encountered in maintaining optical quality in the camera and display. The maximum resolution attained is approximately 1.5 milliradians compared to the 0.5 milliradians that is theoretically possible.

Even with this limitation, system performance was very impressive. The value of the wide field in maintaining observer orientation within the full 160° field-of-regard was readily apparent. Target tracking capability by head control was very good and peripheral cueing by motion and glints proved to be of significant value in the acquisition and tracking task.

Detailed performance analyses of the current design indicate better acuity is possible by fabricating new rear spline elements for the non-linear lenses and redesigning the projector optical relay. Through these efforts, 1 milliradian performance should be readily obtained. Performance better than this appears to be limited by a diffraction problem inherent in the projector Schlierin optics and would require use of a different type of projector.

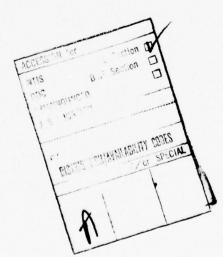


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- A Brief Description of the Remote Viewing System (RVS)
- B Camera Considerations
- C Projector Studies
- D PROM 1, PROM 2, PROM 3 and PROM 4 Computer Program Listings
- E Application of the Night Vision Laboratory (NVL) Thermal Viewing System Static Performance Model to the RVS

Section 1

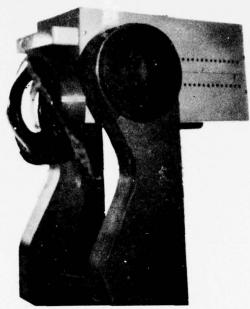
INTRODUCTION AND SUMMARY

This final report documents the results of Contract No. N00014-75-C-0660. The objective of this contract was to design and build a fully operable laboratory brassboard of the MCAIR Remote Viewing System.

Under a previous ONR Contract (Ref. 1), MCAIR proved the feasibility of a unique non-linear lens which made this effort possible. This lens takes advantage of the "variable acuity" characteristics of human vision to reduce the amount of information (or bandwidth) that must be transmitted in a wide field-of-view high resolution imaging system. A brief description of the remote viewing system concept which utilizes this lens is presented in Appendix A. The brassboard system constructed under this contract represents a significant advancement in the state-of-the-art of remote viewing because for the first time a variable acuity picture that is designed to be compatible with human vision was recorded, transmitted, and displayed in real time.

The ONR Brassboard Remote viewing system consists of a two axis gimballed TV camera as shown in Figure 1 and a two axis gimballed TV projector as shown in Figure 2(a) and (b). A serial transmission link and low loss TV cable allow the camera to be located up to 400 ft. from the projector. The operator of the system can steer the camera under servo control using a helmet mounted tracker shown in Figure 2, approximately 90 degrees right and left and can look up and down +45°. A microprocessor implements two axis servo control of the camera and projector servos. The system can track angular rates up to 1 rad/sec. It is capable of looking at the sun with no catastrophic failure. The projector subsystem consists of a 9 ft. dia sphere, a TV projector, and mounting support frame. It requires a floor area of 15 ft. by 15 ft. The lower portion of the sphere is cut away, thus an 8 foot ceiling is adequate. Interconnecting cables between the microprocessor and the operator allow the operator to position himself at the center of the sphere. He is required to be at the spherical center directly below the projector to realize the best optical performance of the system and for optimum head control.

Considering that the system is the first of this type, the results were very encouraging. As should be expected the only serious problems encountered in the development were associated with the state-of-the-art advancement required in non-linear optics. All conventional functions or equipment worked perfectly including the servo control, TV camera, TV projector, Head Tracker, etc. Problems were encountered in maintaining optical quality in the non-linear image when transmitted through the optical relays, both in the camera and display. While most of these problems were overcome, the resulting resolution was still about 3 times lower than anticipated, about 1.5 milliradians compared to the 0.5 milliradians that should be theoretically possible.

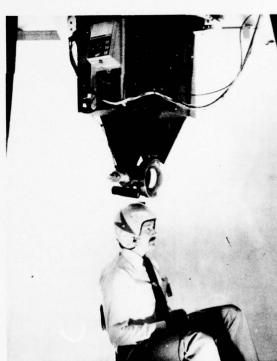


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Figure 1 Two Axis Gimbaled Camera



(a) Left Side Showing Detector Mounted on Helmet



(b) Right Side Showing Source on Projector Assembly

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Figure 2 Projector

Even with this limitation, system performance was very impressive. The value of the wide field in maintaining observer orientation within the full 180° field-of-regard was readily apparent. Target tracking capability with head control was very good and peripherial cueing by motion and glints proved to be of significant value in the acquisition and tracking task.

Detailed performance analyses indicate better acuity is possible by fabricating new rear spline elements for the non-linear lenses and redesigning the projector optical relay. Through these efforts, 1 milliradian performance should be easily obtained. Performance better than this appears to be limited by a diffraction problem inherent in the light valve's Schlierin optical output. Further improvement would require use of a different type light valve projector.

Finally it appears that the laboratory demonstration which involved viewing a scene in which most of the spatial detail is stationary does not show the true potential of the system for the highly dynamic airborne application. It is therefore highly recommended that the brassboard hardware be flight tested in order to obtain a true performance assessment in a dynamic environment.

Section 2

APPROACH

The basic design philosophy is discussed and the rationale for the approach used is presented in this section. In subsequent sections detailed design of the equipment is developed. As a starting point for these discussions the original design goals from our proposal are listed below.

Electro-Optical Subsystem

The design goal of the video subsystems is to generate a projected display that fully supports human vision in both field-of-view and resolution. More specifically the goals are:

- o 160° hemispherical FOV
- o Image transfer characteristics as shown in Figure 3
- o Resolution as a function of viewing angle as shown in Figure 3
- o Display brightness greater than 1 ft-lambert over the entire FOV
- o Standard TV bandwidth video transmission between camera and projector

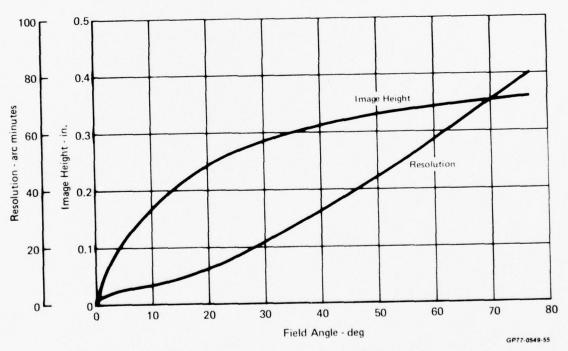


Figure 3 Remote Viewing System Optical Requirements

Control Subsystem

Camera platform with motion capabilities of:

- o Coverage 360° azimuth, + 60° elevation
- o Acceleration 3000°/second
- o Slew rate 300°/second

Projector platform with the same specifications Servo static position accuracy — 30 arc minutes

The starting point for the design was presented in the proposal for this study (References 2 and 3). As this design evolved, considerable change was dictated by practical considerations. Salient differences occurred in the gimballing philosophy and electronic servo control system. The basic design and these changes are summarized below.

The camera electro-optical design followed the proposal very closely. A silicon vidicon camera was used for solar damage protection (See Appendix B). This necessitated use of an optical relay with a mechanical iris for light level control. A significant change from the proposal was the decision to utilize a 1023 line raster TV system which was selected to obtain greater resolution. The basic non-linear lens has an on-axis focal length of 2 inches and an image plane height of 0.72 inches (for maximum FOV of 160°). In a 525 line raster system (488 effective lines), the angular separation between scan lines is:

$$\frac{.72}{488 \times 2} = 0.738 \text{ milliradians}$$

2.5 minutes of arc

ngular resolution results when this separation is multiplied by the Kell $\,$ r which is 1.4. Thus the angular resolution is:

 $2.5 \times 1.4 = 3.5 \text{ minutes of arc}$

By utilizing a 1023 line system, the scan line separation is:

$$\frac{.72}{937 \times 2}$$
 = .384 milliradians = 1.32 minutes of arc.

The angular resolution then is:

1.85 minutes of arc

This value is much closer to the desired performance. It will be shown later, however, that only a small fraction of this improvement was actually achieved for various technical reasons.

The camera gimbal approach changed somewhat from that outlined in the proposal. The azimuth gimbal axis was not at the lens nodal point but was offset as illustrated in Figure 4. The primary reason for this was simplicity of fabrication and the wide azimuth coverage available with this arrangement. The use of gimbal position encoders shown on Figure 4 reflects our decision to employ digital electronics wherever possible. This approach eliminated the need for rate and acceleration sensors on the camera platform because these functions can be derived digitally from the position encoder outputs.

The projector design deviated substantially from that outlined in the proposal, the difference being primarily in the mechanical gimballing arrangement. After consultation with General Electric Co. (G.E.) on mechanical constraints of the light valve projector, we decided to gimbal the projector in azimuth. This simplified the optical relay because it required articulation in one dimension only, the pitch direction. This could be handled by a simple half-angle mirror and eliminated the need for image derotation. Besides making the relay much simpler and easy to align, this approach assured a much higher level of light output, a critical concern with this system (See Appendix C). The resulting projector gimballing arrangement is shown in Figure 4. Other minor problems that impacted on the projector system design were:

- o Focus correction is required because of the close proximity of the projection screen to the projector. This arises because the lens has a flat focal plane when focused at infinity. When the plane is shifted to obtain correct on-axis focus, the variable focal length makes this location incorrect for all other field angle points. An additional lens element was required to correct this problem. Design of this element is discussed in Section 3.2.3.
- o Incompatibility between the projector Schlerin optics and the nonlinear lens. This rather complex problem is described in Section 3.2 and caused inefficient optical relay performance for projection angles near the optical axis.

This problem required refabrication of the rear element of the projection lens and relay design revision to obtain greater magnification between projector and non-linear lens.

A digital control system was selected primarily because of its flexibility. Since a control system for a variable acuity optical link of this type had never been constructed, we felt a great number of changes in control system dynamics and modes would be required before successful operation would be achieved. A digital system with microprocessor control met these requirements. In addition, this approach will make future additions of more sophisticated control modes possible e.g., eye control. Figure 4 shows the basic elements of this control system.

Head position sensing was as outlined in the proposal except that the source and detector locations were interchanged. The IR source had to be mounted on the projector instead of the helmet so that it would be additive with the infrared output from the projector.

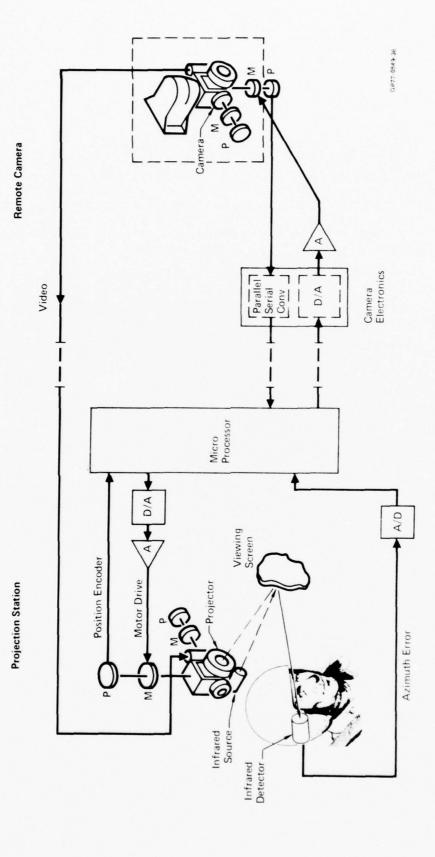


Figure 4 Control System Elements Azimuth Axis Shown Only

Section 3

ELECTRO-OPTICAL SYSTEM DESIGN

The electro-optical design is divided into three separate efforts, those relating to the camera, the projector, and the head tracking system. For the camera, this effort includes TV camera selection and optical relay design to mate the camera with the non-linear lens. For the projector, this effort covers TV projector selection, relay design, and focus corrector design. The head tracking system design uses an infrared source boresighted with the projector and a detector assembly mounted on the helmet and is a part of the control system which is described in Section 5. Each of these items is discussed detail in the following sections.

3.1 CAMERA SUP

The camera subsystem integration and optical relay design.

3.1.1 Camera

The camera electro-optical configuration followed that of the proposal very closely. The original TV camera purchased as the sensor was a GE model 4TE33Al. This camera was selected because it utilized a silicon vidicon which is necessary for solar burn protection. It was compact and self contained and was believed to be compatible with the GE light valve projector which was selected for the display.

During early evaluation of the camera/projector combination, a vertical jitter was noted on the display. This problem was traced to the random scan interlace of the GE camera. The projector however, requires a precise 2/1 interlace to maintain a stable picture. This problem was corrected by using an external sync generator. Later in the systems integration effort, numerous intermittent electrical connections were encountered in the TV camera. This, plus poor optical performance of the automatic iris assembly caused us to conclude that the GE camera would not be suitable for the demonstration system.

Therefore, another camera was selected and we elected to choose one with a higher line rate capability to obtain greater resolution. After a thorough search of available TV cameras, a General Electrodynamics Co. Model 6073B camera was selected. This system had the desired 1023 line rate and a stable 2/1 interlace required by the projector.

3.1.2 Optical Relay

The function of the optical relay system is to relay a good quality image from the non-linear lens to the TV camera vidicon with no loss of field-of-view or any noticeable vignetting. It must also magnify the image to the size compatible with the vidicon requirements and provide exposure control for the camera system. Exposure control is obtained by using an

electronic controlled iris on one of the relay lens. For convenience and to reduce cost, this element was purchased with the camera. Relay design requirements are:

- o Its input must be the non-linear lens image which is 0.72 inches in diameter and is located about 0.070 inches from the last (aft) lens element. An F/5.6 ray bundle must be accommodated and imaging is nearly telecentric where all chief rays are nearly parallel to the optical axis.
- o Its output must be to vidicon faceplate which has an active scanning area of 0.5 X 0.375 inches. Later this was found to be a circular area 0.7 inches in diameter.
- o One relay element must be a 50 mm F/1.4 lens with an installed automatic iris assembly. This iris must be properly integrated to form an aperture stop without vignetting.

Using these optical relay requirements, the design progressed as follows. The relay optics were designed to use lenses that could be purchased off-the-shelf rather than custom designed and fabricated special lenses. The lenses were chosen with sufficient aperture and format to transmit the F/5.6 cone of light forming the non-linear lens image.

Use of the available automatic iris/lens assembly dictated that the relay use lenses operating at infinity conjugates. A pair of lenses are therefore required to relay an image. The first lens collimates the image and the second forms an image from the collimated bundle of light. The lens speed required is the same as the speed of the cone of light to be relayed. The image-to-image distance is approximately the sum of the two focal lengths. Magnification of the relayed image is equal to the ratio of the focal lengths of the two lenses.

If the purchased camera with the automatic iris assembly is used as the second relay lens, the focal length of the first relay can be calculated if the final image size is known. Selection of a final image size, requires a tradeoff of resolution and field-of-view. The problem is that a 4 \times 3 aspect raster is used to scan the circular image from the non-linear lens. The aspect ratio of the TV raster must be 4 \times 3 because the projector system uses a light valve television projector with a fixed 4 \times 3 raster format. The image should cover as much of the raster as possible.

If the raster height is made equal to the image diameter, no FOV is lost but it does waste a large part of the TV format. If the image is larger, the angular resolution would be improved but the top and bottom of the FOV is cutoff. A compromise solution is to let the raster height cover 90% of the image diameter. The part of the image that is lost lies in an area of low interest. The non-linear lens image which is 0.72 inches in diameter should be demagnified to be 0.417 inches in diameter for a standard 1/2 by 3/8 inch television raster. An 86 mm focal length lens when paired with the 55 mm Vicon lens will give the desired image size. An 85 mm F/2.0 Olympus lens was selected for the first relay lens which

has an aperture that is large enough to collect all the light from the non-linear lens without the need for a field lens.

The second relay lens has an auto-iris to provide exposure control. However, this is the case only if the iris is the aperture stop. The aperture stop is defined to be the stop that effectively restricts the cone of rays passing through the lens system.

The following analysis shows how the auto-iris becomes the aperture stop. The first element restricting the light bundle is the non-linear lens which has a speed of F/5.6. The non-linear lens is telecentric in the image plane which means the non-linear lens' exit pupil is located at infinity. Therefore, the next lens, the 85 mm Olympus, will reimage the non-linear lens exit pupil in it's back focal plane. The 85 mm lens is fast enough so that it doesn't restrict the F/5.6 light bundle. Therefore, if the 50 mm lens is positioned so that it's entrance pupil is coincident with the back focal plane of the 85 mm lens, the auto-iris will be the aperture stop.

The relay system described above fulfills all of the optical requirements but is mechanically awkward when coupled to the camera and non-linear lens. It is about three feet long and the two heavy elements are located on the ends. It can't be folded into a more compact package without severe vignetting unless a second relay is added. Therefore, an additional pair of relay lenses are used to fold the optical system 180° so that the vidicon is located directly above the first relay. The back focal distances of the second pair of relay lenses are large enough to accommodate folding mirrors. Each mirror folds the system 90°. Two 80 mm F/2.8 Xenotars are used for the second relay giving it unity magnification.

Adding a second relay makes it necessary to use a field lens to keep the auto-iris as the aperture stop and to keep vignetting from becoming noticeable. The field lens is a double convex lens located in the second image plane. With the image actually being formed inside the lens, the field lens doesn't affect the image and dust particles on the field lens surface are not in focus. The focal length of the lens is chosen to form an image of the exit pupil of the 50 mm relay lens onto the iris of the last relay lens. Using the lens maker's formula the focal length is found to be 52 mm.

The vidicon has typical silicon detector response and is very sensitive to near-infrared energy. However, the non-linear lens and relay optics are not optimized for this spectral band and the image suffers if the infrared is not filtered out. Various narrow band and low pass filters were tried and the one that worked best was a Schott KG-3 infrared absorbing glass. It is placed in the collimated region of the relay. A brassboard of this system was constructed and evaluated. This setup is shown in Figure 5.

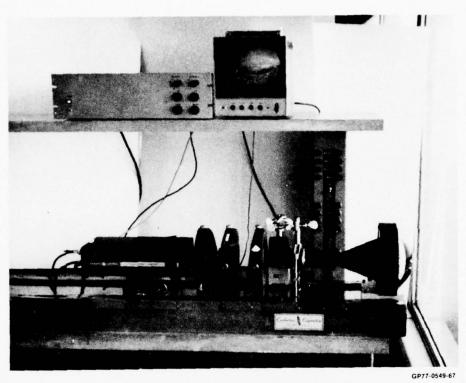


Figure 5 Camera and Relay Brassboard

After initial testing of the camera and projector systems, some modifications were necessary. First the camera vidicon was rotated 90° to compensate for the 90° image rotation which occurs in the projector system. This gave more vertical FOV coverage than horizontal FOV coverage due to the 4 X 3 raster. Previously, the image size was chosen so that the part of the image falling outside of the raster was the top and bottom of the FOV. Now the 10% image loss occurs in the horizontal direction where the full FOV is desired. To get full coverage of the horizontal FOV, the circular image from the non-linear lens must fit within the rectangular raster but the system resolution must not suffer. The solution was to magnify the image and increase the raster size so that the image would cover as many of the discrete diodes that makeup the sensitive surface area of the vidicon as possible. The vidicon has a sensitive area about 0.7 inch in diameter. The image which is also circular is made slightly smaller. The raster size is increased to 0.93 X 0.70 inches so that the raster height is about the same as the image diameter. Consequently the image covers more discrete sensitive elements than before and the resolution is improved with no loss of FOV.

The relative size of the image to the 4 X 3 aspect raster is smaller now than it was because the full image lies within the raster. This causes the projected image to be smaller. Consequently, the projector relay optics must be altered to provide increased magnification of the television image. This modification is described later in Section 3.2.

As in the projector, the camera relay optics had to provide increased magnification. The second relay pair which before operated at unity magnification was made to magnify the 0.417 inch diameter image to 0.703 inches. The larger image was obtained by replacing the last 80 mm Xenotar with a 135 mm, F/4.7 Xenar lens. This required only mirror modifications in mouting hardware. The other optics were unchanged with the same field lens used.

The optical components are located as shown in Figure 6. The television image is formed in the following way. Light from the object enters the non-linear lens from the left and is imaged immediately behind the non-linear lens. This image is collimated by a 85 mm F/2.0 Zuiko Olympus lens. The collimated bundle is imaged a second time by a Vicon 50 mm F/1.4 lens that contains the aperture stop for the system. A field lens is located in the second image plane. The first mirror folds the optical axis up 90° where a 80 mm F/2.8 Xenotar lens collimates the second image. A 135 mm F/4.7 Xenar lens picks up the collimated bundle and the final image. The second mirror folds the optical axis 90° to make the image hit the vidicon. An infrared absorbing filter is placed in the collimated bundle between the last pair of relay lenses.

3.2 PROJECTOR SUB-SYSTEM DESIGN

The second effort in the system design was the electro-optical subsystem design of the projector which consisted of projector selection, relay design, focus corrector design, and projection dome design and is detailed in the following sections.

3.2.1 The Projector Selection

Logic for the original selection of the GE light valve projector is presented in Appendix C. It was the lowest cost approach that could produce adequate display brightness. A PJ7000 light valve was originally purchased for the system. This unit had a 525 line raster and an optical output of 700 lumens. Later this unit was updated to a 1023 line raster for reasons stated earlier in Section 2 and 1000 lumen output thereby making it a PJ7150 projector.

3.2.2 Optical Relay Design

After receiving the projector from GE it was coupled to the non-linear lens with a simple single element optical relay. Problems were immediately encountered with the optical energy transfer. The problem was traced to the Schlieren optical technique used in the projector. This is shown schematically in Figure 7, for the no output case and Figure 8 for full output. The light output is proportional to the rate of change of oil film thickness. This rate of change is generated by an electron beam which writes on the oil film. As can be seen in these figures, the result is a centrally obscurred bundle of illuminated segments. When this bundle is coupled into the non-linear lens a problem results. This is illustrated

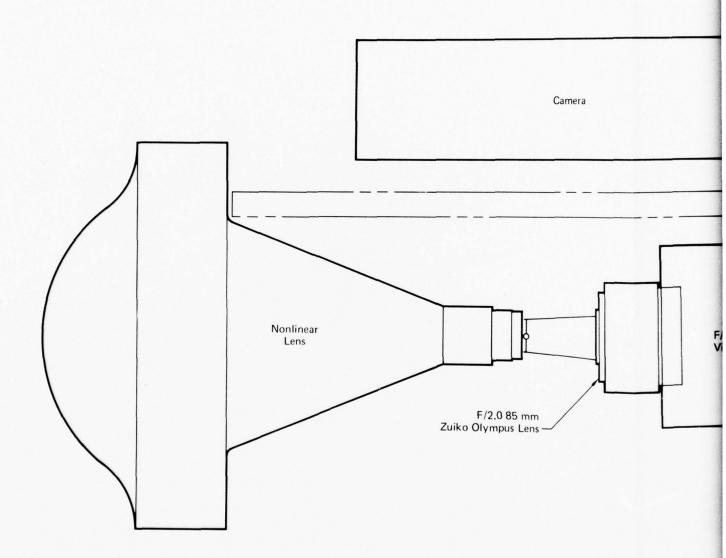
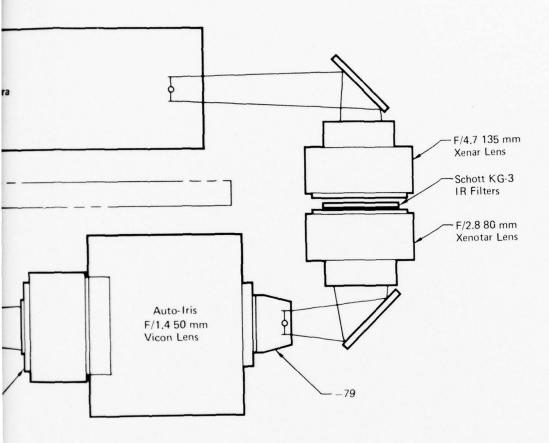


Figure 6 Camera Optical Elements

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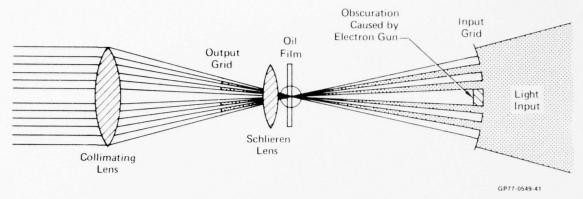
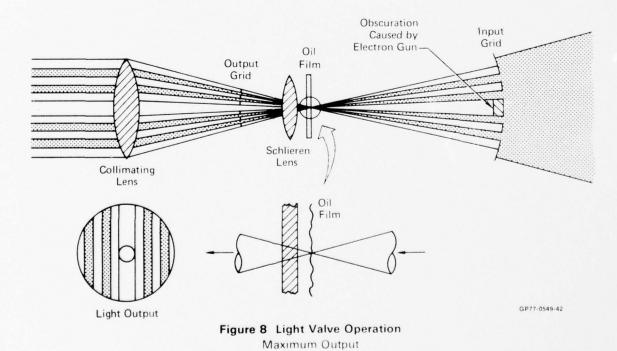


Figure 7 Light Valve Operation
No Output



in Figure 9 for a simplistic one element relay used in our original experiment. In this experiment a dark spot was noted at the center of the projected image. In the on-axis case, the reason for this is that almost all of the energy from the light valve falls outside of the acceptance cone of the non-linear lens as can be seen in Figure 9. This causes two areas of concern. The low light in the central high acuity area of the image can seriously reduce the observer's visual capabilities. In addition, the annular shaped input to the lens provides energy in the worst possible portion of the acceptance ray cone if good image quality is desired. The latter is known from original ray trace data on the lens. In addition, the annular input by itself can cause serious diffraction problems. All of these problems can lead to low display acuity in the central region where the highest acuity is desired.

GE was consulted to see if the projector output could be modified to correct for this situation. After considerable study, they concluded that a major redesign would be required to make the light valve output more compatible with our lens. This left only the relay parameters as a possibility to effect an improvement. From an optical viewpoint, the only relay parameter that can be varied which affects the output ray cone geometry is magnification. This parameter can expand or compress the F/number cone from the projector. In our case, we need to reduce the cone size which requires more magnification within the relay. A derivation will be presented which relates the F/number and magnification to the ratio of source and display brightness.

The entire optical system is shown schematically on Figure 10. The symbols to be used in this derivation are also defined on this figure.

The illumination $(E_{_{\mathbf{S}}})$ of the source is:

$$E_{S} = \frac{F}{A_{S}} \tag{1}$$

Thus the source brightness (B_s) is:

$$B_{S} \stackrel{\sim}{=} \frac{E_{S}}{\omega_{S}} = \frac{F}{A_{S} \omega_{S}}$$
 (2)

Now from cone geometry the solid angle is:

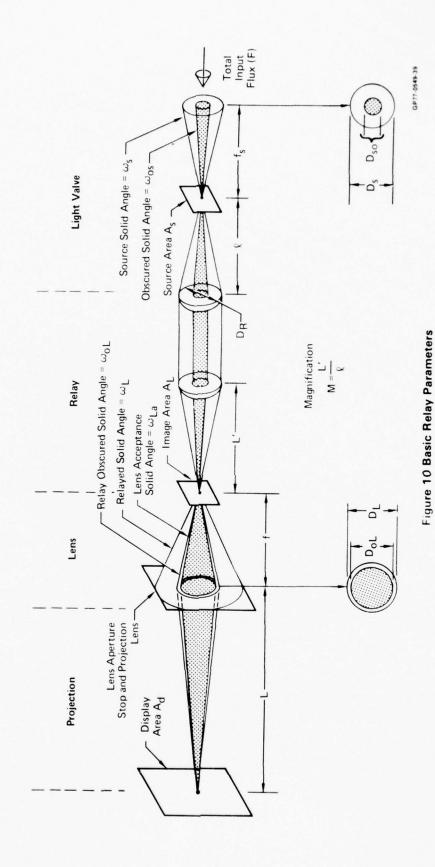
$$\omega_{s} = \frac{\pi}{4 \text{ FNO}_{s}^{2}} \tag{3}$$

where $FNO_s = F/number of source$

$$B_{S} = \frac{4 \text{ FNO}_{S}^{2} \text{ F}}{\pi A_{S}}$$
 (4)

Figure 9 Light Valve Nonlinear Lens Interface for Single Element Relay

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This (B_g) is also the brightness of the image at the lens. For those who are not familiar with this fact it can be proven as follows: The total riux passing through the source area (A_g) will arrive at the lens area (A_L) assuming good relay design. The area A_L is related to A_g by the magnification (M), viz:

$$A_{I} = M^{2} A_{S}$$
 (5)

Since the output of relay is collimated as any good relay is, the output ray \mathbf{d} iameter equals the input diameter. Therefore, the ratio of output to input solid angle is

$$\frac{\omega_{L}}{\omega_{S}} = \frac{\pi D_{R}^{2}}{L^{12}} \frac{1^{2}}{\pi D_{R}^{2}} = \frac{1}{M^{2}}$$
 (6)

In addition, the areas \mathbf{A}_{L} and \mathbf{A}_{s} are also related by the magnification viz:

$$M^2 = \frac{A_L}{A_S} \tag{7}$$

The brightness of the image is:

$$B_{L} = \frac{F}{A_{I} \omega_{I}}$$
 (8)

Substituting Equations (5), (6), and (7) into (8) results in:

$$B_{L} = \frac{F}{A_{S} \omega_{S}}$$
 (9)

The right side of Equation (9) is equal to the right side of Equation (2), thus:

$$B_{L} = B_{S} \tag{10}$$

Now we will determine the effect of the lens obscuration, magnification, and F/number on the screen brightness. The light flux actually entering the projection lens from an incremental image area $(dA_{\underline{I}})$ is:

$$F = B_{L} dA_{L} (\omega_{La} - \omega_{LO})$$
 (11)

where

$$\omega_{\text{La}} = \frac{\pi}{4 \text{ FNO}_{\text{L}}^2}$$

where FNO_{L} = Lens acceptance F/Number

Also:

$$\omega_{Lo} = \frac{\omega_{SO}}{M^2} = \frac{\pi D_{SO}^2}{M^2 f_S^2}$$
 (12)

Now if we define an obscuration factor (K) as the ratio of the obscured diameter (D $_{\rm SO}$) to the aperture diameter (D $_{\rm S}$), viz:

$$K = \frac{D_{so}}{D_{s}} \tag{13}$$

Substituting Equation (13) into (12)

$$\omega_{Lo} = \frac{\pi K^2 D_s^2}{M^2 f_s^2} = \frac{\pi K^2}{M^2 FNO_s^2}$$
 (14)

where FNO $_{\rm S}$ is the F/number of the source. Now substituting into Equation (11).

$$F = B_{L} dA_{L} \left[\frac{\pi}{4 \text{ FNO}_{L}^{2}} - \frac{\pi K^{2}}{4 \text{ FNO}_{S}^{2} M^{2}} \right]$$
 (15)

$$= \frac{\pi B_{L} dA_{L}}{4} \left[\frac{1}{FNO_{L}^{2}} - \frac{K^{2}}{FNO_{S}^{2} M^{2}} \right]$$
 (16)

All of this flux falls within area ${\rm d}{\rm A}_{\dot d}$ on the projection screen. The illumination is then:

$$E_{d} = \frac{F}{dA_{d}} = \frac{\pi B_{L} dA_{L}}{4 dA_{d}} \left[\frac{1}{FNO_{L}^{2}} - \frac{K^{2}}{FNO_{S}^{2} M^{2}} \right]$$
(17)

However the focal lengths and differential areas are related by:

$$\frac{dA_L}{dA_d} = \frac{f^2}{L^2} \tag{18}$$

Substituting Equation (18), (3) and (2) into (17) results in:

$$E_{d} = \frac{F f^{2}}{L^{2} A_{s}} \left[\left(\frac{FNO_{s}^{2}}{FNO_{L}} \right) - \left(\frac{K^{2}}{M} \right) \right]$$
(19)

Now the display screen brightness is:

$$\mathbf{B}_{\mathbf{d}} = \frac{\mathbf{E}_{\mathbf{d}}}{\omega_{\mathbf{d}}} \tag{20}$$

where ω_d is the solid angle over which E_d is reflected. For our purpose of studying the effects of magnification, the relative brightness referenced to an unobscured source will simplify the analysis. For an unobscured source:

$$K = 0$$

And then Equation (19) becomes:

$$E_{r} = \frac{F_{f}^{2}}{L^{2}_{A_{S}}} \left(\frac{FNO_{S}^{2}}{FNO_{L}}\right)$$
 (21)

Therefore:

$$\frac{B_{d}}{B_{r}} = \frac{E_{d}}{E_{r}} = \frac{\left(\frac{FNO_{s}}{FNO_{L}}\right)^{2} - \left(\frac{K}{M}\right)}{\left(\frac{FNO_{s}}{FNO_{L}}\right)} = 1 - \frac{\left(\frac{K}{M} + FNO_{L}\right)^{2}}{\left(\frac{FNO_{s}}{FNO_{L}}\right)}$$
(22)

For our light valve

$$K = 0.36$$

$$FNO_{S} = 2.8$$

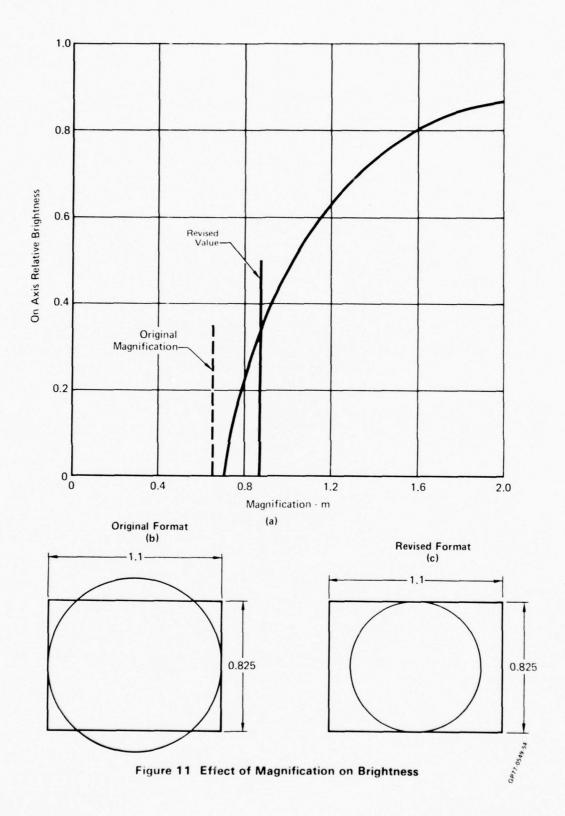
$$FNO_{I} = 5.6$$

Substituting these values into Equation (22) results in:

$$\frac{B_{d}}{B_{r}} = 1 - \frac{.518}{M^{2}}$$
 (23)

This curve is plotted in Figure 11(a). This curve clearly illustrates the problem noted in our first experiments. During this exercise we had the full width of the light valve format filling the lens image plane as shown in Figure 11(b). Here the magnification was the ratio of the non-linear lens diameter of 0.72 inch to the camera scanning width of 1.1 inches, viz:

$$M = \frac{0.72 \text{ inch}}{1.1} = 0.65$$



Under these conditions no light was entering the lens. The system was made useable by reducing the portion of the source area occupied by the lens image as shown on Figure 11(c). Thus, the magnification is:

$$M = \frac{0.72}{0.825} = 0.87$$

Now the display brightness is 0.3 that of an unobscured or conventional optical system. Since considerably more light is available in the central area of the display (See Appendix C), this is an acceptable situation. In fact it helps to make the display brightness more uniform if the relay is correctly designed. Such a design was shown on Figure 9. Note that for the edge ray bundle that the lens acceptance cone shifts to a more desirable portion of the light valve cone. The result is essentially an increase in output when compared to that of an unobscured system at the field edge. Since the above solution appears to be satisfactory from a brightness standpoint the question of acuity was then considered.

While the exact effect of an annular aperture function is very difficult to predict precisely, an approximation of its affect on resolution is rather easy. Figure 12 shows such an aperture and its associated diffraction MTF. For a thin annulus where the inner (D $_{\rm OL}$) and outer (D $_{\rm L}$) diameters are approximately the same, the MTF shows a pronounced drop in response at a spatial frequency (S $_{\rm l}$) proportional to the difference in the diameters divided by twice the light wavelength (2 λ), viz:

$$S_1 = \frac{D_L - D_{OL}}{2 \lambda} \tag{24}$$

Therefore, a good approximation to the MTF is to assume that the spatial frequency (S_1) is the limiting factor in performance. For this reason this frequency was calculated in terms of the parameters of Figure 12.

At the projection lens output, the lens diameter (D $_{\!L})$ and focal length (f) are related to F/number (FNO $_{\!I}$) by:

$$D_{L} = \frac{f}{FNO_{L}}$$
 (25)

Substituting Equation (13) into (25) and relating FNO $_{\rm L}$ to FNO $_{\rm S}$ by the magnification

$$D_{LO} = \frac{K f}{M FNO}$$
 (26)

Substituting into Equation (24)

$$S_1 = \frac{f}{2\lambda} \left[\frac{1}{FNO_L} - \frac{K}{M FNO_S} \right]$$
 (27)

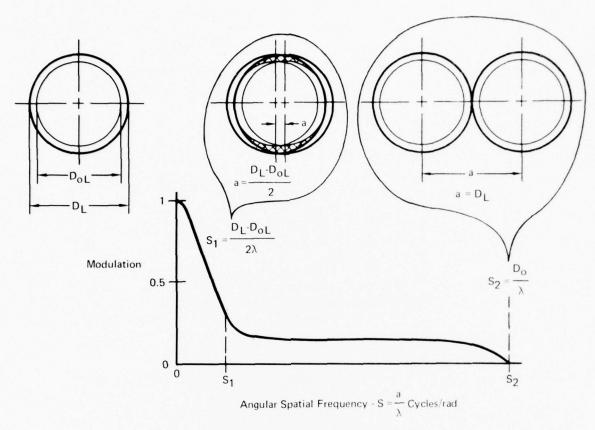


Figure 12 MTF of an Annulus

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In more conventional terms the resolution is approximately the width of a half cycle.

$$\alpha = \frac{1}{2S_1} = \frac{\lambda}{f \left(\frac{1}{FNO_L} - \frac{K}{M FNO_S}\right)}$$
 (28)

This function is plotted in Figure 13 for the light valve output for an obscuration ratio (K = 0.36) and F/number of 2.8 and variable magnification and the non-linear lens focal length of 2 inches and F/number of 5.6. Note that for the revised raster format, the serious MTF degradation occurs at a resolution of 0.34 milliradians or 1.2 minutes of arc. While it would be desirable to have better performance than this, it is comparable to scan line substense and no further improvement could be made. Any further increase in relay magnification would result in an increase in scan line subtense, also shown in Figure 13 for a 1023 line raster. Based on the above effort the design requirements for the relay were established, and are:

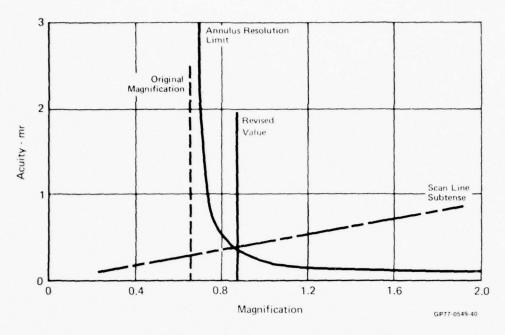


Figure 13 Effect of Magnification on System Acuity

- 1. A magnification of 0.87
- 2. Aperture shift geometry with field angle as shown in Figure 9.

The final requirement was to iterate the overall relay mechanical design including overall length, fold point locations, and diameter with the designer.

Basically, these parameters are:

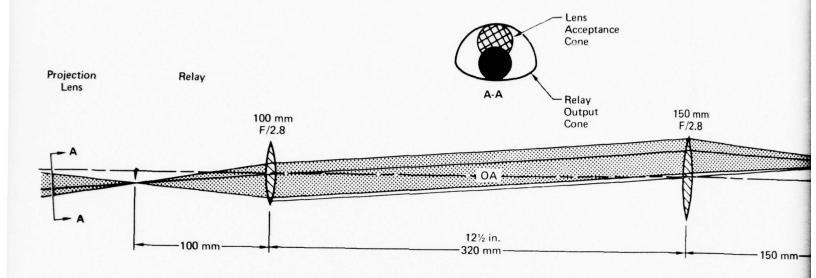
Overall Length = 4 feet

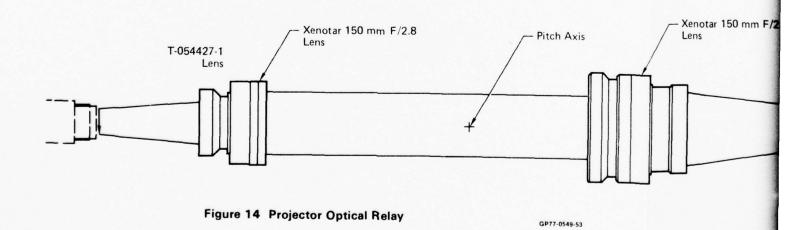
Diameter = 3 inches

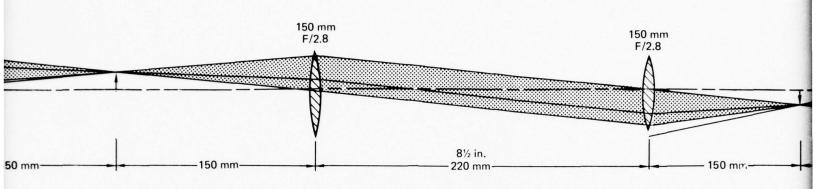
Critical Folds = 12 inches required between last two lenses

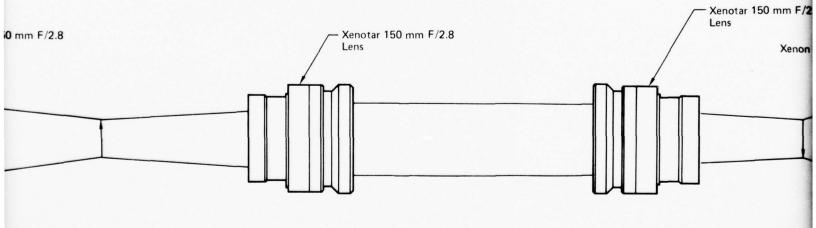
After considerable design effort the relay of Figure 14 evolved. On this figure an edge ray bundle is drawn to show how the desired aperture shift is achieved. Note no field lenses are utilized. This was necessary to maintain the desired aperture shift. The large size penalty normally associated with a relay design of this type is eliminated by allowing vignetting of the unused part of the light valve optical output.

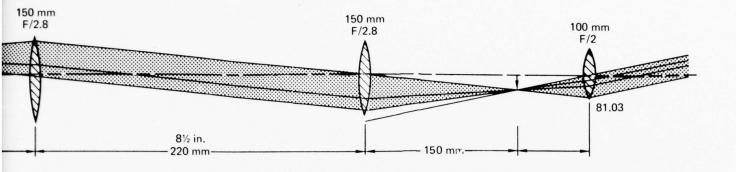
The lens elements were purchased and the relay set up on **a**n optical bench. After a small decollimation at the projector output to achieve the required magnification, performance was exactly as expected. The relay configuration using available lenses is shown in Figure 14. Figure 15 is a photograph of the relay test set-up.

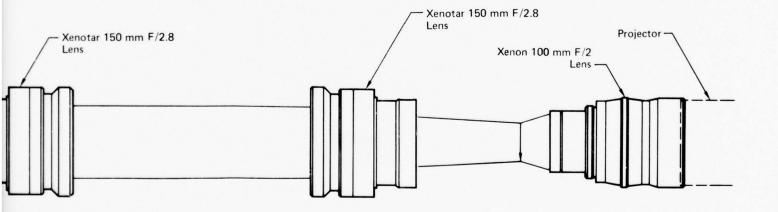












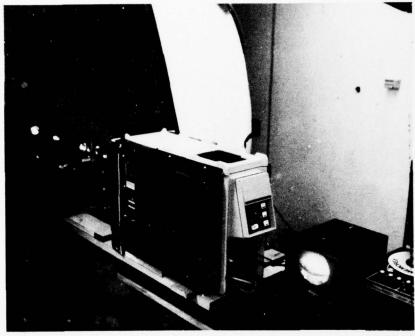


Figure 15 Relay Test Setup

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3.2.3 Focus Correction

The variable focal length nature of the projection lens creates a serious focus problem. This problem arises because the projector lens is identical to the camera lens and designed for an object located at infinity. For projection, the lens focal plane must be shifted aft by about 0.08 inch to obtain optimum on-axis focus where the lens focal length is 2 inches. At an 80° object field angle, the focal length is down to 0.04 inches. An 0.08 inch shifted image plane is obviously grossly out-of-focus for this short off-axis focal plane. To determine the magnitude of this problem, the focal plane profile for optimum focus was computed. The general case geometry of Figure 16 was used for this purpose. Here the lens equivalent optical geometry for on-axis and off-axis object angle θ is shown. For either case the general lens equation applies.

$$\frac{1}{S_1(\theta)} + \frac{1}{S_2(\theta)} = \frac{1}{f(\theta)}$$
 (29)

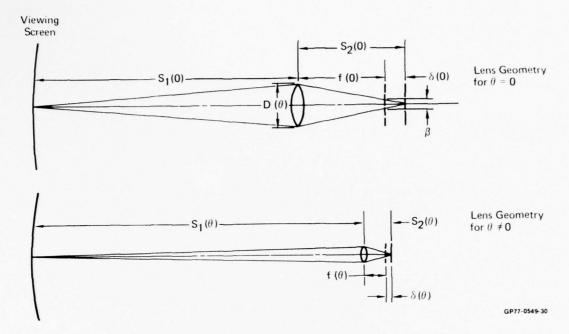


Figure 16 Projection Lens Conjugate Geometry

where $S_1(\theta) = \text{Object distance}$ $S_2(\theta) = \text{Image distance}$ $f(\theta) = \text{Focal length}$

From Figure (16) and because $\mathbf{S}_{1}(\theta)$ is relatively constant, the focus error is

$$\delta(\theta) = S_2(\theta) - f(\theta) \tag{30}$$

Substituting Equation (29) into (30)

$$\delta(\theta) = -\frac{f^2(\theta)}{S_1(\theta) - f(\theta)}$$
(31)

For a 54 inch object distanct, $(S_1(\theta))$, the error in focus for a system focused at infinity is:

$$\delta(\theta) = \frac{f^2(\theta)}{54 - f(\theta)} \tag{32}$$

This equation is plotted in Figure 17. In order to maintain optimum focus, the image plane would have to be **th**e shape of the Figure 17 curve, i.e., 0.08 inch further back in the center relative to its edge.

Now the effect of this defocus will be related to focal plane resolution.

if $\varphi(\theta)$ is the required resolution, the allowable focal plane blur $(\beta(\theta))$ is

$$\beta(\theta) = f(\theta) \phi(\theta) \tag{33}$$

Since the focal plane spatial resolution is uniform; that is the offaxis resolution is equal to the on-axis value,

$$\beta = constant = f(\theta) \phi(\theta) = f(0) \phi(0)$$
 (34)

If ϕ is in minutes of arc the allowable focal plane blur is

$$\beta = \frac{f(0) \phi(0)}{3440} \tag{35}$$

Relating similar triangles on Figure 16

$$\frac{D(\theta)}{S_2(\theta)} = \frac{\beta}{\delta(\theta)} \tag{36}$$

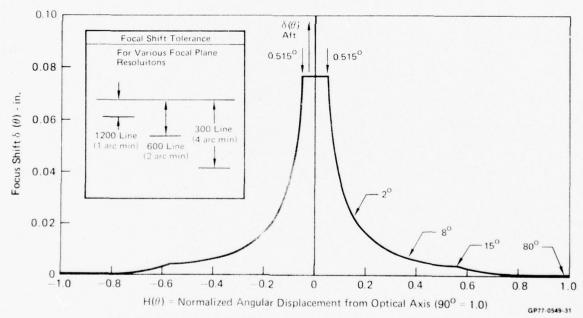


Figure 17 Image Plane Position Relative to Infinity Focus for 54 In.
Conjugate Distance

Solving for $\delta(\theta)$

$$\delta(\theta) = \frac{\beta S_2(\theta)}{D(\theta)}$$
 (37)

Since the F/number is defined as

$$FNO \stackrel{\triangle}{=} \frac{f(\theta)}{D(\theta)} = constant$$
 (38)

and

$$S_2(\theta) = f(\theta) \tag{39}$$

Then

$$\delta(\theta) = \beta F/No. \tag{40}$$

Substituting Equation (35) and (39) into (40)

$$\delta(\theta) = \frac{\phi(0)}{3400} \text{ F/No. } f(0) \tag{41}$$

For our lens the F/No. = 5.6 and f(0) = 2 inch, the allowable focal plane mislocation is

$$\delta(\theta) = 3.294 \times 10^{-3} \phi(0) \tag{42}$$

This equation is plotted on Figure 17 for resolutions $(\phi(0))$ of 1, 2, and 4 arc minutes.

There are two ways of correcting this focus shift problem. The image plane can be tailored to Figure 17 with a corrector element in the lens image plane or the lens can be operated at the infinity focal plane position and a positive optical element placed at the lens output to converge the lens output to a 54 inch conjugate distance. After some experimentation with a focal plane corrector, the latter approach was selected as the only feasible method of focus correction. This is not without its problems however.

The only way of achieving a positive (converging) lens effect outside of the non-linear lens is as shown in Figure 18. It must be a deep double convex element in order to accommodate the entire field-of-view while its thickness must be held down to reduce weight and inertia of the projector pitch axis.

The following technique was used to design this element. Curvature of the surface closest to the lens was selected by fit geometry. Then the second surface radius was computed to converge the on-axis ray bundle at the 54 inch distance. Then the angular blur size as seen from the center of the dome was computed for all other field angles. These results were then compared to the inherent system acuity. The resulting lens curvature are shown on Figure 18 while blur data are shown on Figure 19.

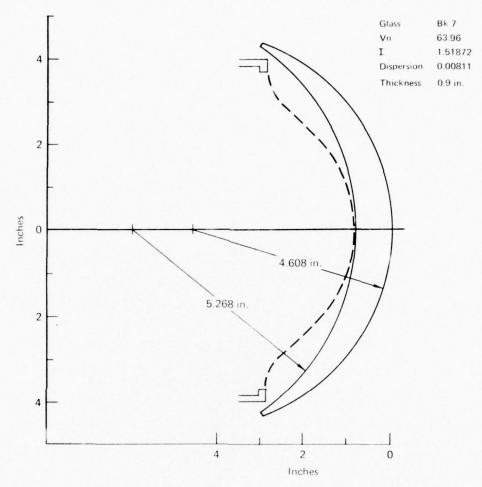


Figure 18 Focus Corrector Geometry

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The blur after correction was well within the acuity tolerance for the entire 160° field.

The problem with this method of focus correction is that it generates a distortion to the non-linear lens output. Rays exiting the non-linear lens are bent towards the optical axis by an increment that increases with field angle. This is to say that while blur is acceptable, the centroid of the blur falls on the screen at the wrong location. This can and will cause false motion of points on the display as gimbal angles vary.

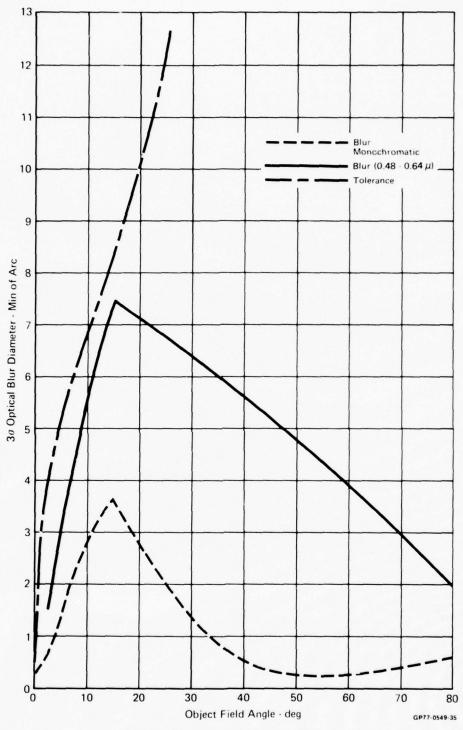


Figure 19 Corrector Angular Blur

To study importance of this effect, rays were traced from the lens to the screen at various field angles without and with the corrector lens. The angular error resulting from both cases as observed from the dome center are shown on Figure 20. With no corrector lens, an error is generated because of the nodal point shift in the non-linear lens. This shift can be seen on the chief ray trace data shown on Figure 21. The gimbal axis of the projection lens intersect very near the 45° nodal point. This make the projection correct only at 0° and 45° .

As the nodal point shifts aft or forward for the angles other than 45°, they fall on the screen at larger or smaller angles (measured from the sphere center) than they should to maintain no distortion. The worst case occurs at 80° where points are advanced by 2°, Figure 20 curve c. If the focus corrector is installed on the lens, points are directed in an opposite direction as shown by the curve e of Figure 20. Here the error increases continuously, reaching about 9° at 80° command angle. This suggests that if the size of the non-linear lens image is increased, this problem must be reduced. This was analyzed and a 2% value was found to produce minimum error over the entire field, curve d. The maximum error is about the same magnitude as it would be with no corrector. The only problem is a slight loss in field-of-view, from 160° to 140°. Since resolution is very low in this region, this is believed to be an acceptable tradeoff for a better acuity close to the optical axis. A corrector of the design shown in Figure 18 was fabricated and installation hardware designed for the projector lens.

3.2.4 Projection Surface Design

It was apparent from early experimental projections on the interior surface of the sphere that a diffuse unity gain white screen surface did not yield enough edge brigthness. This was predicted and the calculations are contained in Appendix C. As also described in the appendix, the projection/viewer geometry was optimized for a specular screen coating. The work of Reference (4) indicated that a silver screen material would increase brightness by a factor of four. Based on this, we evaluated several types of aluminum paint on the surface and we found that a screen gain of four was easily achieved. By visual observations, we concluded that sufficient brightness was being obtained out to field angles of 120°. Beyond this, performance was questionable. However, high contrast objects were easily detected out to 140°.

The aluminum paint, however, caused the imperfections in the dome joints to become very noticeable. This required expenditure of considerable effort to refill and sand the joints smooth.

3.3 HEAD TRACKING SYSTEM DESIGN

The function of the head tracker servo control system is to maintain angular alignment of projector's optical axis and the observor's nominal sightline. A head angular position sensor or a relative head/projector angular position sensor will not accomplish this beacuse of the close proximity of the viewing surface. To correctly accomplish this sensing

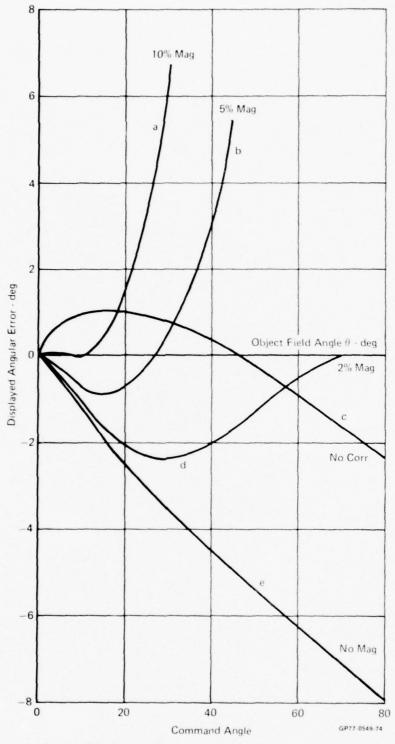


Figure 20 Display Error vs Actual Angle

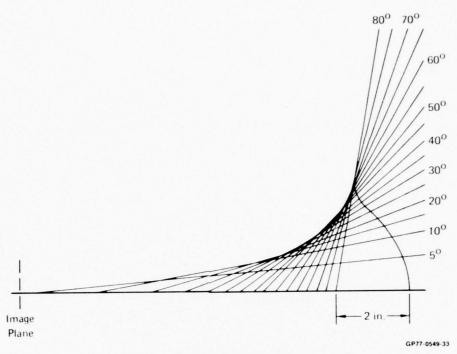
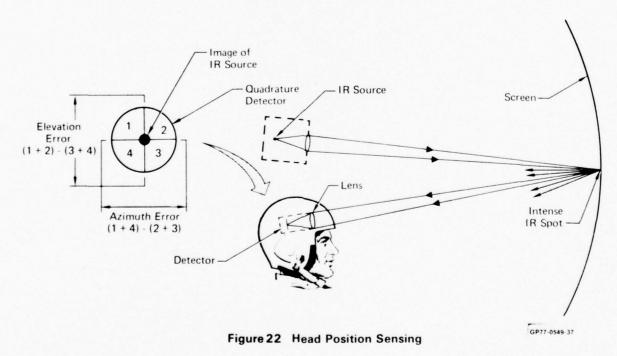


Figure 21 Nodal Point Shift of Nonlinear Lens

task, the head position must be sensed relative to the projection lens coordinates in all six dimensions. To avoid a complex sensing and computational task, an electro-optical approach was devised that inherently senses the required parameters and is shown in Figure 22.



For the head tracker to function properly it must have adequate sensitivity and no significant deadband. From an optical standpoint the image of the source that falls on the detector must be of sufficient size and strength to provide a useable signal/noise ratio around the null point. For this system, where uniform acuity exists over about $\pm 1^{\circ}$, a threshold sensitivity of about 0.20° would seem adequate.

In the following paragraph, the sensitivity of the source will be related to other system parameters. An optical schematic and definition of terms is shown in Figure 23. The source has a radiant emittance of \mathbf{W}_{λ} watts/cm²- μ . Assuming the source has a focal length (f $_1$), and F/number (FNO $_1$), the power output of the source assembly can be computed as follows:

Assuming the source is a Lambertian emitter, its radiance is

$$N_{\lambda} = \frac{W_{\lambda}}{\pi} \quad \text{in } \frac{\text{watts}}{\text{steradian cm}^2 - \mu}$$
 (43)

The power exiting the source is:

$$P_1 = N_{\lambda} \omega_1 A_1 \Delta \lambda \tag{44}$$

where ω_1 = Solid angle subtended by the projection lens (See Figure 23)

A₁ = Source area

 $\Delta \lambda$ = Source emitting bandwidth

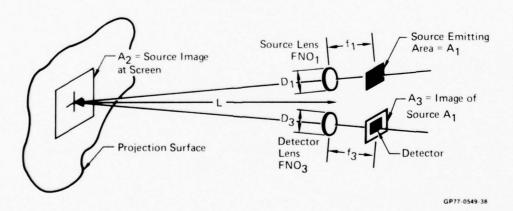


Figure 23 Head Tracking Radiometrics

The solid angle (ω_1) is

$$\omega_1 = \frac{\pi D_1^2}{4 f_1^2} = \frac{\pi}{4 FNO_1^2}$$
 (45)

where

D₁ = Source lens diameter

 f_1 = Source lens focal length

 $FNO_1 = Source lens F/number$

It is also assumed that $\Delta\lambda$ is small enough so that N $_{\lambda}$ remains essentially constant. If the source assembly is focused to form an image on the viewing screen a distance L from the source, the irradiance at the screen surface is

$$H = \frac{P_1}{A_2} \tag{46}$$

where A_2 = Screen area illuminated by source

From geometrical optics the source and screen areas are related by:

$$\frac{A_1}{A_2} = \left(\frac{f_1}{L}\right)^2 \tag{47}$$

and

$$A_2 = A_1 \left(\frac{L}{f_1}\right)^2 \tag{48}$$

Substituting Equation (44) through (48) into (46)

$$H = \frac{W_{\lambda} \Delta \lambda}{4 \text{ FNO}_{1}^{2}} \left(\frac{f_{1}}{L}\right)^{2}$$
 (49)

Assuming a screen gain of G, the radiance of the screen is:

$$N_2 = \frac{G H}{\pi} = \frac{G W_{\lambda} \Delta \lambda}{4\pi F_{DO} 2} \left(\frac{f_1}{L}\right)^2$$
 (50)

The power from the screen entering the detector aperture also located a distance (L) from the screen is:

$$P_3 = N_2 \omega_3 A_2 \tag{51}$$

Where

$$\omega_3 = \frac{\pi D_3^2}{4L^2}$$
 (52)

Substituting Equations (48), (50), and (52) into (51) results in an equation defining the power incident on the detector as a function of system parameters.

$$P_{3} = \frac{G W_{\lambda} \Delta \lambda D_{3}^{2} A_{1}}{16 FNO_{1}^{2} L^{2}}$$
 (53)

The detector selected was a UDT, Inc. PIN SC/25. Saliant characteristics for this cell are:

For this application an output exceeding the dark current of 7.5 μ amps for an angular spot displacement of 0.2° is desired. Thus the desired sensitivity to angular inputs should be:

$$S_o = \frac{\text{Dark current}}{\text{Threshold}} = \frac{7.5 \, \mu \text{amp}}{0.2 \, \text{deg}} = 37 \, \mu \text{amp/deg}$$
 (54)

To define the sensitivity in terms of linear displacements, Equation (54) must be adjusted by the detector focal length, thus the desired position sensitivity is:

$$S_{L} = \frac{2120}{f_{3}} \frac{\mu amps}{cm}$$
 (55)

We can equate the desired position sensitivity to the cell actual position sensitivity, thus

$$S_L = Actual position sensitivity x Incident Power (56)$$

Substituting Equation (55) into (56) and solving for the incident power results in:

$$P_3 = \frac{2120}{f_3} \frac{\mu amp}{cm} \times \frac{1}{32 \frac{amp}{w-cm}}$$
 (57)

$$= \frac{6.62 (10^{-3})}{f_3} \quad \text{watts} \tag{58}$$

The incident power (P_3) was defined in Equation (53). The focal length (f_3) in Equation (58) is defined in terms of F/number and lens diameter, viz:

$$f_3 = D_3 FNO_3 \tag{59}$$

Substituting Equation (53) and (59) into (58) results in an equation which interrelates the detector and source parameters, viz:

$$\frac{G W_{\lambda} \Delta \lambda f_{3}^{3} A_{1}}{16 FNO_{1}^{2} FNO_{3}^{2} L^{2}} = 6.62 (10^{-3})$$
 (60)

Of the above parameters, G and L are available from display geometry. The parameters ω_{λ} , $\Delta\lambda$, A_{1} can be obtained from the source parameters.

A 1763 prefocused incandescent standard light bulb was chosen for mechanical reasons and has the following characteristics:

- o Temperature 4000°K
- o Source Dimensions 0.06×0.12 inches = 0.4645 cm^2

A Wratten No. 88A filter was selected to attenuate the visual and transmit the infrared wavelengths. This filter cuts off below 300 nm. The cell response limits the upper responsitivity to 1000 nm. This establishes the wavelength band to:

$$\Delta \lambda = 200 \text{ nm} = 0.2 \mu$$

The 4000°K source has an average radiance over this wavelength band of

$$W_{\lambda} = 1000 \frac{\text{watts}}{\text{cm}^2 - \mu}$$

Using a screen gain of 4 and distance to the screen of 54 inches (137 cm) and substituting these values into Equation (60) results in

$$f_3 = 1.75 \text{ FNO}_1^{\frac{2}{3}} \text{ FNO}_3^{\frac{2}{3}}$$
 (61)

The aperture diameter of the source and receiver are related by their respective focal lengths, viz:

$$\frac{\mathbf{D}_1}{\mathbf{D}_3} = \frac{\mathbf{f}_1}{\mathbf{f}_3} \tag{62}$$

The detector aperture (D_3) has a diameter of 0.05 cm and if the smaller dimension of the source is equal to its aperture (D_1) , the focal lengths are related by:

$$\frac{f_1}{f_3} = \frac{0.1524}{0.05} = 3.05 \tag{63}$$

A 2 inch focal length lens with a 1.5 inch aperture diameter was selected for the source optics. Thus the F/number is:

$$FNO_1 = \frac{2.0}{1.5} = 1.33$$

The focal length of the detector from Equation (63) is:

$$f_3 = \frac{2}{3.05} = 0.66$$
 inch

The required detector F/number is therefore

$$FNO_{3} = \sqrt{5.35 \frac{FNO_{1}^{2}}{f_{3}^{3}}}$$

$$FNO_{3} = \sqrt{5.35 \frac{(1.33)^{2}}{(.66 \times 2.54)^{3}}} = 1.42$$

The chosen detector field-of-view can be determined by:

$$\tan \frac{\theta}{2} = \frac{\text{detector size}}{2 \times f_3} = \frac{0.74}{2 \times 0.66}$$
 (65)

Thus the field-of-view is:

$$\theta = 58^{\circ}$$

After a search of available lenses, a double convex aspheric was selected. This lens had a focal length of 0.94 inches and a diameter of 1.5 inch. Therefore its F/number is:

$$FNO_3 = \frac{.94}{1.5} = 0.63$$

While the field-of-view would be somewhat reduced with this lens i.e.,

$$\theta = 43^{\circ}$$

the threshold will be improved by the ratio

$$\frac{f_3}{f_{NO_3}} = \frac{.94^3}{.63^2} = \frac{1.42^2}{.66^3} = 14.5$$

This allows sufficient margin for more filtering if required and/or allows operation of the source at a lower power input.

The assembled sensor can be seen on Figure 24 while the source is seen on Figure 25. An additional Wratten 88A filter was found to be necessary on the detector to reduce its sensitivity to visual wavelength band. The response of the final detector system is shown on Figure 26.

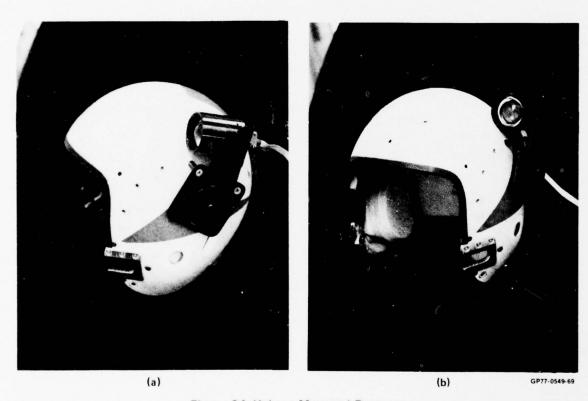


Figure 24 Helmet Mounted Detector



Figure 25 Projector Mounted Source

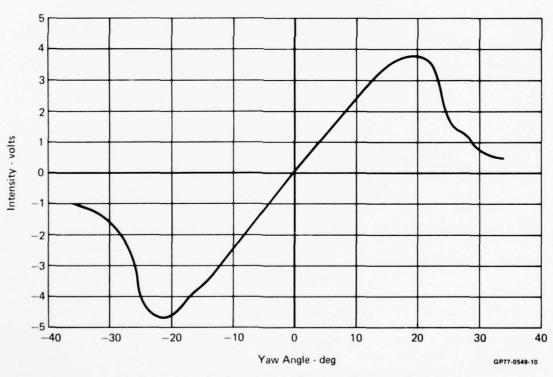


Figure 26 Response of Infrared Head Tracking Detector System

Section 4

MECHANICAL DESIGN AND FABRICATION

Detail drawings of the camera assembly and projector assembly and their components are included in this section.

4.1 CAMERA ASSEMBLY (P/N 71A050002-1001)

The camera assembly is shown in Figure 27. Camera and pitch axis assembly is supported by forks from the yaw axis assembly. Wiring for TV camera and pitch position encoder are flat cables secured to one of the forks.

PITCH AXIS (P/N 71A050002)

The pitch axis assembly is shown in Figure 28. Pitch shaft (-27) is supported on bearings in both forks. Bearings are fully retained in both forks. The pitch axis torque motor (Inland T-5135, 4 lb.-ft.) is mounted in the -49 fork, pitch position encoder (Baldwin 5X232BL) and pitch stops in the -51 fork. The pitch stops, -39 and -41 permit ± 60° rotation (from horizontal) with the yaw axis vertical as in Figure 27 or horizontal. A removeable pin is provided to lock the pitch axis in the horizontal position.

YAW AXIS (P/N 71A050003)

The yaw axis assembly is shown in Figure 29. Fork supported block is mounted on -57. Yaw shaft (-59) is supported by 2 bearings the lower of which is fully retained, the upper is free to move axially in the support housing (-65). The yaw torque motor (Inland T-5730, 7 lb.-ft.) and yaw position encoder are mounted within the support housing. Stops (not shown) limit yaw travel to \pm 90°, and a removable pin locks the yaw axis at 0°.

OPTICAL ELEMENTS AND MOUNTS

The optical elements layout is shown in Figure 30. The attach points are located as shown in Figure 31. The -1 base plate is mounted on -27 pitch shaft and provides the mount for the non-linear lens (T-054427-1) the relay optics, and television camera. The optical centerline of the non-linear lens is 2 inches below the pitch axis. The axial position of all relay optics except a field lens mounted in -79 shown in Figure 30 is adjustable along the optical axis. Folding mirrors are adjustable about 2 axes. A cover (not shown) is provided and is attached to the -1 base plate.

4.2 PROJECTOR ASSEMBLY (P/N 71A050003-1001)

The projector assembly is shown in Figure 32(a) and (b) and is supported by 71A050004 support structure shown on the upper part of Figure 32(b). The yaw axis bearing is a single 25 inch I.D. "x" section bearing designed to carry moments as well as axial and radial loads. This support method was selected to preclude a long yaw axis shaft (and a pair of conrad type bearings) and permit mounting the entire assembly within the dome. The yaw torque motor

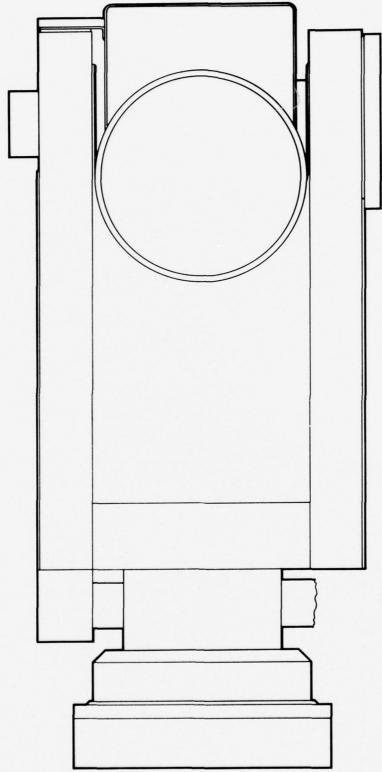
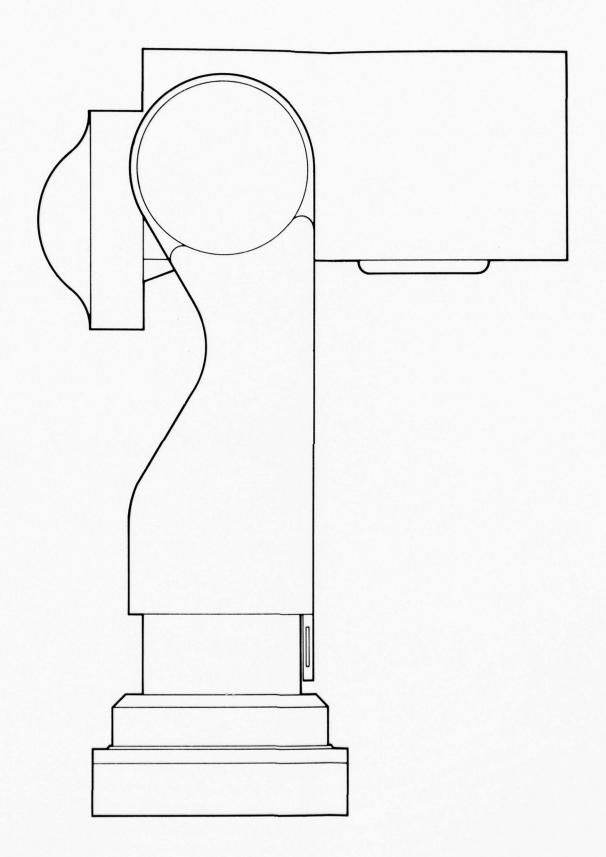


Figure 27 Camera Assembly

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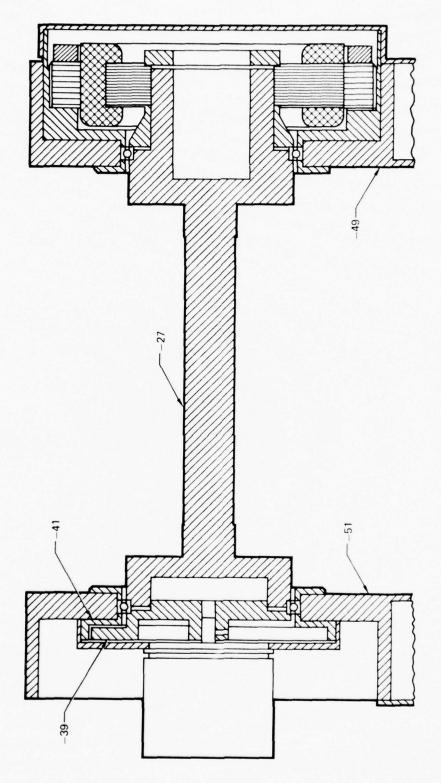
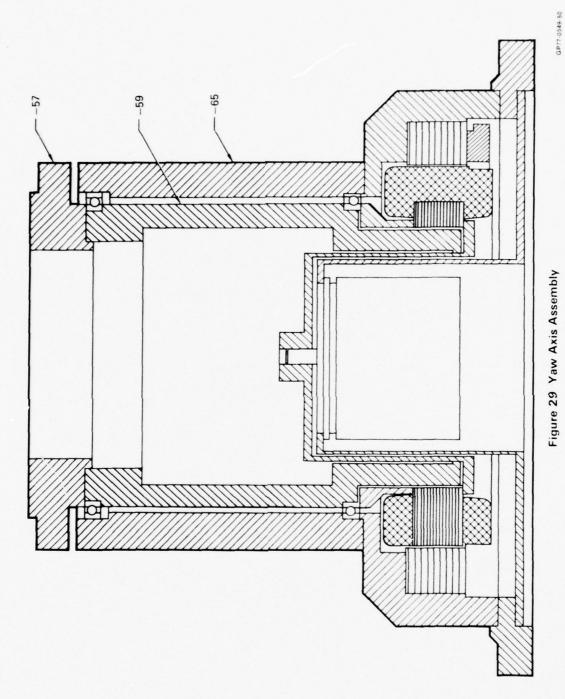


Figure 28 Pitch Axis



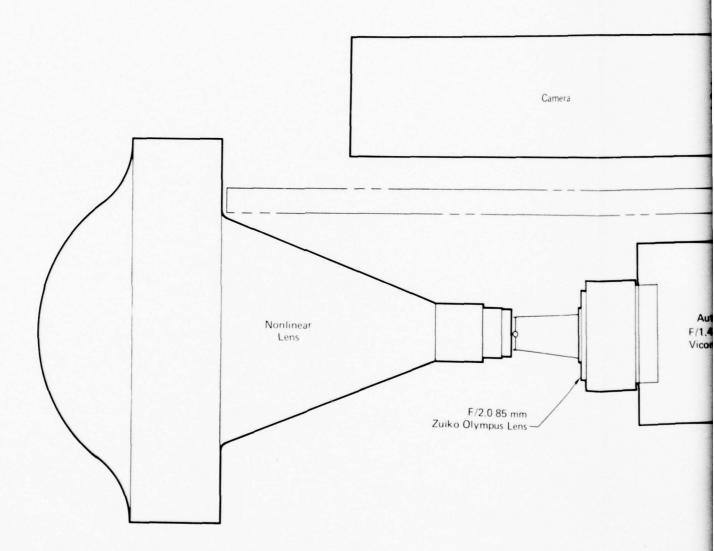
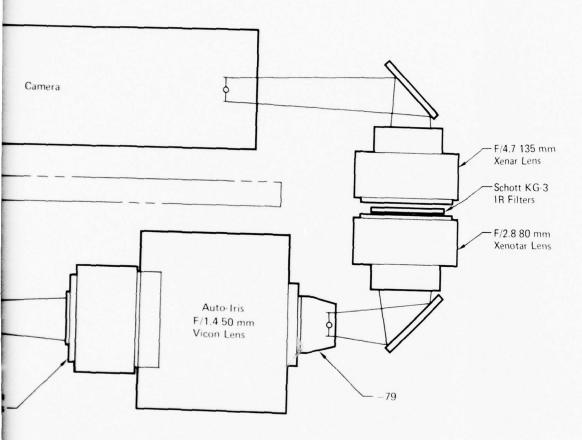


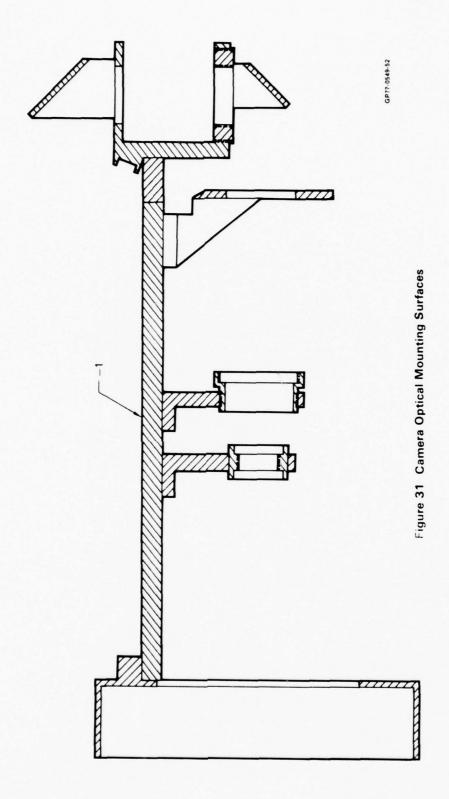
Figure 30 Camera Optical Elements

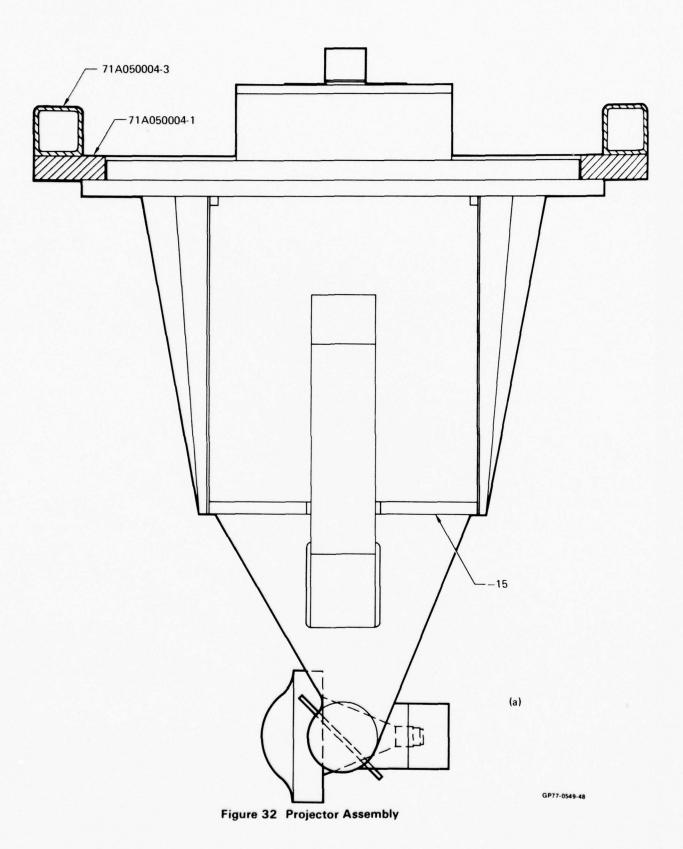
GP77-0549-51

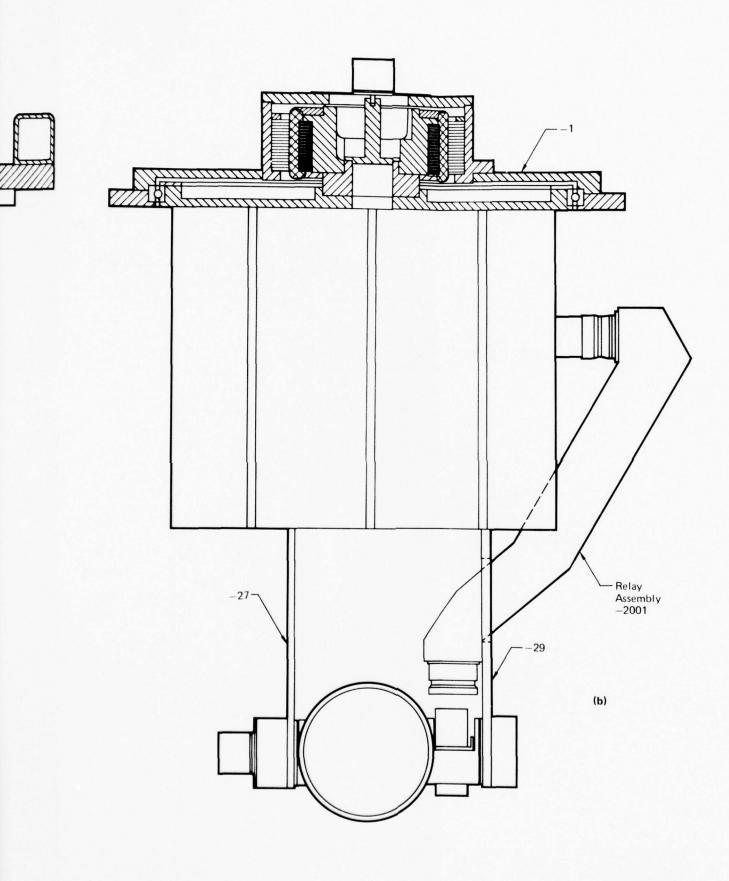


-0549-51

2







(Inland T-10035, 100 lb.-ft.) and yaw position encoder are mounted on the -1 plate above the bearing. The projector is mounted within a box structure supported by the yaw bearing. Holes in the box at appropriate locations provide access to projector controls. Bottom plate of the box supports the pitch axis forks. Yaw travel is limited by stops (not shown) on the 71A050004-1 plate to approximately $\pm 120^\circ$. The stops are spring loaded to provide essentially uniform deceleration for 15° of rotation (of the yaw axis) before becoming "hard" stops. Limit switches short the yaw motor just before engaging either stop.

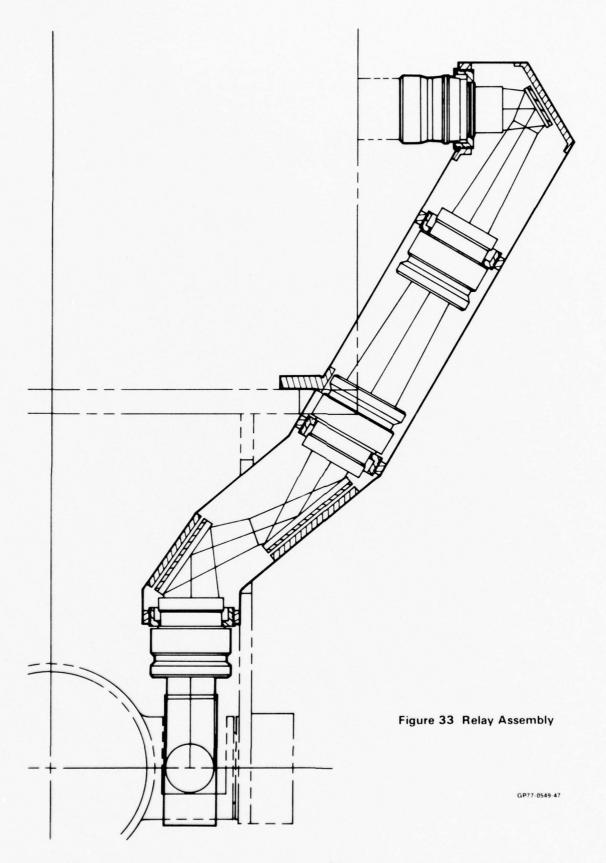
Fork arms (-27 and -29) support the pitch axis assembly. The -29 fork and bottom plate (-15) of box sturcutre support the -2001 relay assembly.

RELAY ASSEMBLY (P/N 71A050003-2001)

The relay assembly is shown on Figure 33 and supports and locates 4 of the 5 required relay lenses and 3 of the 6 required mirrors, the remaining lens and mirrors are mounted within the pitch axis assembly. All lenses in the relay assembly are adjustable along the optical axis, and all mirrors are adjustable about 2 axes.

PITCH AXIS (P/N 71A050003)

The pitch axis assembly is shown in Figure 34. The -27 fork mounts the pitch axis torque motor (Inland T-2950, 1.2 lb.-ft.) and pitch position encoder (Baldwin 5V232BL). Stops are provided to limit pitch travel to +60° (from horizontal). A removeable pin (in the -29 fork) locks the pitch shaft in the horizontal position. A "half angle" drive is provided for the relay mirror mounted on the -55 mirror support. The "half angle" is obtained by a differential on the -29 fork. A ring gear (PIC N3-4-5) is fixed to the differential case (-99, -47, -45), a second gear is fixed to the pitch shaft (-43, -39). The planet gears, to which is mounted the "half angle" mirror (-51, -53, -55), rotate in the same direction as the pitch shaft but at one-half the angular rate. The mirror may be "zeroed" by rotating the -47 cover with respect to the -45 housing. The -109 ring is the mount for the corrector lens (not shown).



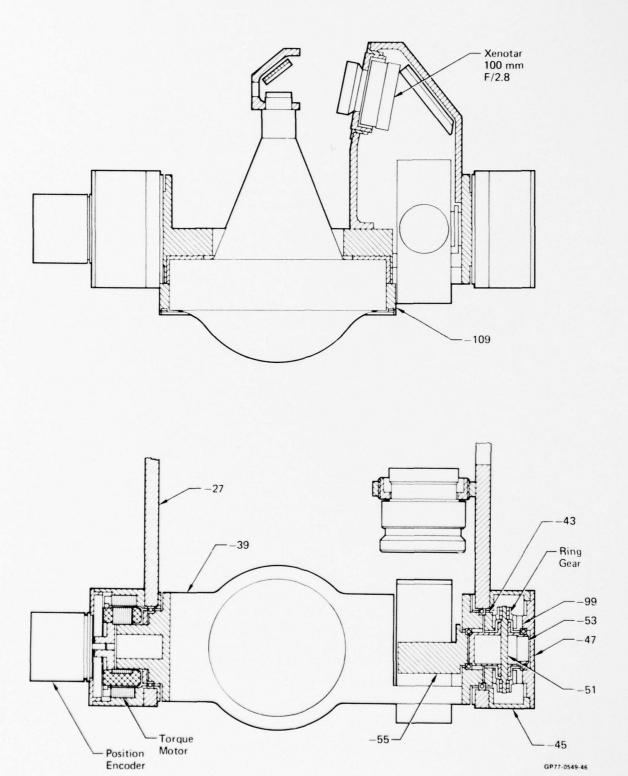


Figure 34 Projector Pitch Axis Assembly

Section 5

CONTROL SYSTEM

The function of the Control System is to command the projector to follow changes in camera angle. Camera angle changes are commanded by changes in operator head position or joy stick input. It consists of these major parts; the microprocessor, the camera electronics box, and the software.

The general design of the control system has been done digitally. The digital design of this system is in general immune from the kind of problems such as drift and error due to the manufacture of position and rate signals that beseech common analog servos. The mathematic production of rate signals from position data and digital (PCM) transmission of control signals eliminate many noise and signal related problems, although some signal errors still show up. In the case of a digital system these errors show up in varying degrees. For example, a lower order bit could be dropped and probably not be noticed by the system, but the system would surely jump if the sign bit or one of the higher bits suddenly is in error. Filters and other protective software have been programmed to help smooth out the results of such signal errors.

Figure 35 is a block diagram showing the camera and projector servos, the microprocessor which is located at the home station and the camera electronics box at the remote site. The system uses serial data to communicate between the microprocessor and the camera electronics box. Figure 36 shows a more complete block diagram of the hard wired control system. The microprocessor allows the system to be operated in three basic modes:

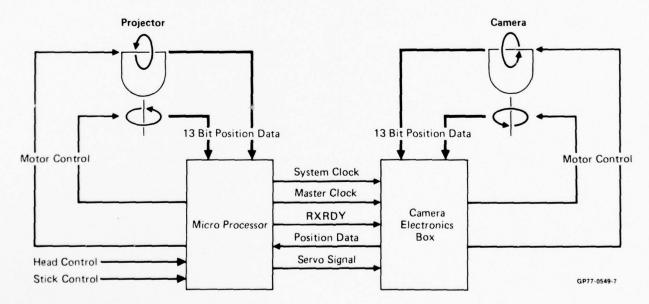
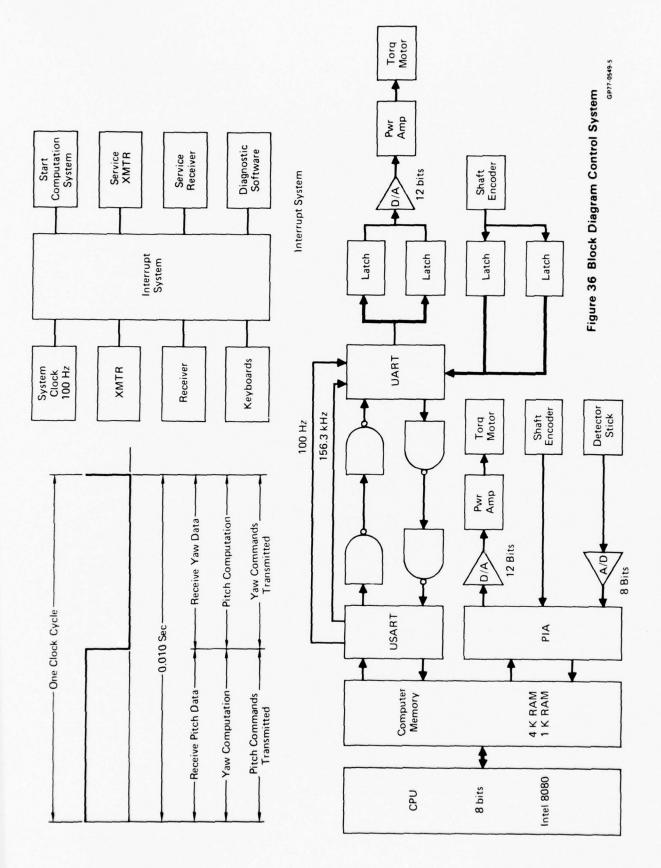


Figure 35 Servo Control Block Diagram



- MODE 1) Camera servo fully operational, the projector axes are pinned and the system is joy stick controlled. In this mode the high acuity spot is stationary while the "whole picture" moves about the dome.
- MODE 2) The camera and projector servos are fully operational. The display picture is stabilized and the system is joy stick controlled. In this mode, the high acuity portion of the image is slewed about, using the joy stick control to the point of interest while the picture as a whole is stationary.
- MODE 3) Camera and projector servos are fully operational and head controlled. This is generally the preferred mode of operation and the display is the same as in Mode 2 except that the camera and projector follow-up are controlled via a helmet mounted position detector.

These three modes allow the user to tailor the remote viewing system to his own particular needs.

This section contains a description of the control system and is divided into task oriented subsections which are: a description of the microprocessor, the camera electronics box, and software. In addition, included is a section on the head tracker and a section on the math models on which the software is based. Finally in the last section are system operation procedures.

5.1 MICROPROCESSOR HARDWARE

The basic microprocessor is the Intel 80/10 packaged in the SBC80 Modular Backplane/Card Cage with an I/O expansion board and prototype board. Diagnostic hardware, real time interrupt logic, power supplies, and some analog hardware were integrated with the Intel SBC 80 into one package resulting in a mini-computer for the Remote Viewing System.

The 80/10 Intel microprocessor board contains:

- 1 8080A Central Processor
- 1 8251 Serial I/O
- 1 8255 Parallel I/O
- 4 8708 1K-PROM-UV eraseable 1 K BYTES OF RAM

and line drivers and terminators. In addition to the hardware listed, a computer emulator and PROM programmer were available at the suppliers for scheduled use.

5.1.1 Diagnostic Hardware

The hardware consists of two hexadecimal keyboards, 5 hexadecimal LED displays, address comparators, and miscellaneous gates and logic and is shown in Figure 37. One keyboard is implemented as a function keyboard via the software and the other keyboard is implemented as a data/address keyborad via the software. The keyboards and LED Displays are front panel mounted on the computer. The rest of the hardware is mounted on the back of the front panel and on the prototype board.

Real Time Interrupt Hardware

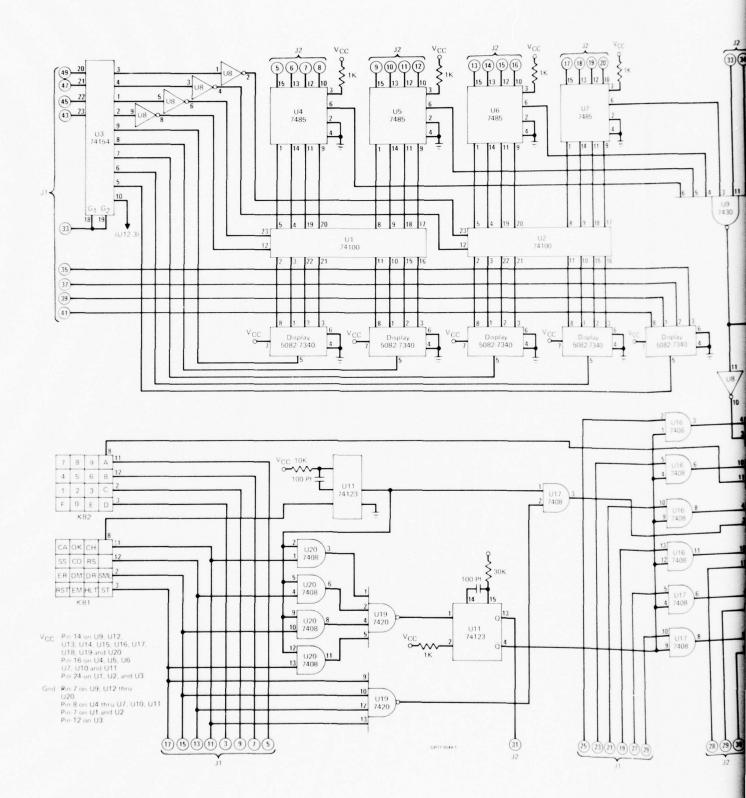
The processor has provisions for six real time interrupts. They are designated MCLR, RXRDY, TXRDY, KB1, KB2, AND COMPARATOR on Figure 37. The basic interrupt channel is shown in Figure 38.

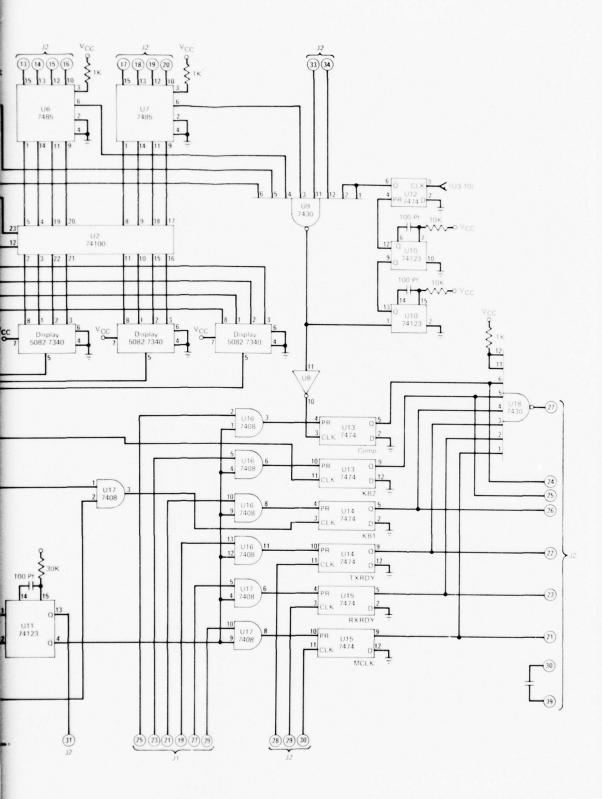
The computer controls the active status of the interrupt channel by inputs to the channel enable and reset gate. When the channel is active, an interrupt from an external device sets the Q output of the flip-flop. The Q output generates an interrupt pulse to the computer and sets a bit in the computer interrupt input port. The computer software interrupt handler service routines polls the interrupt input port to determine the source of the interrupt and thereby takes the desired path. During this time all other low priority interrupts are disabled. For example, if an RXRDY interrupt came in while a TXRDY interrupt was being serviced, it would not recognize the RXRDY request until the TXRDY service was completed. High priority interrupts from the keyboards are always active.

All six interrupt channels operate in the same way and are mixed together at the eight input NAND Gate. The output of this gate drives the computer interrupt line. The hardware involved in an interrupt channel is a computer output port, computer input port, and gate, and a Flip-Flop.

Operation of one interrupt channel can be described as follows. The computer has instructions which enable and disable the external interrupt line. With the interrupt line enabled, the software sets the bit that is assigned to the channel being discussed. This bit appears as input to the 7408 gate as shown in Figure 37. The line from Reset is normally high and so the input to the preset channel of the Flip-Flop is high and active. The inputs to the NAND gate are all high and the channel is ready to accept an interrupt. An interrupt from an external source causes the Flip-Flop to go low. The Flip-Flop output causes the NAND gate output to go high generating a computer interrupt and it also sets a bit assigned to this channel at a computer input port.

Software samples the input port, determines which interrupt channel has requested service, sets the active status of all interrupt channels according to the priority level of the interrupt that has just occurred and proceeds to service the interrupt. When service is complete, software resets the output port which sets the Flip-Flop and then sets the output port so that the channel is again active. It also resets the other channels and





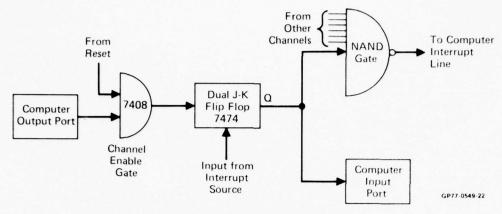


Figure 38 Interrupt Channel

makes them active. In summary, the output of the Flip-Flop is normally high, goes low when an interrupt occurs, stays low during software service, goes high when software is finished, and becomes active when the 7408 and gate output is high.

Restart

The restart function key causes U-19 on Figure 37, to change its output which in turn causes U-11 to trigger. The output of the one shot U-11 goes to the computer reset line which causes the computer to go to memory location zero. No interrupt is generated. When other keys are depressed, an interrupt is generated via U-17 as described above. In addition the keyboard output is routed via J-1, pins 17, 15, 13, 11, 3, 9, 7 and 5 to a computer parallel input port. As a consequence the software can determine which key was depressed and perform the necessary functions.

LEDS and Comparators

Circuits driving the LED displays and compare address functions involve the components U-1, U-2, U-3, U-4, U-5, U-6, U-7. J-1 pins 35, 37, 39, 41 are a computer output port on which the computer outputs the data desired to write to a specific LED. The software then outputs the code on pins 43, 45, 47, 49 of J-1 which causes U-3 to select the appropriate LED. The pins of J-2 are the address bus of the computer. When the comparators U-4, U-5, U-6, U-7 "see" the address set on the latches U-1 and U-2, a computer interrupt

is generated via U-9. The one shots U-10 and U-11, reset U-12 so that the gate U-9 is disabled after the compare address has been executed. U-12 enables the gate U-9 when the software selects it via U-3. In summary, software loads the comparators with the desired address similar to the previous discussion about LED's and then software enables U-9. When the address appears at the comparators, U-9 generates an interrupt and the one shots U-10 disable the U-9 gate. Pins 34, 33 of J-2 are signals from the computer which enable U-9 only when an address is on the address bus of the computer. This is necessary since other computer data appears on the address bus and creates a timing problem solved by these inputs.

Input/Output PORTS

The system uses a total of eight input ports and five output ports. They are assigned as shown in Figure 39. The computer low order bits is shown at the right in the figure. High priority interrupts are at Input Port 1, low priority interrupts come in at Input Port 3. The A/D converters start the A/D conversion process when SYS CLK goes positive. The software checks Port E4 to verify that conversion is complete before reading the data at Ports E5 and E6.

The low order LED is selected by a 4 at port E8 and toggling bit 6 at port E-A.

5.1.2 Diagnostic Software

Intel has a computer emulator designated as ICE-80 which is used with the Intel MDS system. The in-circuit emulator interfaces to any user configured 8080 system. With the ICE-80, the designer can emulate the system 8080 in real time, single step the system program, and substitute Intellec memory and I/O for user system equivalents. It will provide address data and 8080 status information on the last 44 machine cycles emulated. It allows the user to share Intellec memory and I/O facilities and is indispensable for initial debugging. The ICE-80 was used with the RVS during Monitor Program debugging.

The RVS microprocessor has diagnostic software referred to as the Monitor Program designed primarily to facilitate operational program checkout and for enhancement of computer operation. One PROM in the processor is devoted to the Monitor Program. It is the only program input/output the computer has. Initial checkout of the processor monitor program utilized the computer emulator available on the Intel MDS System. After the Diagnostic Software checkout on the emulator was completed, it was used to troubleshoot other PROM software.

PROM #3 is devoted to the Diagnostic Software. It is stand alone software. While the computer is operational with the system software, Diagnostic Software is not used. The Monitor program is accessed when the operator depresses the Halt Key. Exit from the diagnostic software is accomplished when the Return Key is depressed.

The primary purpose of the monitor program is to implement keyboard functions which allow the operator full utilization of processor capability. Under monitor, the operator can display the contents of all memory positions, program RAM, single step the processor, and access all processor registers.

The Monitor Program contains all of the diagnostic software required to couple the two keyboards with the processor. All keyboard functions are implemented by software as opposed to hardware. Keyboard generated interrupts are routed via the interrupt handler to the monitor. Keyboard 1 is a function keyboard and Keyboard 2 is for data in hexidecimal. When a Keyboard 1 key is depressed the monitor jumps to the appropriate routine corresponding to the function represented by the key. Concurrently, the interrupt handler has

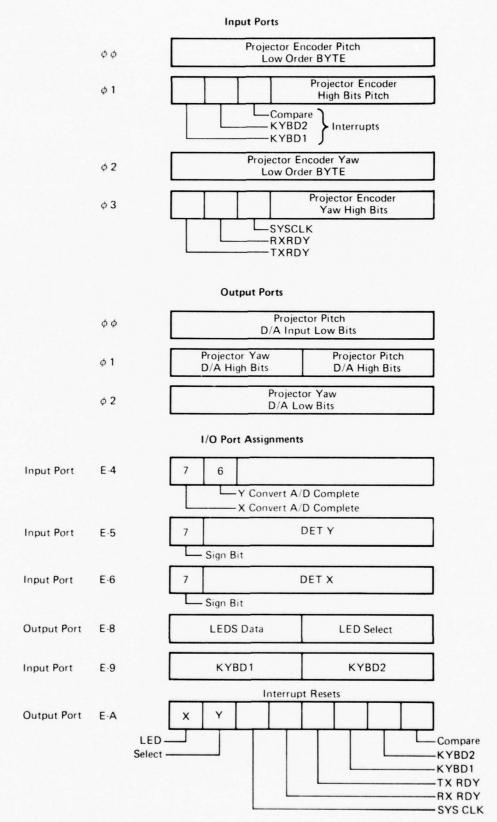


Figure 39 Computer Input and Output Ports

GP77-0549-23

disabled all other interrupts so that once a keyboard interrupt has occurred they have priority. This assumes that the operator desires complete processor control. When the operator has finished his input, depressing the RST key, returns the machine to the program with all interrupts enabled.

Figure 40 is a brief explanation of keyboard functions implemented. These functions allow the operator to display contents of all memory locations, to load data into RAM, to display the contents of registers and load registers, to halt the program, to single step the program, and to stop the program at a specific program address. The SML Key used with the OK and Change Key allow the user to load or change any of the fixed multiply constants. The monitor generates fast multiply routines for each multiply constant and loads RAM with these routines. Twenty multiply constants are programmed and loaded in RAM from 3D90 to the top of RAM.

The stack pointer starts at 3CFF. The lower portion of RAM is assigned to the stack. Scratch pad RAM starts at 3D00 to 3D90. Since there are 608 locations above 3D90 and only 400 are required by the multiply routines, residual memory is available at the top of RAM.

An example of monitor will illustrate how it works. The example will illustrate how to examine a memory position (i.e., display contents of memory on the LEDS). First the operator presses the Halt button. Monitor recognizes the interrupt, halts the computer and displays on the LED's the address of the next program instruction that will be executed. Next the operator presses the display memory key. Monitor determines that the display memory function is required. It displays a 2 on the highest order LED indicating that the EM key was depressed. Then it prepares to fetch a memory location, and then halts and waits for the operator to proceed. Next the operator depresses in succession 4 keys which are the memory address entering, highest order hexidecimal number first. Monitor then moves the memory contents of that position to the LED display and waits for the next keyboard instruction. In summary, the operator presses Halt, EM, XXXX, on the numeric Keyboard and Monitor displays on the LED's the contents of memory location XXXX. If the operator wishes to see the next position he presses the continue button. This button sequences thru memory one step at a time executing the function initially loaded (i.e., deposit, or examine memory).

Keyboard Functions

The following describes the keyboard functions:

Reset

Reset causes the processor to start at location zero. The PROM program at location zero initializes the problem (see related section under software) and then the processor is programmed to Halt. This allows the operator to do necessary tasks prior to system operation. Subsequently, the operator causes the processor to proceed by depressing the <u>Start</u> key.

Examine Memory

Displays the contents of memory on the LED's.

Keyboard Mnemonic	Meaning	Description					
HLT	Halt	Pressing this key causes an interrupt, sending program control to the diagnostic software.					
RST	Reset	Hardware reset. Restores program counter to zero.					
EM	Examine memory	After pressing this function key, the diagnostic software will expect four hexadecimal numbers to be input from the data keyboard, indicating the address to be examined. It will then display the contents of that memory location.					
DM	Deposit memory	The DM routine expects six entries from the data keyboard. The first four of these are formed into the 16-bit address and the last two form the 8-bit data byte to be stored.					
со	Continue	Following an EXAMINE MEMORY or DEPOSIT MEMORY, the operator may automatically increment the address pointer by pressing CONTINUE. The software will then display the contents of this new location or will be ready to accept two hexadecimal digits for data entry.					
ER	Examine register	After pressing ER the software expects one hexadecimal digit from the data keyboard indicating which of 8 registers is to be displayed. The routine will then display the contents of this register.					
DR	Deposit register	After pressing DR the software expects three entries from the data keyboard, the first digit indicating which register is to be modified, and the last two digits formed into the 8-bit byte to be moved into the register.					
		The registers are given the following numerical assignment: Register Number B 0 C 1 D 2 E 3 H 4 L 5 A 6 PSW 7					
RS	Return	(Processor status word) By pressing this key, the software will restore all register contents and condition bits to their values prior to entering the diagnostic software and will then return program control to the location being executed prior to entry into the diagnostics.					
CA	Address compare	This function uses comparators to compare the address bus to a software stored 16-bit number. After pressing this key, the software will expect four entries from the data keyboard which are formed into the 16-bit number loaded into the comparator. A RETURN is executed automatically by the software and upon occurrence of the inserted address, program control is returned to the diagnostic software.					
SS	Single step	After pressing this key, the software will automatically execute the RETURN routine and will execute the instruction prior to entering the diagnostic software. Program control is then returned to the diagnostics.					
ST	Start	Causes the processor to return.					
SML	Set Multiply Constant	This function expects the OK or change key to be depressed. If change is signaled it expects two hexadecimal entries. It will then generate a fast multiply routine load it in RAM and display the next multiply constant for the operator to OK or CHANGE. Twenty constant must be approved.					
ОК	Okay	Indicates to SML approval of constant.					
СН	Change	SML expects two hexadecimal numbers to be input from the data keyboard. SML then generates multiply routine from numbers loaded and loads in RAM.					

Figure 40 Display Processor Keyboard Explanation

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Halt Causes the computer to stop and wait for a

keyboard input. Halt displays the next instruction

address.

Examine Register Displays register contents on LED's.

Deposit Memory Allows user to load any RAM position.

Deposit Register Allows user to deposit register.

Set Multiply User may change any multiply constant. The next

constant is displayed on the LED's. Once this mode is entered all twenty multiply constants must

be OK or changed.

Single Step The computer executes one program step. The

address of the next instruction is shown on the

LED's.

Continue Is used with the Examine memory, a Deposit memory

function. It sequences to the next memory position implementing the same function used

previously.

Compare Address The computer stops at the desired address. The

next instruction is displayed.

OK Is used with SML. It leaves the constant unchanged

and the next constant is displayed.

Change Is used with SML. The user enters the desired

constant on Keyboard 2.

ST After pressing this key, the software will execute

the RVS control software.

5.2 CAMERA ELECTRONICS BOX

The Camera Electronics Box (CEB) interfaces the remote camera shaft encoders and servo amplifier via the serial data transmission line to the home station microprocessor. The CEB's primary function is to send and receive data. It sends gimbal position data to the microprocessor and receives servo-motor commands from the microprocessor.

The transmitter system is split into two identical sections, one handling pitch axis data, and the other yaw axis data. The transmitter section sends the 13 bit shaft encoder word (one for each axis) up the serial line in two eight bit words to the processor. The first byte contains the eight low order bits, the second byte contains the remaining five higher order bits. The 3 excess bits (the highest bits unused) are set to zero.

The receiver system, like the transmitter is split into two identical subsystems; one for each axis, pitch and yaw. The receiver subsystem output consists of two 8 bit parallel-parallel data latches which are input to two 12 bit digital-to-analog converters.

The heart of the camera electronics box is the Universal Asynchronous Receiver/Transmitter (UART). This device is an LSI subsystem which accepts parallel binary words consisting of 5 to 8 data bits, and outputs them as serial words with one or two stop bits and a parity option. The UART is a single monolithic chip, is TTL compatible and its strobed outputs are tristate logic.

Block diagrams of the UART's Transmitting and Receiving sections are shown in Figure 41(a) and (b).

5.2.1 System Clocks and Timing

The basic computation cycle (M-clock) runs at 100 Hz. The system clocks runs at 153.6 KHz, the frequency required by the UART to establish a baud rate of 16. This is the maximum asynchronous baud rate of the Universal Synchronous Asynchronous Receiver/Transmitter (USART) and UART. The transmission of one byte takes approximately 1.2 µsec. Two bytes per half cycle of M-clock are required, thus the timing margin of the system is approximately 50%. No measurements were made of the computation cycle length but some results indicated the computer timing margin is greater than 50%. Thus, the serial transmission line determines the maximum M-clock frequency.

Increasing M-clock would allow the design of a wider bandpass system, however since many factors must be considered (i.e., motor saturation, noise levels, accuracy, load disturbances) there is not a clear cut ratio between bandpass and M-clock frequency.

The initial design proceeded with an M-clock of 100 Hz, thus the basic sample rate of the system is 10 msec, 5 msec for each axis. Synchronous transmission was considered but preliminary work indicated that it might prove difficult to operate a 400 ft. transmission line in the synchronous mode. The asynchronous mode allowed design flexibilities because the 156.3 KHz clock could be a local oscillator or it could, if feasible, be sent over the transmission line. The final design sends the system clock over the transmission line. This required careful attention to the line driver selection and impedance matching of the receiver.

5.2.2 Control Logic

The control logic routes the incoming and outgoing bytes to the transmitter and receiver sections.

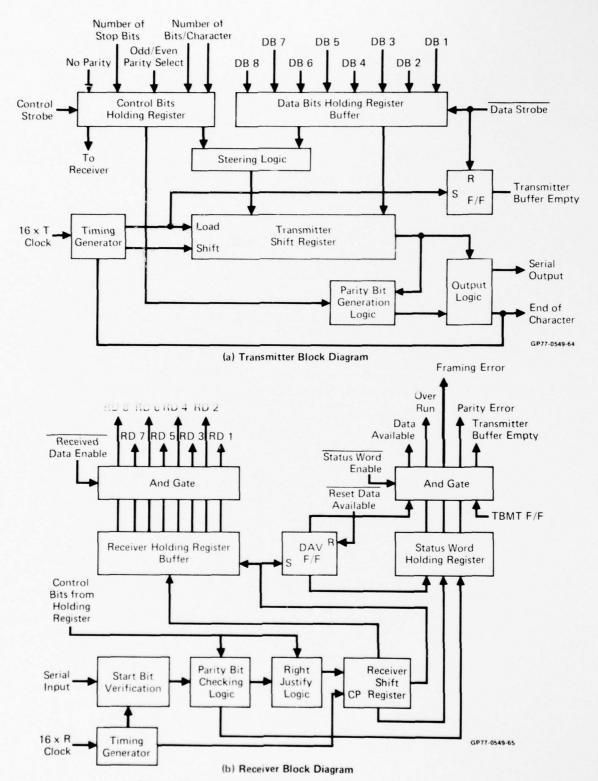


Figure 41 Universal Asynchronous Receiver/Transmitter

Transmitter Section

The control logic is symmetric for both axes. Figure 42 contains a block diagram of the CEB Design. The pitch axis is enabled by the positive

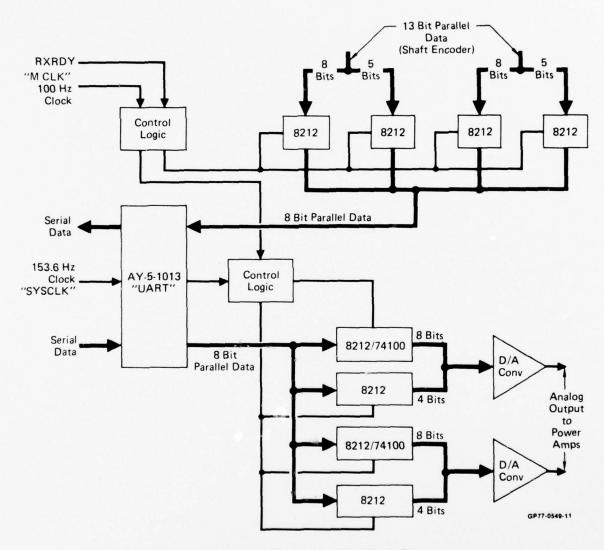


Figure 42 Camera Electronics Box Block Diagram

M-clock and the yaw axis is enabled by the negative M-clock. When M-clock goes positive the pitch axis encoder output is stored in data buffers, (the shaft encoder output continually tracks shaft position). The first buffer, (the low order bits) is strobed onto the data bus, while the other three latches are in the high impedance state. The falling edge of the data strobe (DS) pulse on the UART causes the shift register to transmit the data out on the serial output line. When the first byte of the transmission is complete, a real time interrupt is generated at the microprocessor. The microprocessor services the interrupt and generates a RXRDY pulse to the CEB. Upon receipt of the pulse, the second byte is strobed onto the data bus to the UART and the sequence is repeated. The second RXRDY pulse sent down by the microprocessor is ignored by the CEB and the box now waits for M-clock to go negative and then sends up the yaw information in the same manner. In summary, for each half cycle of the M-clock the encoders are read, stored in latches and sent to the microprocessor in two eight bit words. These bytes are received by the microprocessor and stored in memory and the microprocessor acknowledges receipt of these words to the CEB via the RXRDY pulse.

Receiver Section

The receiver section is independent of the transmitter section including UART functions, thus allowing for complete asynchronous operation. When M-clock goes positive, the microprocessor initiates transmission of the first (high order bits) pitch axis command byte. When this transmission is complete, the receiver's control logic strobes the first byte into a buffer. The microprocessor then initiates transmission of the second byte. Upon completion of the second byte transmission, the control logic loads the second byte into a buffer and then it inputs both bytes to the digital-to-analog converter for the pitch axis. The D/A (12 bits) receives a full word at one instant in time just after the receiver has loaded both bytes of information into data storage. This word remains on the D/A input until the end of the next cycle of the M-clock.

5.2.3 Camera Box Electro Mechanical Description

The camera electronics box is connected to the microprocessor via five twisted pair cables. The processor supplies the system with:

- 1) System clock 153.6 KHz
- 2) M-clock 100 Hz
- 3) RX Data Servo-amp command signal
- 4) RXRDY Microprocessor acknowledgement of receipt of Position Data

The camera box sends

1) TX Data - A Position Data down to the SBC 80 microprocessor.

The shaft encoder words are brought to the CEB from the gimbals using 2-18 wire ribbon cables and DB25 connectors, and the D/A output is sent to the power amp over two twisted pairs, through an MS3106-14S-4P connector. The power amp outputs are run in separate cables to the torque motors. Thirteen (13) bit Baldwin shaft encoders are used to determine shaft position. Figure 43(a) shows the CEB LAYOUT and Figure 43(b) shows the board layout. Component descriptions shown in the board are listed in Figure 44. Figure 45 and 46 are schematics of the transmitter and receiver sections, respectively of the CEB.

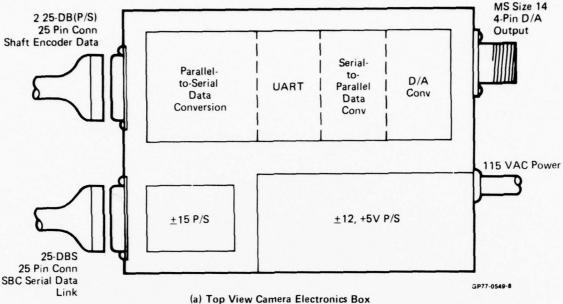


Figure 43 Camera Electronics Box

5.3 SYSTEM SOFTWARE

Each of the four PROMS are assigned a system software function for ease of PROM management. PROM #1 (memory locations 0-3FF) is devoted to the system initialization and the interrupt handler. PROM #2 (memory locations 400-7FF) contains the Yaw Axis control equation software. PROM #3 (memory locations 800-BFF) contains the diagnostic software. PROM #4 (memory location (COO-FFF) has the software for the system pitch axis. The detailed line by line listing of the software for all of the PROMS is included in Appendix D. Software flow diagrams are shown in Figure 47.

5.3.1 PROM Programming

An Intel 8080 Cross assembler is available on the PDP 11 Digital Equipment Computer. To program a PROM, a source program is created on the PDP 11 computer. It is assembled on the PDP-11 by the following commands into an 8080 binary language which is used as an input to the PROM programmer. Commands for useing the assembler are:

\$ AS KB:, CMI RU INXAS

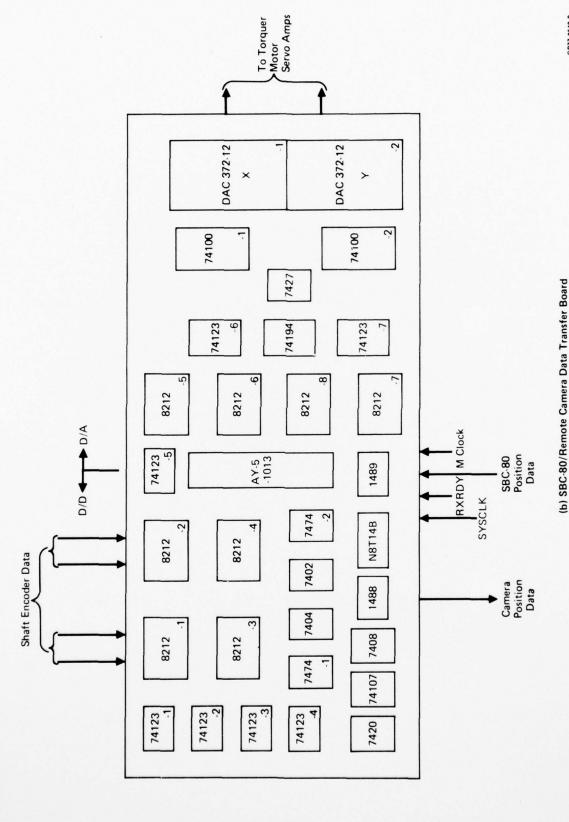
File, LP: < File since the assembler cannot handle a full PROM, a program MERGE can be used to link two programs together:

\$ AS File 1.0BJ, 1 AS File 2.0BJ, 2 AS File 3, 3 RU MERGE

Subsequently, the .OBJ files can be punched on paper tape with commands

\$ AS File 3, 1 \$ AS PP:, 4 RU CHANGE

Figure 43 Camera Electonics Box



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Qty	Part No.	Description		
8	8212	Eight Bit Input/Output Port		
7	DM74123	Dual One-Shot		
2	7474	Dual D-Type Flip Flop		
1	7404	Hex Inverter		
1	7402	Quad 2 Input NOR		
1	7420	Dual NAND		
1	74107	Dual J-K Flip Flop		
1	7408	Quad 2 Input AND		
1	1488	Line Driver		
1	1489	Line Receiver		
1	74194	4 Bit Shift Register		
1	7427	Triple 3 Input Positive NOR		
2	74100	8 Bit Latch		
2	DAC372-12	D/A Converter		
1	AY5-1013	Universal Asynchronous Receiver/Transmitter		
1	N8T14B	Line Receiver		

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Figure 44 Camera Electronics Box Component List

The resulting paper tape can then be read into the PROM programmer.

5.3.2 PROM #1, Interrupt Handler Software

As real time interrupts are generated to the microprocessor, it is routed to memory location 38, whereas the reset button causes the computer to start at memory location zero. As a consequence, memory locations zero thru 37 are devoted to the necessary housekeeping functions required to initialize the system. The processor than encounters a program Halt. The start button causes it to advance past the Halt where it enters the active system program. The program enables all interrupts and waits in a backward forward loop located at memory positions 30 and 33 for interrupts. A listing of the program is shown in Figure D-1, Appendix D.

Interrupts

There are three system interrupts; system clock, receiver, and transmitter. The system clock generates an interrupt every 0.010 sec. Each system interrupt causes the computer to proceed thru the yaw and pitch control equations and update the system commands. Concurrently, the receiver and transmitter send data over the serial transmission line link to the remote camera. The receiver interrupt causes the computer to store the received word in memory. The transmitter interrupt causes the computer to load the transmitter with a new command word.

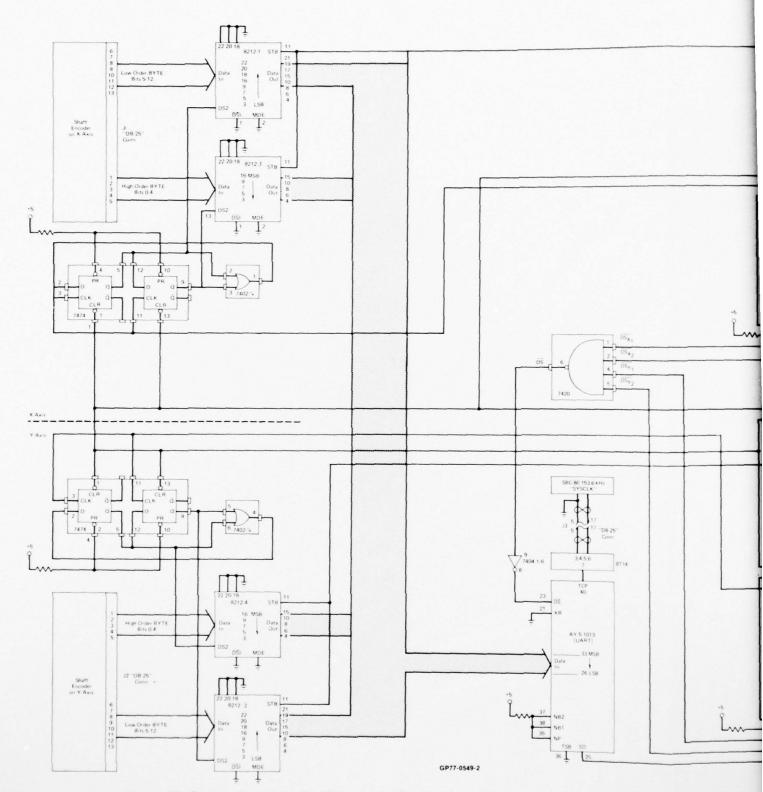
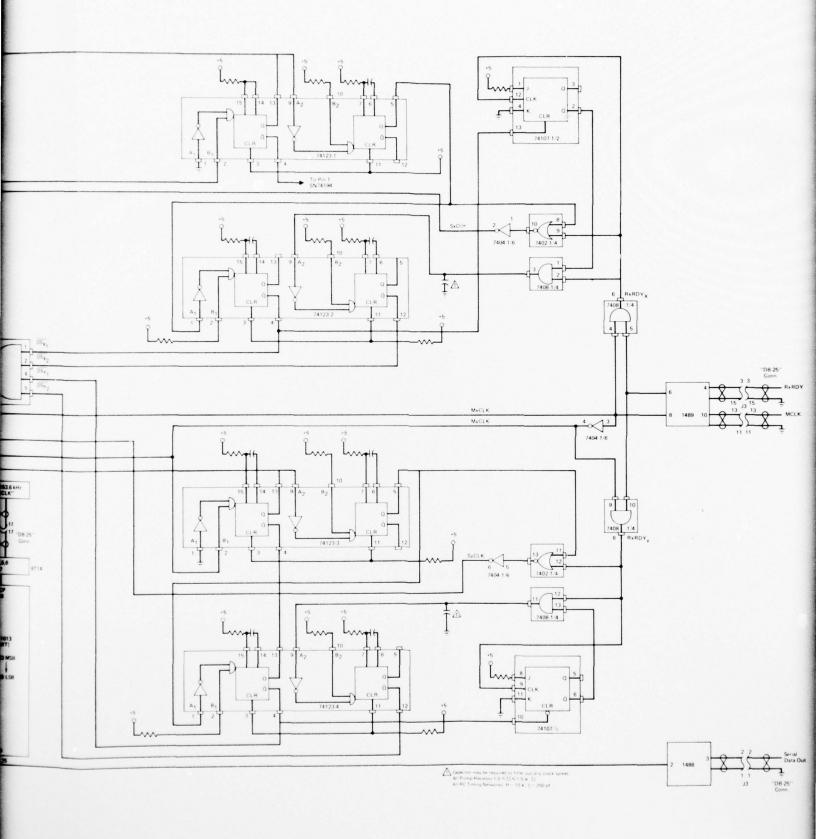


Figure 45 Camera Electronic Box Transmitter Circuit Diagram



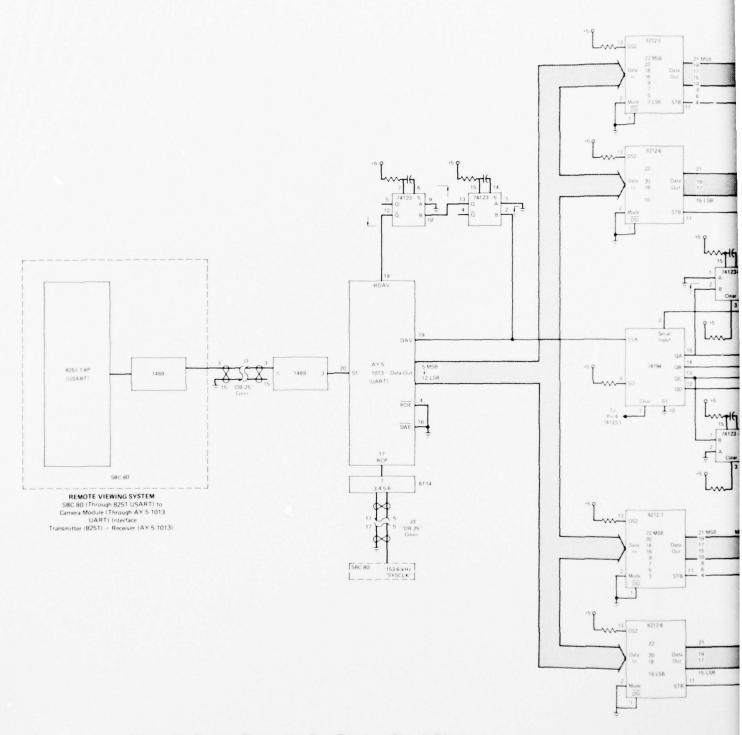
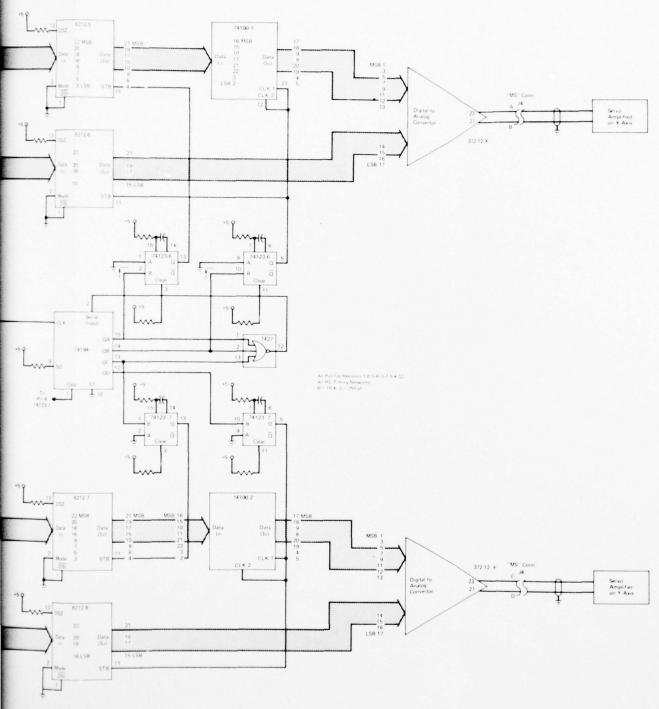


Figure 46 Camera Electronics Box Receiver Circuit Diagram

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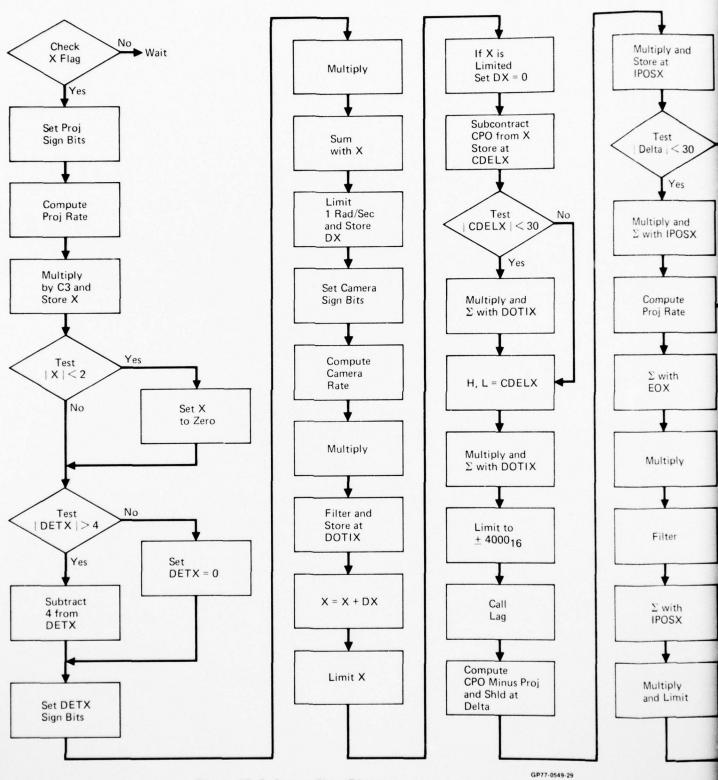
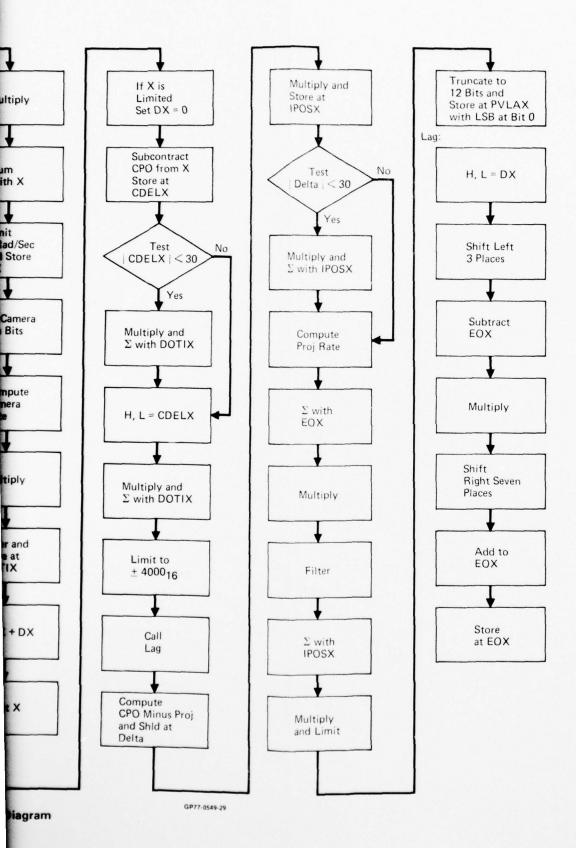


Figure 47 Software Flow Diagram



When an interrupt occurs, the following sequence is executed:

- 1) Save the status of the machine,
- 2) Poll the interrupt ports and determine the specific interrupt.
- Reset all interrupts, make appropriate interrupts active determined by priority of interrupt requesting service,
- 4) Jump to interrupt service routine,
- 5) Restore status of machine and return to sequence prior to the interrupt.

System Interrupt Service Routine

This routine is the primary or key interrupt which determines the system data sample rate. The following events happen after a system interrupt:

- Load the transmitter with a new word and load the transmitter counter used by the transmitter service routine
- 2) Initialize the receiver counter, Y Flag, and X Flag
- 3) Read the Projector Encoders
- 4) Output command updates to the Projector Power Amplifiers
- 5) Read the X and Y A/D's
- 6) Compute the Yaw and Pitch control equations. The output of these equations update system commands.

Receiver Interrupt Service Routines

This routine stores the word just received in memory and resets the receiver. The receiver is then ready for the next word. Initially, the service routine loads the receiver counter. Since the receiver normally reads four words per sample interval, the service routine checks the counter. If more than four words have been received an error has occurred. In this case, the data from the last cycle is loaded into the yaw axis data memory positions. If the counter is correct, the routine reads in the receiver contents and stores the data in the memory position indicated by the counter. Next the routine increments the counter and stores it in memory. When two words have been received, X Flag is loaded. This indicates that new data is ready for processing in the pitch axis control equations. If the third word has been received the routine loads the transmitter and returns to the previous program before the interrupt occurred.

5.3.3 Transmitter Service Routine

This routine loads the transmitter with words from memory each time the transmitter is ready to send a new word. The pitch data is sent out first since it is always ready at the beginning of a clock cycle. The routine loads the transmitter counter, decrements the counter and sends a word to the transmitter from memory. When the counter indicates Yaw commands (i.e. third word) the routine checks Y Flag. Y Flag indicates that the Yaw axis computation is done and that new Yaw commands are ready. If data is available the routine sends the data and returns the computer to the previous program.

5.3.4 PROM #2 Yaw Control Equations

These equations are on PROM #2 and start at location 400. Due to the complexity of the system every effort was made to keep the pitch and yaw equations alike. As a consequence PROM #4 except for changes peculiar to the pitch axis is similar in program flow to PROM #2. First the new data word is called from memory and the bits 14 thru 16 are set so that the computer treats the encoder as a double precision word with the LSB of the encoder located at the LSB of the computer. The compensation equations are calculated and placed in intermediate storage at CAMAY. CAMAY is limited to 1 rad/sec and is input to the integrator driving the camera. Next the camera servo equations are processed. These equations represent a rate command position hold servo. The camera encoder is converted to a double precision word with the LSB of the encoder corresponding to the LSB of the computer. The unfiltered first difference is computed and stored at location 3FFO. The filtered gimbal rate is limited and multiplied by the constant MYLVB. MYLVB is stored at location 3D82 as a double precision word with the decimal at the left with one sign bit. At location 4F9 the digital integrator sums in the update CAMAY and limits the integrator output at gimbal stops of +90°. These stops are inside the mechanical stops of the gimbal. The camera encoder is subtracted from the integrator output and stored at CDEL location 3D72. Next the position feedback is limited and multiplied by the constant MYLVK. Subsequently, the rate feedback signal is summed with the position signal. This sum is multiplied by TORQY and limited. It is stored at CCMAY as a double precision word. The transmitter service routine sends CCMAY to remote station where via the hardware it is truncated to a 12 bit word as input to the D/A driving the power amp. Next LAG is called. This subroutine is a filter which provides a signal to the projector. The signal provides accurate projector to camera tracking. The camera error is tested. If it is too large, the camera input is taken as the input to the projector. The program now computes the projector servo equations which are a basic position servo with a modified rate command from LAG. The program proceeds as follows: Beginning at memory position 696 the position feedback is calculated, limited and multiplied by MULBY. This result is stored at IPOSY. At location 6C5 an integral channel is implemented for small input errors. If the error is large the integral channel is bypassed. The output is summed with the contents of IPOSY and stored at IPOSY. Next the first difference of position is calculated and stored at 3FF2 with the LSB of the result at the LSB of the computer. This unfiltered difference is summed with EOY, the LAG signal mentioned previously and the result is limited and filtered to provide a suitable rate feedback signal. It is stored at 3D64.

This result is summed with the position feedback signal. The computer word has one sign bit and the decimal to the left in the double precision word. Since the hardware requires the decimal to the right, the software truncates the word to 12 bits and shifts the result to the right so that the LSB of the word is at the LSB of the computer. This result is stored at PVLAY. Y Flag is set to indicate that new data is at PVLAY. Next the routine checks if the transmitter has already tried to send the data. This is indicated by the high order bit of Y flag. If it has the routine it initiates the transmission. If not, the routine continues to the Pitch control. The computer listing is contained in Figure D-2.

5.3.5 PROM #3 Diagnostic Software

Discussed earlier in Section 5.1.2. Computer listing is contained in Figure D-3.

5.3.6 PROM #4 Pitch Control Equations

X Flag is tested to determine if new data has been received. If not the routine waits for new data. After new data has been received, the pitch equations are processed similar to the yaw equations described above. Different multiply constants are used. A complete listing of the equations are shown in the Figure D-4. In the software equations X is used to designate the Pitch Axis and Y is used to designate the Yaw Axis of the system. The new Pitch Axis commands are stored at PVLAX as a double precision word. At the beginning of the next clock cycle the transmitter service routine sends the new words to the remote station. At the end of the Pitch Axis equations, the computer has completed all required up data processing per clock cycle and returns to loop waiting for the next interrupt. Timing margins indicate that the next interrupt will be from the receiver and transmitter routines.

5.4 MATH MODELS

The Remote Viewing System servos can be operated in three different modes. They are:

- MODE 1) Stand alone servos closed around each gimbal.
- MODE 2) Camera as a rate command position hold servo with rate inputs from the stick. The projector in a position servo follower to the camera.
- MODE 3) Camera and Projector in closed loop with the head controller. This option includes capability to insert the stick control in lieu of the head controller without changing the control equations.

The first mode allows the camera to be used with the projector servos disabled. It simplifies system power up because the system is stable for all gain modes.

The second mode uses the stick control as input. It can be implemented by minor program changes in the microprocessor. It can be used to achieve accurate pointing and projector to camera tracking. It is ideally suited for fine pointing but is less advantageous for tracking moving targets.

The third mode is the final system configuration which provides helmet mounted control by the operator.

MCDONNELL AIRCRAFT CO ST LOUIS MO REMOTE VIEWING SYSTEM.(U) JUN 77 R W FISHER AD-A046 704 F/G 17/2 ONR-CR213-129-2F UNCLASSIFIED 2 OF 3 ADA 046704

5.4.1 MODE 1

Linear Transfer Function

A simplified linear model of the servo used for each gimbal is shown in Figure 48. The integral channel was implemented and used as required for fine pointing. The equivalent transfer function is:

$$H_1(s) = \frac{AKS + AM}{s^3 + ACs^2 + AKS + AM}$$
(66)

where

A = 1/I

K = ft #/rad

M = ft #/sec/rad

C = ft #/rad/sec I = Gimbal Inertia

Computer studies of ramp type inputs to the servo showed that the 100 ft. lb. torque motor on the projector azimuth axis was the system limiting factor and significant saturation occurred around 1 rad/sec. The servos were designed to minimize this saturation and provide the best possible frequency response. Figure 49 shows the response of the camera to a ramp input of

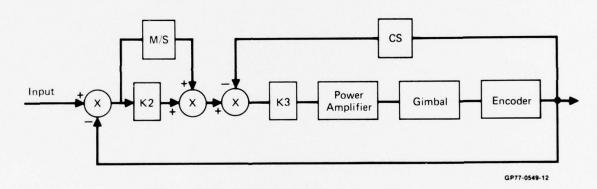


Figure 48 Mode 1 Servo Block Diagram

1 rad/sec for 0.25 sec. The camera lag is less than 0.1 sec as shown in the figure. This is consistent with an operators reaction time using stick control. Note that this lag does not cause image motion on the projector screen. The image will move because the projector remains stationary in Mode 1 operation.

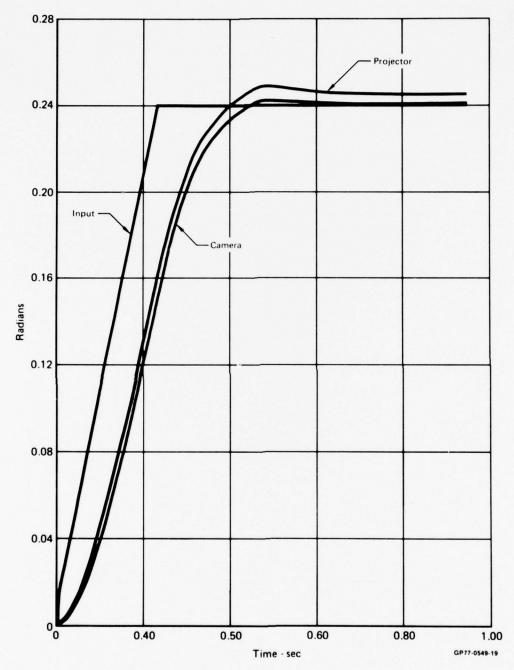


Figure 49 System Response for Stick Input Lag = 0.12 Sec

Gains

Important gains for each axis are summarized in Figure 50. Inertias shown in the table were results of measurements made when the system was first assembled. Subsequent changes in optics and mechanical design caused these inertia figures to change. Accurate information on inertias associated with the final design are unavailable. The channel gains shown in the table were used to derive the first estimate of computer gains cognizant of the effects of non-linearities. The important non-linearities in the system are saturation, threshold, and friction. These result in overall gain reduction and apparent increase in damping.

The gains of Figure 50 were required during software development. They served as a basis for software scaling and for sizing multiply routines. Subsequently, they were used during initial system checkout.

Name	Symbol	Units	Projector Azimuth	Projector Pitch	Camera Azimuth	Camera Pitch
Inertia	1	ft-lb/sec ²	1.58	0.052	0.28	0.113
Proportional Channel Gain	К	ft-lb/rad	632	20.8	112.5	45.2
Rate Channel Gain	С	ft-lb/rad/sec	63.2	2.08	11.25	4.52
Integral Channel Gain	М	ft-lb/rad/sec	7015	230	1249	501
Pwr amp Gain	KA	ft-lb/Computer Volt	19.2	0.493	2.28	1.4
Computer Rate Gain	G _C	_	413	530	619	406
Computer Prop Gain	GK	_	165	212	15	10
Computer Integral Gain	G _M	_	18	23	1.7	1.1
	С	ft-lb/rad/sec	0.1528 G _C	0.0039 G _C	0.018 G _C	0.011 G _C
	к	ft-lb/rad	3.82 G _K	0.098 G _K	7.27 G _K	4.45 G _K
	М	ft-Ib/sec/rad	380 G _M	9.76 G _M	726 G _M	445 G _M

Figure 50 Servo Gains

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Gains for Camera Azimuth Axis

An example of the gains involved for the camera azimuth axis are shown in Figure 51. Non-linearities not shown in the figure cause gain reduction and some phase shift. Consequently, gains were adjusted on the actual hardware to optimize gimbal performance and camera-to-projector tracking as evidenced by picture motion. The non-linearities of the system make

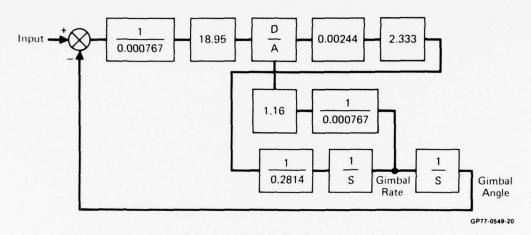


Figure 51 Camera Azimuth Axis

the frequency response of the system a function of amplitude and frequency. They tend to reduce the system bandwidth. The system was designed and optimized for ramp type inputs.

5.4.2 MODE 2

Using the projector in a servo follower mode to the camera requires careful system servo design. Any error in projector-to-camera tracking causes picture motion on the spherical screen as viewed by the observer. The servo follower inherently has dynamic lag even though integral feedback could be used to reduce steady state errors. From qualitative considerations some dynamic error is allowable because the observer cannot follow dynamic motion faster than a few hundreths of a second. Consequently, camera and projector instantaneous rates can be unequal for short time intervals providing the steady state position error remains within acceptable limits of approximately 0.01 radians.

While there are several approaches to the problem, the **one used** in the RVS was to feed the camera rate command signal forward to the projector. This required insertion of a lag network in series, which compensates the projector for camera velocity lag. Since the camera lag is insensitive to component changes by virtue of the feedback in the servos the circuit should remain in calibration. Adjustment of the lag can cause the projector to lead

the camera or to lag the camera. Computer studies indicate the system is easier to stabilize in Mode 3 if the projector leads the camera by a slight amount. While some dynamic error still exists its magnitude and time of decay are such that no deleterious system operation is evident to the observer. A simplified linear block diagram is shown in Figure 52.

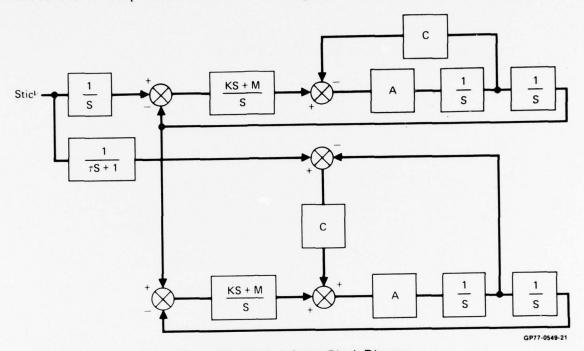


Figure 52 Mode 2 Servo Block Diagram

The output of the lag network approximates the camera velocity. If the camera velocity were being fed forward the linear projector response can be shown to be

$$H_2(s) = \frac{ACS^2 + AKS + AM}{S^3 + ASC^2 + AKS + AM}$$
 (67)

System Response Versus Lag

Figures 49, 53, and 54 show the response of the system to maximum stick inputs of 1 rad/sec for .25 sec with values of lag in the forward loop of 0.1 and .12 sec and .14 sec. The figures show that this range of lag causes the projector to cross over the camera and change from lag to lead. The parameter was adjusted on the actual hardware to enhance projector tracking and achieve minimum picture motion.

The Figure 55 shows the system response for a stick input of max plus for 0.25 sec and then max negative for 0.25 sec. with the lag set at an optimum of 0.14 sec.

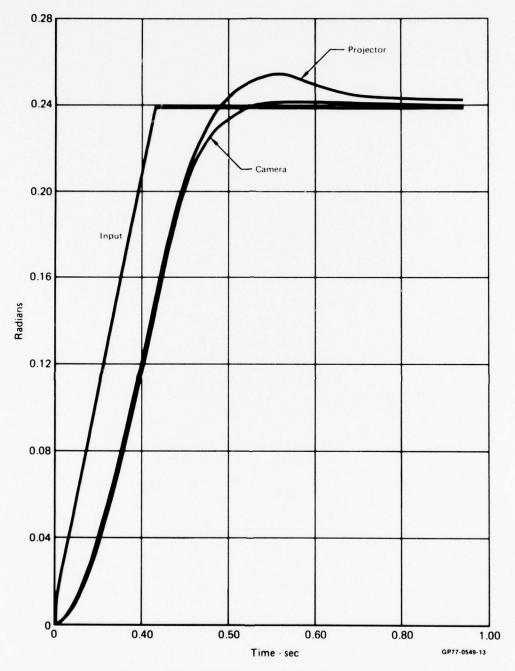


Figure 53 System Response for Stick Input Lag = 0.10 Sec

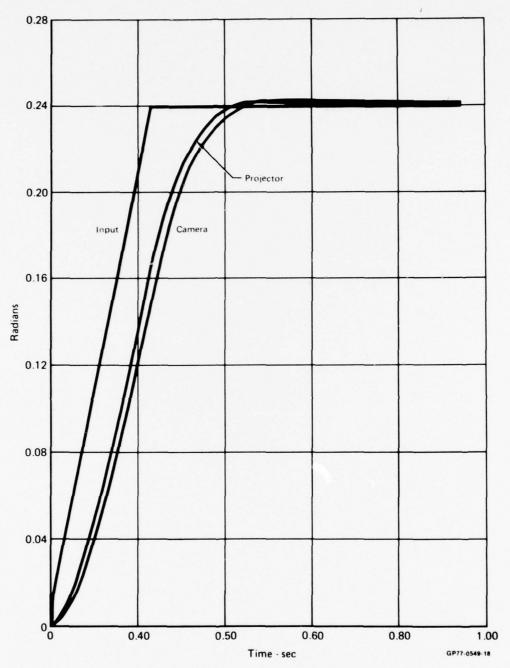


Figure 54 System Response for Stick Input Lag = 0.14 Sec

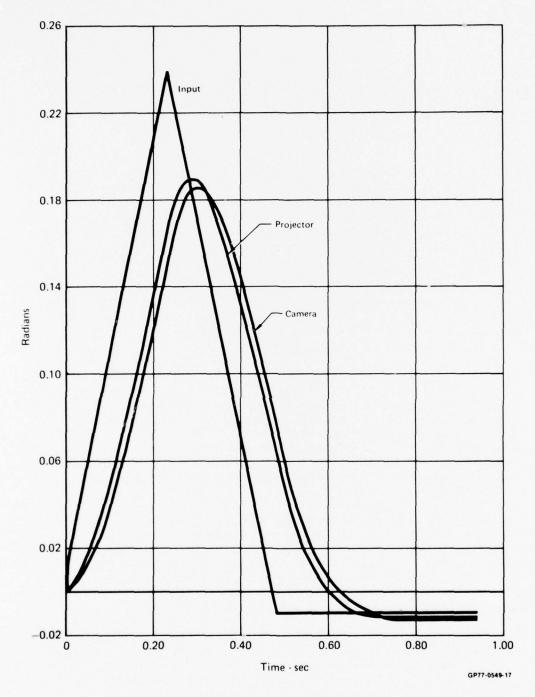


Figure 55 System Response for Stick Input Lag = 0.14 Sec

5.4.3 MODE 3

Non-Linear Block Diagram

Figure 56 is a block diagram of the final mechanization showing the feedback loops implemented in the microprocessor. For simplicity the gimbal model is not included. The gimbals are shown as a double integration of the accelerating torque. Figures 57 and 58 show the response of the system to step inputs of the detector and for smooth Lead motion of 7 rad/sec for 0.25 sec.

Digital Model

A digital simulation of one axis of the system is shown in Figure 59. This model was used to conduct parametric studies and to determine the effects of various system non-linearities. The arithmetic and sample times of the microprocessor inherent in a sampled data system were included in the model to the extent possible. This was required to accurately predict hardware performance. Dynamic friction for the camera and the projector gimbals are included. The power amplifiers were modeled as voltage amplifiers. The torque motors for each gimbal were modeled from motor specificationa and gimbal inertias were taken from experimental results. The actual or final gains used in the microprocessor are in good agreement with those predicted by the model and in general correlation between hardware performance and that predicted by the model was very good. Quanitative information on the as built system non-linearities would further improve the simulation results.

5.5 SYSTEM OPERATION

At a base, the microprocessor has two ribbon cables with DB25 common from the pitch and yaw shaft encoders, an analog output with connector (MS3106-MS-2P) which goes to two potentiometers on the input of the servo amplifiers, and the serial I/O cable (DB25) box. Also, located on the rear panel of the microprocessor are connections for the joystick control and helmet control.

The camera electronics box requires the serial cable from the microprocessor, two ribbon cables (DB25 connectors) from the shaft encoders and the analog output (MS3106-14S-4P) cable to the servo amplifiers. When all cables have been connected and the servo amplifier input gain pots turned to the off position, the microprocessor, camera electronics box, and servo amplifiers can be powered in any order. The microprocessor may now be started by pushing the "RST" button (reset) and then the "Go" button.

Next the operator turns each camera servo gain pot to maximum. He verifies that the camera is pointed straight ahead and that it is stable. Subsequently, the projector servo pots should be set to a maximum, one at a time. When the pot is maximum the camera and projector should be stationary and both pointing at the same position.

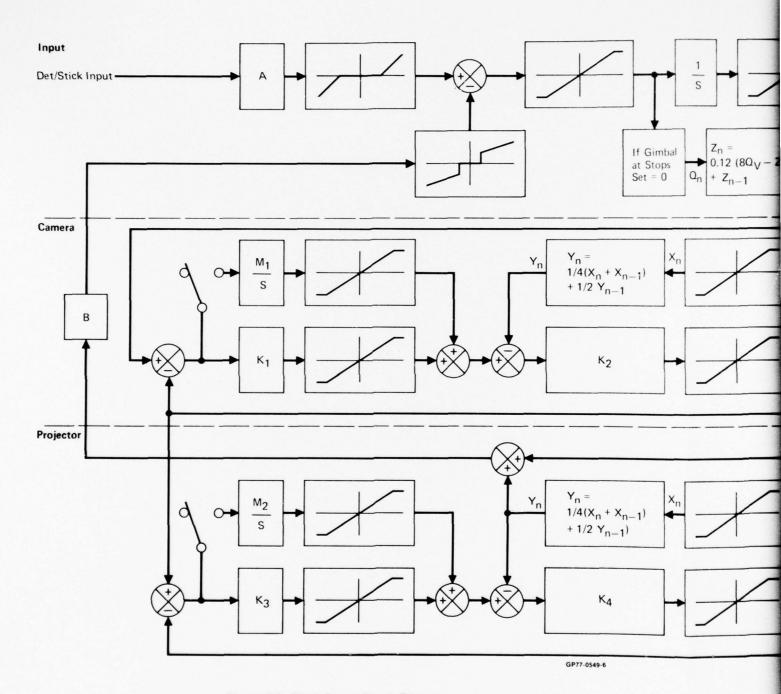
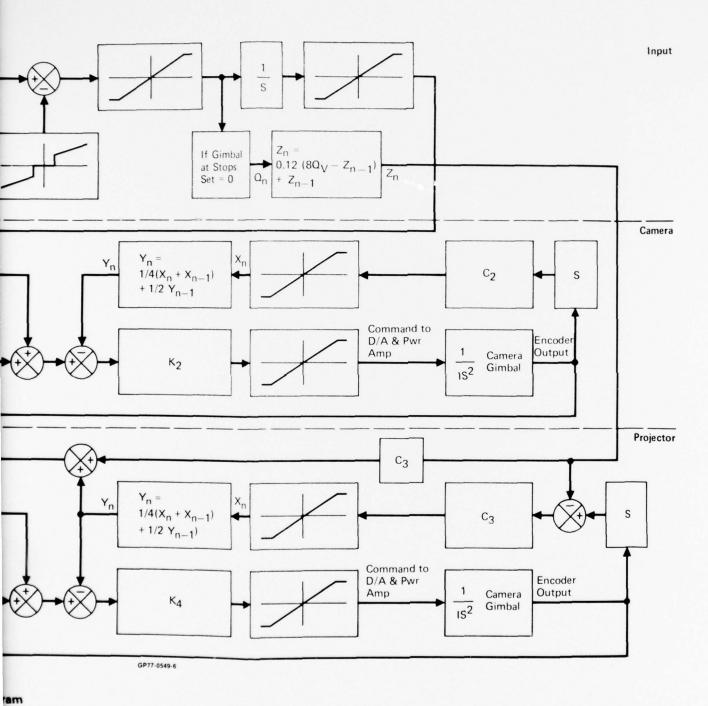


Figure 56 Pitch Servo Block Diagram



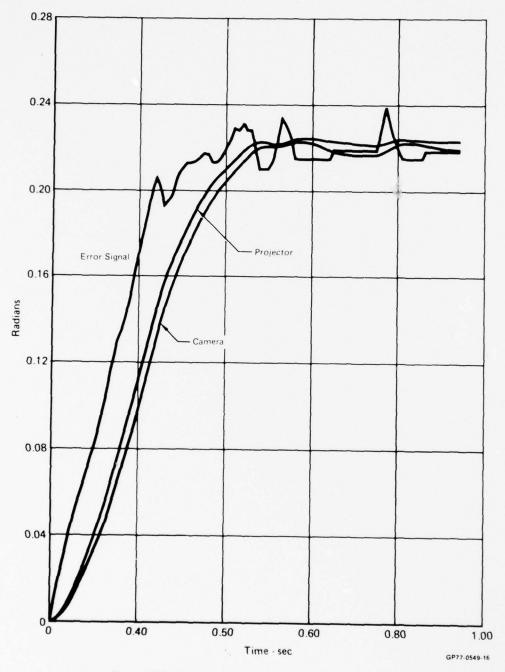


Figure 57 System Response for Ramp Input

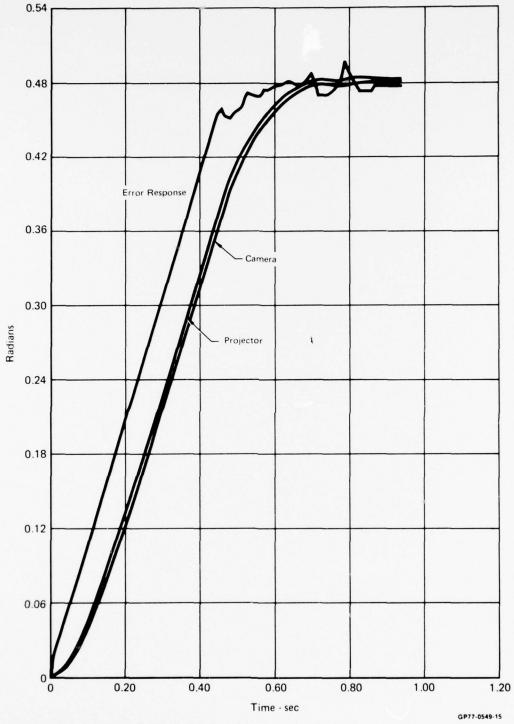


Figure 58 System Response for Step Input

```
PROJ CAMERA POSITION SERVOS STEP INPUT TORO MTRS LIMITED
        C
               INTEGER A.B.CAMR.DX.X.CER
1000
              DIMENSION K(100), J(100), PROJ(100), V(100), M(100), JR(100), IDET(100)
0002
              DIMENSION IV(100), IP(100), FST(100), DFST(100)
0003
0004
              DO 5 I=1,100
0005
              IV(1)=0.0
0006
              K(1) -0.0
0007
              J(I) = 0.0
8000
              PROJ(I) =0.0
              DFST(1)=0.0
0009
              FST(1)=0.0
0010
0011
              V(I)=0.0
0012
              M(I) = 0.0
0013
              IP(I)=0.0
0014
               JR(1)=0.0
            5 CONTINUE
0015
0016
              A=4
0017
              B=-5
              E1N=0.25
0018
0019
              N=0
0020
              CAMR=0
0021
              DP=0.0
0022
              PDOT=0.0
0023
              DPDOT=0.0
0024
              DT=0.010
0025
              Z=0.00
              CR=0.0
0026
0027
              CRTE=0.0
0028
              T=0.0
              RS=0.0
0029
0030
              DVDOT=0.0
0031
              DV=0.0
9032
              DO 100 I=3.100
              IDET(I) = ((E1N-PROJ(I-1))*0.2)/0.000767
0033
0034
               KDRT = ((PROJ(I-1)-PROJ(I-2))*2.0)/0.000767
               ID=IDET(I)*IDET(I)
0035
0036
               IF(ID.LE.16) IDET(I) =0
0037
              CAMR=B*KDRT+A*IDET(I)
0038
               IF (CAMR.GE.13) CAMR=13
              IF(CAMR.LE.-13)CAMR=-13
0039
0040
              DX=13
0041
              H=N+1
              IF (N.GE. 25) DX-0
0042
0043
              X=X+DX
0044
              C=X*0.000767
0045
              CER=X-IV(I-1)
. 46
              M(I) = CER
              DIP=M(I)*10.7*0.00244*1.333
2047
0048
              JR(I) = (V(I-2)-V(I-1))/0.000767
              CR=JR(I)/DT
0049
0050
              CRTE=CR*1.07*0.00244*1.333
0051
              T=CRTE+DIP
```

Figure 59 System Math Model

```
0052
              RS = T*T
          · IF(T.GE.0.1)TQ=T-0.1
0053
0054
               IF (T.LE.0.1) TQ=T+0.1
0055
               IF(RS.LE.0.01)TQ=0.0
               IF (RS.LE.0.01.AND. VDOT.GT.0.0) TQ=T-0.1
0056
               IF(RS.LE.0.01.AND. VDOT.LT.0.0) TQ-T+0.1
0057
0058
               IF(T.GE.4.0)TQ-4.0
0059
               IF(T.LE.-4.0)TQ=-4.0
0060
              XLC=TQ-(VDOT*0.1824)
0061
              DVDOT=(XLC*DT) /0.113
0062
               VDOT-VDOT+DVDOT
              DV=VDOT*DT
0063
0064
               V(I) = V(I-1) +D7
0065
               IV(I) = V(I) /0.000767
0066
              ERR=V(I)-PROJ(I-1)
              AERR=(ERR*180.0)/3.14159
0067
8300
              K(I)=ERR/0.000767
0069
              DISP=K(I)*13.8*0.00244*0.5
0070
              J(1) = (PROJ(1-2) - PROJ(1-1)) / 0.000767
0071
              DFST(1) = (DX-FST(1-1)) *0.10
              FST(I) =FST(I-I)+DFST(I)
0072
0073
              PRT=(J(I)+FST(I))/DT
0074
              RATE=PRT*1.38*0.00244*0.5
0075
              DZ=K(1)*0.000152
              Z=Z+DZ
0076
              CHK=K(1)*0.0*K(1)
0077
0078
               IF (CHK.GE.4900) Z=0.0
0079
               TRQE=RATE+DISP
9889
               TRS=TRGE*TRQE
0081
               IF (TRQE.GE.0.1) TRE-TRQE-0.1
0082
               IF (TRQE.LE.-0.1) TRE=TRQE+0.1
0083
               IF(TRS.LE.0.01)TRE=0.0
0084
               IF (TRS.LE.0.01.AND.PDOT.GT.0.0) TRE=TROE-0.1
0085
               IF(TRS.LE.0.01.AND.PDOT.LT.0.0)TRE=TRQE+0.1
0086
               IF (TRQE.GE.1.2) TRE=1.2
0087
               IF (TRGE.LE.-1.2) TRE--1.2
               XL=TRE-(PDOT*1).02466666)
0088
0089
               DPDOT=(XL*DT).40.052
0090
              PDOT- PDOT+DPDOT
0091
              DP- PDOT*DT
0092
              PROJ(I) = PROJ(I-1) +DP
0093
               IP(1)=PROJ(1).00.000767
0094
              WRITE(1,300) AERR.C.V(I),PROJ(I).TQ
0095
          100 CONTINUE
0096
         300 FORMAT (5(E11.4,1X))
0097
               CALL EXIT
0098
              END
        ROUTINES CALLED:
        EXIT
        OPTIONS =/OP:2./GO
                     LENGTH
        BLOCK
                       (015454)*
        MAIH.
```

Figure 59 System Math Model (Concluded)

The system may be stopped at any time by turning the projector gain pots to zero. The processor may be stopped by pushing the HALT or the RESET button. The system may be restarted by repeating the sequence described above.

The mode of control may be switched between stick to head by actuating the toggle switch located on the rear panel of the microprocessor. When all gain pots are on, the system is in the stabilized mode. The projector pots can be left off for operation of system in non-stabilized display mode.

Basic Monitor Functions

The microprocessor has a self contained monitor program. An operator with an understanding of the servo control program, (See software section of this report) can use the monitor to troubleshoot not only software problems but also pin down the point of many electronic failures.

Using the monitor, the user can for instance examine the position data coming from the shaft encloders. To examine data, the following sequence should be used:

- o Depress halt (HLT) button
- o Depress examine memory (EM) button
- o Punch in memory address

When the address has been entered, the processor will display the 8 bit word stored at that address in a hexidecimal code. The location in memory following this address may be addressed by pushing the "CO" button. The low order bits are stored in the first location and the higher bits in the second. Carra pitch data is located at computer memory address 3D40 and 3D41. Came yaw data is located at 3D42 and 3D43. The lower 13 bits contain the position information, highest order bits are not used and can be ignored. The lowest or bit is approximately equal to 2.6 minutes of arc.

A lis of the control program is available in the software section of this re . This listing, along with the monitor description in the same sectic will allow a person familiar with the 8080 programming language to alter the system parameters and fine tune the system. The control systems gains can be adjusted directly from the monitor, but a word of caution is in order. Due to the scaling complexities and interaction of the system gains it is suggested that change not be made without a complete and thorough understanding of the software.

The quad detector mounted on the helmet has approximately a 40° full field-of-view. If the IR spot that it senses is outside its field-of-view, the microprocessor will receive no control signals and the servo will remain at rest. The observer needs to turn his head, pointing the detector toward the high acuity portion of the display. As the user does this, the system will begin to slew toward him. The sensitivity of the head controller can be adjusted by adjusting the intensity of the source. The recommended settings of the light source are 5 Vac and 5 amps.

The joy stick control has zeroing pots so that the joy stick analog output signal can be adjusted within the system's software deadband eliminating servo drift.

The microprocessor LED readouts allow the operator to determine the operational mode of the microprocessor. Upon powerup, the readout will show 43210. The same readout will occur after the RST GO sequence. The halt (HLT) button will cause a "D" to be read into the first digit and the next four show the current program counter. The restart (RS) button changes the halt display to show a 6 in the first digit and leaves the other digits unchanged. If after depressing the halt button, a "D" is not located in the first digit, the microprocessor program is not running correctly is indicated. The user should then repeat to reset sequence (RST, GO).

The torque motor on the yaw axis of the projector can exert 100 ft. 1b. of torque if the power amplifier or its input should fail in a hardover mode. Hard stops and motor shorting switches have been installed on this axis to protect the light valve in the unlikely event that such a failure should occur. If the motor shorting switches are tripped, the operator must stop the system and reset the switches.

In normal operation the software limits the camera and projector axes to $\pm~90^{\circ}$ in yaw and $\pm~45^{\circ}$ in pitch. These software limits prevent the operator from slewing the equipment into the mechanical stops and eliminate undue rapid deceleration of the hardware.

5.6 HEAD TRACKER INTERFACE ELECTRONICS

The control signals required for the head tracking mode are generated by a dual axis position sensor. This sensor provides pitch and yaw position information from a light spot imaged on the detector surface. The source of the light imaged on the detector is a 24 watt bulb in a lens assembly focused to image the filament of the bulb on the dome surface. The detector is helmet mounted, and the light source is mounted on the projector pitch axis. Although some axes crosstalk could have been eliminated by mounting the detector on the pitch axis and the light source on the helmet, the opposite arrangement was chosen in order to keep the helmet assembly as light as possible. Both the light source and the detector are filtered with Wratten 88A filters. The detector (PIN-SC-25) manufactured by United Detector, has a position sensitivity of .32 amp/watt/cm, and a series resistance of 5K Ω . The light source is a 1763, 6 volt, 4 ampere prefocus socket bulb. The detector output signal is amplified using the circuit shown in Figure 60. This amplifier is characterized by its low input impedance and high common mode rejection. The zener diodes located on the output stage clip the signal at approximately 4.7 volts to prevent overdriving the analog to digital input of the microprocessor.

The spot imaged on the detector (the filament of the bulb) nominally has a width of 0.06 inches. The detector has a usable width of 0.74 inch. A rough calculation shows that using a 0.9 inch focal length lens the detector will have an approximate field of view of 40°. If the source imaged on the dome is outside of the field of view of the detector the microprocessor receives no signals from the detector and the system will remain at rest. As the detector is pointed toward the image on the dome surface, the projector will begin to slew toward the detector. As the projector slews toward the detector and locks onto the detector's signal, the system's feedback loop is completed and the system will be fully head controlled.

A typical signal output vs. command angle is shown in Figure 26. The amplitude of the signal output is not only a function of the CMR amplifier, but also of the light source intensity and positioning of the detector within the return cone of the source light.

The head control system may be finetuned by adjusting the light source to provide the appropriate response in the closed loop system.

^{*} In actuality, the detector directs the camera to move and the projector follows the camera.

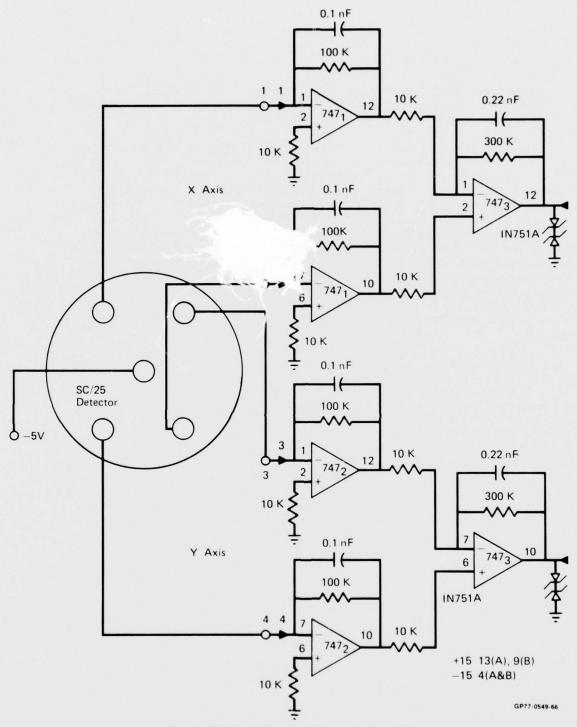


Figure 60 Head Control Detector Amplifier

Section 6

RESULTS AND CONCLUSIONS

This section details the tests that were made to document system performance as measured by system resolution and distortion and compares these data to theoretical predictions.

Resolution measurements of the total system were made using tribar targets. These measurements were made on the system as it was adjusted for the ONR demonstration. The system was set up for best overall focus, a situation which reduces on-axis resolution. The system focus problem is discussed in more detail in the focus corrector section of this report.

The lens distortion function causes no noticeable effect to radial lines while lines perpendicular to these (tangential lines) are compressed. For example, in the vertical direction, a vertical bar target which is readily resolvable has a horizontal counterpart which is not resolvable. These two target orientations were used to measure system resolution along and across the scanning line direction, (i.e. Horizontal bars used for vertical measurements).

The resolution measurements were made as a function of the angle from the optical axis $(\theta).$ These angles were computed from shaft position encoder data read from the microprocessor memory, the system geometry and lens nodal point shift data. Figure 61 shows the geometry involved to render a true θ from the encoder readings in order to determine vertical and horizontal resolution.

The target viewing distance was selected to be always greater than the lens hyperfocal distance as determined with an F11 system, and the focal length for the corresponding θ . The camera automatic iris control was disabled and set at the typical outdoor setting which was about F111. The resolution targets were illuminated using photoflood lamps, to provide proper target contrast. Now the vertical and horizontal resolution as a function of incoder reading will be determined.

The lens is located vertical distance (a) and horizontal distance (b) from the pivot point. The lens nodal point is located a horizontal distance (b-n) from the pivot point. The lens optical axes labeled 0a is pointed on azimuth angle (βa) and elevation angle (βe) with respect to the reference co-ordinate system xyz. The tibar target is located at angle θ with respect to the lens optical axis in a vertical plane.

For the vertical resolution, from the triangle with apex's labeled as 1-5-8 in Figure 61(a).

$$S_1 = \frac{L}{\cos \beta_a} \tag{68}$$

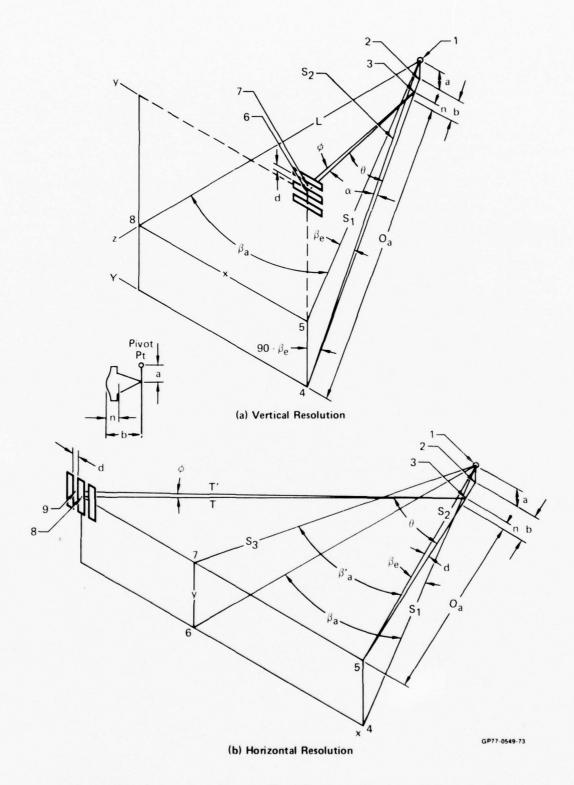


Figure 61 Geometry to Convert Shaft Encoders Readings to True Angles

From triangle 1-4-5

$$Y = S_1 \tan \beta_e \tag{69}$$

Also from triangle 1-4-5

$$S_2 = \frac{S_1}{\cos \beta_e} \tag{70}$$

From triangle 1-2-4

$$\alpha = \arcsin \frac{a}{S_2} \tag{71}$$

From triangle 1-2-4, the distance 0 is

$$0_{a} = S_{2} \cos \alpha - b \tag{72}$$

From the oblique triangle 3-4-6, the distance T is

$$T = \sqrt{(Y + y)^2 + (0_a + n)^2 - 2(Y + y) (0_z + n)} \cdot \cos (90 - \beta_a + \alpha)$$
(73)

From the oblique triangle 3-4-6 the angle θ is defined as:

$$\Theta = \operatorname{arc} \cos \left(\frac{-(Y + y)^2 + (0_a + n)^2 + T^2}{2 T (0_a + n)} \right)$$
 (74)

From oblique triangle 3-4-7

$$T' = \sqrt{(Y + y + d)^2 + (0_a + n)^2 - 2 (Y + y + d)}$$

$$(0_a + n) \cos (90 + \alpha - \beta_e)$$
(75)

Also from oblique triangle 3-4-7 the angle $\theta\, \hat{}$ is defined as:

$$\theta' = \operatorname{arc\ cos} \frac{-(Y + y + d)^2 + (O_a + n)^2 + (T')^2}{2 T' (O_a + n)}$$
 (76)

The resolution Ø is then

$$\emptyset = \theta' - \theta \tag{77}$$

Now the horizontal resolution case shown on Figure 61(b) where the optical axis and line to target are in the horizontal plane. From the triangle with apex labeled 1-4-6.

$$S_1 = \frac{L}{\cos \beta a} \tag{78}$$

From triangle 1-4-5

$$S_2 = \frac{S_1}{\cos \beta_e} \tag{79}$$

From triangle 1-2-5

$$\alpha = \arcsin \frac{a}{S_2}$$
 (80)

From triangle 1-4-6

$$X = L \tan \beta_a \tag{81}$$

From triangle 1-2-5

$$0_{a} = S_{2} \cos \alpha - b \tag{82}$$

From triangle 1-6-7

$$S_3 = \sqrt{L^2 + y^2}$$
 (83)

From triangle 1-5-7

$$\beta'_{a} = \arctan \frac{X}{S_{3}}$$
 (84)

From oblique triangle 3-5-8

$$T = \sqrt{(X + x)^2 + (0_a + n)^2 - 2(X + x) (0_a + n)} \cdot \cos (90 - \beta_a)$$
 (85)

Also from oblique triangle 3-5-8

$$\theta = \arccos\left(\frac{-(X + x)^2 + (0_a + n)^2 + T^2}{2 T (0_a + n)}\right)$$
 (86)

From oblique triangle 3-5-9

$$T' = \sqrt{(X + x + d)^2 + (0_a + n)^2 - 2(X + x + d)(0_a + n)}$$

$$\cos (90 - \beta_a)$$
(87)

Also from triangle 3-5-9

$$\theta' = \arccos \left(\frac{-(X + x + d)^2 + (0_a + n)^2 + (T_i)^2}{2 T_i' (0_a + n)} \right)$$
(88)

The horizontal resolution is:

$$\emptyset = \theta - \theta \tag{89}$$

6.1 CAMERA PERFORMANCE

Results of the camera performance tests are shown in Figures 62 and 63. Figure 63 shows resolution in the horizontal plane while Figure 63 is the same data for the vertical plane. The expected resolution as discussed in Section 3.0 is also shown on the figures. Note that in either case the on-axis angular resolution is about 1.7 times worse than was anticipated. In order to make some meaningful comparisons the computer model of Appendix E was degraded until the measured on-axis performance was achieved. This degradation was accomplished by increasing the Guassian blur of the nonlinear lens function. This required an increase from the ray trace data value of 5.5 microns (one sigma) to 50 microns. These data are shown by the solid line on the figures. Note that this data which was matched on-axis is near the actual performance for most other field angles. This indicates a uniform optical blur at the vidicon faceplate. A notable exception is the considerably worse performance in the 0.4 to 1.0 degree region caused by an incorrect aspheric element profile in the rear optical assembly of the non-linear lens. We attempted to correct for this during the contract by fabricating new elements using a new state-of-the-art pantagraph grinding technique and an air bearing spindle. Unfortunately, this was a failure. The new elements were even worse than the original hand fabricated elements. The fabricator is presently remaking these elements which will hopefully correct this problem in the near future. In this abnormal acuity region, performance drops by a factor of three. This is very distracting because performance should be best in this region to support foveal vision.

6.2 TOTAL SYSTEM PERFORMANCE

The measured performance of the overall system is shown in Figure 64 and 65 for the horizontal and vertical planes. Employing the same analytical method as in the camera case it was necessary to degrade display performance from the anticipated 15 microns (equivalent light valve spotsize) to 90 microns in order to predict horizontal on-axis performance. Then when

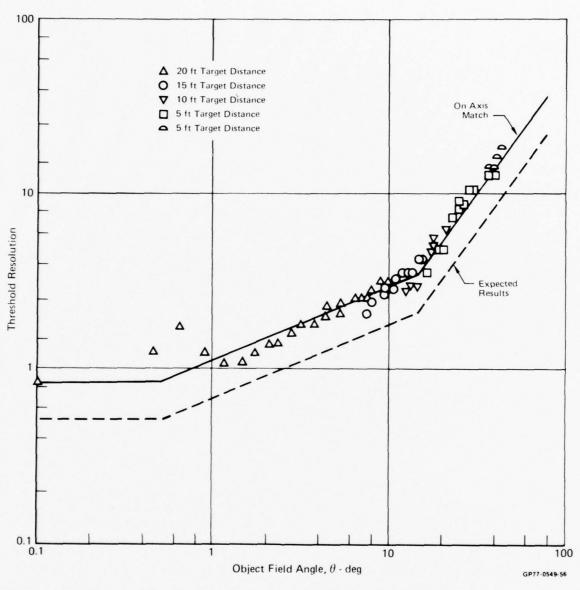


Figure 62 Threshold Resolution vs Angle from Optical Axis
Camera Only (Horizontal)

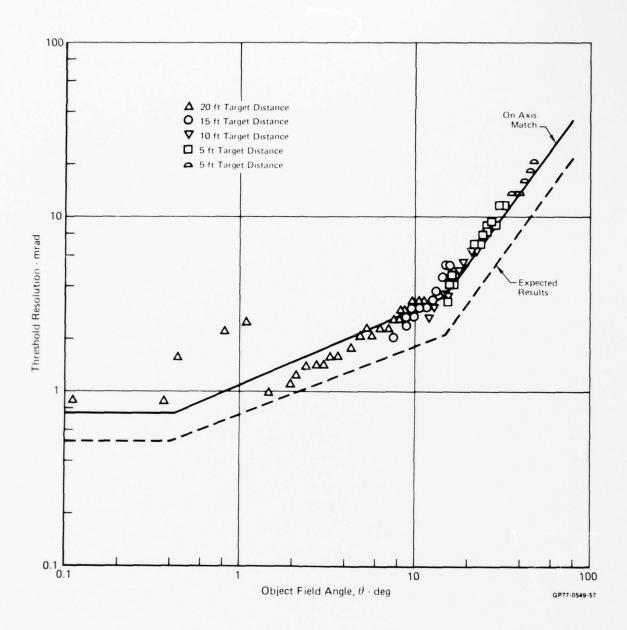


Figure 63 Threshold Resolution vs Angle from Optical Axis
Camera Only (Vertical)

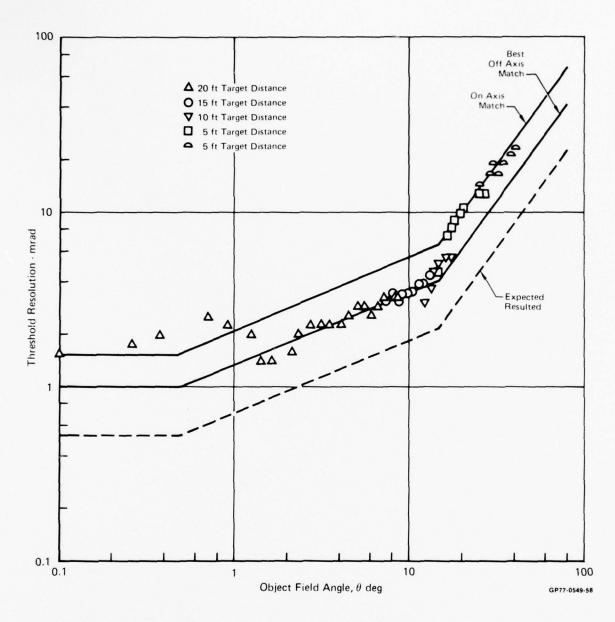


Figure 64 Threshold Resolution vs Angle from Optical Axis
Total System (Horizontal)

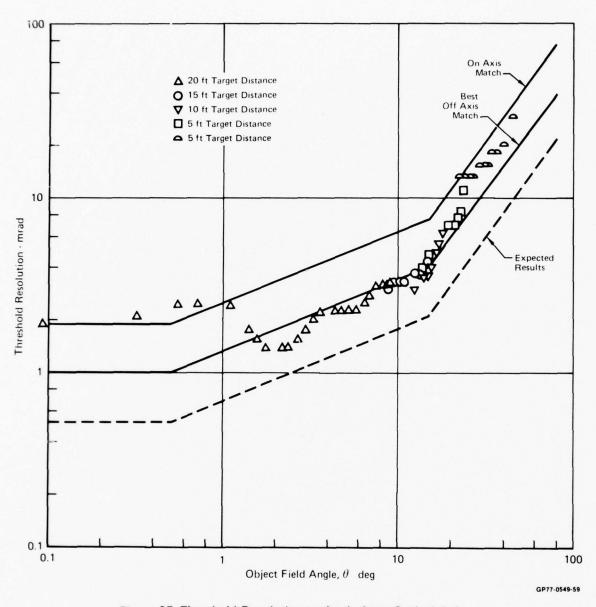


Figure 65 Threshold Resolution vs Angle from Optical Axis
Total System (Vertical)

these data were plotted on Figures 64 and 65 very poor prediction of off-axis data is obtained. This implies a nonuniform degradation at the object plane of the projection non-linear lens with much higher blur on-axis. The reason for this is the diffraction problem created by the schlierin optics which was discussed in Section 3. However it appears to be considerably worse than anticipated. To assess the remainder of the field, the display blur was reduced until a good match was obtained off-axis. (The greatest emphasis was placed on the less than 15° region because of expected magnification problems which will be discussed later). A display blur of 30 microns matched the data very well for both horizontal and vertical planes as can be seen on the figures. This is a reasonable display quality value which would produce very little additional degradation to the camera if it applied on-axis as well. The on-axis performance would only degrade from 0.85 to 1.0 milliradian if the 30 micron display quality was maintained on-axis.

The disparity in on-axis system performance between horizontal and vertical planes (1.5 to 1.9 milliradians) is undoubtedly due to schlierin alignment (horizontal at the non-linear lens focal plane) which will yield a higher diffraction cutoff spatial frequency in the horizontal direction.

The system resolution, Figures 64 and 65, show the same local region of poor performance (around 1°) that was seen on the camera only curves. The projector appears to aggrevate this region very little. The reason for this lies in the fact that the projector lens produces much better quality in this region apparently because it has a better rear lens cell.

The apparent lower system resolution at field angles larger than 20° is caused by incorrect magnification. This can be seen on Figures 66 and 67 which show measured vs. computed angular error in the projected display. Here the measured data is compared to 2%, 5% and 10% magnified images. The desired value is 2% while the horizontal magnification appears to be about 7% and the vertical about 4%.

6.2.1 Low Contrast Performance

Because of time constraints, direct measurement of low contrast performance was not possible. Therefore it is necessary to use the analytic model adjusted to yield the measured high contrast performance, to estimate performance at lower contrasts. These data are shown on Figure 68. Here the input modulation (contrast) required to resolve targets at various spatial frequencies are shown. Two curves are required for the system because of the projector problem noted above. It should be noted that the linear spatial frequency scale applies everywhere on the non-linear lens focal plane while the angular spatial frequency scale applies only on-axis. These two spatial frequency parameters are related as described in Appendix D.

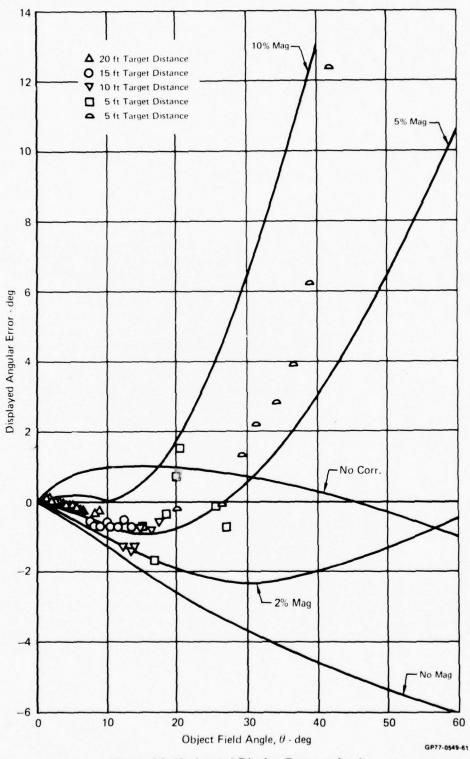


Figure 66 Horizontal Display Error vs Angle

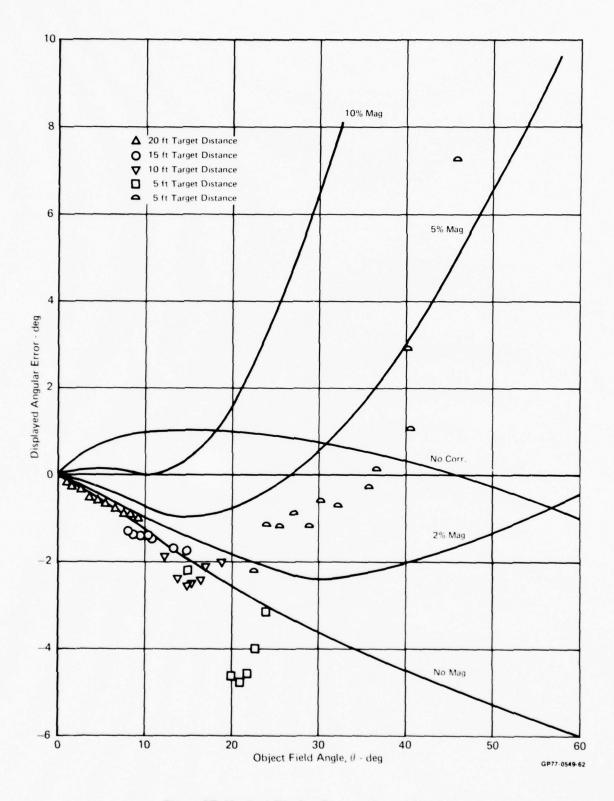


Figure 67 Vertical Display Error vs Actual Angle

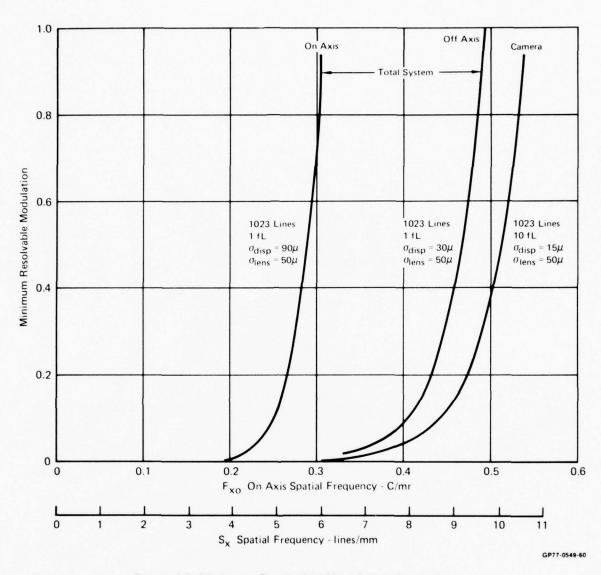


Figure 68 Minimum Resolvable Modulation Predictions

6.2.2 Demonstration Results

The system was demonstrated in the laboratory by placing the camera on the northwest corner of the roof of MCAIR Bldg. 102. A hard wire link was established to the display station which was located in the laboratory about 300 ft. away. The camera overlooked Lambert Field and Brown Road which borders the airport. A field of regard of 180° in azimuth and \pm 60° in elevation was established. For comparison a 525 line conventional TV camera with a remote control zoom lens was also placed on the roof. This camera was pointed towards a sign board about 1000 ft. distant. This sensor was displayed adjacent to the RVS camera video CRT display.

To compare resolution, the RVS camera was pointed toward the same sign board and the conventional camera was zoomed until the same detail could be seen on its display as the on-axis RVS was producing. This field-of-view was about 10° x 14° . The RVS projection field-of-view was then reduced by masking to this field-of-view. The operator was then given the task of searching the field of regard of the RVS sensor using joy stick control. The usual problems with narrow fields-of-view were noted in maintaining orientation in the total field of regard and in smooth tracking of moving vehicles.

Next the mask was removed so the operator could see the entire RVS field of view and the full up head control operation established. In general all viewers liked the wide field display, especially the ease in tracking moving targets. It should be noted here that the servo control performance was excellent. No perceptible display motion occurred under any dynamic condition. This requires that the camera and projector servos track within about 0.5 milliradian under the most extreme dynamic conditions.

Most observers noted the low on-axis performance even when made aware that it was comparable to a 14° FOV conventional system. Some observers were impressed by motion and glint cueing in the peripheral very low resolution area of the display while others felt lack of sharp spatial detail in these regions would degrade these visual cues.

6.3 CONCLUSIONS AND RECOMMENDATIONS

Considering this is the first device of this type, we feel the results were very encouraging. As should be expected the only serious problems were with the new technology or state-of-the-art advancement in non-linear optics. All conventional functions within the state-of-the-art worked perfectly including the servo control, TV camera, TV projector, head tracker, etc. The value of the digital control system was demonstrated through its outstanding performance and reliability which could have been achieved only with great effort if an analog system was employed.

It appears the greatest improvement in performance could be obtained by (a) replacing the rear splines elements of the non-linear lenses and (b) solving the diffraction problem in the projector relay. The first is underway and if successful should be corrected within one to two months. The latter has no easy solution at this time. As discussed in Section 3, increased relay magnification may help but complete correction may require a different type of light valve that does not require Schlerin optics. At least two are presently under development. A KDP light valve is being developed in France while a liquid crystal light valve is under development at Hughes Aircraft in the USA. Both of these operate on a controlled polarization principle and can use conventional optics. Another possibility is to construct a new non-linear lens with a small F/number so that it can utilize more of the light valve optical ray cone.

Finally we believe the laboratory demonstration, where a scene is viewed in which most spatial detail is stationary, does not show the true potential of the system in flight control and navigation. We have seen this when projecting tape recorded video taken through the windshield of an aircraft. It appears that the somewhat low on-axis resolution is not so objectionable under these dynamic conditions. Based on these observations it may be desirable to fly the sensor in order to obtain a true performance assessment in a dynamic environment.

Section 7 REFERENCE LIST

- RVS Display Feasibility Study, Report No. MDC A3392, 28 Feb. 1975 McDonnell Aircraft Co., St. Louis, Mo. 63166
- Remote Viewing System Technical Proposal Report No. MDC A2486, 21 Sept. 1973, McDonnell Aircraft Co., St. Louis, Mo. 63166
- 3. <u>Head Controlled Remote Viewing System Technical Proposal</u> Report No. MDC A3020, 3 Sept. 1974, McDonnell Aircraft Co., St. Louis, Mo. 63166
- 4. Klaiber, R.J., Physical and Optical Properties of Projection Screens; Technical Report NAVTRADEVCEN IH-63, December 1966

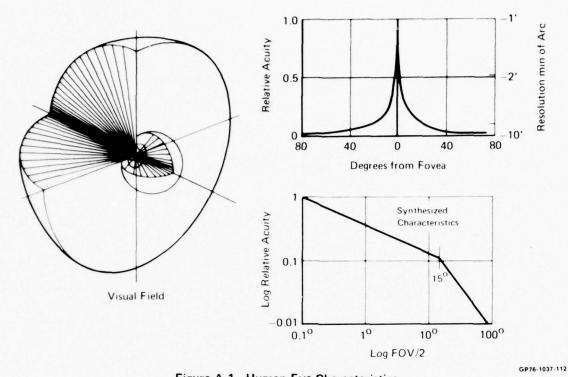
Appendix A

BRIEF DESCRIPTION OF THE REMOTE VIEWING SYSTEM (RVS)

The RVS concept is based on the fact that the human visual capability can be represented by a resolution capability of about 130,000 elements, provided that these elements are sized non-linearly according to the acuity function as shown in Figure A-1. An image with this characteristic requires only about 2 MHz video bandwidth at 30 Hz frame rates. In comparison, standard techniques would require over 1,000 MHz bandwidth for this field-of-view (180°) and resolution. Even at smaller fields-of-view, the bandwidth saving is significant. A comparison of bandwidth requirements for varying fields-of-view for the conventional linear acuity function and for the RVS foveal concept is shown in Figure A-2. Approximately two orders of magnitude decrease in BW is achieved with the foveal system at FOV's greater than 20 degrees. In order to mechanize the concept described above, a method must be devised to generate an image which satisfies the optical requirements of the eye. The RVS concept contains a lens system that creates optical "distortion" by varying the spacing of the angular resolution elements to duplicate the acuity function shown in Figure A-1. This process is illustrated in Figure A-3. The lens transfer characteristic required and the technique for reconstructing the image at a remote location is also shown on this figure. System operation is as follows:

The image transmission system scans the photocathode of the vidicon or photodetectors of an imaging array, transmits this signal to the remote location, and recreates the image on a CRT or light valve tube. In the original RVS concept, the distorted image is expanded using a lens system with a transfer characteristic identical to the sensor lens and imaged on a spherical screen concentric with the nodal point of the lens.

Obviously, for the above image transmission system to perform adequately, the optical axes of both the sensor and projector must have the same alignment as the viewer's eye. The initial RVS system concept used the approach outlined in Figure A-4. The position of the projector is slaved to the camera by a high accuracy position servo, with the camera's angular position commanding the projector's position relative to fixed ground station reference coordinates. The viewer at the ground station thus has the same angular perspective as he would if he were located in the remote vehicle. The sensor and projector must also be aligned with the viewer's foveal axis. In the original concept a Honeywell oculometer was employed for this function. The oculometer measures the angle between the eye's foveal axis and the projector's optical axis. This error signal is transmitted to the remote vehicle and commands the camera to move until the angular error is reduced to zero. As the camera moves, the projector follows through the slaving loop. The control mode, presently under study, is somewhat different, however. The observer's head position instead of his eye position is utilized to point the remote camera. The operational difference resulting from this simplification is that when the viewer uses his peripheral vision, he must learn to rotate his head towards the area of interest rather than his eyes. A reticle may be required to show the observer the location of the highest acuity area of the display.



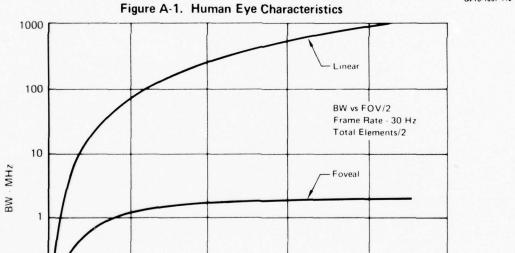


Figure A-2. Bandwidth Requirements

40

0.1

0 L

20

FOV/2 - deg

60

80

100

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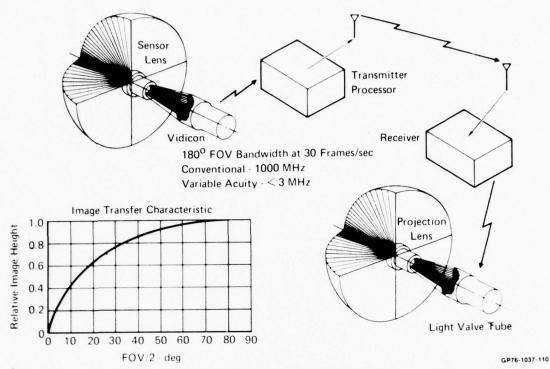


Figure A-3. Electro-Optical Schematic

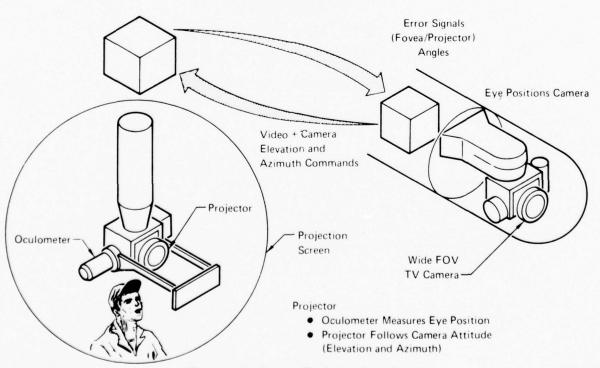


Figure A-4. Camera/Projector Interface

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Appendix B

CAMERA CONSIDERATIONS

LIGHT LEVEL CONTROL

Light level control must be accomplished by an iris in the camera optical relay. The relay is required for this purpose because no iris control is available in the non-linear lens. An iris control was not initially considered necessary because an $S_{b_2}S_3$ vidicon was contemplated which had sufficient dynamic range for good daylight performance with electronic light control. Solar damage considerations later dictated the use of a silicon vidicon which cannot be adapted to electronic light level control. The range required of the iris control is discussed below.

Assuming a GE Z7978 Epicon vidicon is utilized an average faceplate illumination of .25 ft-candles is recommended. Using conventional formulas, this relates to a scene brightness as follows:

$$E = \frac{\pi B}{4(F_{NO})2}$$
 (B-1)

If

$$E = .25$$
 ft-candles

$$B = \frac{.25 \times 4}{\pi} (F_{NO})^2 = .318 (F_{NO})^2 \frac{Lumens}{Steradian-ft^2}$$

Assuming a 1:1 relay between lens and vidicon the effective F number at the vidicon is identical to that of the non-linear lens - F/5.6. The brightness is:

$$B = 9.97 \frac{Lumens}{Steradian-ft^2} = 31.32 \text{ ft-lambert}$$

This is the minimum brightness level capability of the camera. It is sufficient to operate anywhere in the U.S., even under heavy cloud cover.

The maximum terrain brightness anticipated is about 5000 ft-lamberts.

This approximates clear weather at 70° solar elevation and .16 terrain

reflectance. The F number required to attenuate this brightness to .25 ftcandles at the vidicon faceplate is (per Equation (B-1))

$$\frac{5000}{\pi} = \frac{.25 \times 4}{\pi} (F_{NO})^{2}$$

$$(F_{NO})^{2} = 5000$$

$$F_{NO} = 70.7$$
(B-2)

This small aperture would cause serious diffraction in the image quality.

For this reason, a filter is considered. Because of sensitivity of the silicon vidicon to IR radiation a Schott KG3 filter is recommended. This filter provides about 20% transmission in the visual spectrum. This reduces the maximum F number requirements to about F/16, which is easily obtainable in the optical relay between camera and lens.

In summary, the camera optical relay must have sufficient aperture to couple all the energy in the F/5.6 non-linear lens image ray bundle to the vidicon. The iris control in the relay must have the capability of reducing this F/5.6 ray bundle at the vidicon to F/16. This variable iris should be servo controlled to maintain the required vidicon faceplate illumination under varying terrain illumination and reflectance characteristics.

The average video level from the vidicon can be used as the drive signal.

This is possible because the foveal region occupies most of the vidicon photocathode area. Therefore an average video level will optimize brightness in this area as desired.

SOLAR DAMAGE CONSIDERATIONS

Utilizing the sun brightness value of:

$$B_S = 2.09 \times 10^3 \frac{Lumen}{Steradian ft^2}$$
 [From Reference (B-1)]

At F/5.6 the vidicon faceplate illumination would be [from Equation (B-1)]

$$E = \frac{\pi}{4} \frac{2.09 \times 10^8}{(5.6)^2} = .523 \times 10^7$$
 foot candles

This gives a 2x safety factor over the 10^7 foot candle maximum rating of the vidicon proposed for the RVS camera. Operationally the safety margin is considerably better than this because any time the sun is visible to the RVS the automatic light level control will certainly have the camera stopped down to F/8 or greater. The margin is at least 4x when this is considered. The IR filter discussed in the previous paragraph also increases the safety margin.

Appendix C

PROJECTOR STUDIES

INTRODUCTION

The projection brightness problem is illustrated in Figure C-1. Here uniform size area elements are shown in the projector object plane at three different distances from the optical axis. If the object plane is of uniform brightness (which is the case for the RVS intermediate image or projector object) the screen illumination decreases as object area elements displace from the optical axis. Each area in the object plane contains the same light flux, which is spread over a greater area on the projection screen. In the actual case, area elements are projected 1000 times larger in the extreme peripheral region (90°) than in the foveal region (0°) of the display. This, of course, is completely unacceptable to the viewer. Two alternatives are possible for solving the above problem.

- (a) A variable density filter to properly attenuate the foveal area of projection so that it matches the peripheral field in screen brightness. This is, of course, feasible only if image brightness is sufficient to generate acceptable brightness in the peripheral field of the displayed image.
- (b) Employ a direct or virtual image viewing system. This is much more efficient and inherently results in uniform display brightness if the exit pupil is large enough to support the entire eye aperture (or the interocular spacing if binocular viewing is to be achieved).

Selection of the best display approach requires a thorough analysis of the two above approaches.

In the past year, MCAIR IRAD on the RVS has been 95% devoted to trade-offs of display concepts. The results of these studies, analyses, and tests are outlined below.

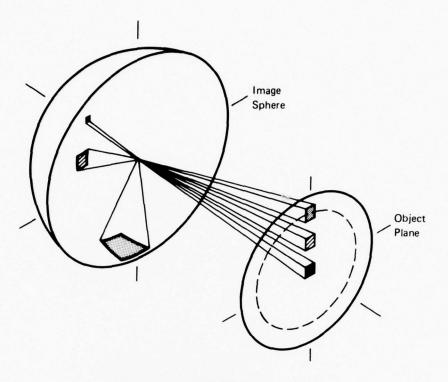


FIGURE C-1
GENERAL PROJECTION GEOMETRY

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PROJECTION SCREEN APPROACH

The geometry of the projection screen approach is shown in Figure C-2. An element of area dA with brightness B is projected through a lens of aperture D and focal length f to a viewing screen located at distance L. The image of dA on the viewing screen appears as dA_S. This area re-radiates over solid angle ω_S . The apparent screen brightness B_S(θ), as seen by the observer also at distance L, but offset by distance ℓ , is calculated as follows.

The light flux through aperture D from image area dA is:

$$F = B \times \omega \times dA \tag{C-1}$$

where

 $\boldsymbol{\omega}$ is the solid angle of light collection by the projector lens.

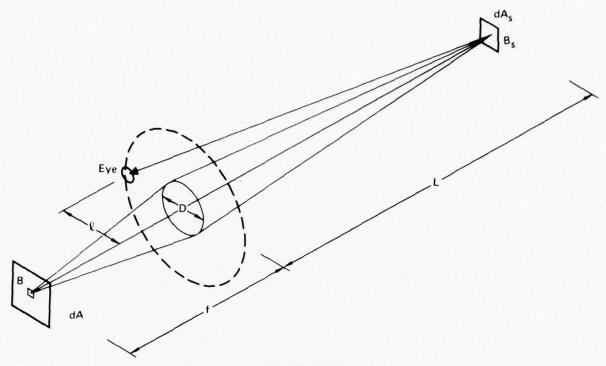


FIGURE C-2
DISPLAY BRIGHTNESS GEOMETRY

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Accordingly:

$$\omega = \frac{\pi \left[D(\theta)\right]^2}{4\left[f(\theta)\right]^2} = \frac{\pi}{4\left(F_{NO}\right)^2}$$
 (C-2)

Development of ω in terms of F_{NO} instead of lens aperture and focal length is preferred because both theory and experiment show that the latter vary with field angle (θ) on the non-linear lens while F_{NO} does not.

Combining these two equations yields:

$$F = \frac{B\pi dA}{4 \left(F_{NO}\right)^2}$$
 (C-3)

This is the total flux that illuminates dA at the screen.

Screen illumination (E) is:

$$E = \frac{F}{dA_s} = \frac{B\pi}{4(F_{NO})^2} \frac{dA}{dA_s(\theta)}$$
 (C-4)

The screen brightness is therefore

$$B_{S}(\theta) = \frac{E}{\omega} = \frac{B\pi}{4 \left(F_{NO}\right)^{2} \omega} \frac{dA}{dA_{S}(\theta)}$$
 (C-5)

Note that B $_{\rm S}$ will have the same units as B if A and A $_{\rm S}$ have identical units.

For the on-axis case, zero subscript is used:

$$\frac{dA}{dA_{S}(0)} = \frac{[f(0)]^{2}}{L^{2}} = \frac{(F_{NO})^{2}[D(0)]^{2}}{L^{2}}$$
(C-6)

Therefore:

$$B_{S_{O}} = \frac{B\pi D(0)^{2}}{4\omega L^{2}}$$
 (C-7)

For the developed lens, D(0) = .356". Accordingly:

$$\frac{B_{s}(0)}{B} = \frac{.0995}{\omega L^{2}}$$
 (C-8)

If L = 60":

$$\frac{B_s(0)}{B} = \frac{2.76 \times 10^{-5}}{\omega}$$
 (C-9)

WORST CASE

If the screen is perfectly diffuse

 ω = π steradians

BEST_CASE

If the screen has optimum characteristics

$$\omega = \frac{\pi \ell^2}{L^2}$$

WORST CASE

$$\frac{B_{s}(0)}{B} = 8.78 \times 10^{-6}$$

For $B_{s}(0) = 1$ ft-lambert
 $B = 114,000$ ft-lambert

If the screen is perfectly diffuse

BEST CASE

If ℓ = 10" (About the minimum projector/eye separation)

$$\omega = \pi \frac{10^2}{60} = .0873 \text{ steradians}$$

If the screen has optimum characteristics

$$\frac{B_s(0)}{B} = 3.161 \times 10^{-4}$$

For
$$B_s(0) = 1$$
 ft-lambert

$$B = 3160 \text{ ft-lambert}$$

The above calculations show an object brightness in the 3000 to 100,000 ft-lambert range is required for acceptable display brightness in the foveal region of the projected display. For reasons shown on Figure C-1, it is not the foveal region, but the peripheral region that puts the greatest requirement on B.

In calculating peripheral display brightness it is most convenient to normalize Equation (C-5) by the on-axis brightness. The result is a fall-off ratio of brightness anticipated in the projected display.

$$\frac{B_{s}(\theta)}{B_{s}(0)} = \frac{dA_{s}(0)}{dA_{s}(\theta)}$$
 (C-10)

Equation (C-10) assumes a constant F_{NO} for the lens and ω for the screen. The former has been verified experimentally while the latter will be assured by spherical screen geometry and uniform coating.

The display brightness at any angle, θ , can be computed by determining the axial brightness using Equation (C-7) or (C-9) and multiplying by the ratio of Equation (C-10). The area ratios of Equation (C-10) are available from lens design data and have been

verified experimentally. These data are plotted on Figure C-3. Note that at 90° , brightness is down by 10^{-3} . It is obvious from this that the 3000 to 100,000 ft-lambert range required for on-axis brightness must be increased to 3,000,000 to

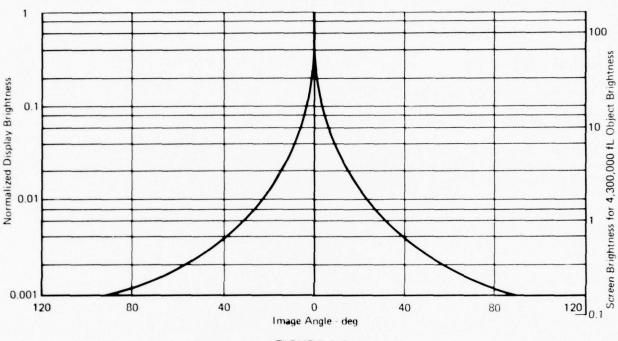


FIGURE C-3
NORMALIZED DISPLAY BRIGHTNESS

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100,000,000 ft-lambert to support peripheral vision. This exceedingly high requirement for object brightness initially led us to discard this approach and proceed to direct view display approaches. Difficulty in achieving sufficient exit pupil size and field of view (to be discussed later) with those approaches directed effort back to screen viewing techniques.

Since Equation (C-10) is constant (a function of the original concept) the clue to increasing display brightness must be found in the equation for axial brightness (Equation (C-7)).

Possible parameters are:

1. Screen Characteristics (ω)

- 2. Projection Lens Aperture (D)
- 3. Screen/Projector Distance (L)
- 4. Object Brightness (B)

Screen Solid Angle - In the previous example a minimum value of ω was computed to determine a lower limit of object brightness for the display projector. Since this minimum may not be practical it was studied in more detail. The first observation was that projector/viewer geometry could be improved for a specular coating. This is illustrated in Figure C-4. The eye and lens are equally displaced on each side of the sphere center. This aligns the centroid of the reflected light towards the eye position - making a large ω unnecessary.

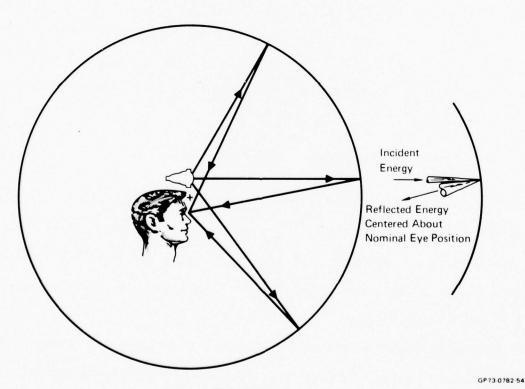


FIGURE C-4
OPTIMUM GEOMETRY FOR SPECULAR SCREEN COATINGS

In reviewing available screen materials from Reference (C-1) Stewart Filmscreen Silvergrain appears good for our application. This screen has a gain of four. While higher gain screens exist, they tend to be retroreflective rather than specular.

Calculating object brightness requirements using this $\boldsymbol{\omega}$ yields:

 $B = 25 \times 10^6$ ft-lambert for a 1 ft-lambert screen brightness and full hemispheric projection

The Stewart screen coating discussed above develops a considerably larger dispersion than is required by our concept - i.e., about $\frac{\pi}{4}$ steradians, which is equivalent to 30" dispersion at the head location if L = 60 inches. Using the geometry of Figure C-4 the dispersion required could be as small as half the interocular distance plus anticipated head motion. Allowing a 2 inch head motion, about 3 inches would be sufficient. Allowing an additional 2 inches for surface irregularities (about 2°) the solid angle would be

$$\omega = \frac{\pi 5^2}{60^2} = .0218 \text{ steradians}$$

From Equation (B-9)

$$\frac{B_{s}(0)}{B} = \frac{2.76 \times 10^{-5}}{.0218} = .00126$$

at 90° this requires

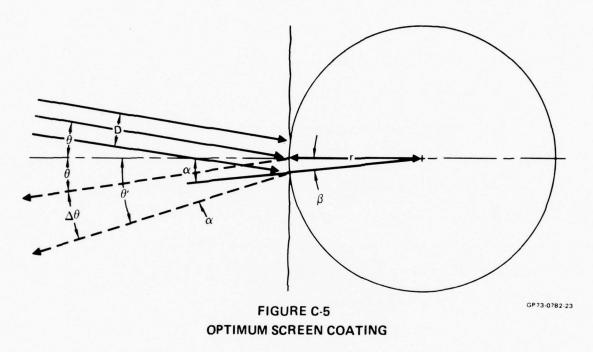
$$\frac{B_{90}}{1000} = \frac{B_{s_0}}{1000} = 1.26 \times 10^{-6} B$$

For $B_{90} = 1$ ft-lambert

$$B = \frac{1}{1.26 \times 10^{-6}} = 794,000 \text{ ft-lambert}$$

This is a substantial reduction below the 25 x 10^6 required using the Stewart coating.

The natural question at this point is if this type of screen could be fabricated. Theoretically it could be - as shown in Figure C-5. This figure shows the



general construction that would receive the minimum beam dimension D and expand it into a diverging cone having a radius ℓ at distances L (D<<1).

From simple geometry it can be seen that

$$\cos \theta = \frac{D}{2h} \qquad B = (\alpha - \theta)$$

$$h = \frac{D}{2 \cos \theta} \qquad \alpha = \theta + B$$

$$\sin B = \frac{h}{r}$$

$$\theta' = \alpha + B = \theta + 2B$$

$$\Delta \theta = \theta' - \theta$$

$$\Delta \theta = 2 \arcsin \frac{h}{r}$$

$$\Delta\theta = 2 \arcsin \frac{D}{2r \cos \theta}$$

$$\sin \left(\frac{\theta}{2}\right) = \frac{D}{2r \cos \theta}$$

$$r = \frac{D}{2 \cos \theta \sin \left(\frac{\Delta\theta}{2}\right)}$$

The $\Delta\theta$ required to make 5" dispersion at 60" is

$$\theta = \arctan \frac{5}{60} = 4.76^{\circ}$$

For our lens the minimum D = .00356"

 θ is obtained from the projector lens/eye geometry which also is (by coincidence)

$$\theta = 4.76^{\circ}$$

Therefore,

$$r = \frac{.00356}{2 \cos 4.76 \sin \frac{4.76}{2}} = .043 \text{ inch}$$

Spacing of sphere centers would be 2h ≅ D

The optimum screen would therefore use specular reflective sections of .043 inch radius spheres - spaces at .0035" centers.

The above calculations show how the projector object brightness requirements could be reduced over 30 times through an optimized screen coating. Construction of such a coating might be expensive however.

Exit Aperture - Brightness requirements reduce by the square of the lens aperture D. Therefore, a new lens design would appear to be of significant value. For instance, if F_{NO} = 1 could be achieved, object brightness could be reduced by $(5.6)^2$ or about 30 times. Unfortunately the size of the projection lens would grow at least by 5.6 times. This means the present 9" diameter would increase to about 50". Besides being very expensive, a lens this size would force expansion

of screen geometry. If everything was scaled by 5.6, the advantage of the large would be exactly negated by the increase in projection distance L.

Barring a completely different lens design, it appears that questionable advantage can be gained by scaling lens geometry.

If through a new projector lens design, aperture could be made to increase with image angle θ , some compensation in B_S could be achieved while reducing B requirements. The limit of this would probably be F_{NO} = 1 in the peripheral field. Applying Equation (C-7), the object brightness requirements would now be:

B = 800,000 ft-lambert (Stewart Screen Coating)

This level of improvement may be achievable through the expense and effort of a completely new non-linear lens design for projection only.

Considering the degree of technical advancement that was required to design a lens with correct distortion, such a redesign for projection appears to be a high risk.

Projection Distance L - Reducing the projection distance, L, is as effective as increasing D is reducing object brightness requirements. However, shown in Figure C-4, parallax angles of both projector/screen and viewer/screen are increased. Also, binocular viewing becomes impaired as L is reduced.

Quite arbitrarily at this time, a parallax of 5° is considered the maximum acceptable. Laboratory tests in projecting transparencies show that this value is acceptable in maintaining focus of the projected image. Since at the time of this writing a full hemispherical projection has not been achieved, it is impossible to determine if 5° is acceptable to the viewer.

It will be shown later that parallax can be eliminated and L reduced through .ybrid projection techniques. They require considerable development, however, involving some technical risks.

Maintaining the 5° parallax angle with the existing non-linear lens requires about 60" projection distance. This is considered the minimum acceptable (L) at this time.

Object Brightness - At this point in the analysis it appears that between $.8 \times 10^6$ to 25×10^6 ft-lambert object brightness is required. Standard CRT's are in the 1000-3000 ft-lambert categories and are obviously unusable. Projection CRT's are better but still fall considerably short of the brightness requirements (10,000-20,000 ft-lambert) and add a x-ray radiation hazard that would probably make them unacceptable in the RVS application.

Eidophor light valve approaches eliminate the x-ray problem, but are quite large and have a mechanical pointing limit. Their high output, however, makes them a promising candidate. For this reason an available G.E. light valve was studied. The PJ 700 light valve has a monochrome output of 750 lumens and requires approximately F/3 relay optics. This indicates the geometry shown on Figure C-6. Since the non-linear lens requires only F/5.6 solid angle input and an image reduction is required to relay the light valve to the lens, the image brightness is equal to the light valve object brightness. This brightness can be computed as follows:

$$B = \frac{\text{Flux}}{\text{Area x Solid Angle}} = \frac{750 \text{ lumens}}{6.3 \text{ x } 10^{-3} \text{ x } .0872}$$

B = 1,365,000
$$\frac{1 \text{umens}}{\text{Ft}^2 \text{ steradian}}$$
 = 4,290,000 ft-lambert

This value lies between requirements of the two screen coatings discussed above. For the Stewart coating, this value is about six times below that desired, or would deliver only .17 ft-lambert at 90° projection.

The scale to the right of Figure C-3 shows actual screen brightness that would be achieved versus field angle for the Stewart screen coating. This figure shows the desired 1 ft-lambert could be achieved out to 32° view angle. At 80°, the

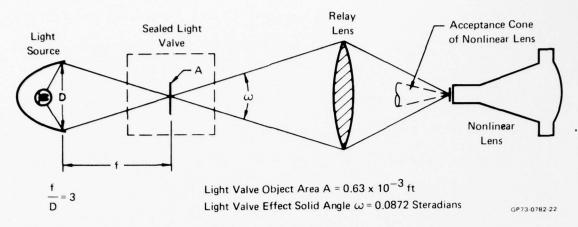


FIGURE C-6 LIGHT VALVE GEOMETRY

assured max field from the existing non-linear lens, the brightness is about .2 ft-lambert.

While Eidophor light valves exist with outputs as high as 4000 lumens, which is sufficient to achieve the desired display brightness, problems such as price, bulkiness, and reliability lead to the off-the-shelf G.E. system being a better choice for a near-term demonstration model. The .2 ft-lambert minimum screen brightness, we believe, is sufficient for these purposes. In the more distant future, single-crystal ferroelectric light valves can be expected to replace the Eidophor type [Reference (C-2)]. In addition to furnishing more light, these devices have a storage capability which will eliminate flicker in the peripheral field of the projected display - (an inherent problem in wide field displays). Therefore, we believe the light valve projection technique, using the existing non-linear lens and existing screen coatings, is a very feasible approach. If performance proves to be marginal, a specialized screen coating can correct the deficiency and assure a display brightness of over 5 ft-lambert.

Appendix D

PROM 1, PROM 2, PROM 3, AND PROM 4 COMPUTER PROGRAM LISTINGS

Figure D-1 is a listing of the PROM No. 1 Computer Program. Figure D-2 is PROM No. 2, Figure D-3 is PROM No. 3, and Figure D-4 is PROM No. 4.

Figure D-1 Prom No. 1 Service Interrupt Handler Software

I	NTEL 6	J CRO	DSS ASSE	MBLER		14:33:02	09-MA.	17	PAGE	2
51	0000		MULBY	EQU	MULAY+20					
52	0000		NCPOX	EQU	*H3D22					
53	0000		NCPOY	EQU	*H3D24					
54	0000		PRJAX	EQU	*H3D26					
55	0000		PRJBX	EQU	*H3D27					
56	0000		PRJAY	EQU	*H3D28					
57	0000		PRJBY	EQU	#H3D29					
58	9000		PRLAX	EQU	#H3D2A					
59	0000		PRLBX	EQU	*H3D2B					
60	0000		PRLAY	EQU	*H3D2C					
61	0000		PRLBY	EQU	#H3D2D					
62	0000		PVLAX	EQU	*H3D2E					
63	6666		PVLBX	EQU	*H3D2F					
64	0000		PVLAY	EQU	*H3D30					
65	0000		PVLBY	EQU	#H3D31					
66	0000		RSTRT	EGU	*H3D40					
67	0000		RINT	EQU	*H3D34					
63	0000		TSTRT	EQU	*H3D47					
69	0000		TINT	EQU	*H3D38					
70	0000		X	EQU	#H3D3A					
71	0000		XY	EQU	#H3D3C					
72	0000		XFLAG	EQU	#H3D3E					
73	0000		YFLAG	EQU	#H3D3F					
74	0000		USCMD	EQU	#H00ED					
75	0000		USDAO	EQU	*H00EC					
76	0000		USDAI	EQU	#HOOEE					
77	0000		PRTAI	EQU	#H0000					
78	0000		PRTB1	EQU	#H0001					
79	0000		PRTC1	EQU	#H0002					
80	0000		PRTA2	EQU	#H0003					
81	0000		PRTB2	EGU	#H00E5					
82	0000		PRTC2	EQU	#H0000					
83	0000		PRTD1	EQU	#H0001					
84	0000		PRTD2	EQU	#H0002					
85	0000		PRTD3	EQU	*H00E6					
86	0000		PIOII	EQU	#H00E7					
87	0000		PIOIZ	EQU	#H00EB					
88	0000		MDWI	EQU	#H009B					
89	0000		MDWZ	EQU	*H0082					
90	0000		N1	EQU	*H0001					
91	0000		N2	EQU	*H0003					
92	0000		PRSET	EQU	*HEA					
93	0000		KYBD1	EQU	*H800					
94	0000		KYBD2	EQU	*H9AB					
95	0000		COMPAR	EQU	*H816					
96	0000			ORG	0					
97	0000	F3		DI						
86		31FF3C		LXI	SP, #H3CFF					
99	0004	CD8101		CALL	INIT					
100	0007	3EC0		MYI	A. #HC0					

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

```
14:33:09 09-MA. /7 PAGE 3
   INTEL & J CROSS ASSEMBLER
101 0009 328A3D
                         STA
                                 OSTAT
102 000C 2F
                         CMA
103 000D D3EA
                         OUT
                                 PRSET
104 000F 3EC7
                         MYI
                                 A. #HC7
105
    0011 2F
                        CMA
    0012 D3EA
106
                        OUT
                                 PRSET
107
    0014 CDC301
                        CALL
                                 SETUP
108
    0017 FB
                         EI
                                 B. #H04
109
    0018 0604
                         M'/ I
110 001A 78
                 LEDS:
                         MOV
                                 A.B
    001B CD8109
                                 OUTPT
111
                         CALL
112 001E 3E11
                         MYI
                                 A. #H11
113 0020 80
                         ADD
                                 В
114 0021 47
                         MOV
                                 B.A
115
    0022 FE59
                         CPI
                                 *H59
116
    0024 C21A00
                         JNZ
                                 LEDS
    0027 CDA401
                                 ZERO
117
                        CHLL
118 002A 76
                        HL.T
                                 A. #HFF
119
    002B 3EFF
                        MYI
120
    002D 2F
                         CI1A
121
     002E D3EA
                         OUT
                                 PRSET
122
     0030 C33300 BKWRD:
                        JIMP
                                 FRWRD
    0033 C33000 FRWRD:
123
                         JI1P
                                 BKWRD
124 0036
                         DS
                                 #H38-$
125 0038 C5
                 SRV:
                         PUSH
                                 В
                                         :5
                         PUSH
126
    0039 D5
                                 D
                         PIJSH
                                         , V
                                                 REG
127
    003A E5
                                 H
    003B F5
                         PIJSH
                                 PSW
                                             E
                                                    ISTERS
128
    003C DB01
                                         INPUT LAST 3 BITS OF PROJ X
129
                         111
                                 N1
130 003E F61F
                        ORI
                                 #H1F
                        CPI
131
    0040 FE1F
                                 #H1F
                        JZ
132
    0042 CASD00
                                 PTY
    0045 47
133
                        MOA
                                 B.A
134
    0046 3EC0
                        MYI
                                 A. #HC0
135
    0048 2F
                         CMA
    0049 D3EA
                                 PRSET
136
                         OUT
    004B 3EC7
137
                         MYI
                                 A. #HC7
138
    004D 2F
                         CMA
                                 PRSET
139
    004E D3EA
                         OUT
140
    0050 78
                         MOA
                                 A.B
141
    0051 217B00
                         LXI
                                 H. JTAB
142
    0054 07 LOOP:
                         RLC
    0055 DA7300
143
                         JC
                                 ST
144 0058 23
                         IHX
                                 H
145 0059 23
                         INX
                                 H
146
    005A C35400
                         JI1P
                                 LOOP
147
    005D DB03
                         HI
                                 N2
    005F F61F
148
                         ORI
                                 #H1F
149
    0061 47
                         MOV
                                 B.A
                         MYI
150
    0062 3EC0
                                 A. *HCO
```

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

```
INTEL BLO CROSS ASSEMBLER
                                                   14:33:16
                                                                09-MA. /7
                                                                             PAGE 4
151
     0064 2F
                           CMA
                                    PRSET
152
     0065 D3EA
                           OUT
153
     0067 3EFF
                           MYI
                                    A. #HFF
154
     0069 2F
                           CMA
     006A D3EA
                                    PRSET
155
                           OUT
156
     006C 78
                           MOV
                                    A.B
157
     006D 218300
                           LXI
                                    H.JTAB+8
158
     0070 035400
                           JIMP
                                    LOOP
159
     0073 FB
                           Εſ
                  ST:
160
     0074 EB
                           XCHG
161
     0075 1A
                           LDAX
                                    D
     0076 6F
162
                           MOV
                                    L,A
     0077 13
163
                           IHX
                                    D
     0078 1A
164
                           LDAX
                                    D
165
     0079 67
                           MOV
                                    H. A
166
     007A E9
                           PCHL
167
     007B 8B00
                  JTAB:
                                    KB1
                                             1J
                           DIJ
                                                      T
168
     007D 9100
                           DIA
                                    KB2
                                             , U
169
     007F 9700
                           DIA
                                    COMPR
170
     0081 AC00
                           DIA
                                    DBRF
171
     0083 9D00
                           DIN
                                    RXRDY
                                                          L
172
     0085 A300
                           DIA
                                    TXRDY
173
     0087 A900
                           DIA
                                    SYSCLK
     0089 AC00
174
                           DIJ
                                    DBRF
175
     008B CD0008 KB1:
                           CALL
                                    KYBD1
                                             SERVICE
176
     008E C3AC00
                           JI1P
                                    DBRF
                                             ;
177
     0091 CDAB09 KB2:
                           CALL
                                    KYBD2
                                             ;
                                                      ROUTINES
178
     0094 C3AC00
                           JI1P
                                    DBRF
179
     0097 CD1608 COMPR:
                           CALL
                                    COMPAR
                                                              FOR
180
     009A C3AC00
                           JIMP
                                    DBRF
181
     009D CD5001 RXRDY:
                           CALL
                                                                        INTER
                                    RX
182
     00A0 C3AC00
                           JI1P
                                    DBRF
     00A3 CD2D01 TXRDY:
183
                           CALL
                                    TX
                                                                            RUPTS
184
     00A6 C3AC00
                           JIMP
                                    DBRF
185
     00A9 CDB100 SYSCLK: CALL
                                    SYS
186
     OOAC FI
                  DBRF:
                           POP
                                    PSW
                                             REST
187
     OOAD E1
                           POP
                                    H
                                                  ORE
     OORE DI
                                                       REGIS
188
                           POP
                                    D
189
     OOAF CI
                           POP
                                    В
                                                               TERS
190
     00B0 C9
                           RET
191
     00B1 21473D SYS:
                                    H, TSTRT
                           LXI
192
     00B4 7E
                           VOM
                                    A.M
193
     00B5 D3EC
                           OIJT
                                    USDAO
194
     00B7 2B
                           DCX
                                    H
195
     00B8 22383D
                           SHLD
                                    TINT
196
     00BB 21403D
                           LXI
                                    H. RSTRT
197
     00BE 22343D
                           SHLD
                                    RINT
198
     00C1 213F3D
                           LXI
                                    H. YFLAG
199
     00C4 3E00
                                    A. #H00
                           MYI
200
     00C6 77
                           MOV
                                    M. A
```

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

INT	TEL B. J CRO	SS ASSE	MBLER		14:33:23	09-MA?	PAGE	5
201 0	OC7 213E3D		LXI	H. XFLAG				
	OCA 77		MOV	M. A				
	OCB DBOO		IH	PRTA1				
	OCD 2F		CMA	· ····				
	OCE 32263D		STA	PRJAX				
	OD1 DB01		IN	PRTB1				
	eD3 2F		CMA					
	0D4 E61F		ANI	*H1F				
	0D6 32273D		STA	PRJBX				
	PD9 DB02		IH	PRTC1				
	ODB ZF		CMA					
	ODC 32283D		STA	PRJAY				
	ODF DB03		IH	PRTA2				
	OE1 2F		CMA					
215 0	0E2 E61F		AHI	*H1F				
	0E4 32293D		STA	PRJBY				
	eer 3A2E3D		LDA	PVLAX				
218 0	OEA 2F		CI1A					
	0EB D300		OUT	PRTC2				
	OED 3A2F3D		LDA	PVLBX				
221 0	OFO EGOF		AHI	#HOF				
	0F2 322F3D		AT'S	PVLBX				
	0F5 3A313D		LDA	bhi 3A				
	OF8 E6F0		AHI	#HF0				
	0FA 57		MOA	D.A				
	OFB 3A2F3D		LDA	PVLBX				
	OFE B2		ORA	D				
	OFF 2F		CITA					
	100 D301		OUT	PRTD1				
	102 3A303D		LDA	PVLAY				
	105 2F		CMA					
	106 D362		OUT	PRTD2				
	108 DBE4	PORT:	IH	*HE4				
	10A 67		RLC	00112				
	10B DA1101		JC	SCND				
	10E D20801	CCND.	JHC	PORT				
	111 DBE4	SCND:	IH	*HE4				
	113 07		RLC					
	114 07 115 D21101		RLC JHC	SCND				
	118 DBE6		IN	PRTD3				
	11A C680		AD I	*H80				
	11C 32103D		STA	DETX				
	IIF DBES		IN	PRTB2				
	121 C680		AD I	#H80				
	123 32113D		STA	DETY				
	126 CD0004		CALL	YAW				
	129 CD000C		CALL	PITCH				
	12C C9		RET					
	12D 2A383D	TX:	LHLD	TINT				

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

I	NTEL B. J CR	OSS ASSE	MBLER		14:33:30	09-MA?	PAGE	6
251	0130 7D		MOV	A.L				
252	0131 FE45		CPI	◆H45				
253	0133 CA4101		JZ	YF				
254	0136 DA4001		JC	ET				
255	0139 7E	CONT:	MO▼	A.M				
256	013A 2B	CONT	DCX	H				
257	013B 22383D		SHLD	TINT				
258	013E D3EC		OUT	USDAO				
259	0140 C9	ET:	RET	USDAO				
260	0141 213F3D		LXI	H, YFLAG				
261	0144 7E	11.	MOV	A.M				
262	0145 C680		AD I	*H80				
263	0147 77		MOA	M. A				
264	0148 1F		RAR	11) II				
265	0149 2A383D		LHLD	TINT				
266	014C DA3901		JC	CONT				
267	014F C9		RET	CONT				
268	0150 2A343D	DV.	LHLD	RINT				
269	0153 7D	MA.	MOV	A.L				
270	0154 FE44		CPI	*H44				
271	0156 CA7A01		JZ	CM				
272	0159 DBEC		IH	USDAO				
273	015B 77		MOA	M. A				
274	015C 23		INX	Н				
275	015D 22343D		SHLD	RINT				
276	0160 7D		MOV	A.L				
277	0161 FE42		CPI	*H42				
278	0163 CA7301		JZ	BM				
279	0166 FE43		CPI	◆H43				
280	0168 CH6C01		JZ	AM				
281	016B C9		RET	•••				
282	016C 2A3B3D	AM:	LHLD	TINT				
283	016F 7E		MOV	A.M				
284	0170 D3EC		OUT	USDAO				
285	0172 C9		RET					
286	0173 213E3D	BM:	LXI	H. XFLAG				
287	0176 3E01		M'II	A. *H01				
288	0178 77		MOV	M. A				
289	0179 C9		RET					
290	017A 2A0E3D	CM:	LHLD	CPLAY				
291	017D 220A3D		SHLD	CPOAY				
292	0180 C9		RET					
293		INIT:	MYI	A.MDW1				
294	0183 D3E7		OUT	PIOII				
295	0185 3E82	INIT1:	MVI	A. MDWZ				
296	0187 D3EB		OUT	PIOIZ				
297	0189 AF	UCLEAR:		A				
298	018A D3ED		OUT	USCMD				
299	018C D3ED		OUT	USCMD				
300	018E D3ED		OUT	USCMD				

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

I	NTEL & J CRO	OSS ASSE	MBLER		14:	33:37	09-MA7	PAGE	7
301	0190 3E40		MVI	A. #H40					
302	0192 D3ED		OUT	USCMD					
303	0194 3E6E		MYI	A. *H6E					
304	0196 D3ED		OUT	USCMD					
305	0198 3E37		MYI	A. #H37					
306	019A D3ED		OUT	USCMD					
307	019C DBEE		IH	USDAI					
308	019E DBEE		IH	IAGZU					
309	01A0 AF		XRA	A					
310	OTAL DREC		OUT	USDAO					
311	01A3 C9		RET						
312	01A4 210000	ZERO:	LXI	H. #H0000					
313	01A7 223A3D		SHLD	*H3D3A					
314	01AA 223C3D		SHLD	*H3D3C					
315	01AD 22603D		SHLD	*H3D60					
316	01BA 22703D		SHLD	#H3D70					
317	01B3 22743D		SHLD	*H3D74					
318	01B6 22783D		SHLD	*H3D78					
319	01B9 22EE3F		SHLD	*H3FEE					
320	01BC 22EA3F		SHLD	*H3FEA					
321	01BF 22EC3F		SHLD	*H3FEC					
322	01C2 C9	CETUE	RET						
323	01C3 AF	SETUP:	XRA	A		-	PIDOT - PP		
324	01C4 32E03F		STA		FIRST-0.	IHEN	FIRSI-FF		
325	OIC7 CD140A		CALL	SETML					
326	OICA SEFF		MYI	A. #HFF					
327	01CC 32E03F		STA	FIRST					
328	01CF C9 01D0		RET						
329	0100		EHD						

Figure D-1 Prom No. 1 Service Interupt Handler Software (Continued)

INTEL 8086	ROSS ASSEMBLER	SYMBOL TABLE	3			
PVLBX- 3D2F	PVLAY-	3D30	PVLBY-	3D31	RSTRT-	3D40
RINT - 3D34	TSTRT-	3D47	TINT -			3D3A
XY - 3D3C	XFLAG=	3D3E	YFLAG-	3D3F	USCMD -	
USDAO- 00EC	USDAI -		PRTA1=		PRTB1-	9 5 5 5
PRTC1 - 0002	PRTA2=	0003	PRTB2-		PRTC2=	
PRTD1 = 0001	PRTD2=	0002	PRTD3-		PRSET-	9 00 000
COMPA= 0816		001A	BKWRD	0030	FRWRD	0033
SRV 0038		0054	PTY	005D	ST	0073
JTAB 007B		0097	RXRDY	009D	TXRDY	6003
SYSCL 00A9		00B1	PORT	0108	SCHD	0111
TX 012D		0139	ET	0140	YF	0141
RX 0150		0001	KB1		KB2	0091
N2 = 0003		0173	CM	017A		0007
INIT 0181		0185		0000	UCLEA	0189
ZERO 01A4	The state of the s	0001	SETUP			0002
E = 0003	KYBD1=		KYBD2=		P 10 I 1 =	
MDW1 = 009B	MDW2 =		PIOI2=		DBRF	OAC
AM 016C		0004	CAMAX=		CAMBX=	
CAMAY= 3D02	CAMBY=		CCMAX=		CCMBX=	
CCMAY- 3D06	CCMBY=		PITCH=		CPOAX=	
CPLAX= 3D0C		0005	CPOBX=		CPOAY=	
M = 0006	SETML =		CPOBY=		FIRST=	
CPLBX= 3D0D	CPLAY-		OSTAT=		CPLBY=	
DETX = 3D10	YAW -		DETY =		IPRJX=	
SP = 0006	IPRJY-	0.7.3.7	IPOSX-		OUTPT=	
IPOSY= 3D20	MULCX=		PSW =		MULDX=	
MXLVB= 3DB8	MXLVK-	22.5	MULAX-		MULBX=	
WATCA = 3E08	MULDY=	17.77	MYLVB=		MYLVK=	
MULAY= 3E58	MULBY=		NCPOX=		NCPOY=	200
PRJAX= 3D26	PRJBX=	10,000	PRJAY=		PRJBY=	100000000000000000000000000000000000000
PRLAX= 3D2A	PRLBX=	3D2B	PRLAY=	3DZC	PRLBY=	3D2D
PVLAX= 3D2E						

ERRORS DETECTED: 0

Figure D-1 Prom No. 1 Service Interupt Handler Software (Concluded)

Figure D-2 Prom No. 2 Yaw Control Software

MICHY+20

50

0000

CMIY

EQU

```
14:35:38
                                                               09-MA -77
                                                                             PAGE 2
   INTEL 6. JO CROSS ASSEMBLER
 51
     0000
                  CMIX
                           EQU
                                    CMIY+20
 52
     0000
                  TORQY
                           EQU
                                    CMIX+20
                                    TORQY+20
 53
     0000
                  TORQX
                           EQU
 54
     9000
                  PTQX
                           EQU
                                    TORQX+20
 35
     0000
                  NCPOX
                           EQU
                                    *H3D22
 56
     0000
                  NCPOY
                           EQU
                                    *H3D24
 57
     0000
                  PRJAX
                           EQU
                                    *H3D26
 58
                  PRJBX
     0000
                           EQU
                                    *H3D27
 59
     0000
                  PRJAY
                                    *H3D28
                           EQU
 60
     0000
                  PRJBY
                           EQU
                                    *H3D29
 61
     0000
                  PRLAX
                           EQU
                                    #H3D2A
 62
     0000
                  PRLBX
                           EQU
                                    #H3D2B
 63
     0000
                  PRLAY
                           EQU
                                    *H3D2C
 64
     0000
                  PRLBY
                           EQU
                                    *H3D2D
 65
     0000
                  PVLAX
                           EQU
                                    *H3D2E
 66
     0000
                  PVLBX
                           EQU
                                    #H3D2F
                  PVLAY
 67
     0000
                           EQU
                                    *H3D30
 68
     0000
                  PVLBY
                           EQU
                                    *H3D31
 69
     0000
                  RINT
                           EQU
                                    *H3D34
 70
     0000
                  TINT
                           EQU
                                    *H3D38
 71
     0000
                           EQU
                  X
                                    *H3D3A
 72
                  XY
     0000
                           EQU
                                    #H3D3C
 73
                  XFLAG
     0000
                           EQU
                                    *H3D3E
 74
     0000
                  YFLAG
                           EQU
                                    *НЭДЭГ
 75
     0000
                  DELY
                           EQU
                                    *H3D68
 76
                   ICHY
     8000
                           EQU
                                    *H3D70
 77
     0000
                  CDEL
                           EQU
                                    *H3D72
 78
     0000
                   CIY
                           EQU
                                    *H3D74
 79
     0000
                   USDAO
                           EQU
                                    *H00EC
 80
                  PRTA1
     0000
                           EQU
                                    *H0000
 81
     0000
                  PRTB1
                           EQU
                                    *H0001
 82
     0000
                  PRTC1
                           EQU
                                    *H0002
 83
     0000
                  PRTA2
                           EQU
                                    *H0003
 84
     0000
                  PRTB2
                           EQU
                                    *H00E5
 85
     0000
                  PRTC2
                           EQU
                                    *H0000
 86
                  PRTD1
                                    *H0001
     0000
                           EQU
 87
     0000
                  PRTD2
                           EQU
                                    *H0002
 88
     0000
                  PRTD3
                           EQU
                                    *H00E6
 89
 90
                       SYSTEM COMPENSATION NETWORK
 91
 92
93
                           ORG
                                    ♦H400
     0.400
 94
     0400 2A2C3D YAW:
                           LHLD
                                    PRLAY
 95
     0.403 EB
                           XCHG
                                    PRJBY
 96
     0494 3A293D
                           LDA
 97
     0407 E610
                           AHI
                                    *H10
 36
     0409 FE10
                           CPI
                                    *H10
 99
     040B C21604
                           JHZ
                                    RA
100
     040E 3A293D
                           LI)A
                                    PRJBY
```

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

INTEL 8	36.08	CROSS	ASSEMBL:	ER
---------	-------	-------	----------	----

101	0.411	C6E0		AD I	*HE0
102	0.413	322930		STA	PRJBY
103	0416	2A283D	RA:	LHLD	PRJAY
104	0.419	CD4305		CALL	MINUS
105	0.410	19		DAD	D
106	0.41D	CD4305		CALL	MINUS
107	0.420			XCHG	
108		CD083E		CALL	MULCY
109		22143D		SHLD	DDOTY
110	0.427			IH	USDAO
111		3A113D		LDA	DETY
112	0.12C	FE04		ChI	•H04
113		F24004		Jp.	PAA
114	0.431	FEFC		CPI	*HFC
115		FA3B04		J11	MAN
116	0.436			MYI	A. #H00
117	0.438			JIMP	ING
118		C604	MAN:	AD I	*H04
119	043D		, 121111	J11P	ING
120	0.440		PAA:	AD I	*HFC
121	0.442		ING:	MOV	E.A
122		FE00	1110:	CPI	*H00
123	0.145			MYI	A. #H00
124	0447			JP	ZP
125	044A				25
126	0.44B		ZP:	CMA	D.A
127	0440		2.51	MOV	
128				CALL	MULDY
129	0.44F 0.450			XCHG	D D O TWY
130	0.453			LHLD	DDOTY
	0.454	7.7		DIAD	D
131				XCHG	^ D
132	0.455 0.456			MO♥	A.D
133 134				RLC	E 1110
	0.457			JC	FIVE
135		ZICCFF		LXI	H. *HFFCC
136	0.45D			DAD	D
137		DA7204		JC	PL
138	0.461	C36B04		JMP	TWO
139		213400	FIVE	LXI	н. #Н0034
140	0467	19		DAD	D
141	0.468	The second secon		JHC	ML
142	0.46B	EB	TWO:	XCHG	
143	046C	22023D		SHLD	CAMAY
144		C38404		JIMP	EXT
1.15		213400	Pl. :	TXI	H. #H34
146		22023D		SHLD	CAMAY
147		C38404		JIMP	EXT
148		21CCFF	ML:	LXI	H. *HFFCC
149	0.47E	22023D		SHLD	CAMAY
150	0.481	C38404		JIMP	EXT

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

*H3D80

*H3D82

SHIFT

XCHG

SHLD

LHLD

CALL

DAD

196 04E3 EB

200 04F2 13

199

197 04#9 22803D

198 04EC 2A823D

04EF CD4B05

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

A.H

A.H

H. A

A.L

RET

MOV

RLC

MOV

RAR

MOV

MOV

SHIFT:

244

245

246

247

248 249

250

054A C9

054B 7C

054C 07

054D 7C

054E 1F

054F 67

0350 7D

	INTEL 8060	CROSS	ASSEMBLER		14:36:06	09-MAY-/7	PAGE	6
251	0551 1F		RAR					
252	0552 6F		MOV	L.A				
253	0553 C9		RET					
254	0354		END					

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

MYLVK+20

50

0000

MULAY

EQU

1	NTEL 6. 3 CR	SS ASSE	MBLER		14:37:49	09-MA. 77	PAGE	2
51	0000	MULBY	EQU	MULAY+20				
52	0000	MICH	EQU	MULBY+20				
53	0000	MICHY	EQU	MICH+20				
54	0000	CMIY	EQU	MICHY+20				
55	0000	CMIX	EQU	CMIY+20				
56	0000	TORQY	EQU	CMIX+20				
57	0000	TORQX	EQU	TORQY+20				
58	0000	PTQX	EQJ	TORQX+20				
59	9000	NCTOX	EGU	*H3D22				
60	0000	NCPOY	EQU	*H3D24				
61	0000	PRJAX	EQU	*H3D26				
62	0000	PRJBX	EQU	#H3D27				
63	0000	PRJAY	EQU	#H3D28				
64	0000	PRJBY	EQU	*H3D29				
65	0000	PRLAX	EQU	*H3D2A				
66	0000	PRLBX	EQU	*H3D2B				
67	9999	PRLAY	EQU	*H3D2C				
68 69	0000 0000	PRLBY PVLAX	EQU EQU	#H3D2D #H3D2E				
70	0000	PVLHA	EQU	#H3D2F				
71	0000	PVLAY	EQU	#H3D30				
72	0000	PVLBY	EQU	#H3D31				
73	0000	RINT	EQU	#H3D34				
74	0000	TINT	EQU	*H3D38				
75	0000	X	EQU	#H3D3A				
76	9000	XY	EQU	#H3D3C				
77	0000	XFLAG	EQU	*H3D3E				
78	0000	YFLAG	EQU	#H3D3F				
79	0000	DELY	EQU	#H3D68				
80	0000	ICHY	EQU	#H3D70				
81	0000	USDAO	EQU	#H00EC				
82	9000	PRTH1	EQU	#H0000				
83	0000	PRTB1	EQU	#H0001				
84	0000	PRTC 1	EQU	#H0002				
85	0000	PRTA2	EQU	*H0003				
86	0000	PRTB2	EQU	*H00E5				
87	0000	PRTC2 PRTD1	EOU	#H0000				
68 89	0000 0000	PRTD2	EQU	#H0001 #H0002				
90	0000	PRTD3	EQU	*H00E6				
91	0600	INIDS	ORG	*H600				
92	0600 7A		MOV	A.D				
93	0601 07		RLC					
94	0602 DA1506		JC	YMI				
95	0605 2100F0		LXI	H, #HF000				
96	0608 19		DAD	D				
97	0609 DA0F06		JC	YLM				
98	060C C32506		JIMP	KV				
39	060F 210040	YLM:	LXI	H. #H4000				
100	0612 C32806		JI1P	YE				

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

```
0615 210010 YMI:
                                    H. #H1000
101
                           LXI
102
     0618 19
                           DAD
                                    YDE
103
     0619 DZ1F06
                           JHC
                           JIMP
                                    KV
     061C C32506
104
     061F 2100C0 YDE:
                           LXI
                                    H. #HC000
105
106
     0622 C32806
                           J11P
                                    YE
     0625 CD443E KV:
                                   MYLVK
107
                           CALL
108
     0628 EB
                  YE:
                           XCHG
                                    DOTIY
109
     0629 ZA183D
                           LHLD
110
     062C 19
                           DIJD
                                    D
                           XCHG
111
     062D EB
                           MOV
                                    A.D
112
     062E 7A
113
     062F 07
                           RLC
                                    KAD
                           JC
     0630 DA4306
114
115
     0633 21BCFB
                           LXI
                                    H. *HFBBC
     0636 19
                           DAD
                                    D
116
     0637 DA3D06
                                    OH
                           JC
117
                           JIMP
                                    GOSH
     063A C35306
118
119
     063D 210040 OH:
                           LXI
                                    H. #H4000
120
     0640 C35606
                           JIMP
                                    OSH
                                    H. #H444
     0643 214404 KAD:
                           LXI
121
122
     0646 19
                           DAD
     9647 D24D06
                                    MOM
123
                           JHC
124
     064A C35306
                           JI1P
                                    GOSH
     064D 2100C0 WOW:
                                    H. #HC000
125
                           LXI
126
     0650 C35606
                           J11P
                                    OSH
     0653 CDD03E GOSH:
                                    TORQY
127
                           CALL
128
     0656 22443D OSH:
                           SHLD
                                    CCMAY
129
130
                  : PROJECTOR SERVO
131
132
133
     0659 CDAC07
                           CALL
                                    LAG
                                    CDEL
134
     063C 2A723D
                           LHLD
135
     065F EB
                           XCHG
136
     0660 7A
                           MOV
                                    A.D
137
     0661 07
                           RLC
     0662 D26F06
138
                           JHC
                                    YYY
     0665 210004
                           LXI
                                    H. #H400
139
140
     0658 19
                           DAD
                                    D
                                    LARGE
141
     0669 D27906
                           JHC
142
     066C C38806
                           JIMP
                                    GO
143
     066F 2100FC YYY:
                           LXI
                                    H. #HFC00
     0672 19
144
                           DAD
                                    D
145
     0673 DA7906
                           JC
                                    LARGE
146
     0676 C38806
                           JI1P
                                    GO
147
     0679 2A3C3D LARGE:
                           LHLD
                                    XY
148
     067C CDA407
                           CALL
                                    MINUS
     067F CD9B07
149
                           CALL
                                    SHIFT
150
     0682 CD9B07
                           CALL
                                    SHIFT
```

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

PB

PC

JC

JITP

06F1 DA0107

06F4 C30C07

199

200

```
201 06F7 210040 PA:
                           LXI
                                    H. #H4000
                           DAD
                                    D
202
     06FA 19
                                    PD
203
     06FB D20807
                           JHC
204
                           JI1P
                                    PC
     06FE C30C07
205
                           LXI
                                    H. #H4000
     0701 210040 PB:
206
     0704 EB
                           XCHG
                           JIMP
                                    PC
     0705 C30C07
207
208
     0708 2100C0 PD:
                           LXI
                                    H. #HC000
209
     070B EB
                           XCHG
     070C 2A203D PC:
                                    IPOSY
                           LHLD
210
     070F 19
                           DAD
                                    D
211
     0710 22203D
                           SHLD
                                    IPOSY
212
     0713 2A2C3D OUT:
                                    PRLAY
213
                           LHLD
     0716 EB
                           XCHG
214
     0717 2A283D
                           LHLD
                                    PRJAY
215
216
     071A 222C3D
                           SHLD
                                    PRLAY
                                    IPRJY
                           LHLD
217
     071D 2A1C3D
218
     0720 19
                           DAD
219
     0721 22F23F
                           SHLD
                                    #H3FF2
                                    H
220
     0724 29
                           DAD
221
     0725 29
                           DAD
                                    H
222
     0726 29
                           DAD
                                    H
                                    Н
223
     0727 29
                           DIAD
                                    H
224
     0728 29
                           DAD
225
     0729 EB
                           XCHG
226
     072A ZAEC3F
                           LHLD
                                    EOY
     072D CDA407
                           CALL MINUS
227
     0730 19
                                    D
228
                           DAD
229
     0731 29
                           DAD
                                    H
230
     0732 EB
                           XCHG
                           MOV
                                    A.D
231
     0733 7A
232
     0734 07
                           RLC
233
     0735 D24207
                           JHC
                                    OGDR
     0738 210004
                           LXI
                                    H. #H400
234
235
     073B 19
                           DAD
                                    D
236
     073C D25207
                           JHC
                                    DECR
237
     073F C35807
                           JIMP
                                    BXR
     0742 2100FC OGDR:
                                    H. #HFC00
238
                           LXI
     0745 19
239
                           DAD
                                    LMAR
240
     0746 DA4C07
                           JC
                           JIMP
241
     0749 C35807
                                    BKR
     074C 210040 LMAR:
                           LXI
                                    H. #H4000
242
243
     074F C35B07
                           JIMP
                                    IPR
                                    H. #HC000
244
     0752 2100C0 DECR:
                           LXI
245
                           J11P
     0755 C35B07
                                    IPR
246
     0758 CD583E BXR:
                           CALL
                                    MULAY
247
     075B CD9B07 IPR:
                           CALL
                                    SHIFT
                           CALL
     075E CD9B07
                                    SHIFT
248
249
     0761 EB
                           XCHG
250
     0762 2A623D
                           LHLD
                                    *H3D62
```

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

Figure D-2 Prom No. 2 Yaw Control Software (Continued)

H

DAD

300

07B0 29

IN	THE BUD CROSS	ASSEMBLER		14:38:24	09-MA. /7	PAGE	7
	07B1 29 07B2 EB	DAD XCHG	н				
303	07B3 2AEC3F	LHLD	EOY				
304	07B6 CDA407	CALL	MINUS				
305	07B9 19	DIAD	D				
306	07BA EB	XCHG					
307	07BB CDA83E	CALL	CMIY				
308	07BE CD9B07	CALL	SHIFT				
309	07C1 CD9B07	CHLL	SHIFT				
310	07C4 CD9B07	CALL	SHIFT				
311	07C7 CD9B07	CALL	SHIFT				
312	07CA CD9B07	CALL	SHIFT				
313	07CD CD9B07	CALL	SHIFT				
314	07D0 CD9B07	CALL	SHIFT				
315	07D3 EB	XCHG					
316	07D4 ZAEC3F	LHLD	EOY				
317	07D7 19	DAD	D				
318	07D8 22EC3F	SHLD	EOY				
319	^7DB C9	RET					
320	0?DC	EHD					

Figure D-2 Prom No. 2 Yaw Control Software (Concluded)

```
11:25:50
                                                            27-MA. 77
                                                                         PAGE 1
  INTEL 6. JO CROSS ASSEMBLER
    0800
                           ORG
                                  *H800
                                           *KEYBOARD INPUT PORT
 2
    0800
                 KB1N2
                           EQU
                                  #HE9
                                           INTERRUPT FLIP-FLOP PRESET OUTPUT PO
 3
    9989
                           EQU
                                  #HEA
                 PRSET
                                  #HE8
                                           :LED AND COMPARATOR OUTPUT PORT
    6630
                 LEDCM
                           EQU
 5
    0800
                 SETML.
                         EQÜ
                                  *HA14
 6
    0800
                           EQU
                                    #HA64
                 OK
    0800
                 CHANG
                           EQU
                                    *HA52
                                           :3D80 THRU 3D83 IS ADDRESS INFORMATIO
 8
    0300
                 READA
                           EQIJ
                                  ₩H3D80
                                           :3D84 THRU 3D85 IS DATA INFORMATION
 9
    0800
                                  READA+4
                 READD
                           EQU
10
    0800
                 READF
                           EQU
                                  READD+2
                                          3D86 IS FUNCTION INFORMATION
11
    0000
                 FSTAT
                           EQU
                                  READF+1
                                           :3D8? IS FUNCTION STATUS
12
    0880
                           EQU
                                           PSAYE IS THE RETURN LOCATION IN INTE
                 PSAVE
                                  FSTAT+1
                           EQU
13
    0800
                 OSTAT
                                  PSAVE+2
                                           CODE FOR EXPECTED KEYBOARD INPUT
                 * KBRD1 IS THE FUNCTION KEYBOARD SERVICE ROUTINE
14
15
    0800 DBE9
                 KBRD1:
                           IN
                                  KB1N2
                                           : INPUT FUNCTION INFORMATION
                                           :WE'RE ONLY INTERESTED IN HIGH ORDER
    0802 E6F0
16
                           ANI
                                  #HF0
                                           SAVE FOR LATER
17
    0894 47
                           MOV
                                  B.A
18
    0805 32863D
                           STA
                                  READF
                                           STORE INFORMATION READ
19
                                           ; IS THIS A MAJOR OR MINOR FUNCTION
    0808 FEB0
                           CPI
                                  *HBO
20
    030A DA1F08
                           JC
                                  FUNCT
                                           ;MINOR FUNCTION-JUMP TO FUNCT
2.1
                      OSTAT=CO MEANS MAJOR FUNCTION IS EXPECTED
                 ;
                      OSTAT=20 MEANS MINOR MONITOR FUNCTION IS EXPECTED
22
                      OSTAT=10 MEANS MINOR SETML FUNCTION (CHANG OR OK) IS EXPE
23
                      OSTAT=8 MEANS NUMBER IS EXPECTED
24
25
                            LDA
                                   OSTAT
26
                            CPI
                                   #HC0
27
                            JNZ
                                   ERROR
28
    080D 78
                           MOV
                                  A.B
29
    0803 FED0
                           CPI
                                  #HD0
                                           FIND PARTICULAR MAJOR FUNCTION
    0810 DA:40A
                                           JUMP TO SETML IF ACC-CO
30
                           JC
                                  SETML
                                           JUMP TO START IF ACC-DO
31
    0813 CAB309
                           JZ
                                  START
32.
                 COMPR IS THE ENTRY POINT WHEN A COMPARE INTERRUPT OCCURS
33
    0816 3E20
                 COMPR:
                           MVI
                                  A. #H20
                                           MONITOR OR COMPARE INTERRUPT HAS OCC
34
    0318 328A3D
                           STA
                                  OSTAT
                                           GET READY FOR MONITOR MINOR FUNCTION
35
    081B 78
                           MOV
                                  A.B
    081C C32E08
                                 MONITR
36
                           JMP
37
    081F FE80
                FUNCT:
                           CPI
                                  *H80
                                           CHECK FOR MONITOR MINOR OR SETML MIN
38
    0821 DA2D08
                           JC
                                  DIAG
                                           ; IF LESS THAN 80 IT'S A MONITOR MINOR
39
                                            CHECK FOR SETML MINOR FUNCTION
                            LDA
                                  OSTAT
40
                            ANI
                                  #H10
41
                                   ERROR
                            JZ
42
    0824 78
                           MOV
                                  A.B
                                           IS IT AN OK OR A CLANG
    0825 FE90
43
                           CPI
                                  #H90
44
    0827 FA640A
                           JC
                                  OK
                                           : IF ACC=80.OK
45
    082A C3520A
                           JMP
                                  CHANG
46
                 DIAG IS THE ROUTINE FOR MONITOR MINOR FUNCTIONS
47
                                  OSTAT
                 :DIAG:
                            LDA
                                            CHECK TO SEE THAT THE
48
                            ANI
                                   #H20
                                               FUNCTION SELECTED
                ;
                                            ;
49
                            JZ
                                   ERROR
                                               NAS EXPECTED
    082D C9
50
                 DIAG:
                           RET
```

Figure D-3 Prom No. 3 Monitor Program

Figure D-3 Prom No. 3 Monitor Program (Continued)

D. 1

I VI

DEPR:

100

0895 1601

```
11:26:15 27-MA. /?
                                                                       PAGE 3
   INTEL 6 3 CROSS ASSEMBLER
     0897 CD0509
                          CALL E2
                                          READ IN ONE DIGIT
                          CALL MEMORY
                                          FIND LOCATION OF REGISTER IN STACK
192
     089A CD3709
103
     089D EE
                          KCHG
104
     089E CD4309
                          CALL
                                TWO
                                          FREAD IN TWO MORE DIGITS, CONCATENATE
105
     0EA1 C34A08
                          JMF
                                NEXT
196
                 ISSTEP CAUSES ONE INSTRUCTION OF THE SOURCE PROGRAM TO BE
107
                 . EXECUTED FOLLOWED BY A RETURN TO THE DIAGNOSTIC
108
                 : ROUTINE
109
                           MVI A.9
                                          SET THE CMPR FLIP-FLOP
     08A4 3E09
                 SSTEP:
                           CALL OUTPT
110
    6886 CD8109
111
     08A9 C3BD08
                          JMP.
                                RESET
                 CONT AUTOMATICALLY INCREMENTS THE TABLE POINTER FOR THE
112
                 : DEPM AND EXAMM FUNCTIONS
113
114
     08AC 3A873D CONT:
                         LDA FSTAT
                                          EXAMM OR DEPM?
     08AF CD1B09
                          CALL JUMP
                                          GET READY TO JUMP TO SRD
115
                                          : INSTRUCTION OF DEPM OR
                          1 V11
                              A.6
116
     08B2 3E06
                                          : EXAMM. ADDRESS IN RAM IS
117
     08B4 65
                          ADD
                               I.
                          MOV L.A
                                             INCREMENTED BY ONE
113
     06B5 6F
                                          ;
119
     08B6 3E00
                          MVI A.O
                                          : ADD
120
     93B3 17
                          RAL
                                          : CARRY
121
     28B9 34
                          ADD H
                                          ; OUT OF
122
     08BA 67
                           MOV H.A
                                          1 L TO H
                                          ; INFORMATION AT READA THRU
     08BB 13
123
                           INX D
124
     08BC E9
                          PCHL
                                          : READA+3 IS UNCHANGED.
                 RESET RESTORES THE REGISTERS AND INTERRUPT STATUS AND
125
126
                 ; OLD PROGRAM COUNTER
127
     08BD 3EC0
                 RESET:
                           MVI A, #HC0
                                          :GET READY TO
    00BF 328A3D
                           STA OSTAT
                                          : SAY GOODBYE
128
123
    08C2 3EFF
                           MVI A. #HFF
                                          : ENABLE
139
     €8C4 2F
                           CMA
                                          : ALL INTERRUPT
131
                           OUT PRSET
     08C5 D3EA
                                          ; FLIP-FLOPS
     08C7 2A883D
132
                           LHLD PSAVE
                                          GET THE INTERRUPT HANDLER ADDRESS
103
     08CA E9
                           PCHL
                                          : RETURN
134
                 COMPA LOADS THE COMPARE ADDRESS BUFFERS AND THEN RETURNS
135
                 ; TO THE SOURCE PROGRAM
    08CB CD3309 COMPA:
136
                         CALL E1
                                          READ IN FOUR HEX DIGITS
137
     08CE C3A403
                          JMP
                                SSTEP
                                         ; AND LOAD COMPARE ADDRESS
                 SPLIT DECODES A 16-BIT NUMBER SO THAT IT CAN BE DISPLAYED
138
                 : ON THE FRONT PANEL
139
                              C.4
140
     08D1 9E04
                 SPLIT:
                          14V I
                                          REGISTER C POINTS TO LED DISPLAY
                                          SPLIT ACCUMULATOR INTO TWO HEX
141
     08D3 6F
                 SECND:
                          VON
                                L.A
142
     08D4 29
                          DAD
                                H
                                          : DIGITS, CONTAINED IN LOW ORDER
143
     08D5 29
                          DAD
                                H
                                          : FOUR BITS OF H AND L REGISTERS.
     0BD6 29
144
                          DAD
                                H
145
     08D7 29
                          DAD
                                H
145
     08D8 7D
                          VON
                                A.L
147
     08D9 CDEF08
                          CHL
                                LOW
148
    BBDC OC
                          INR
                                C
145
    930D 44
                          VON
                                B, H
150
    OHDE CDESOS
                          CALL
                               DATA
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

```
C
151 08E1 0C
                         INR
152 08E2 C9
                        RET
                STORE STORES THE DATA READ IN
153
                STORE:
                                        LOOK AT LOW ORDER FOUR
154
    08E3 E60F
                        INFI
                              #HF
    08ES 77
                                        ; BITS READ IN. MOVE INTO READA
                        140 V
                              M. A
155
                                        ; INCREMENT READA TABLE POINTER.
156 08E6 23
                         INX
                             H
    00E7 47
                         MOV B.A
157
                                        : MOV DATA TO
158
    08E8 78
                DATA:
                        V Ort
                              A.B
                                        : HIGH ORDER
159
    08E9 E60F
                         IME
                              #HF
160
    06EB 07
                         RLC
                                        ; FOUR BITS
161
    98EC 07
                         RLC
    08ED 07
                         RTC
162
163
    08EE 07
                        RLC
               LOW: ORA C
                                       OUTPUT DATA
    ecef Bi
164
                                      TO APPROPRIATE
DISPLAY
                        CALL OUTPT
    08F0 CD8109
165
                        MOV B.A
LDA FSTAT
CPI #H70
166
    08F3 47
                                        IS THIS PERHAPS A COMPARE
167
    08F4 3A873D
                        LDA
    08F7 FE70
168
                        CPI
                                        : ADDRESS OR SINGLE STEP?
                       RNZ
    08F9 C0
169
170
    08FA 78
                       140 V A. B
                                      ; IF AC OR SS LOAD
171
    08FB D604
                       SUI 4
                                      ; COMPARE ADDRESS BUFFERS
                        XRI #HF0
                                       COMPLEMENT THE HIGH ORDER FOUR BITS
172
    08FD EEF0
173
    08FF CD8109
                         CALL OUTPT
174
    0902 C9
                        RET
175
                ;E1.E2.SHARE AND REPEAT ARE ENTRY POINTS TO A ROUTINE THAT
                : CONTROLS THE READING AND STORING OF DATA.
176
                E1: MVI D.4 :D CONTAINS . OF DIGITS OT BE READ.
177
    0903 1604
178
    0905 0107
               E2:
                        11VI C.7
                                       C CONTAINS DISPLAY POINTER
                        LXI H, READA
MVI A.8
STA OSTAT
    0907 21803D SHARE:
179
180
    090A 3E08 REPEAT: 090C 328A3D
181
                         CALL READ
    090F CD7109
182
    0912 CDE308
183
                        CALL STORE
                                       STORE AND
                        DCR C
184 0915 9D
                                       : DISPLAY DIGIT
                        DCR
185 0916 15
                             D
                                        : READ
186
    0917 CB
                        RZ
187
    0918 C30A09
                        JMP
                              REPEAT
                JUMP DETERMINES THE ADDRESS OF THE DIAGNOSTIC ROUTINE
188
189
                ; TO BE USED
                JUMP: CPI
190 091B FE50
                              #H50
                                        STORE THE FUNCTION READ AT FSTAT
191
    091D CAZA09
                         JZ
                             AROUND
                                      ; AND DISPLAY ON HIGH ORDER
                        STA FSTAT
192
    0920 32873D
                                        ; DISPLAY (IF OTHER THAN CONT).
193
    0923 F5
                        PUSH PSW
194
    0924 F608
                        ORI
                              8
    0926 CD8109
                         CALL OUTPT
195
    0929 F1
                        POP PSW
196
                AROUND: RRC
197
    092A 0F
                                        : COMPUTE
198 092B 0F
                         RRC
                                        ; JIJMP
199
    092C 0F
                                        ; TABLE
                        RRC
200 092D 215708
                                             POSITION
                        LXI
                              H. EM
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

```
INTEL 8000 CROSS ASSEMBLER
                                                11:26:29 27-MA: /?
                                                                        PAGE 5
201 0930 85
202 0931 6F
                          VON
                                L.A
203
     0932 7E
                          VON
                                 A.M
204
     0933 23
                           INX
                                 H
205
     0934 66
                          VON
                                 H.M
                          V 011
206
     0935 6F
                                 L.A
207
     0936 C9
                          RET
208
                 *MEMORY DETERMINES WHERE IN THE STACK A PARTICULAR
209
                 REGISTER IS STORED
                                READA
                                           STEP BACK 11 LOCATIONS THRU
210
     0937 3A803D MEMORY: LDA
                                           ; STACK. H AND L POINT
; TO B REGISTER
211
     093A 210900
                           LXI H.9
212
     093D 2F
                           CMA
     093E 3C
                                           HOW STEP FORWARD NUMBER
213
                           INR
                                           ; OF LOCATIONS CORRESPONDING
214
     093F 85
                          HDD
                                L
                                           ; TO REGISTER NUMBER.
215
     0940 6F
                          V 011
                                 L.A
                          DAD
216
     0941 39
                                 SP
217
     0942 C9
                          RET
218
                 ; TWO READS IN TWO DIGITS FROM THE KEYBOARD AND FORMS
                 : THESE INTO AN 8-BIT BYTE
219
                                           STORE ADDRESS OF LOCATION
220
     0943 D5
                          PUSH D
                 TWO:
221
     0944 1602
                          I Vri
                                           ; TO BE MODIFIED
                                D.2
222
     0946 0E05
                          I VM
                                C.5
                                           ; READ IN
223
     0948 21843D
                          LXI
                                H. READD
                                           : TWO DIGITS
224
     094B CD0A09
                          CALL REPEAT
225
     094E E1
                          POP
                                 H
     094F 01843D
                                           ; CONCATENATE
226
                                B. READD
                          LXI
227
     0952 CD5B09
                                           : AND
                          CALL ENT
228 0955 72
                          V 011
                                                STORE
                                 M. D
                                           ;
229
                                           ;D AND E ARE AGAIN THE
     0056 EB
                          KCHG
230
     0957 C9
                                           : ADDRESS POINTER
                          RET
231
                 CONCAT TAKES FOUR 4-BIT NUMBERS (STORED IN THE LOW ORDER
                 : FOUR BITS OF FOUR MEMORY LOCATIONS) AND CONCATENATES THEM
232
                 ; INTO A 16-BIT NUMBER
233
     0958 01803D CONCAT: 1.XI B.READA
234
                                          ;LOAD ACCUMULATOR WITH
235
     095B 0A
                 ENT:
                          LDAX B
                                           ; HIGH ORDER HEX DIGIT
236
     0950 07
                           RLC
                                           ; OF READA TABLE
     095D 07
237
                          RLC
238
     095E 07
                          RLC
239
     095F 07
                          RLC
240
     0960 57
                          110 V
                                D.A
2.41
     0961 03
                          INX
                                 B
                                           CONCATENATE TWO HIGHEST ORDER
242
     0962 0A
                          LDAX B
                                           ; DIGITS BY OR-ING A WITH D.
243
     0963 B2
                          ORA
                                 D
244
     0954 57
                          V 011
                                D.A
                                           STORE RESULT IN D.
2.45
     0965 03
                           (NX
                                 B
                                           SAME THING WITH NEXT
     0966 OA
                          LDAX B
                                           ; TWO DIGITS AND
246
247
     0367 07
                          RLC
                                           : STORE IN E REGISTER.
     0968 07
                          RLC
248
249
    0969 07
                          RLC
25C
   096A 07
                          RLC
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

```
INTEL 6 & CROSS ASSEMBLER
                                              11:26:36
                                                         27-MA. 77
                                                                     PAGE 6
                         VOM
                               E,A
251 096B 5F
252
    0960 03
                          [NX
                               B
253 096D 0A
                         LPAX B
                               E
254 996E B3
                         ORA
235 096F 3F
                         V 011
                               E,A
256
    0970 C9
                         RET
                 FREAD READS IN THE DATA LINES OF THE KEYBOARD AND STORES
257
258
                 ; THIS SEQUENCE IN REGISTER A.
                                         ; WAIT FOR KEYBOARD INTERRUPT
259
    0971 76
                          HLT
                 READ:
                                KB1N2
260
    0972 DBE9
                           IN
261
     0974 C9
                          RET
                 :ZERO WRITES ZEROES ON THE DISPLAYS
262
263
    0975 3E04
                ZERO:
                         1 V11
                               A. 4
                                         OUTPUT ZEROES TO
    0977 CD8109 LCOP:
                          CALL OUTPT
                                         ; FOUR LOWEST ORDER DISPLAYS
264
265 097A 3C
                          INR A
                          CPI
266
     097B FE08
                                8
                          JNZ
                               LOOP
267
     097D C27709
268
    0980 C9
                          RET
                 OUTPT:
                          PUSH PSW
                                         SAVE THE ACCUMULATOR
269
    0981 F5
270
    0982 2F
                          CMA
                                         OUTPT THE NUMBER TO THE
271
                          OUT
    0983 D3E8
                               LEDCM
                                         ; LED AND COMPARATOR OUTPUT PORT
272
    0985 3E47
                          MVI A. #H47
                                         ; TOGGLE
273
   0987 2F
                          CMA
                                         ; THE
274 0988 D3EA
                          OUT PRSET
                                         : 74154
275
    098A 3EC7
                          MVI A. #HC7
                                             ENABLE
                                         ;
276
                          CMA
    098C 2F
                                              PIN
277
     098D D3EA
                          OUT
                               PRSET
278 098F F1
                          POP PSW
                                         RESTORE THE ACCUMULATOR
279 0990 09
                          RET
280 0991 3EB8
                 ERROR:
                          PIVI A. #HB8
                                          OUTPUT ERROR MESSAGE
                          CALL OUTPT
281
    0993 CD8109
    0996 3E07
                          MVI A.7
282
     0998 CD8109
                          CALL OUTPT
283
284
    099B 3E06
                          MVI A.6
285 099D CD3109
                          CALL OUTPT
286
    09A0 3EB5
                          MVI A. #HB5
287
    0982 CD8109
                          CALL OUTPT
    09A5 3EE4
                          MVI A, #HB4
288
289
     09A7 CD8109
                          CALL OUTPT
290
     09AA C9
                          RET
291
     09AB 3A8A3D KBRD2:
                          LDA OSTAT
                                         HUMBRIC KEYBOARD SERVICE RETURN
    09AE FE08
                          CPI 8
292
293
    09B0 C29109
                          JNZ ERROR
294 09B3 C9
                START:
                          RET
295 99B4
                          END
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

```
INTEL BE _ CROSS ASSEMBLER
                                               14:46:02
                                                          09-MA' ?
                                                                       PAGE 1
    0040
                           ORG
                                 *HA00
    00A0
                FIRST
                        EQU
                                 #H3FE0
                                 #H3D87
 3
    0R90
                FSTAT
                        EQU
                          EQU
                                 #H975
    00A0
                ZERO
 5
    OBAO
                SPLIT
                           EQU
                                 *H8D1
                          EQU
                                 #H943
    00H0
                THO
 7
    00A00
                OSTAT
                          EQU
                                 #H3D8A
 8
    00A0
                TEMP
                           EQU
                                 OSTAT+1
 9
    00A0
                COUNT
                           EQU
                                 TEMP+1
10
    0 H00
                CHSHT
                          EQU
                                 COUNT+1
                                         33080 HAS MULTIPLY CONSTANT READ FROM
11
    0 A00
                MPNTR
                           EQU
                                 CNSNT+1 ;3D8E THRU 3D8F HAS MULTIPLY ROUTINE
                                 MPNTR+2 33D90 HAS STARTING LOCATION OF 1ST MU
12
    0000
                MULCX
                           EQU
                CTAB IS THE MULTIPLY CONSTANT TABLE
13
14 eR00 08
                CTAB:
                          DB
                                 8
15
    0A01 01
                        DB
    0A02 3C
                        DB
16
                                 #H3C
17
    0H03 01
                        DB
                                 1
18
    01104 04
                        DB
                                 4
19
    0A05 01
                        DB
                                 1
20
                        DB
    01106 06
                                 6
21
    0A07 01
                        DB
                                 1
22
    0A08 3C
                        D13
                                 #H3C
23 0109 04
                        DB
                                 4
24 0R0A 1A
                        D13
                                 #HIA
25 OAOR AO
                        DB
                                 #HA0
26 0A0C 00
                        DB
27
    000 DOR
                        D13
                                 #H0
28
    000E 0D
                        DB
                                 #HD
29
    0A0F 00
                        DB
                                 0
30
   0A10 09
                        D13
                                 9
31 0A11 0A
                        DB
                                 #HA
   0A12 D0
                        D13
                                 #HDØ
32
                        DB
                                 #HD
33
  0A13 0D
                SETML CREATES FAST MEMORY ROUTINES IN RAM
34
                                         GET READY FOR SETML MINOR FUNCTION
35
    0A14 3E10
                SETML:
                          MVI
                                 H. #H10
    0A16 328A3D
                           STA
                                 OSTAT
   9A19 3E00
                                         CERTAIN SUBROUTINES USED BY THIS
                        MYI
37
                                 A.0
38 0A1B 32873D
                        STA
                                 FSTAT
                                         SECTION REQUIRE A VALUE FOR FSTAT
39 0A1E CD7509
                          CALL ZERO
                                          ¿ZERO OUT DISPLAY
40 0A21 21903D
                          LXI
                                 H. MULCX
41 0A24 228E3D
                          SHLD MPHTR
                                          ; INITIALIZE MULTIPLY ROUTINE POINTER
    0A27 11000A
                          LXI
                                 D. CTAB
                                          : INITIALIZE TABLE POINTER
42
                                          ; INITIALIZE LOOP COUNT
43
   0A2A 3E14
                          MVI
                                 A. 20
44 0A2C 328C3D INSPT:
                          STA
                                 COUNT
   eazr 1a
                          LDAX D
                                          ; LOAD THE ACCUMULATOR WITH TABLE ENTR
   0830 328D3D
                          STA
                                 CHSHT
                                          STORE TABLE ENTRY
                                SPLIT
47 0433 CDD100
                          CALL
                                          DISPLAY TABLE ENTRY
                        LDA
                                 FIRST
48 8436 31E03F
                                         CHECK FOR FIRST TIME THRU
49 8439 17
                        RAL
                                         :MSB DECIDES
58 7435 084388
                        JC
                                 HALT
                                         : IF C=1. FIRST TIME THRU
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

```
INTEL BE CROSS ASSEMBLER
                                               14:46:24 09-MAY . ?
                                                                      PAGE 2
                                         OTHERWISE, SET UP MULTIPLIES
51
    easd cd64ea
                        CALL
                                 OK
                                 NEXT
                        JI1P
                                         ; INTERACTION FROM KEYBOARD
52
    0A40 C3440A
                HALT:
                        HL T
                                         ; WAIT FOR A KEYBOARD INTERRUPT
    0A43 76
    0:144 13
                NEXT:
                          INX
                                          PREPARE FOR NEXT TABLE ENTRY
                                 COUNT
55
    0A45 3A8C3D
                          LDA
    0A48 3D
                                          DECREMENT LOOP COUNTER
                          DCR
                                 A
56
                          JNZ
                                 INSPT
    0A49 C22C0A
                                          PREPARE FOR NEXT MAJOR FUNCTION
    0A4C 3EC0
                          MVI
                                 A. #HC0
59
    0A4E 328A3D
                          STA
                                 OSTAT
    0A51 C9
                          RET
                CHANG READS TWO HEX NUMBERS FROM THE NUMERIC KEYBOARD AND USE
61
                NUMBER RATHER THAN THE ONE IN CTAB TO GENERATE A MULTIPLY ROU
62
    0A52 118B3D CHANG:
                                 D. TEMP
63
                          LXI
    ∂A55 CD4309
                          CALL TWO
                                          READ KEYBOARD
                          MVI
                                 A. #H10
                                          PREPARE FOR NEXT SETML MINOR FUNCTIO
    0ASB 3E10
65
66
    0A5A 328A3D
                          STA
                                 OSTAT
                                          :THIS THE NEW MULTIPLY CONSTANT
67
    0A5D 3A8B3D
                          LDA
                                TEMP
    0A60 CD700A
                          CALL MULT
                                          SET UP THE MULTIPLY ROUTINE
68
    0A63 C9
                          RET
                OK TAKES THE NUMBER FROM THE TABLE AND GENERATES THE
70
71
                CORRESPONDING MULTIPLY ROUTINE
    0A64 3E10
                                A. #H10
                          IVI
                                         PREPARE FOR NEXT SETML MINOR FUNCTIO
                          STA
                                OSTAT
73
    0A66 328A3D
    0A69 3A8D3D
                          LDA
                                CHSHT
                                          :LOAD THE TABLE ENTRY INTO THE ACCUMU
    OA6C CD700A
                          CALL MULT
                                          SET UP MULTIPLY ROUTINE
75
76
    0A6F C9
                          RET
77
                MULT WRITES A ROUTINE IN RAM TO MULTIPLY VARIABLE BY SOME CON
                THE CONSTANT IS IN THE ACCUMULATOR. THE ROUTINE CORRESPONDING
78
79
                :TO THE CONSTANT 5 IS GIVEN BELOW:
80
81
                                            LXI H.0
                                            DAD D
82
                                            DAD H
83
                                            DAD H
85
                                            DAD D
86
                                            RET
87
    0A70 ZABE3D MULT:
                          LHLD MPNTR
                                          GET THE STARTING LOCATION FOR THE MU
                                          :WRITE AN 'LXI H.O' INTO MEMORY
   0973 3621
                          MVI
                                M, #H21
88
89
   0A75 23
                          INX
                                H
   0A76 3600
                          MVI
                                M. 0
91
    0A78 23
                          INX
                                H
92
    0A79 3600
                          MVI
                                M. 0
93
    0h7B 23
                          IHX
                                 H
94
   0A7C 0508
                          MVI
                                B.8
                                          B IS THE LOOP COUNTER
   0A7E 07
95
                LPCY:
                          RLC
                                          ; IGNORE LEADING ZEROES
    0A7F DA890A
                          JC
                                 OWT
                                          JUMP OUT WHEN FIRST ONE IS FOUND
97
    0A82 05
                          DCR
                                 В
98
    0A83 C27E0A
                          JNZ
                                LPCY
99
    0A66 C3990A
                          JMP
                                ZCHT
                                          HO ONES, NUMBER IS ZERO
    0A39 0F
                OWT:
                          RRC
```

Figure D-3 Prom No. 3 Monitor Program (Continued)

II	NTEL 8000 CI	ROSS ASSET	MBLER		14:46:31 09-MAY 7 PAGE	3
101	0A8A 07	TOP:	RLC		CHECK FOR ZERO OR ONE	
102	018B D2910	A	JNC	G02	; IF ZERO, JUST WRITE A 'DAD H'	
103	0A8E 3619	GO1:	MVI	M, #H19	OTHERWISE WRITE A 'DAD D'	
104	0A90 23		INX	H	; AND THEN A 'DAD H'	
105	0A91 3629	G02:	MVI	M. #H29		
106	0A93 23		INX	H		
107	0A94 05		DCR	В	CONTINUE FOR ALL REMAINING BITS	
108	0A95 C28A01	A	JNZ	TOP		
109	0A98 2E		CX	H	WRITE OVER LAST 'DAD H'	
110	0A99 36C9	ZCNT:	MVI	M, #HC9	: WITH A 'RET'	
111	0A9B ZA8E31)	LHLD	MPN'TR	: ADD 20 TO THE OLD STARTING	
112	0A9E 011400	9	LXI	B.#H14	: LOCATION TO GET THE NEW	
113	0AA1 09		DAD	В	STARTING LOCATION	
114	0AA2 228E31)	SHLD	MPHTR		
115	onas co		RET			
116	9999					

INTEL 8080 CROSS ASSEMBLER SYMBOL TABLE

A = 0	9007	B =	0000	G01	OASE	G02	0891
C = 6	1001	D =	0002	E =	0003	CTAB	0000
CHANG 6	9A52	H =	0004	HALT	0943	L =	0005
M = e	9006	FSTAT=	3D87	TEMP =	3D8B	FIRST=	3FE0
COUNT= 3	BDBC	CHSHT=	3D8D	SPLIT=	08D1	OSTAT-	3DBA
MULCX= 3	3790	ZERO -	0975	MPNTR=	3D8E	SETML	0A14
SP = 0	9006	INSPT	0A2C	NEXT	0844	OK	0864
MULT e	07A	LPCY	0A7E	PSW =	0006	TWO -	0943
OWT 6	9A89	TOP	0A8A	ZCNT	0A99		

ERRORS DETECTED: 0

Figure D-3 Prom No. 3 Monitor Program (Concluded)

```
14:47:34
                                                             09-MAY-77
                                                                          PAGE 1
  INTEL 8080 CROSS ASSEMBLER
                 хжжжжжжж
 2
 3
                 ;
 4
                        SYSTEM EQUATES
 5
 6
 7
                 *******
 8
 9
10
                 ;
11
                                  *H3D00
                         EQU
    0000
                 CAMAX
12
13
    0000
                 CAMBX
                         EQU
                                  #H3D01
14
    0000
                 CAMAY
                         EQU
                                  *H3D02
15
    0000
                 CAMBY
                         EQU
                                  #H3D03
    0000
                 CCMAX
                         EQU
                                  #H3D46
16
    0000
                 CCMAY
                                  *H3D44
17
                         EQU
                                  #H3D40
18
    0000
                 CPOAX
                         EQU
    0000
19
                 CPOBX
                         EQU
                                  #H3D41
20
    0000
                 CPOAY
                         EQU
                                  #H3D42
21
    0000
                 CPOBY
                         EQU
                                  *H3D43
22
    0000
                 CPLAX
                                  #H3D0C
                         EQU
23
    0000
                 CPLBX
                         EQU
                                  #H3D0D
    0000
                 CPLAY
                         EQU
                                  #H3D0E
25
    0000
                 CPLBY
                         EQU
                                  #H3D0F
26
    0000
                 DETX
                         EQU
                                  &H3D18
27
    0000
                 DETY
                         EQU
                                  #H3D11
28
    0000
                 DDOTX
                         EQU
                                  #H3D12
29
    0000
                 DDOTY
                                  #H3D14
                         EQU
30
    0000
                 DOTIX
                         EQU
                                  #H3D16
31
    0000
                 DOTIY
                         EQU
                                  #H3D18
    0000
                 IPRJX
                         EQU
                                  #H3D1A
32
    0000
                 IPRJY
                                  #H3D1C
33
                         EQU
34
    0000
                 IPOSX
                         EQU
                                  #H3D1E
35
    0000
                 IPOSY
                         EQU
                                  *H3D29
36
    0000
                 MULCX
                         EQU
                                  #H3D90
37
    0000
                 MULDX
                         EQU
                                  MULCX+20
38
    0000
                 MXLVB
                         EQU
                                  MULDX+20
    0000
                 MXLVK
                         EQU
                                  MXL VB+20
33
40
    0000
                 MULAX
                         EQU
                                  MXLVK+20
41
    0000
                 MULBX
                         EQU
                                  MULAX+20
    0000
                 MULCY
                         EQU
                                  MULBX+20
42
    0000
                 MULDY
                                  MULCY+20
43
                         EQU
44
    0000
                 MATAB
                         EQU
                                  MULDY+20
45
    0000
                 MYLVK
                         EQU
                                  MYLVB+20
46
    0000
                 MULAY
                         EQU
                                  MYLVK+20
47
    0000
                 MULBY
                         EQU
                                  MULAY+20
    0000
48
                 MICH
                         EQU
                                  MULBY+20
49
    0000
                 MICHY
                         EQU
                                  MICH+20
50
    0000
                 CMIY
                         EQU
                                  MICHY+20
```

Figure D-4 Prom No. 4 Pitch Control Software

I	NTEL 80	80 CRC	oss i	ASSEI	MBLER					14:47	156	09-1	1AY-7	7	PAGE	2
Si	0000		CMI	X	EUU		CMIY+20									
52	0000		NCP		EQU		*H3D22									
53	0000		NCP		EQU		*H3D24									
5-4	0000		PR.I	AX	EQU		#H3D26									
55	0000		PRJ	BX	EQU		#H3D27									
56	0000		PR	AY	EQU		*H3D28									
57	0000		PRJI	BY	EQU		*H3D29									
58	0000		PRL	XA	EQU		#HJD2A									
59	0000		PRLI	ВX	EQU		#H3D2B									
60	0000		PRI	PΥ	EQU		#H3D2C									
61	0000		PRL	BY	EQU		#H3D2D									
62	0000		PVL	XE	EQU		#H3D2E									
63	0000		PALL	3X	EQU		#H3D2F									
64	0000		PVLI	Y	EQU		*H3D30									
65	0000		PVLI	3 Y	EQU		#H3D31									
66	0000		RIN		EQU		#H3D34									
67	0000		TIT	Γ	EQU		#H3D38									
68	0000		X		EQU		#H3D3A									
69	0000		XY		EGU		#H3D3C									
70	0000		XFL		EGU		#H3D3E									
71	0000		YFLE		EGU		#H3D3F									
72	0000		CDE	. X	EQU		#H3D76									
73	0000		CIX		EOU		#H3D76									
74	0000		USDA		EQU		#H00EC									
75	0000		PRT		EQU		#H0000									
76	0000		PRT		EQU		#H0001									
77	0000		PRT		EGU		#H0002									
78	0000		PRT		EQU		#H0003	1								
79	0000		PRT		EQU		#H00E5									
80	6.000		PRT		EQU		\$H0000									
81	0000		PRTI		EQU		#H0001									
82	0000		PRTI		EGU		#H0002									
83	0000		PRT	03	EQU		#H00E6									
84	0C00				ORG		#HC00									
85			;	~ ***												
85			;	SYS	LEI CO	MPE	HOITAZH	HET	WOR	K						
87			;													
88	0000 0		,		* 1/*		** ****									
89	0000 2		PITO	H:	LXI		H. XFLAG									
90	0003 7				MOA		A.M									
91	0004 1				RAR		DITCH									
92 93	0005 D				JHC		PITCH									
	OCOB E				LHLD		PRLAX									
94	0C0C 3				XCHG		nn inv									
35					LDA		PRJBX									
96	OCOF E				ANI		#H10									
97	0011 F				CPI		#H10									
98	0013 C				JNZ		RA									
99	0016 3 0019 C				LDA		PRJBX									
100	0019 (7.0			AD I		*HE0									

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

```
14:48:03 09-MAY-77
                                                                PAGE 3
  INTEL 8080 CROSS ASSEMBLER
                      STA
                              PRJBX
101 0C1B 32273D
                      LHLD
                              PRJAX
102 OC1E 2A263D RA:
                              RIHUS
103
    OC21 CDD20D
                      CAL.L
104 0024 19
                      DIAD
                              D
105 0C25 EB
                      XCHG
               ********SPECIAL LEAD NETWORK
106
107
               108
                LHLD
109 0026 2A523D
                              #H3D52
110 0C29 EB
                  LHLD
                              #H3FEA
111 0C2A ZAEA3F
112 0C2D CDDA0D
                    CALL
                              SHIFT
                              SUNIM
113 0C30 CDD20D
                     CALL
114 0033 19
                      DAD
                              D
                     CALL
115 0C34 CDDA0D
                              SHIFT
116 0C37 CDDA0D
                              SHIFT
117
                     CALL
                              MULCX
118 0C3A 22123D
                              DDOTX
                     SHLD
119 0C3D EB
                      XCHG
120 0CBE 7A
                     MOV
                              A.D
                     RLC
121 0C3F 07
                    JC
LXI
122
    0C40 DA530C
                              FEE
                              H, #HFFFE
123 0C43 21FEFF
                     DHD
124 0046 19
                              D
                     JC
125 0C47 DA600C
                              DET
                    LXI
125 0C4A 210000
                 SHL
127
   0C4D 22123D
                              DDOTX
                    JMP
LKI
DAD
128 0050 C3600C
                              DET
129 0C53 210200 FEE
                              H. 2
130 0056 19
                              D
131 0057 D2600C
                     JHC
                              DET
132 0C5A 210000
                     LXI
                              H. 0
133 0C5D 22123D
                     SHLD
                              DDOTX
134 0CE0 3A103D DET: LDA
                              DETX
                CPI
135 0C63 FE04
                              #H04
136
    0065 F2770C
                      JP
                              PDL
                     CPI
    0068 FEFC
                              +HFC
137
138 006A FA720C
                              MDL
                     J11
139 0C6D 3E00
                     MYI
                              A. #H00
140 0C6F C3790C
                     JIMP
                              ING
   0072 C604 MDL:
                     AD I
                              #H04
141
142
   0C74 C3790C
                       JI1P
                              ING
    0077 C6FC
143
               PDL:
                       Al) I
                              #HFC
144 0C79 5F
                              E.A
                      MOV
               ING:
145 0C7A FE00
                      CP I
                              #H00
    0070 3E00
                      MYI
                              A. #H00
1.15
    007E F2820C
                       JP
147
                              ZAP
148 0081 2F
                       CMA
                       VOM
149
    0062 57
               ZAP:
                              D.A
150 0C83 CDA43D
                       CALL
                              MULDX
```

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

I	NTEL	8080 CR	OSS ASSE	MBLER	
151	0086	88		XCHG	
152	0087			LHLD	DDOTX
153	0C8A			DAD	D
154	0C8B			XCHG	•
155	0080			MOV	A.D
156	0C8D			RLC	
157		DA9B0C		JC	FIVE
158	0091			LXI	H. #HFFCC
159	0094			DAD	D
150		DAA90C		JC	PLMT
161		C3A20C		JIMP	TWO
162	0C9B		FIVE:	LXI	H. #H0034
163				DAD	D
164	0C9F	D2B20C		JHC	MLMT
165	0CA2	EB	TWO:	ACHG	
166	OCA3	22003D		SHLD	CAMAX
167		СЗВВОС		J11P	EXIT
168		213400	PLMT:	LXI	H. #H34
169		22003D		SHLD	CAMAX
170		C3BB0C		JIMP	EXIT
171	9CB2	ZICCFF	MI.MT:	LXI	H. *HFFCC
172		22003D		HLD	CAMAX
173	0CB8	C3BB0C		J11P	EXIT
174			,		
175			,	CAMERA	SERVO
176			,		
177	OCBB	3A413D	EXIT:	LDA	CPOBX
178	OCBE	E61F		AHI	*H1F
179	0000	32413D		STA	CPOBX
180	0CC3	E610		AHI	*H10
181	0005	FE10		CPI	*H10
182	0 CC7	CZDZ0C		JHZ	WP
183	OCCA	3A413D		I.DA	CPOBX
134	OCCD	C6E0		AD I	#HE0
105	OCCF.	32413D		STA	CFUBX
186	OCDZ	3A403D	WP:	LDA	CPOAX
187	OCD5	2F		CIMA	
1881	ecp6			MOA	L.A
189		38413D		LDA	CPOBX
190	OCDA			CITA	
:31	OCDB			MOV	H.A
192	OCDC.			IHX	H
193		22223D		SHILD	NCPOX
191	OCEO			XCHG	
195	OCE 1			LHLD	CPLAX
196	OCE4			DAD	D
197		22F43F		SHLD	#H3FF4
198	OCE8			DAD	H
199	OCE 9	110000		DAD	H
200	OCEA	3B		XCHG	

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

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1	INTEL 8080 CR05	SS ASSEMBLEI	R	14:48:18	09-MAY-7?	PAGE	5
201	0CEB 28403D	LHLI	CPOAX				
202	OCEE 220C3D	SHLI					
203		MOV					
204		RLC					
205	OCF3 D2000D	JHC	OGDR				
206	0CF6 211101	LXI	H. #H111				
207	0CF9 19	DAD	D				
208	OCFA D2100D	JHC	DECR				
209		JMP	MUL				
210	ODOO ZIEFFE		H. #HFEEF				
211	0D03 19	DAD	D				
212	OD 1 DROROD	JC	LMAR				
213		JMP	MUL				
214	0D0A 210040 I		H. #H4000				
215		JIMP	FLTR				
216	0D10 2100C0 I	DECR: LXI	H. #HC000				
217		JI1P					
218	OD16 CDB83D N	MUL: CALI	MXLVB				
219	edie CDDaed F	FLTR: CALI	SHIFT				
220	ODIC CDDAOD	CALI	SHIFT				
221	ODIF EB	жсно					
222	0D20 2A843D	LHLI	#H3D84				
223	0D23 19	DAD	D				
224	0D24 EB	XCHO	3				
225	0D25 22843D	SHLI	*H3D84				
226	0D28 2A863D	LHLI	*H3D86				
227	ODZB CDDAOD	CALI	SHIFT				
228	0D2E 19	DAD	D				
229	0D2F 22863D	SHLI	#H3D86				
230	0D32 22163D	SHLI	DOTIX				
231	0D35 2A903D	LHLI	CAMAX				
232	ODBE EE	MCHO	i				
233	OD39 ZA3A3D	Lhi'I	X				
234	0D3C 19	DAD	D				
235	ODSD EB	XCHO		165	165		
236	9D3E 7A	MOA	A.D				
237	0D3F 07	RLC					
238	0D40 DA4E0D	JC	PAP				
439	0D43 2100F2	LXI	H. *HF200				
240		DAD	D				
241	0D47 DA590D	JC	OHE				
242	OD4A EB	XCHO	ì				
243	0D4B C36E0D	JI1P	THRE				
244		PAP: LXI	H. #H0B10				
245	ØD51 19	DAD	D				
246	0052 22650D	JHC	FOUR				
247	0D55 EB	ЖСНО					
248	0D56 C36E0D	JIIP	THRE				
249	0D59 210000 C	DNE: LXI	H,#H0				
250	0D5C 22003D	SHLI	CAMAX				

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

297 ODC3 EB XCHG 298 0DC4 2A163D PC: LHLD DOTIX 299 ØDC7 19 DAD D 300 0DCB 22163D SHLD MITTOD

CALL

XCHG

LHLD

DAD

SHLD

XCHG

MOV

RLC JC

LXI

DAD JC

TIMP

LXI

DAD

JHC

JIMP

LXI

XCHG

JIMP

LXI

277 0D97 EB

279 0D9B 19

282 0DA0 7A 283 0DA1 07

286 0DA8 19

290 ODB2 19

294 ODBC EB

278 0198 2A783D

280 0D9C 22783D

284 ODAZ DARFOD

285 0DA5 2100C0

287 ODA9 DAB90D

288 UDAC C3C40D

291 0DB3 D2C00D

292 00B6 C3C40D

295 0DBD C3C40D

289 ODAF 210040 PA:

293 0DB9 210040 PB:

296 0DC0 2100C0 PD:

281 0D9F EB

CMIX

CIX

CIX

H.D

PA

PR

PC

D

PD

PC

PC

H. #HC000

H. #H4000

H. #H4000

H. #HC000

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

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301	ODCB	2A763D	OUT:	LHLD	CDELX
302	ence	EB		XCHG	
303	ODCF	C3E60D		JIMP	◆HDE6
304	0DD2	7C	MINUS:	MOV	A.H
305	ODD3	2F		CMA	
306	ODD4	67		MOV	H.A
307	0DDS	7D		MOA	A.L.
308	0DD6	2F		CMA	
309	ODD?	6F		MOA	L.A
310	opps	23		INX	H
311	opp9	C9		RET	
312	ODDA	7C	SHIFT:	MOV	A.H
313	ODDB	07		RLC	
314	endc	7C		MOV	A.H
315	ODDD	1 F		RAR	
316	ODDE	67		MOV	H.A
317	ODDF	7D		MOV	A.L
318	ODEO	1 F		RAR	
319	ODE 1	6F		MOV	L.A
320	ODE2	C9		RET	
321	oDE3			EHD	

INTEL 8080 CROSS ASSEMBLER

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

```
INTEL 8080 CROSS ASSEMBLER
                                                 14:49:39
                                                             09-MAY-77
                                                                          PAGE 1
                 ; www.www.www
 2
 3
                        SYSTEM EQUATES
 5
8
                 9
10
11
12
    0000
                 CAMAX
                         EQU
                                  *H3D00
    6000
13
                 EOX
                         EQU
                                  #НЗFEA
14
    0000
                 CAMBX
                         EQU
                                  #H3D01
15
    0000
                 CAMAY
                         EQU
                                  *H3D02
    0000
                 CAMBY
                         EQU
                                  *H3D03
16
17
    0000
                 CCMAX
                         EQU
                                  *H3D46
18
    0000
                 CCMAY
                         EQU
                                  #H3D44
19
    0000
                 CPOAX
                         EQU
                                  #H3D40
    0000
                 CPOBX
20
                         EQU
                                  #H3D41
21
    0000
                 CPOAY
                         EQU
                                  #H3D42
22
    0000
                 CPOBY
                         EQU
                                  #H3D43
23
    0000
                 CPLAX
                         EQU
                                  #H3D0C
24
    0000
                 CPLBX
                         EQU
                                  #H3D0D
25
    0000
                 CPLAY
                         EQU
                                  #H3D0E
26
    0000
                 CPLBY
                         EQU
                                  *H3D0F
27
    0000
                 DETX
                         EQU
                                  *H3D10
28
    0000
                 DETY
                         EQU
                                  #H3D11
29
    0000
                 DDOTX
                                  *H3D12
                         EQU
    0000
30
                 DDOTY
                         EQU
                                  #H3D14
31
    0000
                 DOTIX
                         EQU
                                  #H3D16
    0000
32
                 DOTIY
                                  *H3D18
                         EQU
33
    0000
                 IPRJX
                                  *H3D1A
                         EQU
34
    0000
                 IPRJY
                         EQU
                                  #H3D1C
                 IPOSX
35
    0000
                         EQU
                                  #H3D1E
                 IPOSY
                                  #H3D20
36
    0000
                         EQU
37
    0000
                 MULCX
                         EQU
                                  #H3D90
38
    0000
                 MULDX
                         EQU
                                  MULCX+20
39
    0000
                 MXLVB
                         EQU
                                  MULDX+20
40
    0000
                 MXLVK
                         EQU
                                  MXLVB+20
41
    0000
                 MULAX
                                  MXLVK+20
                         EQU
42
    0000
                 MULBX
                         EQU
                                  MULAX+20
43
    0000
                 MULCY
                         EQU
                                  MULBX+20
44
    0000
                 MULDY
                         EQU
                                  MULCY+20
45
    0000
                 MYLVB
                         EQU
                                  MULDY+20
    0000
                                  MYL VB+20
46
                 MYLVK
                         EQU
47
    0000
                 MULAY
                                  MYLVK+20
                         E 1U
    0000
48
                 MULBY
                         EQU
                                  MULAY+20
49
    0000
                 MICH
                         EQU
                                  MULBY+20
50
    0000
                 MICHY
                         EQU
                                  MICH+20
```

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

\$1 0000	I	NTEL 8080 CR	OSS ASSE	MBLER		14:50:02	09-MAY-77	PAGE	2
S2 0000	51	9099	CMIY	ЕОП	MICHY+20				
S3									
S4									
SS 0000									
Section									
ST 0000									
S8									
S9									
60 0000 PRJAY BOU H3D2d 61 0000 PRLAX BOU H3D2B 62 0000 PRLAX BOU H3D2B 63 0000 PRLAX BOU H3D2B 64 0000 PRLAY BOU H3D2C 65 0000 PRLAY BOU H3D2C 66 0000 PVLAX BOU H3D2C 67 0000 PVLAX BOU H3D2C 68 0000 PVLAY BOU H3D3C 69 0000 PVLAY BOU H3D30 69 0000 PVLAY BOU H3D31 70 0000 RINT BOU H3D33 71 0000 X BOU H3D3A 71 0000 X BOU H3D3A 72 0000 X BOU H3D3A 73 0000 XY BOU H3D3C 74 0000 XFLAG BOU H3D3F 75 0000 YFLAG BOU H3D3F 76 0000 PRTAL BOU H3D3F 77 0000 ICHAN BOU H3D3F 78 0000 PRTAL BOU H3D3F 79 0000 PRTAL BOU H3D60 80 0000 PRTAL BOU H000C 81 0000 PRTAL BOU H000C 82 0000 PRTAL BOU H000C 83 0000 PRTAL BOU H000C 84 0000 PRTAL BOU H000C 85 0000 PRTAL BOU H000C 86 0000 PRTAL BOU H000C 87 0000 PRTAL BOU H000C 88 0000 PRTAL BOU H000C 89 000C PRTDL BOU H000C 80 000C PRTDL BOU H000C 80 000C PRTDL BOU H000C 80 000C PRTDL BOU H000C 81 000C PRTDL BOU H000C 82 000C PRTDL BOU H000C 83 000C PRTDL BOU H000C 84 000C PRTDL BOU H000C 85 000C PRTDL BOU H000C 86 000C PRTDL BOU H000C 87 000C PRTDL BOU H000C 88 00CC PRTDL BOU H000C 89 00EC 7A MOV A.D 90 00EC 07 RLC 91 00EB DAFFOD JC ALM 97 00EB 19 DAD D 94 00EF DAFFOD JC ALM 97 00EB 19 DAD D 98 00EF D20S0E JMC CDE									
61 0000 PRLBY EQU #H3D29 62 0000 PRLBY EQU #H3D2B 63 0000 PRLBY EQU #H3D2C 65 0000 PRLBY EQU #H3D2C 66 0000 PRLBY EQU #H3D2C 66 0000 PVLBX EQU #H3D2C 67 0000 PVLBX EQU #H3D2F 68 0000 PVLBX EQU #H3D3C 69 0000 PVLBY EQU #H3D30 69 0000 PVLBY EQU #H3D31 70 0000 RINT EQU #H3D34 71 0000 TINT EQU #H3D36 72 0000 X EQU #H3D3C 73 0000 XY EQU #H3D3C 74 0000 XFLAG EQU #H3D3F 75 0000 YFLAG EQU #H3D3F 76 0000 DELTA EQU #H3D3B 77 0000 ICHAN EQU #H3D5B 78 0000 PRTB1 EQU #H0060 80 0000 PRTB1 EQU #H0001 81 0000 PRTB1 EQU #H0001 81 0000 PRTB2 EQU #H0002 82 0000 PRTB2 EQU #H0003 83 0000 PRTB2 EQU #H0006 84 0000 PRTB2 EQU #H0006 85 0000 PRTB2 EQU #H0006 86 0000 PRTB1 E U #H0000 87 0000 PRTB2 EQU #H0006 88 0000 PRTB2 EQU #H0006 89 0000 PRTB2 EQU #H0006 80 0000 PRTB2 EQU #H0000 80 00									
62 0000 PRLAX EQU									
63 0000 PRLBY EQU					#H3D2A				
64 0000 PRLAY EQU #H3D2C 65 0000 PRLBY EQU #H3D2D 66 0000 PVLAX EQU #H3D2F 67 0000 PVLAX EQU #H3D2F 68 0000 PVLAY EQU #H3D30 69 0000 PVLAY EQU #H3D31 70 0000 RINT EQU #H3D34 71 0000 TINT EQU #H3D30 72 0000 X EQU #H3D3C 73 0000 XY EQU #H3D3C 74 0000 XFLAG EQU #H3D3F 75 0000 YFLAG EQU #H3D3F 76 0000 DELTA EQU #H3D3B 77 0000 ICHAN EQU #H3D58 77 0000 ICHAN EQU #H3D60 78 0000 PRTA1 EQU #H0000 80 0000 PRTA1 EQU #H0000 80 0000 PRTA1 EQU #H0000 80 0000 PRTA2 EQU #H0001 81 0000 PRTA2 EQU #H0001 83 0000 PRTB2 EQU #H0002 84 0000 PRTD3 EQU #H0000 85 0000 PRTD1 EJ #H0000 86 0000 PRTD3 EQU #H0000 87 0000 PRTD3 EQU #H0000 88 0D66 ORG PRTD1 EJ #H0001 89 0D66 ORG PRTD3 EQU #H0000 80 0D67 OR RICC 91 0D68 DAFSOD JC MIB 92 0D68 DAFSOD JC MIB 94 0D6F DAFFOD JC ALM 97 0DF8 19 DAD D 98 0DF9 D2080E JMP XVK		0000	PRLBX	EQU	#H3D2B				
### ### ##############################		0000	PRLAY		#H3D2C				
### ### ##############################	65	0000	PRLBY	EQU	#H3D2D				
68 0000 PVLAY EQU #H3D30 69 0000 PVLBY EQU #H3D31 70 0000 PVLBY EQU #H3D34 71 0000 TINT EQU #H3D38 72 0000 X EQU #H3D36 73 0000 XY EQU #H3D36 74 0000 XFLAG EQU #H3D3F 75 0000 YFLAG EQU #H3D3F 76 0000 PFLAG EQU #H3D58 77 0000 DELTA EQU #H3D58 78 0000 PFTA1 EQU #H3D60 79 0000 PFTA1 EQU #H0000 80 0000 PFTA1 EQU #H0000 80 0000 PFTA1 EQU #H0000 81 0000 PFTA2 EQU #H0001 82 0000 PFTA2 EQU #H0003 83 0000 PFTB2 EQU #H0003 84 0000 PFTB2 EQU #H0006 85 0000 PFTD1 E J #H0001 86 0000 PFTD2 EQU #H0000 87 0000 PFTD2 EQU #H0006 88 0006 PFTD2 EQU #H0006 89 0006 PFTD2 EQU #H0006 80 0000 PFTD3 EQU #H0006 81 0000 PFTD4 EQU #H0006 82 0000 PFTD5 EQU #H0006 83 0000 PFTD6 EQU #H0006 84 0006 PFTD7 EQU #H0006 85 0000 PFTD7 EQU #H0006 86 0006 PFTD7 EQU #H0006 87 0000 PFTD7 EQU #H0006 88 0006 FFTC2 EQU #H0006 89 0006 TQ TQ RLC 91 0008 DAFFOD JC MIB 90 000F DAFFOD JC MIB 91 000F DAFFOD JC ALM 92 000F DAFFOD JC ALM 93 00FE 19 DAD D 94 00FF DAFFOD JC ALM 95 00FF 23080E JMP XVK		0000	PVLAX	EQU	*H3D2E				
69 0000 PVLBY EQU #H3D31 70 0000 RINT EQU #H3D38 71 0000 TINT EQU #H3D38 72 0000 X EQU #H3D3A 73 0000 XY EQU #H3D3C 74 0000 XY EQU #H3D3C 75 0000 YPLAG EQU #H3D3F 76 0000 DELTA EQU #H3D3F 77 0000 ICHAN EQU #H3D58 77 0000 ICHAN EQU #H3D60 78 0000 PRTA1 EQU #H00C 79 0000 PRTA1 EQU #H00C 80 0000 PRTA1 EQU #H0000 81 0000 PRTA1 EQU #H0000 82 0000 PRTA2 EQU #H0003 83 0000 PRTA2 EQU #H0005 84 0000 PRTD2 EQU #H0006 85 0000 PRTD1 E I #H000 86 0000 PRTD1 E I #H000 87 0000 PRTD1 E I #H000 88 0000 PRTD2 EQU #H0000 89 0000 PRTD3 EQU #H0000 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DE8 2100C0 LXI H.#ICO00 93 0DEE 19 DAD D 94 0DEF DAFFDD JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF9 D205E JMC CDE 99 0DFC C30E0E JMP XVK	67	0000	PVLBX	EQU	#H3D2F				
70 0000 RINT EQU #H3D34 71 0000 TINT EQU #H3D38 72 0000 X EQU #H3D3A 73 0000 XY EQU #H3D3C 74 0000 XFLAG EQU #H3D3F 75 0000 YFLAG EQU #H3D3F 76 0000 YFLAG EQU #H3D3F 77 0000 DELTA EQU #H3D5B 77 0000 ICHAN EQU #H3D5B 78 0000 USDAO EQU #H0000 80 0000 PRTAI EQU #H0001 81 0000 PRTAI EQU #H0001 81 0000 PRTC1 EQU #H0001 82 0000 PRTE2 EQU #H0003 83 0000 PRTE2 EQU #H0003 83 0000 PRTE2 EQU #H0006 85 0000 PRTD1 EU #H0006 86 0000 PRTD1 EU #H0006 87 0000 PRTD1 EU #H0001 88 0000 PRTD2 EQU #H0000 89 0000 PRTD3 EQU #H0000 90 0DE7 7R 90 0DE7 7R 90 0DE7 07 RLC 91 0DE8 DAFF0D JC MIB 91 0DEF DAFF0D JC MIB 92 0DEB 2100C0 LXI H.#H0000 93 0DEE 19 DAD D 94 0DFP C30B0E JMP XVK	68	0000	PVLAY	EQU	#H3D30				
71 0000		0000	PVLBY	EQU	#H3D31				
72 0000 X EQU #H3D3A 73 0000 XY EQU #H3D3C 74 0000 XFLAG EQU #H3D3E 75 0000 YFLAG EQU #H3D3F 76 0000 YFLAG EQU #H3D3F 76 0000 DELTA EQU #H3D5B 77 0000 ICHAN EQU #H3D60 78 0000 USDAO EQU #H00EC 79 0000 PRTA1 EQU #H0000 80 0000 PRTB1 EQU #H0001 81 0000 PRTC1 EQU #H0003 83 0000 PRTB2 EQU #H0003 83 0000 PRTB2 EQU #H0003 84 0000 PRTD2 EQU #H0000 85 0000 PRTD1 EN #H0001 86 0000 PRTD2 EQU #H0006 87 0000 PRTD3 EQU #H0006 88 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK		0000	RINT		#H3D34				
73 0000	71	0000		EQU	#H3D38				
74 0000		0000							
75 0000									
76 0000 DELTA EQU #H3D58 77 0000 ICHAN EQU #H3D60 78 0000 USDAO EQU #H00EC 79 0000 PRTA1 EQU #H0000 80 0000 PRTB1 EQU #H0001 81 0000 PRTC1 EQU #H0003 83 0000 PRTB2 EQU #H0003 83 0000 PRTB2 EQU #H0006 84 0000 PRTD2 EQU #H0000 85 0000 PRTD2 EQU #H0001 86 0000 PRTD2 EQU #H0000 87 0000 PRTD3 EQU #H00E6 88 0D26 ORG #HDE6 89 0D26 7A MOV A.D 90 0D27 07 RLC 91 0D28 210000 LXI H.#H0000 93 0D28 19 DAD D 94 0D29 DAFFOD JC ALM 95 0D52 C30E0E JMP XVK									
77 0000 ICHAN EQU #H3D60 78 0000 USDAO EQU #H00EC 79 0000 PRTA1 EQU #H0000 80 0000 PRTB1 EQU #H0001 81 0000 PRTC1 EQU #H0002 82 0000 PRTB2 EQU #H0003 83 0000 PRTB2 EQU #H0006 84 0000 FRTC2 EQU #H0000 85 0000 PRTD1 ECU #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD2 EQU #H00E6 88 0D26 ORG #HDE6 89 0D26 ORG #HDE6 89 0D27 07 RLC 91 0D28 DAF50D JC MIB 92 0D28 210000 LMI H,#H0000 93 0D28 19 DAD D 94 0D29 C30B0E JMP XVK 96 0D59 C2002E JMP XVK									
78 0000 USDAO EQU #H00EC 79 0000 PRTA1 EQU #H0000 80 0000 PRTB1 EQU #H0001 81 0000 PRTC1 EQU #H0002 82 0000 PRTA2 EQU #H0003 83 0000 PRTB2 EQU #H0005 84 0000 PRTD2 EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H0006 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DE8 210000 LXI H.#H0000 93 0DEE 19 DAD D 94 0DEF DAFFOD JC ALM 95 0DF2 C30B0E JMP XVK									
79 0000 PRTA1 EQU #H0000 80 0000 PRTB1 EQU #H0001 81 0000 PRTC1 EQU #H0002 82 0000 PRTA2 EQU #H0003 83 0000 PRTB2 EQU #H0005 84 0000 PRTD2 EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DE8 210000 LX1 H.#H0000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LX1 H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE									
## ## ## ## ## ## ## ## ## ## ## ## ##									
81 0000 PRTC1 EQU #H0002 82 0000 PRTA2 EQU #H0003 83 0000 PRTB2 EQU #H0005 84 0000 PRTB2 EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DE8 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF9 D2050E JMC CDE 99 0DFC C30B0E JMP XVK									
82 0000 PRTAZ EQU #H0003 83 0000 PRTBZ EQU #H0005 84 0000 PRTCZ EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DE8 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF3 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE									
83 0000 PRTB2 EQU #H00ES 84 0000 FRTC2 EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF3 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE									
84 0000 FRTC2 EQU #H0000 85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF3 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE									
85 0000 PRTD1 E J #H0001 86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK									
86 0000 PRTD2 EQU #H0002 87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK									
87 0000 PRTD3 EQU #H00E6 88 0DE6 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK									
88 0D26 ORG #HDE6 89 0DE6 7A MOV A.D 90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK									
89									
90 0DE7 07 RLC 91 0DE8 DAF50D JC MIB 92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK									
92 0DEB 2100C0 LXI H.#HC000 93 0DEE 19 DAD D 94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H.#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK		0DE7 07							
93	91	ODES DAFSOD		JC	IIB				
94 0DEF DAFF0D JC ALM 95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H,#H4000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK	92	0DEB 2100C0		LXI	H. #HC000				
95 0DF2 C30B0E JMP XVK 96 0DF5 210040 MIB: LXI H,*#44000 97 0DF8 19 DAD D 98 0DF9 D2050E JMC CDE 99 0DFC C30D0E JMP XVK	93	0DEE 19		DAD	D				
96 @DFS 210040 MIB: LXI H,#H4000 97 @DFB 19 DAD D 98 @DF9 D2050E JNC CDE 99 @DFC C30D0E JMP XVK					ALM				
97 0DF8 19 DAD D 98 0DF9 D2050E JHC CDE 99 0DFC C30D0E JMP XVK		0DF2 C30B0E							
98 0DF9 D2050E JHC CDE 99 0DFC C30D0E JHP XVK			MIB:						
99 ØDFC C3ØDØE JMP XVK									
100 ODFF 210040 BLM: LXI H. #H4000									
	100	ODFF 210040	HLM:	LXI	H. #H4000				

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

BX

H. #H4000

JI1P

LXI

149

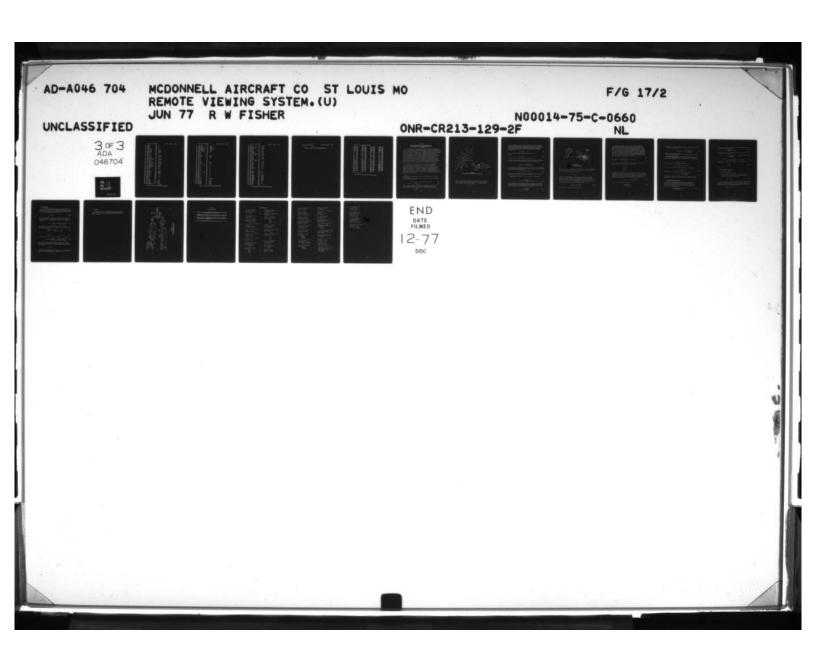
150

0E6A C3790E

0E6D 219940 LMA:

```
IP
151 0E70 C37C0E
                        JIMP
                                H. #HC000
152 0E73 2100C0 DEC:
                        LXI
153 0E76 C37C0E
                        JI1P
                                IP
                       CALL
                                MULBX
154 0E79 CDF43D BX:
155 0E7C 221E3D IP:
                        SHLD
                                IPOSX
                                DELTA
156
    0E7F 2A583D
                        LHLD
157
     0E82 EB
                        XC. HG
    0E83 7A
                                A.D
                        MOV
158
159
    0E84 07
                        RLC
160 0E85 D2920E
                       JHC
                                CLA
                                H. #H30
                       LXI
    0E88 213000
161
                       DAD
162 ØE8B 19
                                D
                                OUT
163
    0E8C DZD00E
                        JHC
                      JI1P
164 9EBF C3990E
                                GAIN
165 0E92 21D0FF CLA: LXI
                                H. #HFFD0
166
    0E95 19
                       DAD
                                D
                                OUT
167
    0E36 DAD00E
                        JC
168
    0E99 CD803E GAIN:
                       CALL
                                MICH
169
     ØE9C EB
                        XCHG
                                ICHAN
170
    0E9D 2A603D
                        LHLD
171
    0EA0 19
                       PAD
                                D
172
    OEA1 22603D
                       SHLD
                                ICHAN
173 0EA4 EB
                       XCHG
174 ØEAS 7A
                       MOV
                                A.D
                       RLC
175
    0EA6 07
    OEA7 DAB40E
                      JC
LKI
176
                                PA
177
    0EAA 2100C0
                                H. #HC000
                       DHD
178 ØEAD 19
                                D
179 OBAE DABEGE
                       JC
                                PB
                     JI1P
LXI
180 0EB1 C3C90E
                                PC
    0EB4 210040 PA:
                                H. #H4000
181
182
    0EP7 19
                        DAD
                                D
183
    OEPS D2C50E
                        JHC
                                PD
184 ØEBB C3C9ØE
                        JI1P
                                PC
                      LXI
185 0EBE 210040 PB:
                                H. #H4000
186 ØEC1 EB
                        KCHG
    0EC2 C3C90F
                       JMP
                                PC
187
188 ØEC5 2100C0 PD:
                       LXI
                                H. #HC000
189
                        XCHG
    OECB EB
                                IPOSX
190 0EC9 2A1E3D PC:
                        LHLD
191 ØECC 19
                       DIAD
                                D
192 ØECD 221E3D
                       SHLD
                                IPOSX
193 ØEDØ ZAZA3D OUT:
                        LHLD
                                PRLAX
194 ØED3 EB
                        XCHG
    0ED4 2A263D
0ED7 222A3D
                                PRJAX
195
                        LHLD
196
                        SHLD
                                PRLAX
197
    eeda zalasd
                        LHLD
                                IPRJX
193
    ØEDD 19
                        DAD
                                D
199
    OEDE 22F63F
                        SHLD
                                #H3FF6
200 ØEE1 29
                        DIAD
```

Figure D-4 Prom No. 4 Pitch Control Software (Continued)



```
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                                                                            PAGE 5
   INTEL 8080 CROSS ASSEMBLER
                                                   14:50:23
201
     0EE2 29
                           DAD
                                    H
202
     0EE3 29
                           DAD
                                    H
203
     0EE4 29
                           DIAD
                                    H
     0EE5 29
204
                           DAD
                                    H
205
     OEF6 EB
                           XCHG
30€
     OEE7 ZAEA3F
                           LHLD
                                    EOX
207
     OEEA 19
                           DIAD
208
     OEEB CD7B0F
                           CALL
                                    SHIFT
209
     OEEE CD7B0F
                           CALL
                                    SHIFT
210
     ØEF1 CD7BØF
                                    SHIFT
                           CALL
211
     ØEF4 EB
                           XCHG
212
     0EF5 7A
                           MOV
                                    A.D
213
     0EF6 07
                           RLC
214
     0EF7 D2040F
                                    OGDR
                           JHC
215
     DEFA 215515
                           LXI
                                    H. #H1555
216
     e"FD 19
                           DAD
                                    D
217
     OEFE P2140F
                                    DECR
                           JHC
     OF0: C31A0F
218
                           JIMP
                                    BXR
219
     0F04 21ABEA OGDR:
                           LXI
                                    H. #HEAAB
220
     0F07 19
                           DIAD
                                    D
221
     OFOR DAGEOF
                           JC
                                    LMAR
222
     eres calaer
                           JI1P
                                    BXR
                                    H. #H4000
223
     OFOE 210040 LMAR:
                           LXI
     OF11 C31DOF
                           JI'IP
224
                                    IPR
225
     0F14 2100C0 DECR:
                           LMI
                                    H. #HC000
226
     0F17 C31D0F
                           JIMP
                                    IPR
227
     OFIA CDE03D BXR:
                           CALL
                                    MULAX
228
     OFID CD7BOF IPR:
                           CALL
                                    SHIFT
229
     0F20 CD7B0F
                           CALL
                                    SHIFT
230
     0F23 EB
                           XCHG
                                    #H3D50
231
     0F24 2A503D
                           LHLD
     0F27 19
232
                           DAD
                                    D
233
     0F28 EB
                           MCHG
234
     0F29 22503D
                           SHLD
                                    #H3D50
     OF2C 2A523D
235
                           LHLD
                                    #H3D52
236
     OF2F CD7B0F
                           CALL
                                    SHIFT
     0F32 19
237
                           DAD
                                    D
238
     0F33 22523D
                                    #H3D52
                           SHLD
239
     0F36 EB
                           XCHG
240
     0F37 ZA1E3D
                           LHLD
                                    IPOSX
241
     0F3A 19
                           DIAD
                                    D
242
     OF3B EB
                           XCHG
243
     0F3C 7A
                           MOV
                                    A.D
244
     0F3D 07
                           RLC
     OF3E D24BOF
                                    VOGD
245
                           JHC
246
     0F41 214E00
                           LXI
                                    H, #H4E
247
     0F44 19
                           DAD
248
     0F45 D25B0F
                           JHC
                                    VDEC
249
     0F48 C3610F
                           JIMP
                                    VBX
250
     0F4B 21B2FF VOGD:
                           LXI
                                    h, #HFFB2
```

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

```
251
    0F4E 19
                          DAD
                                  VLMA
     OF4F DASSOF
                          JC
253
                         JMP
                                  VBX
     0F52 C3610F
254
                                  H. #H4000
     0F55 210040 VLMA:
                         LXI
255
     0F58 C3640F
                         JIMP
                                  VIP
256 0F5B 2100C0 VDEC:
                                  H. #HC000
                         LXI
257 0F5E C3640F
                         JIMP
                                  VIP
258 0F61 CDF83E VBX:
                                  PTQX
                         CALL
259 0F64 7C
                         MOV
                 VIP:
                                  A.H
     0F65 0F
260
                         RRC
261
     OFEE OF
                         RRC
262 0F67 0F
                         RRC
263 0F68 0F
                         RRC
                                 PVLBX
264 0F69 322F3D
                         STA
265 0F6C E6F0
                         ANI
                                  #HF0
366
    0F6E 67
                         MOV
                                 H.A
267
     OFEF 7D
                         MOV
                                  A.L
268 0F70 91
                         RRC
269 0F71 0F
                         RRC
270 0F72 0F
                         RRC
271 0F73 0F
                         RRC
                                  #HOF
272 0F74 E60F
                         I MA
273 0F76 B4
                         ORA
                                  H
274 0F77 322E3D
                         STA
                                  PVLAX
275
     0F7A C9
                         RET
276 9F7B 7C
                 SHIFT:
                         MOA
                                  A.H
277
    0F7C 07
                         RLC
278
    0F7D 7C
                         MOV
                                  A.H
279 0F7E 1F
                         RAR
280 0F7F 67
                         MOV
                                  H.A
281
     0F80 7D
                         MOV
                                  A.L
282
    0F81 1F
                         RIAR
283 GF82 6F
                         MOV
                                  L.A
284 0F83 C9
                         RET
285 0F84 7C
                 MINUS:
                         MOV
                                  A.H
286 0F85 2F
                         CMA
287
     0F86 67
                         MOV
                                  H. A
288 0F87 7D
                         MOA
                                  A.L
289 0F86 2F
                         CMA
290 0F89 6F
                         MOV
                                 L.A
291
    0F8A 23
                         INX
                                 H
292 0F8B C9
                         RET
293 0F8C 21523D
                                 H. #H3D52
                         LXI
294
    0F8F 34
                         INR
                                 M
295 JF90 7E
                         MOV
                                 A.M
296 0F91 FE64
                         CPI
                                 #H64
297 0F93 FABOOF
                         J11
                                 ALED
298 0F96 FEC8
                         CPI
                                  #HC8
299
    0F98 FA9E0F
                         J11
                                 BLED
300
    0F9B 3E00
                         MII
                                 A. #H00
```

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

SHIFT

EOX

EOX

D

CHLL

XCHG

LHLD

DAD

RET

SHLD

345

346

347

348

349

350

OFF2 CD7B0F

OFF6 ZAEA3F

OFFA 22EA3F

OFFS EB

0FF9 19

OFFD C9

INTEL 8080 CROSS ASSEMBLER

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END

Figure D-4 Prom No. 4 Pitch Control Software (Continued)

TAITTEE	CARA	CDACC	ASSEMBI ED	CUMBAI	TABLE
INIHI	HUH!	I WILL	HICKMINI NO	YMMIII	THHIR

PRLAX=	2020	PRLBX=	מכתני	PRLAY-	3020	PRLBY-	3020
PVLAX=		PVLBX=			3030	PVLBY-	
RINT =		TINT =			3D3A	XY -	3D3C
YFLAG=		DELTA=		USDAO=		PRTA1=	
PRTB1=		PRTC1=		PRTAZ=		PRTB2=	
PKTC2=	0000	PRTD1=		PRTD2=		PRTD3=	
MIB	ODF5						OEOE
		ALM	ODFF	XAK	0E0B	ETR	Solita State
OH	0E23	MOM	0E33	GOSH	0E39	OSH	0E3C
OGD	9E63	LMA	0E6D	ВХ	0E79	IP	0E7C
PA	0EB4	PB	GEEE	PD	ØEC5	PC	OEC9
OUT	0ED0	OGDR	0F04	LMAR	OFOE	BXR	OFIA
IPR	OF1D	VOGD	0F4B	VLMA	0F55	ADEC	OF5B
VBX	0F61	AIL	0F64	SHIFT	0F7B		0007
MIHUS	0F84	SLED	OFAO		0000	PLED	0FB2
BOX	OFBF	C =	1000	OUTPT	OFC0	D =	0002
CDE	0E05	DEC	0E73	E =	0003	KAD	0E29
CLA	0E92	BLED	OF9E	ALED	OFBO	LAG	OFCE
ICHAN=	3D60	GAIN	0E99	DECR	0F14	H -	0004
CAMAX=	3D00	CAMBX=	3D01	CAMAY=	3D02	CAMBY=	3D03
CCMAX=	3D46	CCMAY=	3D44	MICH =	3E80	XFLAG=	3D3E
CPOAX=	3D40	CPLAX=	3Dec	CPLBX=	3D0D	L -	0005
CPOBX=	3D41	CPOAY=	3D42	M =	0006	CPOBY-	3D43
CPLAY=	3D0E	EOX =	3FEA	CPLBY=	3D0F	DETX =	3D10
PETY =		DDOTX=	3D12	DDOTY=	3D14	DOTIX=	
DOTIY=	3D18	IPRJX=			0006	IPRJY=	
IPOSX=		IPOSY=		MUI CX=		MULDX=	
PSW =		MXLVB=		MXLVK=		MULAX=	
MULBX=		MULCY=		MULDY=		MYL VB=	
MYLVK=		MULRY=		MULBY=		MICHY=	
CMIY =		CMIX =		TORQY=		TORQX=	
	3EF8	NCPOX=	100	NCPOY=		PRJAX=	
PRJBX=		PRJAY=	100000000000000000000000000000000000000	PRJBY=		I KOHA-	3020
L KODA=	3041	KUHI	3020	LKABI	3027		

ERROKS DETECTED: 0

Figure D-4 Prom No. 4 Pitch Control Software (Concluded)

Appendix E

APPLICATION OF THE NIGHT VISION LABORATORY (NVL) THERMAL VIEWING SYSTEM STATIC PERFORMANCE MODEL TO THE RVS

It was suggested that the NVL Thermal Viewing System Static Performance Model, Reference (E-1) be used to evaluate the performance of the Remote Viewing System (RVS). However, repeated attempts to convert the RCS parameters directly to the NVL model have led to the following problem. The radial distortion function of the foveal lens does not lend itself to an MTF analysis as a function of object field angular spatial frequency as called for in the NVL model. All parameters can be converted successfully except for the scan velocity term because a linear raster scan on the lens image plane will create a variable angular velocity and variable direction scan in the object field. This is depicted in Figure E-1. Extreme complexity results when attempts are made to convert spatial into temporal frequency. This is illustrated by the rotation of the $f_{_{\boldsymbol{X}}}$ bar pattern in the lens image plane shown in Figure E-1. Given enough time, an analysis could be made in a manner compatible with the NVL model. However, the analysis is much simpler if performed, not in object field angular frequency (cycles/milliradian) but in spatial frequency terms (cycles/millimeter). For our purpose of optimizing the RVS lens, it is simpler to work in terms of spatial frequency on the foveal lens focal plane.

This simplicity arises because seven of the nine MTF's are independent of object field angle at this foveal lens focal plane location, and the scan velocity is undirectional and uniform at this location, thereby making easy conversion from spatial to temporal parameters. The only non-linear conversions necessary are simple geometrical ones which translate from focal plane to object field and display space. The advantages of working in the spatial frequency terms will become clear as the analysis is developed. In the following development, the NVL model approach will be used precisely but will be applied in the foveal lens focal plane as a function of linear spatial frequency (cy/mm). Parameters will be covered in the same order as they are in the NVL Report Reference E-1, which describes the model in detail.

E.1 MTF's

 $\underline{\text{Optical MTF}}$ The optical MTF's consist of a diffraction MTF and a Gaussian MTF.

(a) Diffraction In angular terms, the diffraction MTF is referenced as Equations

(9) and (10) of the NVL report:

$$H_{\text{opt}}(f_{\mathbf{x}}, \theta) = \frac{2}{\pi} \left[\cos^{-1} A - A(1 - A^2)^{1/2} \right]$$
 (E-1)

where
$$A = \lambda F_{\#} f_{\mathbf{X}} / L(\theta)$$
 (E-2)

where $L(\theta)$ is the equivalent focal length which changes over a 50/l range as object field angle θ changes. The angle θ is the absolute angle between the point of interest and the lens optical axis. At the foveal lens image plane

$$S_{x} = \frac{f_{\mu}}{L(\theta)} \tag{E-3}$$

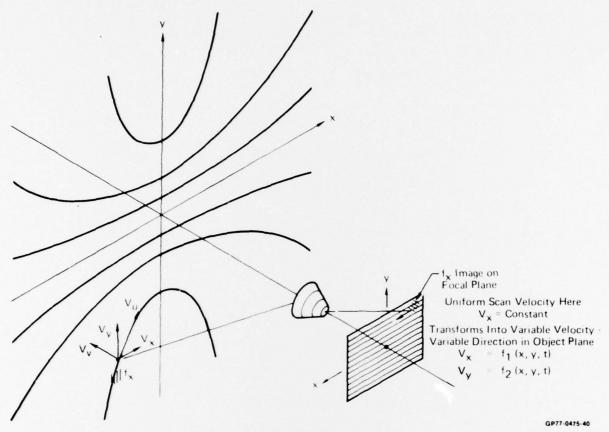


FIGURE E-1
SCAN DISTORTION INTRODUCED BY FOVEAL LENS

where S is the image plane spatial frequency and f is its object field angular equivalent measured along the scan line projection in the object field (μ direction on Figure E-1). Solving for f in Equation(E-3) and substituting this for f in Equation (E-2).

$$A = \lambda F_{\#} S_{\mathbf{x}}$$
 (E-4)

Since the F/number of our lens is constant, the diffraction MTF is no longer a function of object field angle. Thus we may write H (S) which indicates that the MTF is a function of the independent variable S only. Note, however, that conversion to object field angular spatial frequency is very simple because focal length is constant over small angular increments and may be determined from

$$f_{u} = S_{x}L(\theta) \tag{E-5}$$

where μ is along the scan line projection in the object field

likewise

$$f_{w} = S_{v}L(\theta) \tag{E-6}$$

where w is normal to the scan direction in the object field

(b) \underline{Blur} - A similar simplicity exists here. The MTF equation with the angular term b of Equation (11) of Reference(E-1) replaced with its equivalent is:

$$H_{blur}(f_{x},\theta) = \exp\left[-\frac{2\pi^{2}\sigma^{2}}{L(\theta)^{2}}f_{x}^{2}\right] \qquad (E-7)$$

The foveal lens inherently has a constant spatial blur over its entire focal plane, so that the sigma (σ) of Equation(E-7) is a constant. Substituting Equation(E-5) into (E-7) we see the blur MTF simplifies to

$$H_{blur}(S_x) = \exp \left[-2\pi^2 \sigma^2 S_x^2\right]$$
 (E-8)

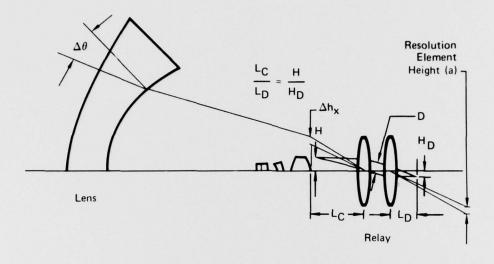
Thus this MTF like the diffraction MTF, is no longer a function of object field angle because the focal length variable has been removed.

Detection MTF - The spatial filter MTF of the detector is defined as:

$$H_{\text{Det}}(f_{\mathbf{x}}, \theta) = \frac{\sin(\pi f_{\mathbf{x}} \Delta \mathbf{x})}{\pi f_{\mathbf{y}} \Delta \mathbf{x}} \stackrel{\triangle}{=} \text{Sinc}(f_{\mathbf{x}} \Delta \mathbf{x})$$
 (E-9)

It is also complex in our system because the angular projection of the detector into the object field $(\Delta\theta)$ in this equation varies with absolute object field angle (θ). Since the detector height is still uniform at the lens focal plane, shown in Figure (E-2) as Δh , Equation (D-9) can be restated as:

$$H_{\text{Det}}(S_{x}) = \frac{\sin(\pi S_{x} \Delta h_{x})}{\pi S_{x} \Delta h_{x}}$$
 (E-10)



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FIGURE E-2 OPTICAL RELAY PARAMETERS

Again the MTF becomes independent of object field angle. Note from Figure(E-2)that the detector height $(\Delta h_{_{\mathbf{X}}})$ is a function of detector size(a), detector system focal length $(\mathbf{L}_{_{\mathbf{D}}})$, and relay focal length $(\mathbf{L}_{_{\mathbf{C}}})$, viz:

$$\Delta h_{x} \approx a_{x} \frac{L_{C}}{L_{D}}$$
 (E-11)

If the detector characteristics are known, the focal lengths are a function of detector size (Δh) projected unto the image plane as shown in Figure(E-2). Detector size Δh can be computed directly from either the on-axis resolution required, the number of scan lines required across the vertical FOV, or bandwidth/response restrictions and frame rate requirements. The focal lengths, L_{C} and L_{D} , are then selected to make the detector dimension appear as the required Δh at

$$H_{Det}(S_x) = Sinc \frac{S_x \cdot a_x \cdot L_C}{L_D}$$
 (E-15)

Again this MTF is independent of object field angle.

the foveal lens focal plane. The detector MTF becomes:

Detector Electronics MTF - It is in the MTF, the detector electrical response, that we get into real trouble trying to work in object field angular space. For a conventional linear optical system, a linear detector scan velocity converts into a scaled but linear angular scan in the object field. This is not true in our system as was shown in Figure E-1. A linear scan in the x direction on the image plane results in angular velocities in both θ and θ directions in the angular object field. Both of these angular components are nonlinear functions of both x and y position on the image plane. Thus, converting from spatial frequency to temporal frequency becomes very complex. All of this can be avoided by working in linear spatial plane terms. If the scanner has an angular scan velocity β , then the linear motion of the instantaneous FOV on the foveal lens image is

$$V_{x} = \beta L_{C}$$
 (E-16)

The conversion to temporal frequency (f) is therefore

$$f = V_{X} S_{X}$$
 (E-17)

This is a constant conversion and not a function of time. Therefore, all electronic MTF's of the NVL model are valid. These are

<u>Display</u> - The RVS display is the inverse of the foveal lens, which results in a conventional linear raster generated on the CRT. The CRT has a constant spot size and the expansion optics has a constant blur at the object focal plane. Again this MTF, if derived in the linear spatial plane, will not be a function of object angle. If the optical blur and CRT spot size are combined and assumed to have a Gaussian MTF, a composite sigma (σ_d) results and the MTF is:

$$H_{Disp}(S_{x}) = \exp\left[-2\pi^{2}(r\sigma_{d})^{2}S_{x}^{2}\right]$$
 (E-18)

where r is the physical ratio of format sizes; viz

$$r = \frac{H_{LENS\ IMAGE}}{H_{DISPLAY\ CRT}}$$
 (E-19)

By contrast, if this were accomplished in the object angular plane, the MTF would be much more complex, viz

$$H_{\text{Disp}}(f_{x,\theta,M}) = \exp \left[-\frac{2\pi^2 (r\sigma_d)^2 f_x^2}{L(\theta)^2 M^2} \right]$$
 (E-20)

where M is any system angular magnification from object field to the viewer. Again the simplicity is obvious.

Stabilization and Eyeball - The remaining two MTF's are the only two that are not simplified by working in linear spatial rather than angular terms. First, stabilization tends to be angular input to the system. Using the MTF from the NVL report:

$$H_{Los}(f_{x}) = \exp(-Pf_{x}^{2})$$
 (E-21)

Converting to the foveal lens image plane results in

$$H_{Los}(S_{x}, \theta) - \exp\left[-PS_{x}^{2}L(\theta)^{2}\right]$$
 (E-22)

Similarly, the eye views the display in angular terms. The NVL MTF is

$$H_{Eye}(f_x) = \exp\left[-\frac{\Gamma f_x}{M}\right]$$
 (E-23)

Equation(E-23) must be converted to the foveal lens image plane

$$H_{\text{Eye}}(S_{x,\theta}) = \exp\left[-\frac{\Gamma S_{x}L(\theta)}{M}\right]$$
 (E-24)

In conclusion, seven MTF's have been simplified at the expense of two that have been made slightly more complex by the conversion to linear spatial frequency.

E.2 NOISE EQUIVALENT MODULATION (NEM)

For visual spectrum applications noise equivalent modulation must replace NEAT in the NVL model. In the visual model, the primary noise source is the detector which is a silicon vidicon. Its NEM was extracted from data of Reference (E-2). These data show vidicon S/N as a function of faceplate illumination for a specific bandwidth. The basic function is approximately

where E is faceplate illumination in LUX. The noise equivalent signal is (signal input that just equal noise)

$$NEM = \frac{\text{noise}}{\text{signal}} = \frac{1}{100E}$$
 (E-22)

assuming that the noise is proportional to the square root of the bandwidth (Δf) of 4(10⁸) Hz. For data given:

NEM =
$$\frac{\Delta f}{100E \sqrt{4 \times 10^6}}$$
 = 5 x 10⁻⁶ $\frac{\sqrt{\Delta f}}{E}$ (E in LUX) (E-23)

For E in footcandles:

NEM =
$$\frac{4.64 \times 10^{-7}}{E}$$
 (E in Foot-Candles) (E-24)

The faceplate illumination can be calculated from system geometry as follows:

$$E_{f} = \frac{B^{T} a^{T} o}{4 F^{2}_{NO}}$$
(E-25)

Where

B=Scene brightness in footlamberts

T= Atmospheric transmission

T = Optical transmission within sensor

 $F_{\mbox{no}}^{-}$ The equivalent F/number or F/number actually supplying the vidicon. This is the lens F/number modified by the relay and from basic geometrical optical theory is:

$$F_{\text{noe}} = F_{\text{no}} \frac{L_{\text{D}}}{L_{\text{C}}}$$
 (E-26)

If the sensor employs an automatic light level control which operates on vidicon target current, E will be accurately maintained. Therefore, Equation (E-24) applies as written for the level of E which is preset. For the silicon vidon under study, best performance is obtained when the level is about 0.1 lumens/ft 2 . Equation(E-23)then becomes:

$$NEM = 4.64 \times 10^{-6} \quad \sqrt{\Delta f}$$
 (E-27)

E.3 MRM CALCULATIONS

The following MRM equation modifications are required so that the computation may be performed in linear spatial frequency terms. First, in the NVL MRT equation, Δy must be replaced by the apparent detector size at the foveal lens image plane, i.e., it must be the Δh defined on Figure E-2. As previously demonstrated in Equation (E-11).

$$\Delta h_y = a_y \frac{L_C}{L_D}$$
 (E-28)

Also, in the MRM equation, it is best to compute the Q integral in terms of temporal frequency. This eliminates the velocity term in the MRT equation and makes the Q integral easier to compute. The Q integral is therefore

$$Q(f,\theta) = \int_{0}^{\infty} \frac{S(f)}{S(f_0)} H_N^2(f) H_W \left(\frac{f}{V_X}\right)^2 H_{Eye} \left(\frac{f}{V_X}\right) df \qquad (E-29)$$

Of these terms, only ${\rm H}$, the transfer function for a rectangular bar of width ${\rm w}$, has not been defined. This transfer function is in linear rather than angular dimensions, i.e.,

$$H_{W}\left(\frac{f_{x}}{V_{x}}\right) = Sinc W\left(\frac{f_{x}}{V_{x}}\right) = Sinc \left(WS_{x}\right)$$
 (E-30)

where

$$W \stackrel{\triangle}{=} \frac{1}{2S_{x}}$$
 (E-31)

The MRM equation written to show the dependency of two variables is

$$MRM(S_{x}, \theta) = \frac{SNR\pi^{2}NEM}{4\sqrt{14} MTF_{TOTAL}(S_{x}, \theta)} \left[\frac{\Delta h_{y} S_{x}Q(f, \theta)}{\Delta f_{N}F_{R} t_{e} \eta_{OVSC}} \right]^{1/2}$$
(E-32)

This equation results in an MRT very weakly dependent on θ . To obtain the MRM for any field angle θ , we convert the spatial frequency term S into an angular frequency term by using Equation(E-9) containing the focal length function:

$$f_{11} = S_{x}L(\theta)$$

Note this will be the angular spatial frequency in the scan direction (target bars normal to the scan direction). It could be related to f and f but this does not appear to be required at this point.

E.4 CONCLUSIONS

To conclude this effort, a block diagram of the NVL model converted to the VARVS Concept in the visual spectrum is shown in Figure E-3. This model was used in the study to compute Minimum Resolvable Modulation to predict performance.

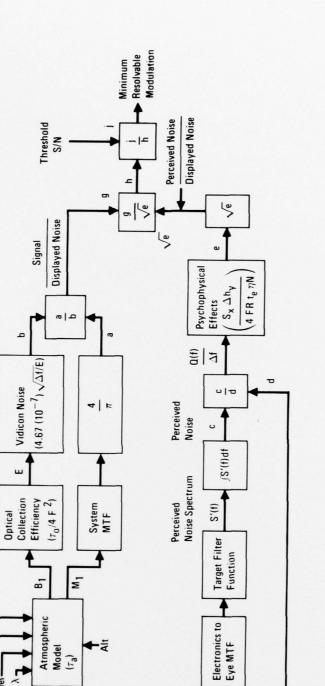


FIGURE E-3 NVL MODEL ADAPTED TO VARVS FOR VISUAL SPECTRUM

GP77-0475-7

Power Spectrum

Vidicon

S(f)

Displayed Noise __

 $\sigma_D^2 = S(t_0) \ \Delta t$

Pointing Az & EL — Solar Az & EL — Haze Level —

Bav Target Mod (MT)

APPENDIX

LIST OF REFERENCES

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- E-2 RCA, Inc., $\frac{4532\text{A Camera Tube Specification Sheet}}{\text{N.J., Jan.}}$ RCA Corp., Harrison,

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