# AD636097

# A SIMPLE PROOF OF A THEOREM ON SELF-SYNCHRONIZING AUTOMATA

Dale M. Landi

July 1966

CLEARINGHOUSE FOR FEDERAL SCIENTIFIC AND TECHNICAL INFORMATION Hardcopy Microfiche \$1.00 5 pp as :.50 ARCHIVE COPY AUG 4 1966 JU U C

P-3242-J.

# A SIMPLE PROOF OF A THEOREM ON SELF-SYNCHRONIZING AUTOMATA

# Dale M. Landi\*

The RAND Corporation, Santa Monica, California

A short proof is offered for verifying that a finite state, completely specified automation is synchronized with probability 1 only if there exists a universal synchronizer for the automation.

\* Any views expressed in this paper are those of the author. They should not be interpreted as reflecting the views of The RAND Corporation or the official opinion or policy of any of its governmental or private research sponsors. Papers are reproduced by The RAND Corporation as a courtesy to members of its staff.

## A SIMPLE PROOF OF A THEOREM ON SELF-SYNCHRONIZING AUTOMATA

WINOGRAD<sup>[3]</sup> has shown that a finite state, completely specified automaton will resynchronize itself with probability 1 if and only if there exists a finite sequence of input letters called a universal synchronizer of the automaton. The result is fundamental<sup>[1,2]</sup> and, as WINOGRAD's proof is unnecessarily long and circuitous, we offer a constructive argument that is more direct.

Following<sup>[3]</sup>, let 1\* represent the free semigroup with identity generated by a finite input alphabet I, i.e.,  $I^* = I^0 \cup I^1 \cup \ldots \cup I^k \cup \ldots$ , where  $I^k$  is the set of all input sequences of length k and  $I^0 = \phi$ , the identity element. A finite state, completely specified automaton A is a mapping from I\* onto some finite output alphabet; the set of states of A, S =  $\{s_1, s_2, \ldots, s_M\}$ , is finite with state transitions defined on all pairs of the cartesian product SXI\* by  $f(s_m, w) = s_n$ , where f is the state transition function,  $s_m$  and  $s_n$  are in S, and w is in I\*. Successive input letters to A are assumed to occur randomly and independently according to some discrete probability law Pr(I) =  $\{p_i\}$ , with  $p_i$  the probability of occurrence of the i<sup>th</sup> input letter  $(p_i \ge 0)$ . A subset  $E = \{(w_i, w_i')\}$  of I\*XI\* is called an error set and subsets  $C_{s_m}^k, (w_i, w_i')$  of I\* are defined by

$$C_{s_{m}}^{k}(w_{i},w_{i}^{*}) = \{w \in I^{k} | f(s_{m},w_{i}w) = f(s_{m},w_{i}^{*}w)\},\$$

i.e.,  $C_{s_m}^k$ ,  $(w_i, w_i^*)$  is the set of all input sequences w with length k that will synchronize A after an error  $w_i \rightarrow w_i^*$  occurs while A is in state  $s_m$ .

-1-

An automaton A with input distribution Pr(I) is synchronized with probability 1 with respect to an error set E if and only if for all  $s_m$  in S and all  $(w_i, w_i')$  in E

$$\lim_{k \to \infty} \Pr\{C_{s_m}^k, (w_i, w_i')\} = 1,$$

where

$$\Pr\{C_{s_{m}}^{k}, (w_{i}, w_{i}')\} = \sum_{w \in C_{s_{m}}^{k}, (w_{i}, w_{i}')} \Pr(w) .$$

It will be convenient to extend E to a larger set  $\mathcal{E} = EI^*$ , i.e.,  $(v_i, v_i^*)$  is in  $\mathcal{E}$  if and only if there exists a  $(w_i, w_i^*)$  in E and w in I\* such that  $(v_i, v_i^*) = (w_i, w_i^*)w = (w_i w, w_i^*w)$ .

A sequence u in I\* is a universal synchronizer of A with respect to E if and only if for all  $s_m$  in S and all  $(v_i, v_i')$  in  $\mathcal{E}$ ,  $f(s_m, v_i u) = f(s_m, v_i'u)$ .

THEOREM. A finite state, completely specified automaton A with input distribution Pr(I) is synchronized with probability 1 with respect to an error set E only if and only if there exists a universal synchronizer of A with respect to E.

The first portion of the proof is straightforward and is included here as it appears in Ref. 3 for the sake of completeness.

<u>Proof.</u> If u is a universal synchronizer of A with respect to E and if u has length m and Pr(u) = p, then the probability that u is not a factor of a sequence w with length km is less than or equal to  $(1-p)^k$ . Hence for all  $s \in S$  and all  $(w_i, w_i') \in E$ ,

$$\lim_{k \to \infty} \Pr\left\{ C_{s,(w_i,w_i^{\dagger})}^{km} \right\} \ge \lim_{k \to \infty} \left[ 1 - (1-p)^{k} \right] = 1.$$

Now, assuming A is synchronized with probability 1, partition  $S \times \mathcal{E}$ into N=1+ M(M-1)/2 equivalence classes,

$$\mathbf{w}_{t} = \{(\mathbf{s}_{m}, \mathbf{v}_{i}, \mathbf{v}_{i}^{\dagger}) \in S \times \mathcal{E} \mid f(\mathbf{s}_{m}, \mathbf{v}_{i}) = \mathbf{s}_{\mu}, f(\mathbf{s}_{m}, \mathbf{v}_{i}^{\dagger}) = \mathbf{s}_{\nu}, \mu \neq \nu\},\$$

one for each unordered, asymmetric pair of states, and one for the remaining triples

$$w_0 = \{(s_m, v_i, v_i') \in S \times \mathcal{E} | f(s_m, v_i) = f(s_m, v_i')\}, (t=0, 1, ..., N).$$

Distinct elements  $(s_m, v_i, v_i')$  and  $(s_n, v_j, v_j')$  of the same set  $w_t$  are indistinguishable with respect to the synchronizing process in that any sequence x in I\* that synchronizes A after the error  $(v_i, v_i')$  occurs in state  $s_m$  also synchronizes A after  $(v_j, v_j')$  occurs in state  $s_n$ . Since A is synchronized with probability 1, there is at least one synchronizing sequence for every non-empty  $W_t$ , call it  $u_t$ .

In the remainder of the proof  $(s_t, v_t, v_t')$  will be used to denote a representative element of  $W_t$ . Construct sequences  $z_1, z_2, \dots, z_N$  as follows:

$$z_0 = u_0 = \phi$$

and

$$z_t = z_{t-1} x_t, (t=1,2,...,N),$$

where

-3-

$$x_t = \phi$$
 if  $f(s_t, v_t z_{t-1}) = f(s_t, v_t z_{t-1})$ ,

or

trailing the

$$x_{t} = u_{n_{i}} \text{ if } f(s_{t}, v_{t}z_{t-1}) \neq f(s_{t}, v_{t}^{\dagger}z_{t-1})$$

and

$$(s_t, v_t^z_{t-1}, v_t^z_{t-1}) \in W_{n_i}$$
  $(1 \le n_i \le N).$ 

Then,  $f(s_j,v_jz_t) = f(s_j,v_jz_t)$  for  $j \le t$ , and it follows that  $u = z_N$ is a universal synchronizer of A.

### REFERENCES

- Gilbert, E. N., and E. F. Moore, "Variable Length Binary Encodings," <u>Bell Systems Tech. J.</u>, Vol. 38, 1959, pp. 933-967.
- Schutzenberger, M. P., "On the Synchronizing Properties of Certain Prefix Codes," <u>Information and Control</u>, Vol. 7, No. 1, March 1964, pp. 23-26.
- Winograd, S., <u>Input Error Limiting Automata</u>, IBM Research Report, RC-966, 1963 (also, <u>J. ACM</u>, Vol. 11, 1964, pp. 338-351).

-4-