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FACTORIZING SYMMETRIC INDEFINITE MATRICES
ON HIGH-PERFORMANCE ARCHITECTURES

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Factoring Symmetric Indefinite Matrices on High-Performance Architectures

Mark T. Jones* and Merrell L. Patrick†

Abstract

The Bunch-Kaufman algorithm is the method of choice for factoring symmetric indefinite matrices in many applications. However, the Bunch-Kaufman algorithm does not take advantage of high-performance architectures such as the Cray Y-MP. Three new algorithms, based on Bunch-Kaufman factorization, that take advantage of such architectures are described. Results from an implementation of the third algorithm are presented.

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†This research was supported by the National Aeronautics and Space Administration under NASA contract Nos. NAS1-18107 and NAS1-18605 and the Air Force Office of Scientific Research under AFOSR grant No. 88-0117 while the authors were in residence at ICASE. Additional support was provided by NASA grant No. NAG-1-466.
1 Introduction

The Bunch-Kaufman algorithm is considered one of the best methods for factoring full, symmetric, indefinite matrices [1], [2]. A modified version has been successfully used to factor sparse, indefinite matrices [3]. Recently, Bunch-Kaufman factorization has been shown to be the method of choice for a subset of banded, symmetric indefinite matrices [4].

The Bunch-Kaufman algorithm maintains the symmetry of the matrix during factorization and yields the inertia of the matrix essentially for free, an important consideration for eigenvalue calculations [1]. A drawback to the Bunch-Kaufman algorithm is its unsuitability for high-performance architectures. Herein, three new algorithms, based on Bunch-Kaufman factorization, are given for architectures such as the Cray Y-MP.

The technique of loop unrolling for vector architectures is discussed in section 2. In section 3, one of several variations of the Bunch-Kaufman algorithm is reviewed and the reason for its unsuitability for high-performance architectures is given. Three new algorithms for high-performance architectures are developed in section 4. Results showing the benefits of the third algorithm are given in section 5. Finally, a summary and description of future work is given in section 6.

2 Loop-Unrolling

Loop unrolling is a well known technique for improving performance on vector architectures. A loop is unrolled by restructuring it to allow more computation to take place at each step. A simple example of loop unrolling from [5] is given in Figure 1. The outer DO-loop has been unrolled to a depth of four. In the original program segment, three vector memory references were required for every two vector floating point operations. The ratio for the unrolled program segment is six vector memory references for every eight vector floating point operations. A significant decrease in the number of memory references has been achieved.

The reduction in the number of vector memory operations reduces the probability of delays due to memory latency times as well as the possibility of memory contention in a parallel computer [6].

Three other benefits of loop unrolling are described in [7]. The first is a
C Original program segment
DO 20 J = 1, N2
   DO 10 I = 1, N1
      Y(I) = Y(I) + X(J) * M(I,J)
   10   CONTINUE
20   CONTINUE
C In this example, the end condition if N2 isn't a multiple of four is ignored
DO 20 J = 4, N2, 4
   DO 10 I = 1, N1
      Y(I) = Y(I) + X(J-3) * M(I,J-3) + X(J-2) * M(I,J-2) +
      c X(J-1) * M(I,J-1) + X(J) * M(I,J)
   10 CONTINUE
20 CONTINUE

Figure 1: Simple loop unrolling example

reduction in loop overhead because fewer incrementing and testing operations
are required. This benefit can be reaped by any computer architecture.

For computers with segmented functional units, such as the CDC 7600,
the higher ratio of floating point operations to overhead operations will allow
a higher level of concurrency within a functional unit.

Computers with independent functional units, such as the Cray-1, benefit
from greater concurrency between the functional units.

The optimal depth of loop unrolling is largely dependent on the target
architecture. For example, if the independent functional units of a computer
are kept busy with loop unrolling of depth then increasing the depth to
p + 1 will not result in increased concurrency among functional units.

In the simple example in Figure 1, the results of iteration j of the outer
loop did not depend on results of previous iterations. Therefore, the outer
loop was easily unrolled. If LDLT decomposition is considered, however, each
iteration of the outer loop depends on the previous iterations (see Figure 2).
Unrolling the outer loop causes several pivot columns to be used simultane-
ously to update the remaining non-zeroes. For the algorithm to be correct,
the first pivot column must be used to update the other pivot columns, then
the second pivot column used to update the remaining pivot columns, and so
forth. After all the pivot columns are updated, they are used to update the
remaining non-zeroes. Conceptually, loop unrolling in LDLT allows the use
1) DO 10 I = 1, N
   C Compute the pivot column
2) DO 20 J = I+1, N
3)   V(J) = A(J,I)
4)   A(J,I) = A(J,I)/A(I,I)
5) 20 CONTINUE
   C Update the remaining non-zeroes
6) DO 30 J = I+1, N
7)   DO 40 K = J, N
8)   A(J,K) = A(J,K) - V(K)*A(J,I)
9) 40 CONTINUE
10) 30 CONTINUE
11) 10 CONTINUE

Figure 2: The $LDLT$ algorithm

of matrix-matrix operations rather than matrix-vector operations. A version of $LDLT$ unrolled to a depth of three is given in Figure 3.

Because three pivot columns are used to update the remaining non-zeroes in step 20, each time an element, $a_{j,k}$, is fetched six floating point computations are done, rather than just two as in step 8 of the original algorithm.

3 The Bunch-Kaufman Algorithm

The Bunch-Kaufman algorithm factors $A$, an $n \times n$ real symmetric indefinite matrix, into $LDLT$ while doing symmetric permutations on $A$ to maintain stability, resulting in the following equation:

$$PAP^T = LDL^T.$$  \hspace{1cm} (1)

Although several variations of the algorithm exist, the focus here is on algorithm D from [1] because it is the simplest to discuss. The methods described in section 4 are also applicable to Algorithm A described in [1], but not to Algorithm C (Algorithm B is mentioned in [1], but it is not described in detail).

The Bunch-Kaufman algorithm maintains stability by using 2x2 pivots combined with symmetric permutations to $A$ when a 1x1 pivot is not stable.
C Loop unrolled version
C The end condition for handling N not divisible by 3 has been ignored
1) DO 10 I = 1, N, 3
   C Compute the upper 3x3 triangle of the pivot columns
   2) V1(I+1) = A(I+1,I)
   3) A(I+1,I) = A(I+1,I)/A(I,I)
   4) V1(I+2) = A(I+2,I)
   5) A(I+2,I) = A(I+2,I)/A(I,I)
   6) A(I+1,I+1) = A(I+1,I+1) - V1(I+1)*A(I+1,I)
   7) V2(I+2) = A(I+2,I+1) - V1(I+1)*A(I+2,I)
   8) A(I+2,I+1) = V2(I+2)/A(I+1,I+1)
   9) A(I+2,I+2) = A(I+2,I+2) - V1(I+2)*A(I+2,I) - V2(I+2)*A(I+2,I+1)
   C Update and compute all three pivot columns
   10) DO 20 J = I+3, N
       11) V1(J) = A(J,I)
       12) A(J,I) = A(J,I)/A(I,I)
       13) V2(J) = A(J,I+1) - V1(I+1)*A(J,I)
       14) A(J,I+1) = V2(J)/A(I+1,I+1)
       15) V3(J) = A(J,I+2) - V1(I+2)*A(J,I) - V2(I+2)*A(J,I+1)
       16) A(J,I+2) = A(J,I+2)/A(I+2,I+2)
       17) 20 CONTINUE
   C Use the 3 pivots columns to update the remaining non-zeroes
   18) DO 30 J = I+3, N
   19) DO 40 K = J, N
       20) A(J,K) = A(J,K) - V1(K)*A(J,I) - V2(K)*A(J,I+1) -
           V3(K)*A(J,I+2)
       21) 40 CONTINUE
   22) 30 CONTINUE
23) 10 CONTINUE

Figure 3: $LDL^T$ unrolled to a depth of three
Because this paper will concentrate on 1x1 pivots, only stability for these pivots will be discussed in detail. A 1x1 pivot for element \( a_{i,j} \) at step \( k \) takes the form:

\[
a_{i,j} = a_{i,j} - a_{i,k}a_{j,k}/a_{k,k}.
\]

Let \( \mu_k \) be the maximum of the absolute values of the uneliminated elements at step \( k \). Step 2 of the algorithm (shown in Figure 4) finds the maximum element, \( \lambda \), in the pivot column. By substituting \( \mu \) and \( \lambda \) into equation 2, the bound on \( \mu_{k+1} \) becomes:

\[
\mu_{k+1} \leq \mu_k + \lambda^2/|a_{k,k}| \leq \mu_k(1 + \lambda/|a_{k,k}|).
\]

Step 4 ensures that a 1x1 pivot occurs if \( \alpha < |a_{i,i}|/\lambda \), where the parameter \( \alpha \) has been chosen to be 0.525 to maximize stability for Algorithm D [1]. By substituting \( \alpha \) into equation 3,

\[
\mu_{k+1} \leq \mu_k(1 + 1/\alpha).
\]

Therefore, the bound on the growth of an element due to a 1x1 pivot is 2.905.

If the test in step 4 is failed, a subsequent row search and another stability test determines if a 2x2 pivot and a permutation are necessary.

The stability checks and possible permutations at each step of the Bunch-Kaufman algorithm prevent the use of the same type of loop unrolling that is used for \( LDL^T \) decomposition. Because the stability checks and permutations must be completed before a pivot column is computed, pivot columns cannot be grouped as they were in Figure 2 without invalidating the bounds on element growth.

4 New Algorithm

This section will develop three ways in which the Bunch-Kaufman algorithm can be modified to allow pivot columns to be grouped together in one step. Because each 2x2 pivot involves a permutation of \( A \), it is not possible to group 2x2 pivots together. However, 1x1 pivots can be grouped with a 2x2 pivot if they follow the 2x2 pivot, allowing the permutation to precede the updating and computing of pivot columns. Each 2x2 pivot can be implemented using loop unrolling of depth 2. The general strategy in this section will be to try to group several 1x1 pivots into a single step in a stable fashion. The
1) for i = 1, n
   begin
   2) \( \lambda = \max_{j=i+1,n} |a_{j,i}| \)
   3) set \( r \) to the row number of \( \lambda \)
   4) if \( \lambda \alpha < |a_{i,i}| \) then
      begin
      5) perform a 1x1 pivot
      end
      else
      begin
      6) \( \sigma = \max_{j=i+1,n} |a_{r,j}| \)
      7) if \( \alpha \lambda^2 < \sigma |a_{i,i}| \) then
         begin
         8) perform a 1x1 pivot
         end
         else
         begin
         9) exchange rows and columns \( r \) and \( i+1 \)
         10) perform a 2x2 pivot
         11) i = i + 1
         end
         end
      end
   end
12) end
13) if inertia is desired, then scan the \( D \) matrix

Figure 4: The Bunch-Kaufman Factorization Algorithm
strategies described in this section are applicable to Algorithms A and D, but not C from [1]. Algorithm C cannot be unrolled using these strategies because a permutation occurs at every step.

The first approach uses pxp pivots in much the same way as the 2x2 pivots in the Bunch-Kaufman algorithm. The update of a single element of A using a 2x2 pivot at step k is

\[
a_{i,j} = a_{i,j} - \frac{(a_{i,k}a_{k+1,k+1} - a_{i,k+1}a_{k+1,k})a_{j,k} + (a_{i,k+1}a_{k,k} - a_{i,k}a_{k+1,k})a_{j,k+1}}{a_{i,i}a_{i+1,i+1} - a_{i+1,i+1}^2}.
\]

To obtain a bound for element growth, first define \( k \) at step \( k \)

\[
\lambda_1 = \max_{l=k+1,n} |a_{l,k}|
\]

and,

\[
\lambda_2 = \max_{l=k+1,n} |a_{l,k+1}|
\]

If \( \mu_k, \lambda_1, \) and \( \lambda_2 \) are substituted into equation 5, then

\[
\mu_{k+2} \leq \mu_k(1 + \frac{\lambda_1^2 + \lambda_2^2 + 2\lambda_1\lambda_2}{a_{i,i}a_{i+1,i+1} - a_{i+1,i+1}^2}).
\]

The bound on growth of \( \mu_{k+2} \) for a 2x2 pivot in Algorithm D is 8.526 [1]. Therefore, a 2x2 pivot can be performed if

\[
1 + \frac{(\lambda_1 + \lambda_2)^2}{|a_{i,i}a_{i+1,i+1} - a_{i+1,i+1}^2|} \leq 8.526.
\]

This derivation is similar to the 2x2 pivot stability analysis in [1]. This test differs from the Bunch-Kaufman 2x2 test because it groups two potential 1x1 pivots into one step. The Bunch-Kaufman 2x2 test is used when a 1x1 pivot is not stable. The new test precedes step 4 of the Bunch-Kaufman algorithm in Figure 4. This approach has two drawbacks: 1) the formulae for bounding the growth due to a pxp pivot become increasingly complicated as \( p \) increases, and 2) the inertia calculation is no longer just a search down the diagonal, it requires the solution of many pxp eigenproblems.

The second approach updates the potential pivot columns one at a time and, after each update, applies the 1x1 pivot stability check to determine if
4a) if $\lambda \alpha < |a_{i,i}|$ then
   begin
4b) compute the $i$th pivot column and use it to update column $i + 1$

4c) $\lambda_2 = \max_{j=t+1,n} |a_{j,i+1}|$

4d) if $\lambda_2 \alpha > |a_{i+1,i+1}|$ then
   begin
4e) compute the $(i + 1)$th pivot column and use it and column $i$ to
      update column $i + 2$

4f) $\lambda_3 = \max_{j=i+3,n} |a_{j,i+2}|$

4g) if $\lambda_3 \alpha > |a_{i+2,i+2}|$ then
   begin
4h) use columns $i$, $i + 1$, and $i + 2$ to update the remaining
      non-zeroes
   end
   else
   begin
4i) use columns $i$ and $i + 1$ to update the remaining non-zeroes
   end
   else
   begin
4j) perform a 1x1 pivot
   end
end

Figure 5: Approach Two for Loop Unrolling
further unrolling is possible. This test replaces steps 4 and 5 of the Bunch-Kaufman algorithm. An example of this test for up to three columns is given in Figure 5.

The third strategy uses an a priori approach to predict stability. The strategy predicts the stability of grouping $p$ 1x1 pivots without updating each potential pivot column. Only the upper $p \times p$ triangle must be updated to bound element growth. From equation 4, for $p$ successive 1x1 pivots to maintain stability, the maximum element growth must be bounded by

$$(1 + 1/\alpha)^p.$$  \hspace{2cm} (10)

At step $k$, let

$$\lambda_2 = \max_{j=k+2}^{n} | a_{j,k+1} |.$$  \hspace{2cm} (11)

The bound on element growth for a 1x1 pivot is $(1 + \lambda/a_{k,k})$, therefore the bound on $\lambda_2$ after a 1x1 pivot is

$$\hat{\lambda}_2 \leq \lambda_2 (1 + \lambda/a_{k,k}).$$  \hspace{2cm} (12)

Because the upper $p \times p$ triangle has been updated, a bound on $\mu_{k+2}$ for a second 1x1 pivot using column $k + 1$ is

$$\mu_{k+2} \leq \mu_{k+1} (1 + \hat{\lambda}_2/a_{k+1,k+1}).$$  \hspace{2cm} (13)

By substituting the bound for $\mu_{k+1}$ into equation 13

$$\mu_{k+2} \leq \mu_k (1 + \lambda/a_{k,k}) (1 + \hat{\lambda}_2/a_{k+1,k+1}).$$  \hspace{2cm} (14)

In general, the bound on $\mu_{k+p-1}$ for $p$ 1x1 pivots is

$$\mu_{k+p-1} \leq \mu_{k+p-2} (1 + \frac{\mu_{k+p-2} \lambda_{k+p-1}}{a_{k+p-1,k+p-1}}),$$  \hspace{2cm} (15)

where

$$\lambda_{k+p-1} = \max_{j=k+p}^{n} | a_{j,k+p} |.$$  \hspace{2cm} (16)

Given the bound for $p - 1$ 1x1 pivots, the only new information necessary is the updated value of $a_{k+p-1,k+p-1}$ and $\lambda_{k+p-1}$. If the bound for $\mu_k(p-1)$ is small enough then the $p$ pivot columns are computed at the same time and all used at the same time to update the remaining non-zeroes. The a
priori strategy has two advantages over the second strategy. First, it allows all the pivot columns to be updated and computed in one loop. The second strategy requires the pivot columns to be updated and computed one after the other. Therefore, the a priori method reaps the benefits of loop unrolling in the pivot column calculation. Second, the a priori method can combine a pivot that fails the 1x1 pivot test with a 1x1 pivot that is very stable if the combination of the two meets the stability criterion. The second method is unable to combine the two pivots if one of the pivots fails the 1x1 stability test. Another benefit the a priori method reaps from this combination is avoiding the search for $\sigma$ in step 5 of the Bunch-Kaufman algorithm. A disadvantage of the a priori method when compared with the second method, is the use of estimated bounds. In some cases these bounds are too pessimistic and thereby prevent the combining of 1x1 pivots when the combination is stable. Because the second method actually computes the pivot columns it does not have to use estimated bounds.

5 Results

A version of the a priori algorithm described in section 4 suitable for variable banded systems was implemented on a Cray Y-MP. The uniprocessor implementation allows loop unrolling up to depth six to take place. When the maximum depth of loop unrolling is fixed at one, this algorithm is identical to the Bunch-Kaufman algorithm. The Cray Y-MP is a register-to-register parallel/vector computer with up to eight processors. Each processor has independent, segmented functional units. An indefinite matrix that arose from an application in structural engineering was factored and the resulting triangular matrices were solved. The order of the matrix was 10,785 and the average bandwidth was 416. A significant reduction in factorization time and solution time for the resulting triangular systems, due to increasing depths of loop unrolling is shown in Figure 6. For this matrix, the combination of six pivot columns never met the stability criterion. The benefits of the algorithm will vary depending on the architecture and on the matrix being solved. For example, the same implementation was executed on the Convex C-220, an architecture with independent, segmented functional units. An indefinite matrix arising from the same application was factored, but with an order of 6734 and an average bandwidth of 336. The
### Figure 6: Effects of different depths of loop unrolling on the Cray Y-MP

<table>
<thead>
<tr>
<th>Depth</th>
<th>Factorization Time (sec.)</th>
<th>Speedup over Triangular Solution Time (sec.)</th>
<th>Bunch-Kaufman Speedup over Bunch-Kaufman</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>16.3</td>
<td>1.00</td>
<td>0.32</td>
</tr>
<tr>
<td>2</td>
<td>13.4</td>
<td>1.22</td>
<td>0.23</td>
</tr>
<tr>
<td>3</td>
<td>12.9</td>
<td>1.26</td>
<td>0.21</td>
</tr>
<tr>
<td>4</td>
<td>12.6</td>
<td>1.29</td>
<td>0.21</td>
</tr>
<tr>
<td>5</td>
<td>12.8</td>
<td>1.27</td>
<td>0.20</td>
</tr>
</tbody>
</table>

### Figure 7: Effects of different depths of loop unrolling on the Convex C-220

<table>
<thead>
<tr>
<th>Depth</th>
<th>Factorization Time (sec.)</th>
<th>Speedup over Triangular Solution Time (sec.)</th>
<th>Bunch-Kaufman Speedup over Bunch-Kaufman</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>71.77</td>
<td>1.00</td>
<td>1.30</td>
</tr>
<tr>
<td>2</td>
<td>50.99</td>
<td>1.41</td>
<td>1.05</td>
</tr>
<tr>
<td>3</td>
<td>46.85</td>
<td>1.53</td>
<td>0.98</td>
</tr>
<tr>
<td>4</td>
<td>45.37</td>
<td>1.58</td>
<td>0.96</td>
</tr>
<tr>
<td>5</td>
<td>45.15</td>
<td>1.59</td>
<td>0.97</td>
</tr>
</tbody>
</table>

maximum speedup due to the *a priori* algorithm is shown in Figure 7 to be significantly better for this combination of architecture and matrix than for the combination in Figure 6.

Observing the number of times each depth of loop unrolling is utilized can be useful in determining the best maximum depth of loop unrolling for a particular application. Shown in Figure 8 is the number of times that each depth of loop unrolling was utilized when factoring the same matrix used in the Convex C-220 test. These results will, of course, be different for every matrix factored, but may be similar for matrices arising from the same application.
<table>
<thead>
<tr>
<th>Maximum Possible Depth</th>
<th>No. of Depth 1</th>
<th>No. of Depth 2</th>
<th>No. of Depth 3</th>
<th>No. of Depth 4</th>
<th>No. of Depth 5</th>
<th>No. of Depth 6</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>6732</td>
<td>1</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>2</td>
<td>658</td>
<td>3038</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>3</td>
<td>516</td>
<td>1195</td>
<td>1276</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>4</td>
<td>543</td>
<td>992</td>
<td>429</td>
<td>730</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>5</td>
<td>561</td>
<td>874</td>
<td>439</td>
<td>432</td>
<td>276</td>
<td>-</td>
</tr>
<tr>
<td>6</td>
<td>561</td>
<td>874</td>
<td>439</td>
<td>432</td>
<td>276</td>
<td>0</td>
</tr>
</tbody>
</table>

Figure 8: Depths of loop unrolled achieved

6 Summary and Future Work

Three algorithms, each based on the Bunch-Kaufman algorithm, suitable for factoring symmetric indefinite matrices on high-performance architectures were given. The third algorithm, called the \textit{a priori} strategy, was determined to be superior to the other two and was implemented on two high-performance architectures, the Cray Y-MP and the Convex C-220. The \textit{a priori} algorithm was shown to be significantly faster than the Bunch-Kaufman algorithm.

The \textit{a priori} algorithm is also suitable for implementation on parallel architectures because it allows the use of matrix-matrix operations, rather than the matrix-vector operations used in the Bunch-Kaufman algorithm. The use of the \textit{a priori} algorithm will increase the ratio of computation to synchronization. The authors are currently implementing this algorithm on a parallel architecture.

Another possible application for the \textit{a priori} strategy is the factorization of sparse matrices. A variant of the Bunch-Kaufman algorithm that includes pivoting to preserve sparsity is given in [3]. A modified version of the \textit{a priori} strategy may improve the performance of this algorithm on high-performance architectures.
References


**Abstract**

The Bunch-Kaufman algorithm is the method of choice for factoring symmetric indefinite matrices in many applications. However, the Bunch-Kaufman algorithm does not take advantage of high-performance architectures such as the Cray Y-MP. Three new algorithms, based on Bunch-Kaufman factorization, that take advantage of such architectures are described. Results from an implementation of the third algorithm are presented.