A LOWER BOUND FOR THE TIME TO ASSURE INTERACTIVE CONSISTENCY

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A Lower Bound for the Time to Assure Interactive Consistency

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1. Introduction

The problem of "assuring interactive consistency" is defined in [PSL]. It is assumed that there are n isolated processors, of which at most m are faulty. The processors can communicate by means of two-party messages, using a medium which is reliable and of negligible delay. The sender of a message is always identifiable by the receiver. Each processor p has a private value $\sigma(p)$. The problem is to devise an algorithm that will allow each processor p to compute a value for each processor r, such that (a) if p and r are nonfaulty, then p computes r's private value $\sigma(r)$, and (b) all the nonfaulty processors compute the same value for each processor r.

It is shown in [PSL] that if $n < 3m + 1$, then there is no algorithm which assures interactive consistency. On the other hand, if $n \geq 3m + 1$, then an algorithm does exist. The algorithm presented in [PSL] uses $m + 1$ rounds of communication, and thus can be said to require "time" $m + 1$. An obvious question is whether fewer rounds of communication suffice to solve the problem.

In this paper, we answer this question in the negative. That is, we show that any algorithm which assures interactive consistency in the presence of m faulty processors requires at least $m + 1$ rounds of communication.

The remainder of the paper is organized as follows. Section 2 contains motivation for our formal model and problem statement, Section 3 contains the notation and definitions. Section 4 contains a reduction of our set of allowable algorithms to a more restrictive set of "uniform" algorithms. Section 5 contains a restatement of the relevant results of [PSL]. Section 6 contains our main lower bound result. Section 7 contains an important open question.

The reader is urged to read [LSP] and [L] for discussion of the practical importance of assuring interactive consistency, and [PSL] for additional results not immediately relevant to this paper. Other related papers are [DW] and [DJ.

2. Motivation for the Definitions

A general model for solving the interactive consistency problem might consist of n processors (automata) communicating by means of $n^2$ one-way "communication channels". Each channel can be formalized as a shared variable which can be modified by exactly one processor and read by exactly one processor. (Such a formalization can be carried out, for example, within the model of [LFJ].) The variables which each processor can modify are called its "out-channels", while the variables it can read are called its "in-channels".

Each processor p starts with an arbitrary private value $\sigma(p)$. Execution of the system proceeds in synchronous "rounds"; at each round, the following two steps occur: (1) First, each nonfaulty processor writes values ("sends messages") derived from its state into all of its out-channels, while each faulty processor writes arbitrary values into all of its out-channels. (2) Second, each processor
reads the values from all of its in-channels. After some specified number, k, of rounds, each processor p outputs a vector of values, one for each processor r. These outputs are required to satisfy conditions (a) and (b) stated in the Introduction.

If the only complexity measure of interest is the number of rounds, then we can assume without loss of generality that the messages sent by each nonfaulty processor p on the first round are all exactly equal to its private value σ(p), and that the messages sent by each nonfaulty processor on subsequent rounds are all exactly the set of messages received from all processors on the previous round. That is, if there is any correct k-round algorithm, then there is a correct k-round algorithm in which exactly the messages described above are sent. This is so because (i) it is clear that the given information is the maximum nontrivial information which could be sent, (ii) it does not hurt to send nonfaulty processors the maximum information, since they can derive any needed information from the given maximum information, and (iii) it does not hurt to send faulty processors the maximum information, since it is assumed that the faulty processors can send arbitrary messages in any case—i.e. they could "guess" any missing information.

In such a maximum-information algorithm, the output vector of each processor p is simply a function of the set of all values received by p at all rounds of the computation. (So far, this reduction is as in [PSL] .) In addition, if p is nonfaulty, then the set of messages received by p at all rounds of the computation is determined by the set of messages received by p at the last round (since p sends messages to itself at each round containing the information p received at earlier rounds). Since the correctness conditions involve only the outputs of nonfaulty processors, it suffices to formalize the output of p as a function of the set of messages received by p at the last round of communication only.

3. Notation and Definitions

If A is any alphabet, i,j ∈ N ∪ {0}, i ≥ j, we use A^{ij} to denote the set of strings of symbols in A, of length at least i and at most j.

Let P be the set of processors, |P| = n, and let m be an upper bound on the number of faulty processors. Fix V to be the domain of values on which the processors wish to reach agreement. Assume {0,1} ⊆ V.

For any k ∈ N ∪ {0}, let U^k denote the set of mappings from P^k into V. (An element of U^k is intended to represent a set of messages which a processor could receive at the last round of a computation.)

A k-round algorithm A (for P) is a set {F_p : p ∈ P} of functions, where F_p : U^k × P → V. A is uniform if F_p = F_q for all p, q ∈ P.

A k-round scenario (for P with m faults) is a mapping σ : P^{1k+1} → V, such that |T_e| ≥ n - m, where T_e (the set of truth-tellers) = {q ∈ P : σ(wqp) = σ(wq) for all p ∈ P and all w ∈ P^{0k-1}}. Intuitively, σ(p_1p_2...p_p) is intended to represent the value in V which p_1 told p_2 that p_1 told p_3 that p_1 told ... that p_1 told p_m was p_1's private value; as a special case, σ(p) represents p's private value. (Note that this definition reverses the direction of the string arguments in the [PSL] definition.)

Let L_e (the set of liars) denote P - T_e. If σ : P^{1k+1} → V and p ∈ P, then p's view of σ is the map σ_p : U^k → V.
(given by $\sigma_p(w) = \sigma(wp)$. Let $\Phi^*_p = \{\sigma_p : \sigma$ is a k-round scenario (for P with m faults) and $p \in T_o\}$. That is, $\Phi^*_p$ is the set of possible views for $p$ when $p$ is a truth-teller in a k-round scenario.

Let $A = \{F_p : p \in P\}$ be a k-round algorithm. Then $A$ assures interactive consistency (for P with m faults) provided for each k-round scenario $\sigma$ (for P with m faults), the following two conditions hold.

(a) (Validity) $F_p(\sigma_p, r) = \sigma(r)$ for all $p, r \in T_o$.

(b) (Agreement) $F_p(\sigma_p, r) = F_q(\sigma_q, r)$ for all $p, q \in T_o$ and all $r \in P$.

4. Reduction to Uniform Algorithms

In this section, we show that it suffices to restrict attention to uniform algorithms.

Lemma 1: Assume $n \geq 2m + 1$. Let $A = \{F_p : p \in P\}$ be a k-round algorithm which assures interactive consistency. Then $F_p(\sigma, r) = F_q(\sigma, r)$ for all $p, q, r \in P$ and all $\sigma \in \Phi^*_p \cap \Phi^*_q$.

Proof: Let $p, q, r \in P$, $\sigma \in \Phi^*_p \cap \Phi^*_q$. Then there are k-round scenarios $\sigma$ and $\tau$ such that $p \in T_\sigma, q \in T_\tau$, and $\sigma = \sigma_p = \tau_q$. Let $s \in T_\sigma \cap T_\tau$. (Such an $s$ is guaranteed to exist because $n \geq 2m + 1$.)

Modify only the last round of $\sigma$ and $\tau$ to obtain new k-round scenarios $\sigma'$ and $\tau'$, as follows. Let $\sigma'(w) = \sigma(wp)$ for all $w \in P^k$, and let $\sigma'(x) = \sigma(x)$ otherwise. Similarly, let $\tau'(w) = \tau(wp)$ for all $w \in P^k$, and let $\tau'(x) = \tau(x)$ otherwise. It is easy to check that $\sigma'$ and $\tau'$ are scenarios, that $\{p, s\} \subseteq T_\sigma, \{q, s\} \subseteq T_\tau$, and that $\alpha = \sigma_p = \sigma_q = \tau_q = \tau'_q$. Thus, $F_p(\alpha, r) = F_q(\alpha, r) = F_p(\sigma, r)$ by the agreement property, $= F_q(\sigma, r) = F_p(\tau, r)$ by the agreement property, $= F_q(\tau, r)$.

Theorem 1: Assume $n \geq 2m + 1$. If there is a k-round algorithm which assures interactive consistency, then there is a k-round uniform algorithm which assures interactive consistency.

Proof: Let $A = \{F_p : p \in P\}$ be a k-round algorithm which assures interactive consistency. Define $F: \forall^k \times P \rightarrow V$ as follows. Let $F(\alpha, r) = F_p(\alpha, r)$ if $\alpha \in \Phi^*_p$, 0 otherwise.

Lemma 1 shows that this definition is consistent. Then the algorithm which uses $F$ for all processors is a k-round algorithm which assures interactive consistency.

5. Earlier Results

In this section, we state the two relevant results from [PSL].

Theorem 2: Assume $n \leq 3m + 1$. Then there is no algorithm which assures interactive
consistency.

**Proof:** [PSL].

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**Theorem 3:** Assume \( n \geq 3m + 1 \). Then there is an \( m + 1 \)-round uniform algorithm which assures interactive consistency.

**Proof:** [PSL].

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6. Lower Bound

In this section, we present our main result.

**Theorem 4:** If \( k \leq m \), then there is no \( k \)-round algorithm which assures interactive consistency.

**Proof:** The theorem is easily seen to be true if \( m = 0 \), so assume that \( m \geq 1 \). Assume that the theorem is false: that \( k \leq m \) and there is a \( k \)-round algorithm \( A = \{ F_p : p \in P \} \) which assures interactive consistency. By Theorem 1, we can assume that \( A \) is uniform, i.e. that \( F_p = F \) for all \( p \in P \). By Theorem 2, we know that \( n \geq 3m + 1 \).

Define a relation \( \sim \) on \( \mathbb{N}^k \) as follows. Let \( \alpha \sim \beta \) provided there exist a \( k \)-round scenario \( \sigma \) and \( p, q \in T_\sigma \) for which \( \alpha = \sigma_p \) and \( \beta = \sigma_q \). Let \( \equiv \) be the smallest equivalence relation containing \( \sim \). By the agreement property, we have:

**Fact 1:** \( F(\alpha, r) = F(\beta, r) \) for all \( \alpha, \beta \in \mathbb{N}^k \) with \( \alpha \equiv \beta \), and all \( r \in P \).

For each \( v \in V \), \( w \in P^k \), let \( \gamma_v(w) = v \). By the validity property, we have:

**Fact 2:** \( F(\gamma_v, r) = v \) for all \( r \in P \) and all \( v \in V \).

Define an arbitrary total order on \( P \), let \( N = n^k \), and let \( I : P^k \rightarrow \{1, \ldots, N\} \) be a bijection corresponding to lexicographic order on the strings in \( P^k \). That is, if \( v, w \in P^k \), \( 0 \leq i \leq k-1 \), \( p, q \in P \), \( v = r_i \ldots r_0 p \), \( w = r_i \ldots r_0 q \) and \( p < q \), then \( I(v) < I(w) \).

For \( 1 \leq a \leq N + 1 \), define \( a : P^k \rightarrow \{0, 1\} \) by

\[
a_a(w) = \begin{cases} 
0 & \text{if } I(w) < a, \\
1 & \text{otherwise.}
\end{cases}
\]

Note that \( a_1 = \gamma_1 \) and \( a_{N+1} = \gamma_0 \).

We claim that \( a_a \sim a_{a+1} \) for all \( 1 \leq a \leq N \). If so, then \( \gamma_1 = a_1 \sim a_2 \sim \ldots \sim a_{N+1} = \gamma_0 \), so that \( \gamma_1 \equiv \gamma_0 \). Fix any \( r \in P \). By Fact 1, \( F(\gamma_1, r) = F(\gamma_0, r) \). However, by Fact 2, \( F(\gamma_1, r) = 1 \) and \( F(\gamma_0, r) = 0 \). This provides the needed contradiction.

It remains to prove the claim. Fix \( a, 1 \leq a \leq N \), and choose \( r_1, \ldots, r_k \) so that \( I(r_1 \ldots r_k) = a \). By
assumption, \( n-k \geq n-m \geq 3m+1-m = 2m+1 > 2 \), so that there exist two distinct participants, \( r_{k+1} \) and \( r_{k+2} \) in \( P \{r_1,...,r_k\} \). Assume without loss of generality that \( r_{k+1} > r_{k+2} \) in the total order on \( P \). We construct a \( k \)-round scenario \( \sigma \) with \( L_\sigma \subseteq \{r_1,...,r_k\} \), in which

\[
\sigma_{r_{k+1}} = \alpha_a \quad \text{and} \quad \sigma_{r_{k+2}} = \alpha_{a+1}.
\]

Let \( \sigma(w) = \begin{cases} 0 & \text{if } w = r_1...r_\alpha, p \in P, x \in P^0, \alpha \leq k+1, p < r_{k+1}, \\ 1 & \text{otherwise.} \end{cases} \)

We show that \( \sigma \) is a \( k \)-round scenario, with \( L_\sigma \subseteq \{r_1,...,r_k\} \). Let \( q \in P \cdot \{r_1,...,r_k\}, \ p \in P \) and \( w \in P^0 \). We must show that \( \sigma(wq) = \sigma(wqp) \). Now, \( |wq| \leq k \), so that \( wq \) is of the form \( r_i...r_j.sx \), where \( i \leq k-1, s \in P, \) and \( s \neq r_{k+1} \). Then \( \sigma(wq) = \begin{cases} 0 & \text{if } s < r_{i+1}, \\ 1 & \text{otherwise}, \end{cases} \)

\[
\sigma(r_1...r_\alpha.sxp) = \sigma(wqp).
\]

Next, we show that \( \sigma_{r_{k+1}} = \alpha_a \). Let \( w \in P^k \). Then \( \alpha_a(w) = \begin{cases} 0 & \text{if } |w| < a, \\ 1 & \text{otherwise}, \end{cases} \)

\[
\sigma(wr_{k+1}) = \sigma_{r_{k+1}}(w).
\]

Finally, we show that \( \sigma_{r_{k+2}} = \alpha_{a+1} \). Let \( w \in P^k \). Then \( \alpha_{a+1}(w) = \begin{cases} 0 & \text{if } |w| \leq a, \\ 1 & \text{otherwise}, \end{cases} \)

\[
\sigma(wr_{k+2}) = \sigma_{r_{k+2}}(w) \quad \text{since } r_{k+2} < r_{k+1}.
\]

Note that Theorem 3 (of the previous section) provides an upper bound on both the number of processors and the number of rounds. Thus, it demonstrates that both the lower bounds of Theorems 2 and 4 are tight.

### 7. Open Question

The most important question remaining involves the amount of communication and storage needed to assure interactive consistency. The algorithm in [PSL], which uses the minimum possible number of rounds, involves sending enormous amounts of information - approximately \( n^{m+2} \) values in \( v \). We would like to know if this amount can be reduced, say to an amount polynomial in \( n \) and \( m \) (using either the minimum number, \( m + 1 \), of rounds, or perhaps a larger number of rounds). An algorithm using such a reduced amount of communication might be of considerably more practical value than the current algorithm.
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References


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then p computes r's private value δ(r), and (b) all the nonfaulty processors compute the same value for each processor r.

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In this paper, we answer this question in the negative. That is, we show that any algorithm which assures interactive consistency in the presence of m faulty processors requires at least m + 1 rounds of communication.